Brice Vadnais

bricevadnais@gmail.com

<u>Campus Address</u>
University of Hartford
200 Bloomfield Avenue
West Hartford, CT 06117
(860) 768-5454

<u>Permanent Address</u>
1071 South Drive
Merrick NY, 11566
(516) 512 - 0278

Objective: Use the knowledge gained from college career to pursue a full time position in a computer engineering related field.

Education: **B.S. in Computer Engineering and a Minor Computer Science**
University of Hartford, West Hartford, CT
GPA: 3.70 / 4.0   Anticipated Graduation Date: May 2018

Honors: Dean's List   Fall and Spring: 2014, 2015, 2016, 2017
President's List   Fall: 2014, 2017; Fall and Spring 2015, 2016

Relevant Courses:

Electrical Circuit Analysis	System Design and Implementation
Simulation and Rapid Prototyping	Calculus-Differential Equations
Microprocessor Applications	Computer Networks
Computer Architecture	Data Structures
Computer Operating Systems	Database Systems

Work Experience: **Critical National Infrastructure (CNI) Intern,** PSEG LI, Hicksville, NY *June 2017 – August 2017*
Built a web application using ColdFusion to track repairs being used in the field and the state of completion. Assisted CNI group with server maintenance and tracked UPS backups.

Subway Lead, Aramark**,** Subway, University of Hartford, CT   *Fall 2016 – Fall 2017*
Ensure that students perform their roles properly, as well as helping out as necessary on the line to guarantee a smooth operation.

Key Projects: **8-bit FPGA Microcontroller**
Capstone project. A custom 8-bit microcontroller to interface with peripheral devices over serial. Bootloader to load different programs as necessary. Implemented on an FPGA.

Instruction Set Simulator
A program built in python to simulate an instruction set for custom microprocessor. Built-in parsing for assembly of custom assembly language.

Design, Simulation, and Implementation of Equalizer
Designed an 8-band equalizer with second order band pass filters. Simulated using PSpice, and implemented on breadboard.

Design, Simulation, and Implementation of an 8-bit Data Latch Memory Module
Designed a memory module for a microcontroller in PSpice and designed a 2-layer PCB to implement it.

Technology Skills: OrCAD PSpice Capture | Allegro AMS Simulator | Python | Java | VHDL | SQL | Bash Shell | Linux | Windows | Xilinx ISE | Quartus Prime | Microsoft Word | Microsoft Excel | Sublime Text | Netbeans IDE | Git | Allegro PCB editor | Matlab | C++| Soldering

Technical Experience: **Computer Building**
Built and constructed computers for personal use and for friends. All started successfully and have worked consistently.

Breadboarding
Familiar with how to implement and test circuits on a breadboard using various logic components.

Lab Equipment
Very familiar with much of the equipment in an electrical lab. Such as: Digital Multimeter, Oscilloscope, and Variable Power Supply