### KNE343 Power Electronics Performance Parameters:

Power converters control energy supply to a source.

Uses feedback loop structure to regulate the supply around a desired set point.

Design aims:

- Efficiency  $\nu = \frac{P_{out}}{P_{in}} \times 100 \%$  Reliability, how does the supply change and controller design affect the running system's supply.

Types of converters:

- AC-DC Recifiers
- DC-AC Inverters
- DC-DC Buck and Boost
- AC-AC Converter

Need for power conversion:

- Distribution based on 50 of 60 Hz signal
- Simplicity of network design
- Inverting DC power sources.

## **Equations:**

Instantanious Power:  $p(t) = v(t) \times i(t) =$  $V_m \cos(\omega t + \theta_v) \times I_m \cos(\omega t + \theta_i) =$  $I_{\rm rms}V_{\rm rms}(\cos(\theta_{\rm diff})+\cos(2\omega t))$ Average Power:  $P = \frac{1}{T} \int_0^T p(t) dt =$  $V_{\rm rms}I_{\rm rms}\cos(\theta_{\rm diff})$ Apparent Power:  $S = V_{\text{rms}}I_{\text{rms}}$ Power Factor:  $\text{pf} = \frac{Pav}{S} = \frac{\text{Average Power}}{\text{Apparent Power}} =$  $I_{1,\mathrm{rms}}$ Reactive Power:  $Q = S \cos(\theta_{\text{diff}})$ Power Factor Correction:  $C = \frac{|Q_{cap}|}{|V_{cap}|^2 \omega}$ Stored Power: Capacitor  $\frac{1}{2}Cv^2(t)$  Inductor  $\frac{1}{2}Li^{2}(t)$ Fourier Series: f(t) $\sum_{\infty}^{n=1} a_n \cos n\omega t + \sin n\omega t$  $= \frac{1}{T} \int_0^T f(t)dt$  $a_0 = \frac{1}{T} \int_0^t f(t)dt \quad ; \quad a_n$   $\frac{1}{T} \int_0^T f(t)dt \cos n\omega t \quad dt \quad ; \quad b_n$  $\frac{1}{T}\int_0^T f(t)\sin n\omega t \ dt$ 

DC/Average: 
$$V_{\text{dc}} = \frac{1}{T} \int_{T}^{0} v(t) \ dt$$

$$V_{\text{rms}} = \sqrt{\frac{1}{T} \int_{0}^{T} v^{2}(t) \ dt}$$
Distortion Factor:  $DF = \frac{I_{1,\text{rms}}}{I_{\text{rms}}}$ 

$$\text{Total Harmonic Distortion:} \quad THD = \sqrt{\frac{I_{1,\text{rms}}^{2} - I_{\text{rms}}^{2}}{I_{1,\text{rms}}^{2}}} = \sqrt{DF^{2} - 1}$$
Form Factor:  $FF = \frac{V_{\text{rms}}}{V_{\text{dc}}}$ 
Ripple Factor:  $RF = \sqrt{FF^{2} - 1}$ 

### Devices

BJT, MOSFET, IGBT diodes and thyristors. BJT are slow, high voltage/current. MOSFET are fast, low voltage/current. IGBT are in the middle.

### **Rectifier Circuits:**

Half Wave Rectifier:  $\int \sin \omega t$ ;  $0 < t < \pi$  $\pi < t < 2\pi$ Diode in series with load.  $V_{\rm dc} = \frac{V_m}{\pi}$  $V_{\rm rms} = \frac{V_m}{2}$  $FF = \frac{\pi}{2} \; ; \; RF = \sqrt{\frac{\pi^2}{2} - 1} \; ; \; DF =$  $\sqrt{2}$ ; THD = 1Inductance:

Extinction angle  $(\beta = \pi + \sigma)$  is proportional to inductor size. Stops the diode turning off,  $\sigma$  is some small time after which the diode should turn off that the inductor keeps current flowing.

$$V_{\rm dc} = \frac{V_{\rm m}}{2\pi} [1 - \cos(\beta)]$$

 $V_{\rm dc} = \frac{V_{\rm m}}{2\pi} [1 - \cos(\beta)] \qquad \qquad V_{\rm dc} = \frac{\tau_{\rm dc}}{\pi} \left(1 + \cos(\beta)\right)$ This reduction is restored by applying a  $V_{\rm rms} = \frac{\sqrt{2}V_m}{2} \sqrt{\frac{1}{2} - \frac{\alpha}{2\pi} + \frac{\sin 2\alpha}{4\pi}}$ freewheeling diode (reverse diode paralell In the full was wave rectifier, source into the load).

Battery, calculate the  $V_{\rm rms}$  etc by superposition.

 $P_{\text{total}}^{1/30} = I_{\text{dc}}V_{\text{dc}} + \sum_{n=1}^{\infty} I_{\text{n,rms}}V_{\text{n,rms}}\cos(\theta_{\text{n,diff}})$  Capacitance makes the voltage continuous. Fourier series:  $v(t) = \frac{V_m}{\pi} + \frac{V_m}{2} \sin \omega t$ 

$$\begin{array}{ll} \sum_{n=2,4,6...}^{\infty} \frac{2V_M}{(n^2-1)\pi} \cos n\omega t \\ \text{Full wave rectifier:} \qquad V_0(t) \qquad = \\ \left\{ \begin{array}{ll} \sin \omega t & ; \ 0 < t < \pi \\ -\sin \omega t & ; \ \pi < t < 2\pi \end{array} \right. \\ V_{\text{dc}} = \frac{2V_m}{\pi} \; ; \ V_{\text{rms}} = \frac{V_m}{\sqrt{2}} \; ; \ FF = \frac{\pi}{\sqrt{2}} \; ; \ RF = \\ \sqrt{\frac{\pi^2}{2} - 1} \end{array} \\ \text{Fourier series:} \end{array}$$

$$V_o(t) = \frac{2V_m}{\pi} + \frac{4V_m}{\pi} \sum_{n=2,4,6}^{\infty} \frac{-1}{(n-1)(n+1)}$$

Three phase half wave: 
$$v(t) = \begin{cases} V_m \sin(\omega t + \frac{2\pi}{3}) & ; t < \frac{\pi}{6} \\ V_m \sin(\omega t) & ; \frac{\pi}{6} < t < \frac{5\pi}{6} \\ V_m \sin(\omega t - \frac{2\pi}{3}) & ; \frac{5\pi}{6} < t < \pi \end{cases}$$
Full wave switches every 30° instead of 120°

Conducts from turn on (gate-sink voltage

> 0) until the source-sink current stops,

synching the gate signal frequency with the

input frequency and adding an offset results

Thyristors:

in a turn on angle  $\alpha$ .

 $v(t) = \begin{cases} V_m \sin \omega t & ; \alpha < t < \pi \\ 0 & ; \text{otherwise} \end{cases}$ 

 $v(t) = \langle -\sin \omega t \ ; \pi + \alpha < t < 2\pi \rangle$ 

: otherwise

ductance stops the thyristors from being

backward biased for some time  $\mu$  after the

gate signal  $\alpha$  occurs reulting in all 4 diodes

being on. on at the same time, which re-

sults in no voltage at the load while energy

 $V_{\rm rms} = V_m \sqrt{\frac{1}{2} - \frac{\alpha}{2\pi} + \frac{\sin 2\alpha}{4\pi}}$ 

Half wave rectifier:

 $V_{\rm dc} = \frac{\dot{V_m}}{\pi} (1 + \cos \alpha)$ 

Full wave rectifier:

 $V_{\rm dc} = \frac{2\dot{V}_{\rm dc}}{1 + \cos\alpha}$ 

from the inductance dissapates.

$$v(t) = \begin{cases} \sin \omega t & ; \alpha + \mu < t < \pi + \alpha \\ -\sin \omega t & ; \pi + \alpha + \mu < t < 2\pi + \alpha \\ 0 & ; \text{ otherwise} \end{cases}$$

Note that the gate pulse is twice every cycle. In 3 phase it as at 6 times the frequency.

Three phase(for  $0 < t < 2\pi$ ):

$$v(t) = \begin{cases} -\sin(\omega t + 120^{o}) & 0 < t < \alpha \\ \sin(\omega t + 120^{o}) & \alpha < t < \frac{\pi}{6} + \alpha \\ \sin \omega t & \frac{\pi}{6} + \alpha < t < \frac{\pi}{2} + \alpha \\ \sin(\omega t - 120^{o}) & \frac{\pi}{2} + \alpha < t < \frac{5\pi}{6} + \alpha \\ \sin(\omega t + 120) & \frac{5\pi}{6} + \alpha < t < \frac{7\pi}{6} + \alpha \\ -\sin \omega t & \frac{7\pi}{6} + \alpha < t < \frac{3\pi}{2} + \alpha \\ -\sin(\omega t - 120^{o}) & \frac{3\pi}{2} + \alpha < t < \frac{11\pi}{2} + \alpha \\ -\sin(\omega t + 120^{o}) & \frac{11\pi}{6} + \alpha < t < 2\pi \end{cases}$$

### DC-DC Converters

Solving converters, consider both switch on and off states, draw assuming ideal parts, apply nodal analysis for switch on and off. Assume steady state,  $\int_0^{T_s} V_{L_1} dt = 0$ , and constant current. Input power is equal to the output power,  $P_{in}=P_{out}$ . Variation when switch on,  $L\frac{\Delta i}{\Delta t}=V \implies \Delta i=\frac{DT_sV}{L}$ for each inductor.

Takes unregulated DC and outputs regulated DC. Buck reduces, Boost increases and Buck-Boost, Cuk and SEPIC increase or decrease.

or decrease. 
$$V_oI_o = V_sI_s$$

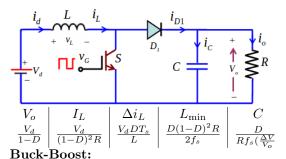
$$D = \frac{T_{\text{on}}}{T_{\text{on}} + T_{\text{off}}} = \frac{T_{\text{on}}}{T_s}$$
**Buck:**

$$i_d$$

$$G$$

$$i_L$$

 $\frac{\frac{\Delta V}{V_o}}{\frac{1-D}{8LCf_s^2}}$ D $\Delta i_L$  $L_{\min}$  $V_o(1-D)T_s$ (1-D)R $2f_s$ 



 $\Delta i_L$ 

 $L_{\min}$ 

 $(1-D)^2R$ 

 $L_{2,min}$ 

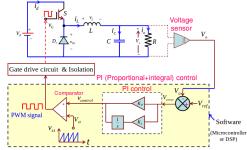
(1-D)R

 $\frac{\frac{V_o}{V_d}}{\frac{D}{1-D}}$ 

Cuk:

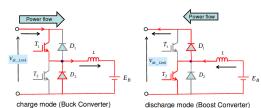
 $I_L$ 

 $\underline{1-D}$ 

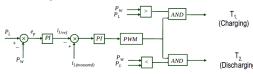


Output volatge is a function of input voltage, and duty ratio, which means that we may need to modify the duty ratio in reaction to a change in input or load, using  $_{R}$  a feedback control loop. The voltage error w.r.t. a reference is amplified by a PI controller, and compared with a saw tooth or triangular signal generating a PWM square wave at a constant frequency.

#### Controlling power flow in rechargeable batteries:

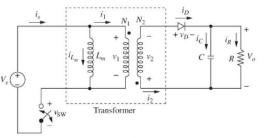


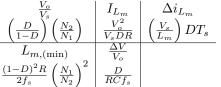
In power generation, some applications such as solar and wind generate primarily DC voltage, this can be stored by these converters, should generation power be greater than load power, and redistributed later by switching the transistor.



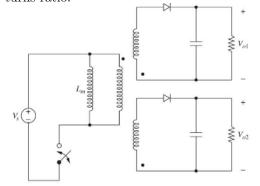
This controller can make up the supply or load deficit by modulating the PWM signal and only using part of the converters total ability to supply or absorb.

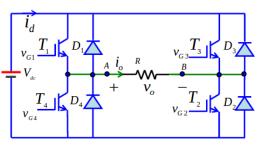
#### Isolated converters:





Can supply multiple outputs with different turns ratio.





Note that  $T_1, T_2$  are switched in parallel and  $T_3, T_4$  are switched together in the opposite half-cycle.

$$v_o(\omega t) = \sum_{n=1,3,5,\dots}^{\infty} \frac{4V_d}{n\pi} \cos n\omega t$$

$$\therefore V_{\rm rms} = V_{dc}, V_{\rm n,rms} = \frac{2\sqrt{2}V_{dc}}{n\pi}, V_{\rm 1,rms} = 0.9V_{dc}$$

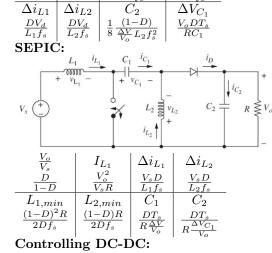
$$i_o(\omega t) = \sum_{1,3,5...}^{\infty} \frac{4V_d}{n\pi} \frac{\cos n\omega t}{\sqrt{R^2 + (n\omega L)^2}}$$

# DC-AC(Inverters):

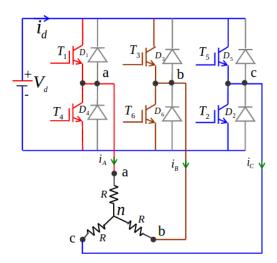
to a set one for distribution.

#### Square Wave Inverters:

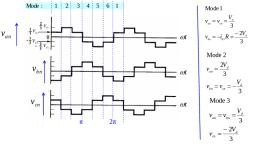
$$I_{\rm n,rms} = \frac{V_{\rm n,rms}}{|Z_n|}$$
 
$$I_{\rm rms} = \sqrt{\sum_{n=1,3,5...}^{\infty} \left(\frac{I_{\rm n,rms}}{\sqrt{2}}\right)^2}$$
 
$$P_{\rm n,rms} = I_{\rm n,rms}^2 R$$
 Aim to supply variable magnitude and frequency AC signal, in conjunction with AC-DC converters can convert an AC generation of variable frequency and magnitude to a set one for distribution. 
$$I_{\rm max} = -I_{\rm min} = \frac{V_{dc}}{R}, B = I_{\rm max} + \frac{V_{dc}}{R}, \text{ and } I_{\rm max} = -I_{\rm min} = \frac{V_{dc}}{R} \left[\frac{1 - e^{-\frac{T}{2\tau}}}{1 - e^{-\frac{T}{2\tau}}}\right]$$



 $L_{1,min}$  $(1-D)^2 R$ 



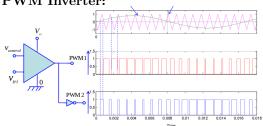
This results in a set of voltages across each resistor.



Line Voltage:

$$\sum_{n=1,5,7,11...}^{\infty} \frac{4V_d}{n\pi} \sin \frac{n\pi}{2} \sin \frac{n\pi}{3} \sin n(\omega t + \frac{\pi}{6})$$

Cannot regulate output voltage magnitude. **PWM Inverter:** 



Using the duty of the signal and an inductor, we can generate the required frequency and magnitude with the reference

voltage (amplifying it. While controlling the harmonic content with the triangle wave frequency. Amplitude modulation ratio:  $m_a = \frac{v_{\rm ref}}{v_{\rm tri}}$  Related to the amplification of the ref signal to output through  $V_{\rm o,fundamental} = m_a V_{\rm dc}$  for  $m_a \leqslant 1$ .

Frequency modulation:  $m_f = \frac{f_{\rm tri}}{f_{\rm ref}}$  which describes the separation of the fundamental harmonic frequencies  $\Longrightarrow$  THD (hence SNR), but also increases switching losses. Using a Fourier coefficient table:

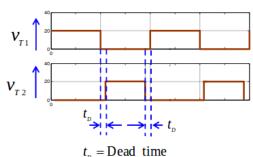
	$V_n/V_{dc}$									
	m <sub>a</sub> =1	0.9	0.8	0.7	0.6	0.5	0.4	0.3	0.2	0.1
n = 1	1.00	0.90	0.80	0.70	0.60	0.50	0.40	0.30	0.20	0.10
$n = m_f$	0.60	0.71	0.82	0.92	1.01	1.08	1.15	1.20	1.24	1.27
$n=m_f\pm 2$	0.32	0.27	0.22	0.17	0.13	0.09	0.06	0.03	0.02	0.00

Reading down from the desired modulation amplitude ratio gives the Fourier coefficients at each frequency.

#### Unipolar, Bipolar, 3 Phase:



**Deadtime inverter:** avoids the shoot through during switch on/off that would result in a short circuit across the transistor bridge.



Normally in  $\mu s$  range. It depends on turn on/tumoff time of IGBT.