Tove's thesi's title

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Sammanfattning

This is the abstract

Innehåll

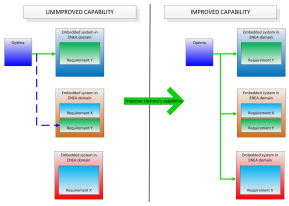
Figurer

Tabeller

Kapitel 1

Figures

This is a included figure [1]



Figur 1.1: Illustration of demonstration application

Kapitel 2

Table

This is a table ...

2.1 Tables

... described in a subsection.

	Scenario 1	Scenario 2	Scenario 3
Avg. cycle count	5293	5978	9585
Avg. cache miss count	39	52	75

Tabell 2.1: Average cycle count and cache misses for demonstration scenarios

Litteraturförteckning

[1] A. B. T. Hopkins and K. D. McDonald-Maier, "Debug support for complex systems on-chip: a review," *IEE Proc.-Comput. Digit. Tech.*, vol. 153, July 2006.