

A diagram of a DLX processor architecture. It features a yellow rectangular block labeled 'DATAPATH' at the top and a light blue rectangular block labeled 'CONTROL UNIT' at the bottom. A vertical double-headed arrow connects the bottom of the Datapath block to the top of the Control Unit block. To the right of the Datapath block, there is a horizontal double-headed arrow pointing outwards. The entire diagram is enclosed in a green rectangular border. The text 'DLX' is located in the bottom-left corner of the green border.

DATAPATH

CONTROL
UNIT

DLX