

A block diagram of a DLX processor architecture. The entire system is enclosed in a large rectangular frame. Inside, there are two main components: a 'DATAPATH' block at the top and a 'CONTROL UNIT' block at the bottom. The 'DATAPATH' block is a rectangle containing the text 'DATAPATH'. The 'CONTROL UNIT' block is a rectangle containing the text 'CONTROL UNIT' on two lines. A vertical double-headed arrow connects the bottom of the 'DATAPATH' block to the top of the 'CONTROL UNIT' block. To the right of the 'DATAPATH' block, a horizontal double-headed arrow points outwards from the system boundary.

DATAPATH

**CONTROL
UNIT**

DLX