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University: Illinois Institute of Technology

Course: Cloud Computing (CS 553)

Assignment: PA1

Design:

CPU Benchmarking:

- > Programming language C has been used to implement this benchmarking task.
- In this program we are calculating the Giga Char operations, Giga Short operations, Giga Integer operations and Giga Floating point operations.
- Aim of the program is to utilize CPU to full capacity by performing various Arithmetic operations.
- > Functions qops, hops, sops, dops are implemented for calculating Giga Char operations, Giga Short operations, Giga Integer operations and Giga Floating point operations respectively.
- Function qops: Total 1trillion operations are performed in an order to calculate the Giga operations. As Char datatype has a limit of 256, Interger data type has been used in iterations in For loop. The separate Forloop function has been created and executed 1st to extract time information for iterating For loop. Once the qops function is executed and timing are extracted, actual time of executing Char operations time is calculated by subtracting For loop time. Later, it calculates the Giga Char operations.
- Function hops: Total 1trillion operations are performed in an order to calculate the Giga operations. As Short datatype has a limit of 65,535, Interger data type has been used in iterations in For loop. The separate Forloop function has been created and executed 1st to extract time information for iterating For loop. Once the hops function is executed and timing are extracted, actual time of executing Short operations time is calculated by subtracting For loop time. Later, it calculates the Giga Short operations.
- Function sops: Total 1trillion operations are performed in an order to calculate the Giga Integer operations (GIOPS).
- Function dops: Total 1trillion operations are performed in an order to calculate the Giga Floating point operations (GFLOPS).
- Each of the functions are executed for 1, 2 and 4 threads.
- Strong scaling has been implemented in multithreading i.e. each thread will work on same workload.
- pthread_barrier_wait is utilized to synchronize all the threads. It has been included at multiple places to make sure only time taken by each function to iterate and perform arithmetic operations and not the variable creation and assignment operations.
- Scripts are created to execute the code and writing the output in a file.

Memory Benchmarking:

- Programming language C has been used to implement this benchmarking task.
- In this program we are calculating the throughput and latency of the main memory i.e. RAM.
- Design includes the different methods, RWS: read+write (e.g. memcpy) with sequential access pattern and RWR: read+write (e.g. memcpy) with random access pattern.
- Each method is implemented for various block sizes of 1B,1KB, 1MB and 10MB.
- For calculating throughput, functions are executed for block sizes 1KB, 1MB and 10MB for total 100GB data.
- ➤ Latency calculations are limited for the 100 million operations with 1B as block size.
- memset and memcpy functions are implemented to read and write from RAM.
- > Strong scaling has been implemented in multithreading i.e. each thread will work on same workload.
- Each of the functions are executed for 1, 2 and 4 threads.
- pthread_barrier_wait is utilized to synchronize all the threads. It has been included at multiple places to make sure only time taken by each function to perform read and write operations on RAM and not the variable creation and assignment operations.
- Scripts are created to execute the code and writing the output in a file.

Disk Benchmarking:

- Programming language C has been used to implement this benchmarking task.
- In this program we are calculating the throughput, latency and IOPS of the Disk (Hard Disk).
- ➤ Design includes the different methods, RS: read with sequential access pattern, WS: write with sequential access pattern, RR: read with random access pattern and WS: write with random access pattern.
- Each method is implemented for various block sizes of 1KB,1MB, 10MB and 100MB.
- For calculating throughput, functions are executed for block sizes 1MB, 10MB and 100MB.for total 10GB data.
- Latency and IOPS calculations are limited for the 1GB of data with 1KB as block size.
- Various system calls such as open, write, read, etc. has been used to implement this benchmark.
- For sequential access for read, pread system call is used so there is no need to update the file pointer for subsequent read. For write operation, append mode has been used to make sure resulting file is of 1 or 10GB.
- For random access, a thread block size has been utilized with rand function to generate a random number and, is used as a location for read or write from file.
- O_SYNC option in open system call and fsync system call has been used to avoid reading from and writing to cache.

- Each of the functions are executed for 1, 2 and 4 threads except when block size is 1KB. In that case, additionally 8,16,32,64 and 128 threads are also executed.
- Strong scaling has been implemented in multithreading i.e. each thread will work on same workload.
- > pthread_barrier_wait is utilized to synchronize all the threads. It has been included at multiple places to make sure only time taken by each function to perform read and write operations on Disk and not the variable creation and assignment operations.
- Scripts are created to execute the code and writing the output in a file.

Network Benchmarking:

- Programming language C has been used to implement this benchmarking task.
- In this program we are calculating the throughput and latency of the network.
- Design includes the different protocols, TCP and UDP.
- Each method is implemented for various block sizes of 1B, 1KB and 32KB.
- ➤ For calculating throughput, functions are executed for block sizes of 1KB and 32KB for total 100GB data.
- ➤ Latency calculations are limited for the 1million of operations with 1B as block size.
- The implementation is for transmitting packets from client to server over the network and back to get RTT of transmission.
- TCP is connection oriented protocol which requires the connection setup and acceptance between client and server before transmitting any data/packets.
- The UDP protocol is connection less protocol in which data/packets are transmitted without any established connection. This may result in data loss.
- Each of the functions are executed for 1, 2, 4 and 8 threads.
- Scripts are created to execute the code and writing the output in a file.

Improvement which can be done:

CPU Benchmarking:

Benchmarking could have been more efficient if implemented more complex instructions such as solving Liner equations. The implementation of AVX instructions would have provided more accurate results. LINPACK also uses such instructions and Matrix Multiplications which results in more FLOPS and IOPS. For future improvement, Linear equations and AVX instructions can be implemented.

Memory Benchmarking:

The memory availability was limited due to shared resource allocated for this experiment. The better understanding on AVX instructions and usage could have been helped to implement this benchmark more efficiently. For future enhancements, along with AVX instructions, handling cache in more efficient way can be implemented.

Disk Benchmarking:

As we were provided shared cluster to run this experiment, resource was not always available to execute the test cases. The limitation of creating files in tmp folder also made this benchmark execution difficult. In future to improve design and achieve better efficiency, we can extend the design to multicore environment with more memory allocation.

Network Benchmarking:

The network loss was not accounted much in this benchmark. Though TCP handles it implicitly, there were several occasions when package loss was noted at client and server. Also, server was not stable, there were time when either client or server is executing. This was resulting in job time out due to limited allocated time to each job. For future, network loss can be counted to acquire accurate results.

Performance report:

The results of all the test cases and experiments are presented here. All testing has been done on Hyperion cluster.

CPU Benchmarking:

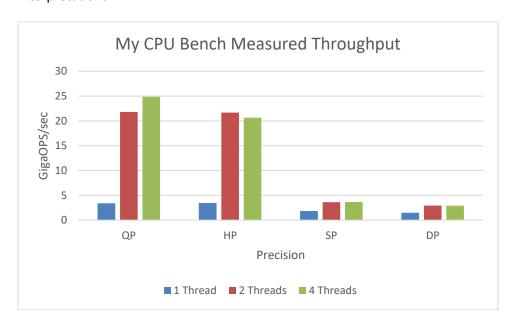
Model Name	CPU	Cache	CPU Cores
Intel(R) Xeon(R) CPU	2.30GHz	256 KB	1
E5-2670 v3			

All test cases were carried for three times and average benchmark value was calculated which is shown in below table. The standard deviation of all three runs is also calculated and present in table.

Workload	Concurrenc y	MyCPUBench Measured Ops/Sec (GigaOPS)	HPL Measure d Ops/Sec (GigaOPS	Theoretica I Ops/Sec (GigaOPS)	MyCPUBench Efficiency (%)	HPL Efficiency (%)	Standard Deviation
QP	1	3.402684	N/A	588.8	0.606563	N/A	0.162719
QP	2	21.81213	N/A	588.8	4.354811	N/A	3.591331
QP	4	24.8573	N/A	588.8	4.24594	N/A	1.534984
НР	1	3.449062	N/A	294.4	1.163271	N/A	0.0635
НР	2	21.68376	N/A	294.4	8.087506	N/A	1.853281
НР	4	20.65276	N/A	294.4	6.532216	N/A	1.839364
SP	1	1.844318	N/A	147.2	1.284217	N/A	0.065036
SP	2	3.603588	N/A	147.2	2.506829	N/A	0.106707
SP	4	3.641645	N/A	147.2	2.434948	N/A	0.068509
DP	1	1.490745	38.1844	73.6	2.09353	51.88098	0.051015

DP	2	2.929472	73.4327	73.6	4.117276	99.77269	0.107182
DP	4	2.898467	70.1092	73.6	3.984461	95.25707	0.038219

Based on above result, few graphs have been created to organize observed information for easy interpretations.



Above graph shows the throughtput achieved by my code for various precisions. As we can see from graph, multithreading achieves the more throughput compared to single threaded execution. Also, the throughput achieved for quarter and half precisions is more than single precision and double precision operations.

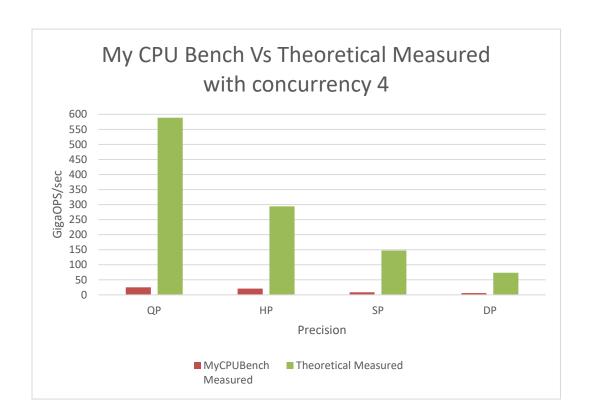
Through put has been calculated by formula:

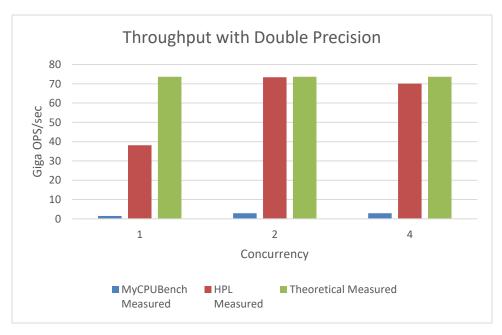
$$\text{FLOPS} = \text{sockets} \times \frac{\text{cores}}{\text{socket}} \times \frac{\text{cycles}}{\text{second}} \times \frac{\text{FLOPs}}{\text{cycle}}$$

Where, sockets =2, cores/socket = 1, cycles/second=2.3, FLOPs/cycle = 16,32,64,128 for DP,SP,HP and QP respectively.

Ref: https://en.wikipedia.org/wiki/FLOPS

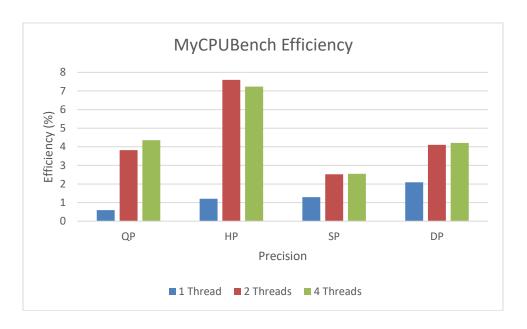
Below graph indicates that the throughput achieved by my code with multithreading with 4 threads is way too low compared to theoretical value. This is due to the same reason explained in improvement section above. The implementation of normal arithmetic operations doesn't push CPU to its limit to achieve high throughput which can be achieved if Matrix Multiplication or linear equations were solved. Through put can also be achieved by implementing AVX and FMA instructions.





We also executed the LINPACK which is a standard benchmark program to evaluate CPU performance. The results can be visualized in above graph. As we can see that LINPACK is providing throughput closer to the actual theoretical value of the CPU.

It is always better to provide the result in percent format to easy grasping and understanding the performance. Due to same, efficiency was calculated and plotted in bar chart below:



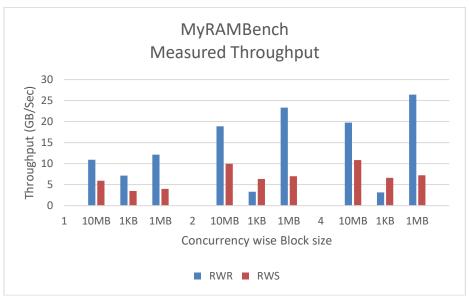
Memory Benchmarking:

Specification: 8GiB DIMM Synchronous 2400 MHz (0.4 ns, 64bit) Like CPU benchmarking all test cases were executed thrice and average and standard deviation has been calculated.

Worklo ad	Concurre ncy	BlockSi ze	MyRAMBe nch Measured Throughpu t (GB/sec)	pmbw Measure d Through put (GB/sec)	Theoreti cal Through put (GB/sec)	MyRAMBe nch Efficiency (%)	pmbw Efficien cy (%)	Standa rd Deviati on
RWS	1	1KB	3.480392	16.7557	63.56835	5.475039	26.358 62	0.6258 41
RWS	2	1KB	6.357561	16.1617	63.56835	10.00114	25.424 05	0.9772 29
RWS	4	1KB	6.60822	14.8422	63.56835	10.39546	23.348 33	0.9566 74
RWS	1	1MB	3.973819	27.8342	63.56835	6.251255	43.786 24	0.6477 99
RWS	2	1MB	6.980057	29.4591	63.56835	10.9804	46.342 46	1.2336 54
RWS	4	1MB	7.242063	28.4649	63.56835	11.39256	44.778 42	1.0452 04
RWS	1	10MB	5.931106	34.5599	63.56835	9.330281	54.366 58	1.6066 89
RWS	2	10MB	9.97151	34.2737	63.56835	15.68628	53.916 3	1.9738 47

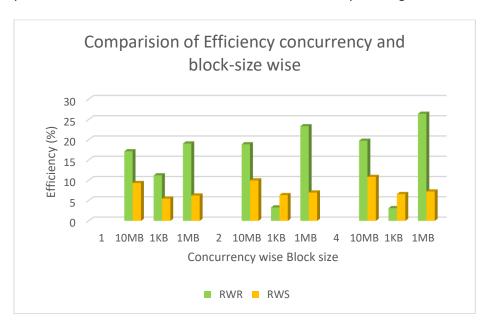
1	1	1	1	1		ı		
RWS	4	10MB	10.83333	30.6455	63.56835	17.04202	48.208	1.4433
11113	,	TOIVID	10.03333	30.0433	03.30033	17.04202	66	76
DIAID	4	41/5	7 420702	4.04250	62 56025	44 24 720	7.6194	0.4580
RWR	1	1KB	7.130703	4.84358	63.56835	11.21738	77	8
DIAID	2	1 I/D	2 244600	0.45305	62 56025	E 200640	0.7125	0.2712
RWR	2	1KB	3.311688	0.45295	63.56835	5.209649	45	12
DIAID	4	4 I/D	2 422044	0.35564	62 56025	4.020622	0.4021	0.4344
RWR	4	1KB	3.133044	0.25561	63.56835	4.928622	07	19
DVVD	1	1140	12 12215	0.2621	C2 FC02F	10.07165	14.570	0.8859
RWR	1	1MB	12.12315	9.2621	63.56835	19.07165	29	2
DIAID	2	4.4.0	22 2222	1 10000	62.56025	26 7050	1.8658	2.8867
RWR	2	1MB	23.33333	1.18606	63.56835	36.7059	09	51
DIAID	4	4.4.0	26 40024	0.50467	62.56025	44 54450	0.9197	0.4695
RWR	4	1MB	26.40921	0.58467	63.56835	41.54459	52	11
DWD	1	10140	10 00067	22 1720	62 56925	17 14706	36.453	1.7142
RWR	1	10MB	10.90067	23.1729	63.56835	17.14796	52	6
DWD	2	10140	10 00000	2 06045	62 56925	20.7142	4.5123	1.9245
RWR	2	10MB	18.88889	2.86845	63.56835	29.7143	89	01
D\A/D	1	101/10	10.75224	0.60226	62 56025	21 07244	1.0750	0.9832
RWR	4	10MB	19.75224	0.68336	63.56835	31.07244	05	9

The theoretical value has been calculated with formula: (Clock frequency) * (lines per clock) * (Memory Bus Width) * (Number of Interfaces) Where clock freq = 2133, lines per clock is 2, Memory Bus width being 64 and Number of Interfaces are 2. This gives the theoretical through put of memory as 63.568354.



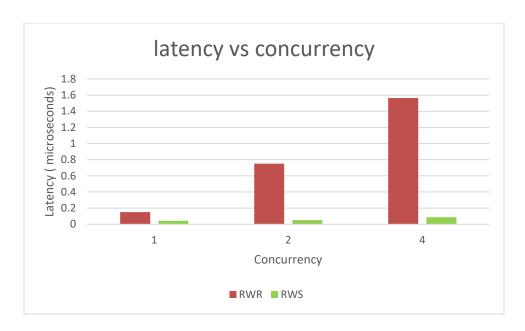
We can see that random-access pattern for read and write providing us more throughput then sequential access. Also, as thread count is increased there is an increase in through put. The efficiency

of my code in terms of throughput is calculated and compared with different block sizes and access pattern. As we can see that read write random access is providing me more efficiency.



Worklo ad	Concurre ncy	BlockSi ze	MyRAMBe nch Measured Latency (us)	pmbw Measur ed Latency (us)	Theoreti cal Latency (us)	MyRAMBe nch Efficiency (%)	pmbw Efficien cy (%)	Standard Deviatio n
RWS	1	1B	0.0071773	0.00631	0.01406	51.047866	44.943	0.000296
	_		0.007.2770	9	0.02.00	3	1	66
RWS	2	1B	0.009313	0.00314	0.01406	66.237553	22.389	0.000187
KWS	2	ID	0.009515	8	0.01406	3	76	45
DVAC	4	10	0.0000353	0.00187	0.01406	69.952560	13.328	0.000167
RWS	4	1B	0.0098353	4	0.01406	5	59	7
D)A/D	1	1 D	0.126504	0.14393	0.01406	971.50782	1023.7	0.010754
RWR	1	1B	0.136594	7	0.01406	4	34	3
D)A/D	2	1 D	0.240252	0.06761	0.01406	1766 2700	480.91	0.026884
RWR	2	1B	0.248353	7	0.01406	1766.3798	75	9
RWR	4	1B	0.307336	0.03324	0.01406	2185.8890 5	236.42 25	0.023884 9

Below diagram indicates the performance of the RAM in terms of latency when no. of threads is increased. The latency stays almost same for sequential access but increases with thread count for random access. This can happen as random access can increase the seek time for subsequent read/write operations.



The PMBW benchmark has been executed on memory to compare the performance of it with theoretical value. Benchmark has been executed for both throughput and latency. The result and % efficiency has been provided in the table earlier.

Disk Benchmarking:

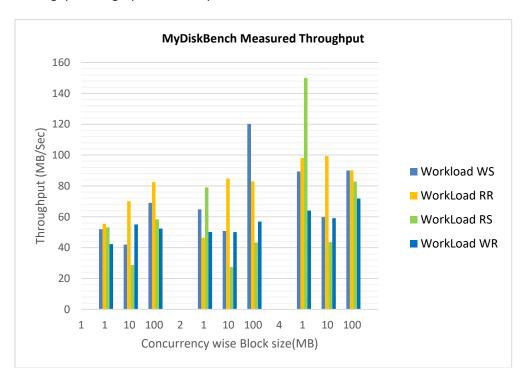
The disk benchmarking has been executed three times and average and standard deviations are calculated as below:

			MyDiskBe	IOZone				
			nch	Measur	Theoreti			
			Measure	ed	cal		IOZon	
			d	Through	Through	MyDiskBe	e	
			Throughp	put	put	nch	Efficie	
Workl	Concurre	Block	ut	(MB/sec	(MB/sec	Efficiency	ncy	Standard
oad	ncy	Size	(MB/sec)))	(%)	(%)	Deviation
			55.47619				52.196	
RR	1	1MB	03	313.18	600	9.246032	67	13.57474
			46.44444				58.863	
RR	2	1MB	43	353.18	600	7.740741	33	39.41378
			98.22222					
RR	4	1MB	23	303.18	600	16.37037	50.53	19.70689
			70.11111				87.726	
RR	1	10MB	1	526.36	600	11.68519	67	9.853445
			84.77777				67.726	
RR	2	10MB	77	406.36	600	14.12963	67	43.88933
			99.45787				94.393	
RR	4	10MB	86	566.36	600	16.57631	33	344.8706
			82.39037				75.453	
RR	1	100MB	43	452.72	600	13.73173	33	189.692

			82.88888				55.453	
RR	2	100MB	9	332.72	600	13.81481	33	344.8706
	_		90.05050				88.786	0.11101.00
RR	4	100MB	5	532.72	600	15.00842	67	44.78838
	-		53.14628				51.753	
RS	1	1MB	17	310.52	600	8.857714	33	3.742875
			79.04781	010.01		0.007721	54.253	0.7 .2070
RS	2	1MB	33	325.52	600	13.17464	33	15.98345
			150.0562	010.01			50.253	
RS	4	1MB	27	301.52	600	25.00937	33	34.39568
1.0		11110	28.83404	301.32	000	23.00307	53.506	3 1133303
RS	1	10MB	2	321.04	600	4.805674	67	21.52195
11.5		TOWID		321.01	000	1.003071	83.506	21.32133
RS	2	10MB	27.3944	501.04	600	4.565733	67	2.321074
11.5		TOIVID	43.55216	301.04	000	4.505755	93.506	2.521074
RS	4	10MB	23	561.04	600	7.258694	67	13.38196
11.5	7	TOIVID	58.33920	301.04	000	7.230054	07	13.30130
RS	1	100MB	23	442.08	600	9.7232	73.68	13.89876
11.5		TOOIVID	43.28502	772.00	000	3.7232	50.346	13.03070
RS	2	100MB	43.28302	302.08	600	7.214171	67	213.602
11.5		TOOIVID	82.77683	302.00	000	7.214171	50.346	213.002
RS	4	100MB	93	302.08	600	13.79614	67	12.28252
N3	4	TOOIVID	42.26739	302.00	000	13.79014	07	12.20232
WR	1	1MB	42.20739	284.25	600	7.044565	47.375	0.850862
VVK	1	TIVID	50.11924	264.25	600	7.044303		0.650602
WR	2	1MB	50.11924 87	219.25	600	8.353208	36.541 67	0.61584
VVK		TIVID	63.91073	219.25	600	6.333206	30.708	0.01364
WR	,	1MB		184.25	600	10.65179	30.708	F 240F22
WK	4	TIVID	1	184.25	600	10.65179	33	5.318532
\A/D	1	10140	55.04780	F00 F	coo	0.174624	00.75	C 222CEC
WR	1	10MB	43	598.5	600	9.174634	99.75	6.333656
\A/D	,	10140	50.03124	4C0 F	coo	0 220544	78.083	C 222CEC
WR	2	10MB	43	468.5	600	8.338541	33	6.333656
)A/D		4 ON 4 D	59.08965	F00 F	600	0.040375	98.083	6 222656
WR	4	10MB	23	588.5	600	9.848275	33	6.333656
14/5		400140	52.37512	426	600	0.720407	74	4 047447
WR	1	100MB	37	426	600	8.729187	71	1.817117
		400145	56.94669	267	600	0.404445	61.166	0.057564
WR	2	100MB	07	367	600	9.491115	67	8.857561
	_	400145	71.81878			44.0500	00 =	00.07.470
WR	4	100MB	57	483	600	11.9698	80.5	23.97478
			51.93622				30.938	
WS	1	1MB	77	185.63	600	8.656038	33	1.850086
			64.75881		_			
WS	2	1MB	23	195.63	600	10.79314	32.605	4.718009
			89.33113				. <u> </u>	
WS	4	1MB	9	105.63	600	14.88852	17.605	1.969421

			41.97867				61.876	
WS	1	10MB	8	371.26	600	6.996446	67	3.981788
			50.73560				58.543	
WS	2	10MB	1	351.26	600	8.455934	33	1.787333
			59.82762				41.876	
WS	4	10MB	73	251.26	600	9.971271	67	2.358361
			69.05453				68.411	
WS	1	100MB	83	410.469	600	11.50909	5	4.216329
			120.1460				93.753	
WS	2	100MB	88	562.52	600	20.02435	33	16.55188
			89.95093				90.076	
WS	4	100MB	77	540.458	600	14.99182	33	4.084427

Throughput bar graph has been plotted as below.

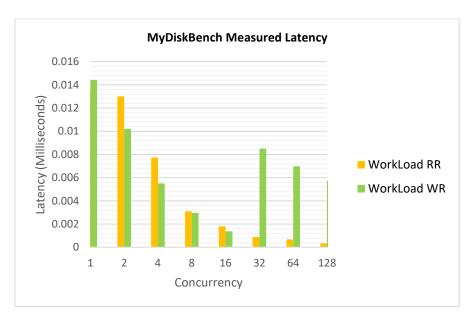


Latency performance can be seen in below table:

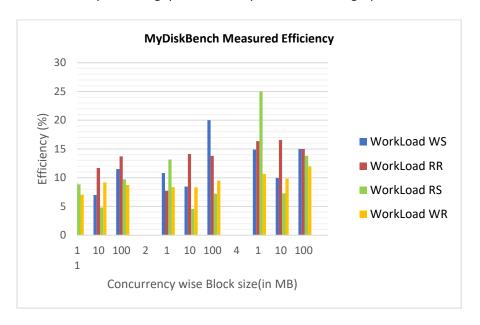
			MyDiskBen	IOZone				
			ch	Measur	Theoreti	MyDiskBen	IOZone	
			Measured	ed	cal	ch	Efficien	
Worklo	Concurren	BlockSi	Latency	Latency	Latency	Efficiency	су	StandardDeviat
ad	су	ze	(ms)	(ms)	(ms)	(%)	(%)	ion
			0.0134000			0.0322115	0.5288	
DD.	1	1KB	00	0.022	4.16	38	46	0.002042058
RR			0.0130000				0.6634	
	2	1KB	00	0.0276	4.16	0.03125	62	0.001345362

ĺ	I		0.0077333			0.0185897	0.8413	
	4	1KB	33	0.035	4.16	44	46	0.000757188
		IND	0.0031000	0.055	4.10	0.0074519	0.6418	0.000737100
	8	1KB	0.0031000	0.0267	4.16	23	27	0.000300000
		IND	0.0018000	0.0207	4.10	0.0043269	0.1682	0.000300000
	16	1KB	00	0.007	4.16	23	69	0.000200000
	10	IND	0.0008742	0.007	4.10	0.0002323	0.8653	0.00020000
	32	1KB	75	0.036	4.16	72	85	0.000057735
	32	1110	0.0006758	0.000	1110	0.0001602	0.4086	0.000037703
	64	1KB	38	0.017	4.16	56	54	0.000057735
	0.		0.0003400	0.027		0.0081730		0.000007700
	128	1KB	00	0.078	4.16	77	1.875	0.002535744
			0.0144166			0.3465544		
	1	1KB	67	0.156	4.16	87	3.75	0.021813834
			0.0101866			0.2448717	0.9134	
	2	1KB	67	0.038	4.16	95	62	0.001289703
			0.0055000			0.1322115		
	4	1KB	00	0.364	4.16	38	8.75	0.003306055
			0.0029600			0.0711538		
\A/D	8	1KB	00	0.026	4.16	46	0.625	0.001777639
WR			0.0013666			0.0328525	1.0961	
	16	1KB	67	0.0456	4.16	64	54	0.000923760
			0.0085000			0.0204326	2.5480	
	32	1KB	00	0.106	4.16	92	77	0.000458258
			0.0069666			0.0167467	1.3365	
	64	1KB	67	0.0556	4.16	95	38	0.000450925
			0.0057333			0.0137820	0.9831	
	128	1KB	33	0.0409	4.16	51	73	0.000550757

Latency with respect to the number of threads and random read write pattern has been plotted. One can notice that as number of threads increases the latency decreases.



Disk efficiency of throughput has been plotted in below graph:



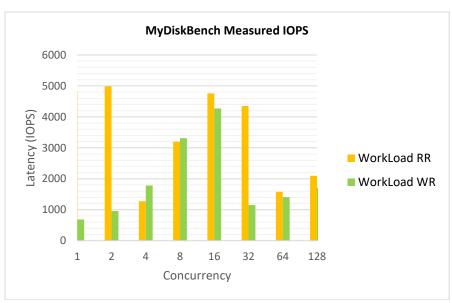
IOPS details are provided in below table:

			MyDiskB					
			ench	IOZone		MyDiskB	IOZone	
			Measure	Measure	Theoret	ench	Efficienc	
Workl	Concurr	BlockS	d	d	ical	Efficiency	У	Standard
oad	ency	ize	IOPS	IOPS	IOPS	(%)	(%)	Deviation
			4822.889	5321.987	5782.49	83.40493	92.0361	
RR	1	1KB	785	561	91	8	157	12.2180961
			4983.665	5497.987	5782.49	86.18533	95.0797	
RR	2	1KB	943	561	91	02	824	8.325151721
			1276.284	36895.14	5782.49	22.07151	638.048	
RR	4	1KB	976	572	91	19	448	11.94870544
			3195.155	3589.447	5782.49	55.25562	62.0743	
RR	8	1KB	825	851	91	17	348	29.1672035
			4760.044	4896.044	5782.49	82.31812	84.6700	
RR	16	1KB	739	767	91	34	481	58.8521369
			4350.259	4865.258	5782.49	75.23147	84.1376	
RR	32	1KB	394	694	91	55	473	55.0359669
			1581.186	2564.986	5782.49	27.34433	44.3577	
RR	64	1KB	032	032	91	69	42	144.1418181
				3987.512	5782.49	36.26722	68.9582	
RR	128	1KB	2097.152	415	91	57	886	3.56448E-13
			687.5862	1587.586	5782.49	11.89081	27.4550	
WR	1	1KB	213	221	91	42	189	0.999236558

Name: Brijesh Mavani CWID: A20406960 Assignment: 1

			958.8734	3558.873	5782.49	16.58233	61.5455	
WR	2	1KB	217	422	91	59	941	0.121031293
			1780.481	2480.436	5782.49	30.79087	42.8955	
WR	4	1KB	961	961	91	31	875	1.055482213
			3309.236	4567.245	5782.49	57.22848	78.9839	
WR	8	1KB	347	347	91	01	353	2.066807157
			4273.064	4973.045	5782.49	73.89650	86.0016	
WR	16	1KB	618	618	91	3	67	5.150556157
			1150.450	3420.225	5782.49	19.89537	59.1478	
WR	32	1KB	133	486	91	94	776	6.340631364
			1407.792	2245.875	5782.49	24.34575	38.8391	
WR	64	1KB	813	691	91	07	879	8.573895255
			1711.152	2687.457	5782.49	29.59192	46.4757	
WR	128	1KB	574	874	91	12	163	16.53557076

IOPS performance graph for various number of threads are provided below:



Theoretical value calculation:

IOPS = 1/(average seek time)+(average latency)

average seek time (read/write)= 0.0085/0.0095ms average latency=4.16ms

IOPS = 78.98894(Read) and 73.20644(Write). Total: 5782.4991

Ref for calculating Theoretical values:

https://www.seagate.com/files/www-content/product-content/constellation-fam/constellation/constellation-2/en-us/docs/constellation2-fips-ds1719-4-1207us.pdf

https://www.cnet.com/products/seagate-constellation-2-st9250610ns-hard-drive-250-gb-sata-6gb-s/specs/

IOZONE tool has been also executed to compare our results. The resulted values are shown in different format in above tables.

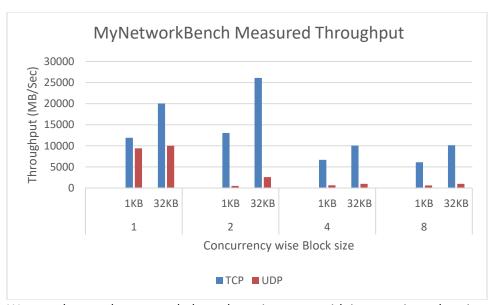
4 threads

```
Immavani@compute-3:-/assignments/benchmarks0 srun iozone -s 10g -r lm -o -14 -u4 -1 0 -1 1 -1 2 -e -I -F -f /tmp/iozonetmpl_b.txt /tmp/iozonetmpl_b.t
```

Network Benchmarking:

The network benchmark evaluation is performed for TCP as well as UDP. Different Packet sizes are transferred ranging from 1B, 1KB, 32KB and the throughput has been calculated. The RTT has been calculated for packet size 1B.

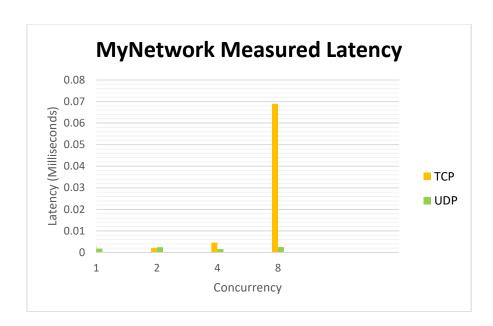
Protoc ol	Concurren	Block size	MyNETBen ch Measured Throughpu t (Mb/sec)	iperf Measured Throughp ut (Mb/sec)	Theoretic al Throughp ut (Mb/sec)	MyNETBen ch Efficiency (%)	iperf Efficiency(%)
TCP	cy 1	1KB	11920.43	1485.25	56000	21.28648	2.652232143
TCP	1	32KB	20020.714	2756.25	56000	35.75128	4.921875
ТСР	2	1KB	13045	1706.13	56000	23.29464	3.046660714
TCP	2	32KB	26106.35	2910.12	56000	46.61848	5.196642857
TCP	4	1KB	6669.366	6941.24	56000	11.90958	12.39507143
TCP	4	32KB	10063.88	7970.15	56000	17.97122	14.23241071
TCP	8	1KB	6119.104	5418.12	56000	10.92697	9.675214286
TCP	8	32KB	10114.7	8214.19	56000	18.06196	14.66819643
UDP	1	1KB	9420.43	2941.18	56000	16.8222	5.252107143
UDP	1	32KB	10010.47	3719.14	56000	17.87583	6.641321429
UDP	2	1KB	511.3977	3641.25	56000	0.91321	6.502232143
UDP	2	32KB	2600.635	4648.49	56000	4.643991	8.300875
UDP	4	1KB	656.9366	3751.04	56000	1.173101	6.698285714
UDP	4	32KB	995.3827	5419.27	56000	1.777469	9.677267857
UDP	8	1KB	601.9104	5717.18	56000	1.07484	10.20925
UDP	8	32KB	1001.467	4971.13	56000	1.788334	8.877017857



We can observe that network through put increases with increase in packet size which is being sent over network with TCP or UDP protocol. As UDP is connection less protocol there might be a lot of data loss during the transmission.

Latency performance are given in below table.

Protoc	Concurrenc	Messag	MyNETBenc	ping	Theoretic	MyNETBenc	iperf
ol	У	e Size	h Measured	Measure	al Latency	h	Efficicency(
			Latency(ms)	d	(ms)	Efficiency(%	%)
				Latency)	
				(ms)			
			0.00353016			504.309310	284285.714
TCP	1	1B	5	1.99	0.0007	9	3
			0.00200353			286.219343	284285.714
TCP	2	1B	5	1.99	0.0007	9	3
			0.00453360			647.658116	284285.714
TCP	4	1B	7	1.99	0.0007	1	3
			0.06891255			9844.65016	284285.714
TCP	8	1B	1	1.99	0.0007	3	3
						2.25494285	284285.714
UDP	1	1B	1.58E-05	1.99	0.0007	7	3
						3.52285714	284285.714
UDP	2	1B	2.47E-05	1.99	0.0007	3	3
						2.21014285	284285.714
UDP	4	1B	1.55E-05	1.99	0.0007	7	3
						6.54074285	284285.714
UDP	8	1B	4.58E-05	1.99	0.0007	7	3



Above graph shows the latency factor of both the protocol for various concurrency. Iperf benchmarking was executed and results are shown in above tables.