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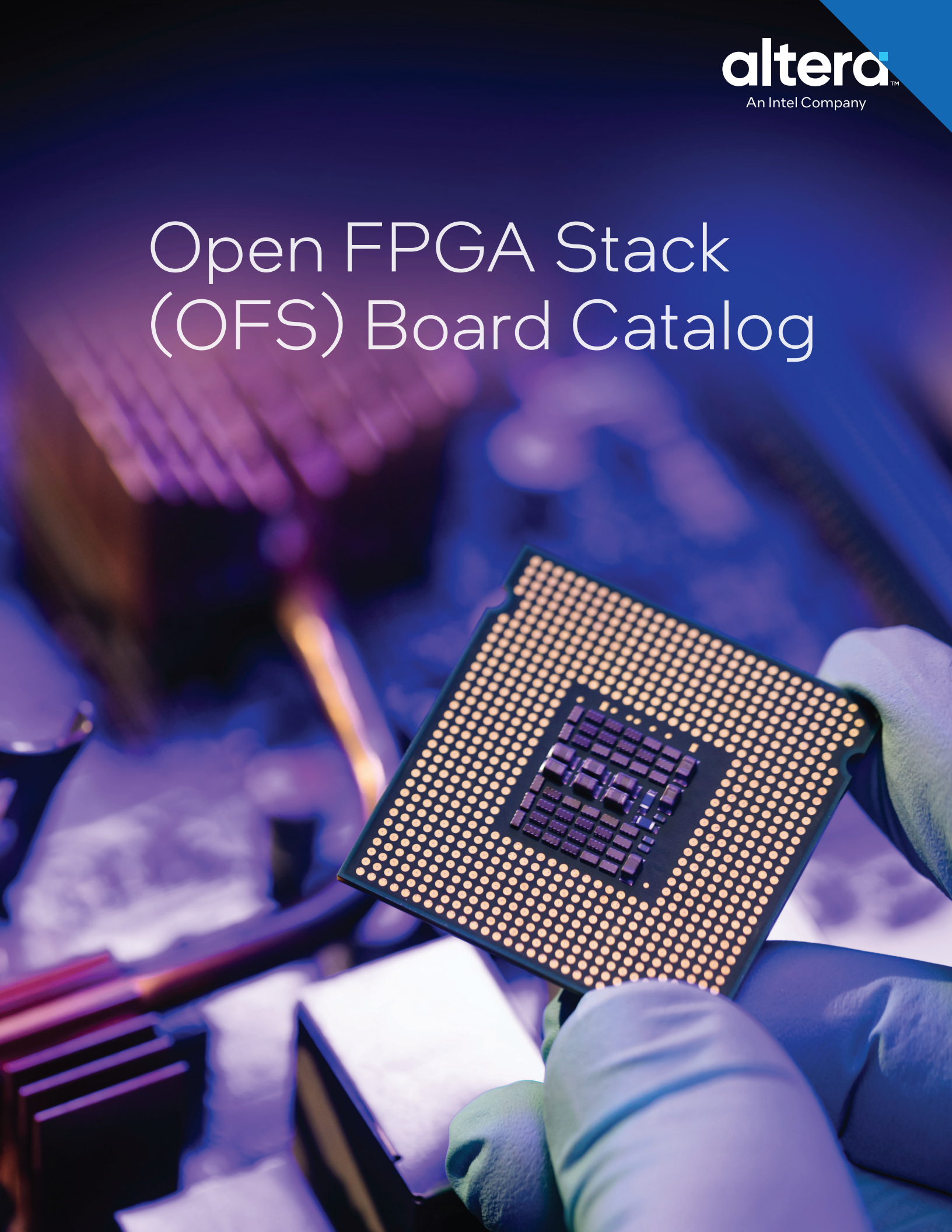
Open FPGA Stack (OFS) Board Catalog



Open FPGA Stack Board Catalog

Altera and Intel Partner Alliance (IPA) partners provide Open FPGA Stack (OFS) enabled platforms for development and deployment. These Stratix® 10 and Agilex™ FPGA-based platforms enable you to build OFS-based software and applications faster without significant hardware development or expertise. Many of these same platforms are also enabled with support for oneAPI, providing more flexibility for application development. This quarterly updated catalogue is intended to list all currently available platforms. You can find more detail and collateral for these platforms as well as information for contacting the IPA partner on marketplace.intel.com.

OFS-Enabled Partner Platforms

Partner	FPGA	Product Name	Available for Evaluation	oneAPI Support
Artiza Networks	Agilex™ FPGA	Griffin N6060/61	Now	N6061 planned
BittWare	Agilex FPGA	IA-420F	Now	Yes
	Agilex FPGA	IA-840F	Now	Yes
EmbedWay	Agilex FPGA	PA8921	Now	Yes
Flyslice Technologies	Stratix® 10 FPGA	FA728Q	Now	Yes
	Agilex FPGA	FA927S	Now	No
	Agilex FPGA	FA925E	Now	Yes
Hitek Systems	Agilex FPGA	Agilex FPGA Low Profile NC100 PCIe* Card	Now	Yes
	Agilex FPGA	Agilex FPGA Network, Computational Storage PCIe Card	Now	Yes
	Agilex FPGA	Agilex FPGA Low Profile NC200 PCIe Card	Now	Yes
IBEX Technology	Agilex FPGA	IPAC-1000	Now	Coming Q2 2025
prodesign	Stratix 10 FPGA	FALCON Acceleration Card	Now	Yes, on request
Silicom	Stratix 10 FPGA	Silicom FPGA SmartNIC N5010 Series	Now	No
	Agilex FPGA	Silicom FPGA SmartNIC N6010/N6011	Now	Now
WNC	Agilex FPGA	WNC FPGA SmartNIC WSN6050 Series	Now	No

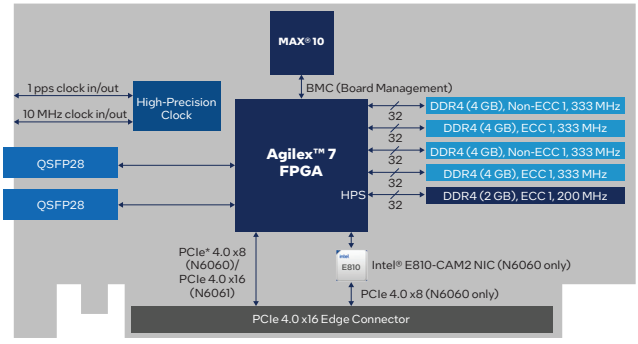
Griffin N6060/61

Provided by Artiza Networks
View this solution on the Artiza Networks website

Artiza Networks' PCIe-based SmartNIC, the Griffin N6060/61, uses the latest Agilex 7 FPGA F-series and OFS to address a wide range of applications including vRAN/NFV acceleration and Multi-Access Edge Computing (MEC).

Targeted Applications

- 4G/5G Virtualized Radio Access Network (vRAN)
- Network Function Virtualization (NFV)
- Multi-Access Edge Computing (MEC)



Griffin N6060/N6061 Block Diagram

Hardware

Agilex FPGA
- AGFB027R25A212V

1x Internet Network Controller E810 CAM2 (N6060 only)

Hard Processor System
- Quad-core Arm Cortex*-A53

Onboard Memory
- 16 GB DDR4 (FPGA)
- 2 GB DDR4 (embedded CPU)

Interfaces
- PCIe 4.0 x2 x8 bifurcation (N6060)
- PCIe 4.0 x2 x16 (N6061)
- 100GbE x2 / 25GbE x2 x2 (Optional) / 10GbE x8 (Optional)
- PTP/SyncE compatible, with 1 PPS in/out SMA connector

Form Factor
- Full height, ¾ length
- Max 185 W

Software

- OFS

Ordering Information

Part Number	Configuration
Griffin N6060	Artiza Networks FPGA SmartNIC N6060 (base + Intel E810-CAM2 NIC)
Griffin N6061	Artiza Networks FPGA SmartNIC N6061 (base)

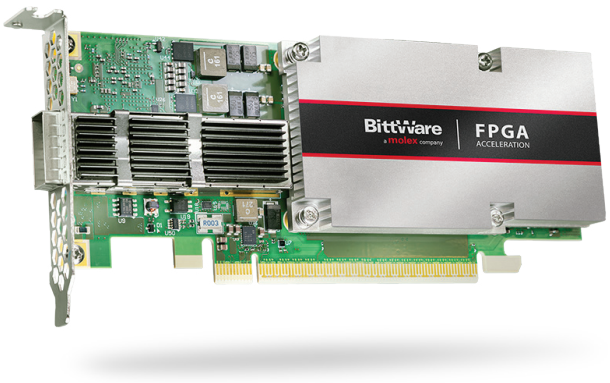
IA-420F

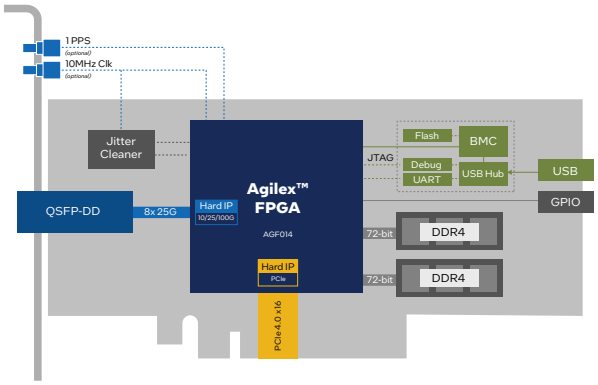
Provided by BittWare
View this solution on the Intel Partner Showcase or the BittWare website

BittWare's IA-420F is an Agilex FPGA-based card designed to deliver next generation performance for data center, networking, and edge compute workloads. The NIC-sized card provides a balance of I/O and memory using the Agilex FPGA chip's unique tiling architecture with a QSFP-DD (1× 200G), DDR4 SDRAM, PCIe 4.0 x16, and a GPIO port for diverse applications. The card also supports Intel® oneAPI, which enables an abstracted development flow for dramatically simplified code re-use across multiple architectures.

Targeted Applications

- Advanced Analytics
- Artificial Intelligence
- Cloud Computing
- Factory Automation
- High-Performance Computing





Hardware

Agilex FPGA
- AGFB014R24B2E2V

Onboard Memory
- 2 Gb flash
- 2x 8 GB DDR4 (16GB total)

Interfaces
- PCIe 4.0 x16
- QSFP-DD 1x 200 Gbps
- USB for BMC, FPGA JTAG, FPGA UART
- 4x GPIO expansion connector

Form Factor
- ½ height, ½ length
- Max 75 W

Software

- OFS[1]
- Intel oneAPI Base Toolkit
- BittWare SDK

[1] OFS is used by BittWare to enable oneAPI but is not separately productized. Contact BittWare for more information.

Ordering Information

Contact BittWare for ordering information.

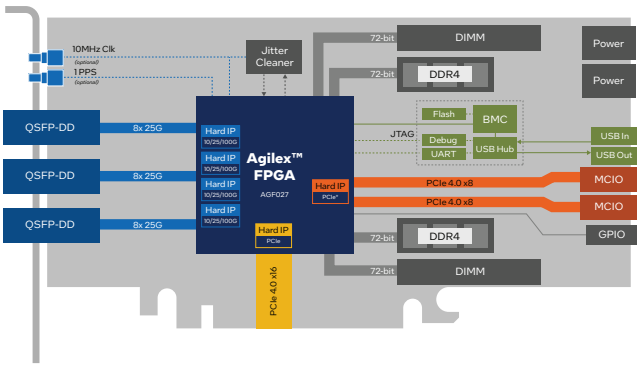
IA-840F

Provided by BittWare
View this solution on the Intel Partner Showcase or the BittWare website

BittWare's IA-840F is an Agilex FPGA-based card designed to deliver up to 40% higher performance for data center, networking, and edge compute workloads. BittWare maximized I/O features on the card using the Agilex FPGA chip's unique tiling architecture with three QSFP-DDs (3× 200G), PCIe 4.0 x16, and two MCIO PCIe expansion ports for diverse applications. The card also supports Intel oneAPI, which enables an abstracted development flow for dramatically simplified code re-use across multiple architectures.

Targeted Applications

- Advanced Analytics
- Artificial Intelligence
- Cloud Computing
- Factory Automation
- High-Performance Computing



Hardware

Agilex FPGA
- AGFB027R25A2E2V

Onboard Memory
- 2 Gb flash
- 2x 32 GB DDR4 SDRAM
- 2x DIMM supporting 32 GB DDR4 SDRAM

Interfaces
- PCIe 4.0 x16
- Three QSFP-DD cages supporting 3x 200GbE
- USB for BMC, FPGA JTAG, FPGA UART
- 2x MCIO edge connectors supporting PCIe expansion

Form Factor
- Full height, ¾ length; dual slot

Software

- OFS[1]
- Intel oneAPI Base Toolkit
- BittWare SDK

[1] OFS is used by BittWare to enable oneAPI but is not separately productized. Contact BittWare for more information.

Ordering Information

Contact BittWare for ordering information.

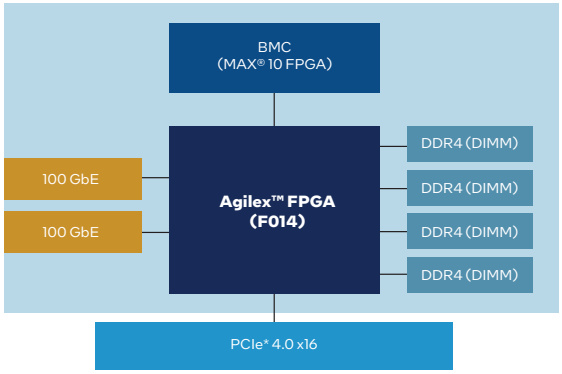
PA8921 FPGA Acceleration Card

Provided by EmbedWay
View this solution on the Intel Partner Showcase or the EmbedWay website

The PA8921 is a PCIe-based FPGA acceleration card leveraging the Agilex 7 FPGA F-Series. It provides customers with an ideal data center application acceleration platform. The PA8921 FPGA acceleration card provides four 100GbE ports to accelerate high-performance passive and inline application deployment. The acceleration card provides developers with a complete development interface including drivers, application programming interfaces (APIs), and an FPGA Interface Manager ('FIM,' or FPGA shell).

Targeted Applications

- Load Balancing
- Network Security
- Traffic Monitoring
- Service Gateway



Hardware

Agilex FPGA
- Agilex 7 F014 FPGA

Onboard Memory
- 4x 16 GB DDR4 DIMM (total 64 GB)

Interfaces
- 100GbE; 4x25G NRZ or 2x50G PAM4
- PCIe 4.0 x16

Form Factor
- Full height, ¾ length
- Dual slot
- TDP: 70 W

Software

- OFS
- Intel oneAPI Base Toolkit

Ordering Information

Part Number	Configuration
PA8921-101	PCIe FPGA acceleration caard Agilex 014 FPGA FH¾L, Dual slot 2x 100GbE (QSFP28) PCIe 4.0x16 4x 16 GB DDR4 DIMM

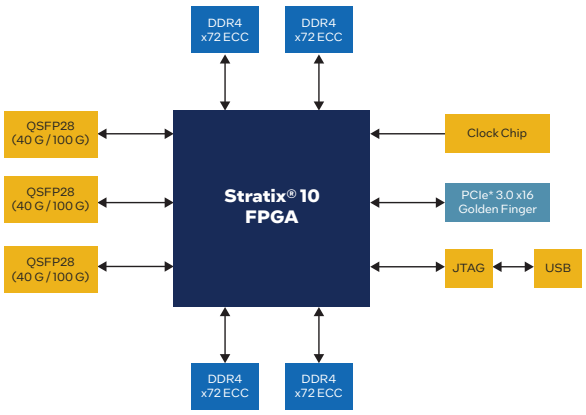
FA728Q

Provided by Flyslice Technologies
View this solution on the Flyslice website

FA728Q is a high-performance PCIe-based acceleration card equipped with the Stratix 10 FPGA. The FA728Q offers 32 GB on-board DDR4-2400 memories and three QSFP28 sockets to support up to 100GbE for each interface. The board provides the performance and versatility of FPGA acceleration for data centers and computing-intensive applications. It is also supported by OFS, which provides an FPGA Interface Manager ('FIM,' or FPGA shell), drivers, and APIs to ensure users can customize their own unique acceleration platform solutions.

Targeted Applications

- Time-Critical Network Applications
- FPGA-Based Accelerating Applications



Hardware

Stratix 10 FPGA
- Stratix 10 SX 2800

Onboard Memory
- 4x 8 GB DDR4-2400 with ECC

Interfaces
- 3x QSFP28: 3x100G/40G
- PCIe 3.0 x16

Form Factor
- Full height, ¾ length
- Dual slot
- TDP: 75W

Software

- OFS
- Intel oneAPI Base Toolkit

Ordering Information

	F728Q(A)	
Heatsink	A	Blank=Passive heatsink /A=Active heatsink

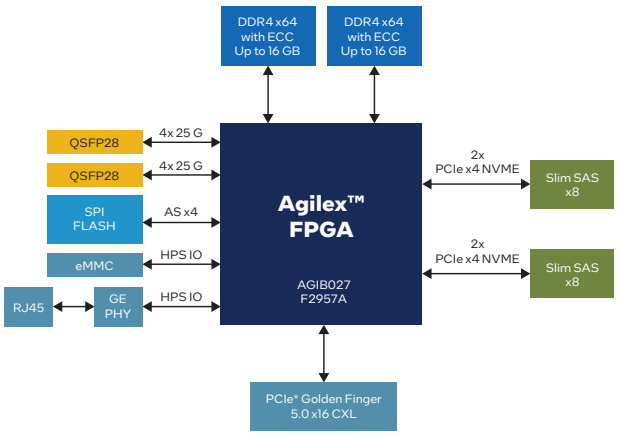
FA927S

Provided by Flyslice Technologies
View this solution on the Intel Partner Showcase or the Flyslice website

The FA927S is a full-height, half-length, PCIe Add-In acceleration card equipped with the latest Agilex 7 SoC FPGA I-Series device, offering 2.7M LEs, transceiver rates up to 116 Gbps, PCIe 5.0 and Computing Express Link (CXL) support. The FA927S combines several high-end hardware interfaces, including PCIe 5.0 x 16, two 100G QSFP-28 connectors, and two DDR4-2400 memory channels up to 16 GB capacity to provide excellent computing power and high bandwidth. The FA927S comes with full-height dual slot front brackets and with optional active heatsink or passive heatsink systems.

Targeted Applications

- Financial Trading
- Networking Storage
- Artificial Intelligence
- High Performance Computing



Hardware

Agilex FPGA
- AGIB027R29A1E2VR3

Onboard Memory
- 4x 8 GB DDR4-2400 with ECC

Interfaces
- 2x QSFP28: 2x 100GE/40GE
- PCIe 5.0 x16
- 2 x8 Slim SAS connectors for PCIe 4.0 extension

Form Factor
- Full height, half length
- Dual slot
- 200 W

Software

- OFS

Ordering Information

FA927S(D/H)(P/A)		
DDR4 Memory	D/H	D=16GB DDR4 on board H=32GB DDR4 on board
Cooling	P/A	BLANK: Single slot passive heatsink P=Dual slot passive heatsink A=Dual slot active heatsink

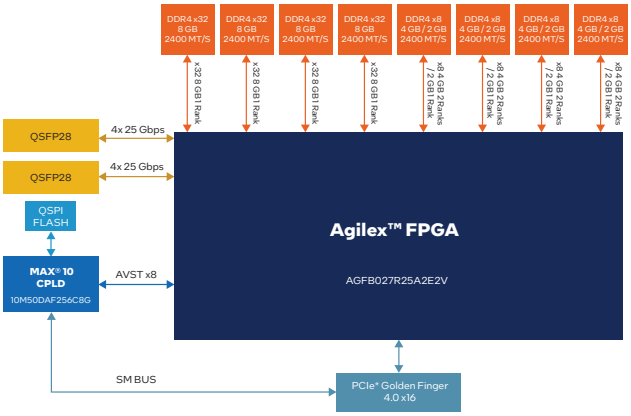
FA925E

Provided by Flyslice Technologies
View this solution on the Flyslice website

The FA925E is a full-height, 3/4 length PCIe add-in card featuring the powerful and efficient Agilex 7 FPGA F-Series. The FA925E offers two front QSFP28 sockets, each supporting one 100GbE or 40GbE network interface, and eight separate banks of DDR4 memory onboard. The first four 32-bit DDR4-2400 memory banks each provide up to 8 GB and the latter four 8-bit DDR4-2400 memory banks each provide up to 4GB. The PCIe golden finger supports 4.0 x16 protocol. This card comes with full-height dual slot front brackets and with optional active heatsink or passive heatsink systems.

Targeted Applications

- High Performance Computing
- Compression/ Decompression
- Cryptographic Applications



Hardware

Agilex FPGA
- AGFB027R25A2E2V

Onboard Memory
- 4x 8 GB 32-bit DDR4-2400 with ECC
- 4x 4 GB 8-bit DDR4-2400 with ECC

Interfaces
- 2x QSFP28: 2x100GE/40GE
- PCIe 4.0 x16

Form Factor
- Full height, ¾ length
- Dual slot
- 150 W

Software

- OFS

Ordering Information

FA925E(D/H)(P/A)		
DDR4 Memory	D/H	D=24GB DDR4 on board H=48GB DDR4 on board
Cooling	P/A	P=Dual slot passive heatsink A=Dual slot active heatsink

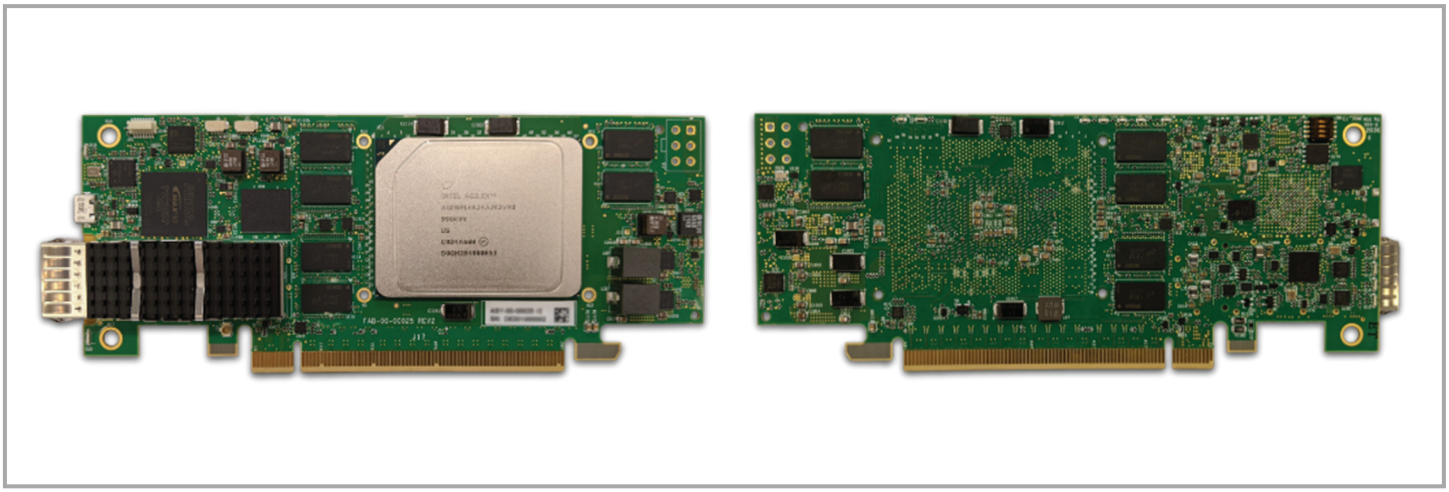
Agilex FPGA Low Profile NC100 PCIe Card

Provided by Hitek Systems
View this solution on the Intel Partner Showcase or the Hitek Systems website

The Agilex FPGA Low Profile PCIe Card from Hitek Systems is the first module to support 200G PAM4 Ethernet and 16x PCIe 4.0 in a low profile (HHHL) form factor. It is also the first board with fully ported and hardware verified compatibility with OFS, OpenCL™, and oneAPI support on Agilex FPGA.

Targeted Applications

- SmartNIC
- Data Center
- Networking and High-Performance Computing
- Machine Learning, Network, Compute Acceleration



Hardware

Agilex FPGA
- AGF014 and AGF012 device support

Onboard Memory
- 24 GB DDR4

Interfaces
- PCIe 4.0 x16
- QSFP56 supports up to 200 Gbps (4x 56 Gbps PAM4)
- Micro-USB 2.0 with on-board USB hub
- HPS GigE network interface

Form Factor
- ½ height, ½ length
- Single/dual slot, passive cooling available
- 75 W (edge powered) and ~100 W (6-pin PCIe connector)

Software

- OFS
- Intel oneAPI Base Toolkit
- OpenCL Board Support Package (BSP)

Ordering Information

Part Number	Configuration
AGF-NC100-B34-01	AGFB014, 24GB DDR4, 75W, SW passive
AGF-NC100-B34-03	AGFB014, 24GB DDR4, 100W, DW passive

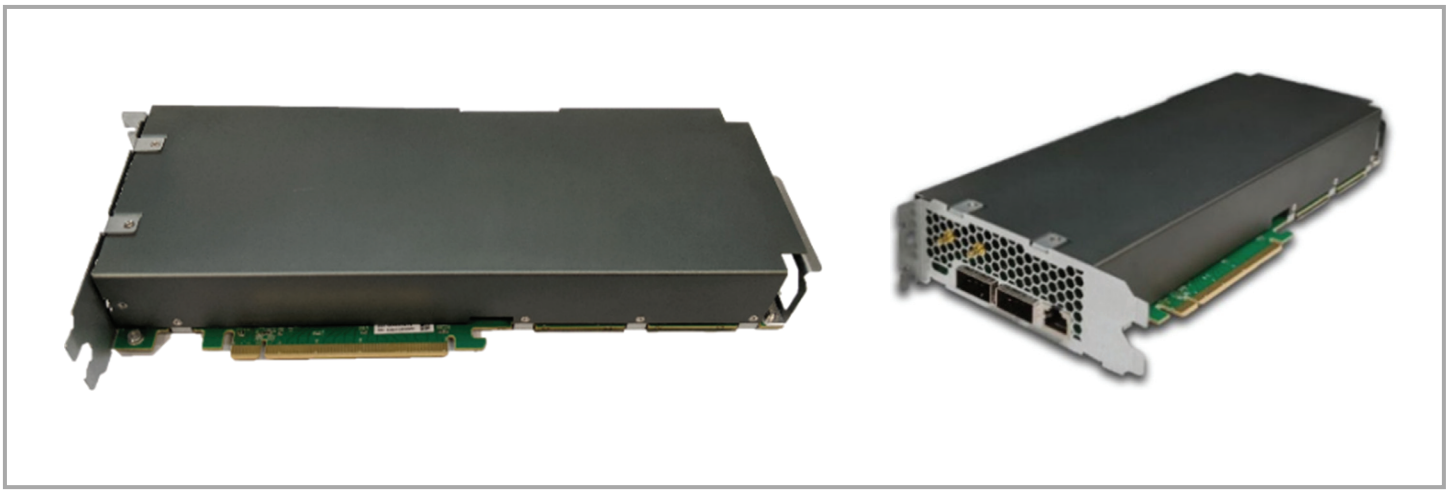
Agilex FPGA Network, Computational Storage PCIe Card

Provided by Hitek Systems
View this solution on the Hitek Systems website

The Agilex FPGA Computational Storage PCIe Card is designed for high-bandwidth network and computational storage workloads. The computational storage card offers over 200 Gbps bandwidth across network, computation, and storage. It also includes up to 32 TB of on-board Gen4 M.2 NVMe SSDs.

Targeted Applications

- SmartNIC Solutions
- Embedded Designs
- Signal Processing/Image Processing
- Up to 4x100G/50G/40G and up to 16x 25G/10G Networked Signal Processing Devices and Platforms



Hardware

Agilex FPGA
- AGF027 device support (F019, F022, F023 device support as build SKUs)

Onboard Memory
- Supports up to four Gen4 M.2 NVMe SSDs
- Up to 2 banks of DDR4 ECC interfaces for DIMM(s)

Interfaces
- PCIe 4.0 x16
- 200 Gbps network interface
- Micro-USB 2.0 with on-board USB hub
- HPS GigE network interface

Form Factor
- Full height, ¾ length; dual slot
- 75 W from PCIe edge connector and 225 W from auxiliary CPU/GPU connector

Software

- OFS
- Intel oneAPI Base Toolkit
- OpenCL BSP

Ordering Information

Part Number	Configuration
AGF-NCS200-B74-xx	AGFB027, xx = DIMM/SSD options

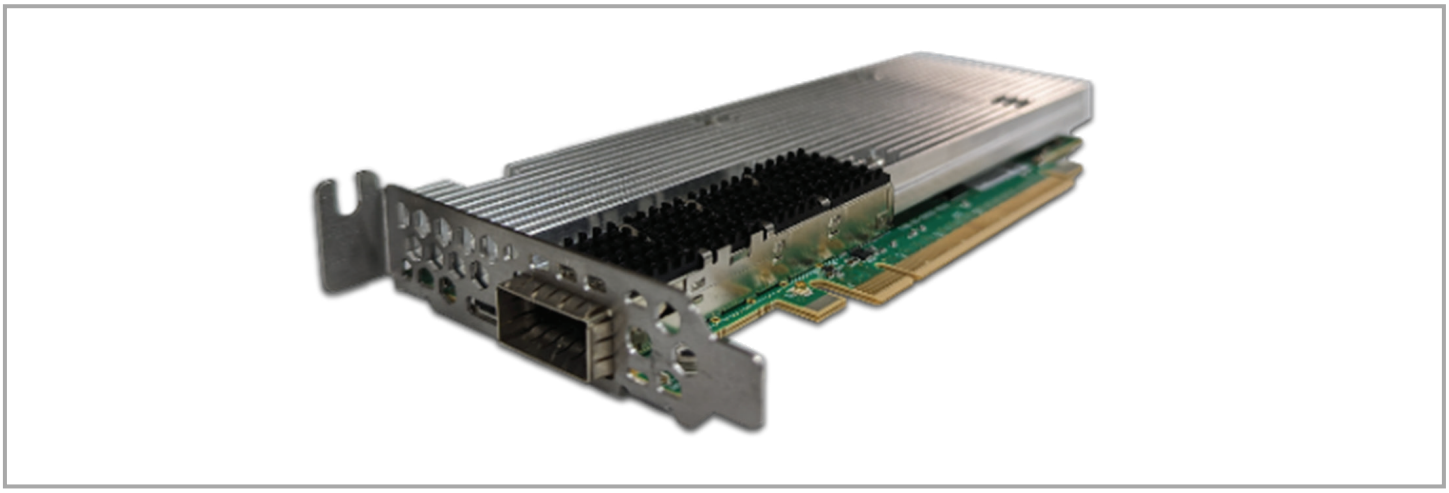
Agilex FPGA Low Profile NC200 PCIe Card Card

Provided by Hitek Systems
View this solution on the Hitek Systems website

The Agilex FPGA Low Profile NC200 PCIe Card is next generation of low profile PCIe card from Hitek Systems with 2x F-Tile support (Agilex FPGA R24C package) and SKUs covering entire range of densities from 006 through 027. It is designed for broad market applications including edge acceleration and 5G vRAN stack. It is designed for full support of PTP/1588 network synchronization with support for external clock and sync signals.

Targeted Applications

- SmartNIC
- Data Center
- Networking and High-Performance Computing
- Machine Learning, Network, Compute Acceleration



Hardware

Agilex FPGA
- AGF006 – AGF027 devices supported as build SKUs

Onboard Memory
- 2x fabric DDR4 Banks (8 GB each)
- 1x HPS DDR Bank (4 GB)

Interfaces
- PCIe 4.0 x16
- QSFP-DD network interface (up to 400G; 8x 56G PAM4)
- Si5402 based network PTP/1588 synchronizer
- Micro-USB 2.0 with on-board USB hub
- HPS GigE network interface

Form Factor
- ½ height, ½ length
- Single/dual slot, passive cooling available
- 75 W (edge powered) and ~100 W (6-pin PCIe connector)

Software

- OFS
- Intel oneAPI Base Toolkit
- OpenCL BSP

Ordering Information

Part Number	Configuration
AGF-NC200-B74-01	AGFB027, 24GB DDR4, 75W, SW passive
AGF-NC200-B74-03	AGFB027, 24GB DDR4, 100W, DW passive

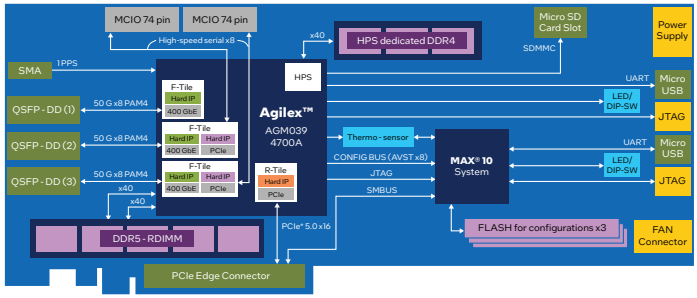
IPAC-1000

Provided by IBEX Technology
View this solution on the IBEX Website

The IPAC-1000 is a PCIe standard programmable acceleration card with an Agilex 7 FPGA M-Series. It comes equipped with 32 GB of HBMe2, three 400G optical transceiver ports, 16 lanes of PCIe 5.0, two 74-pin MCIO ports and 32 GB of DDR5 SDRAM. It comes in a full height, ¾ length, dual slot form factor in the PCIe standard. IPAC supports Open FPGA Stack (OFS) and will soon support oneAPI. P4 may be supported at a later date.

Targeted Applications

- High Performance Computing
- High Speed Network
- Quantum Computing
- Video Processing
- Artificial Intelligence



Hardware

Agilex FPGA
- Agilex 7 FPGA M-Series AGMF039R47A2E2V

Onboard Memory
- HBMe2 32GB (FPGA)
- DDR5-4800 RDIMM 32GB
- DDR4-3200 GB

Interfaces
- PCIe 5.0 x16
- 400G QSFP-DD x3
- 72 pin MCIO x2
- MicroSD, MicroUSB, JTAG

Form Factor
- Full height, ¾ length
- Dual slot

Software

- OFS
- Intel oneAPI BAse Toolkit (coming Q2 2025)

Ordering Information

Contact IBEX Technology for ordering information.

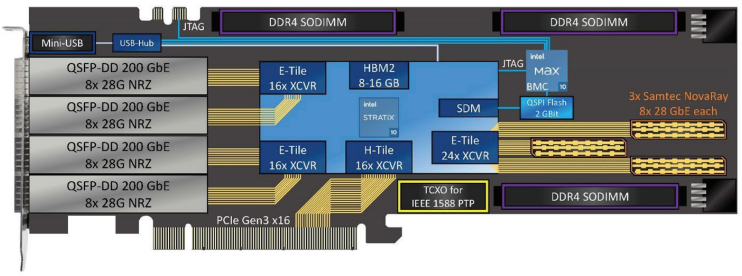
FALCON Acceleration Card

Provided by prodesign
View this solution on the Intel Partner Showcase or the predesign website

The predesign FALCON Acceleration Card is designed for compute acceleration and fulfills the highest needs in the area of FPGA-based High Performance Computing. It addresses customers who need an acceleration of memory-bound applications (MX) and AI applications (NX). The PCIe Card offers the Stratix 10 FPGA technology maximum resources, performance, and HBM2 memory (Stratix 10 MX version). The board is based on PCIe 3.0 x16 and offers four 100GE QSFP-DD front ports, which are connected directly to the FPGA. Further, it offers 3 SO-DIMM connectors for DDR4 modules or other interfaces like flash memories, debug interfaces, etc. The board is available in the PCIe ¾ length form factor as single-slot and dual-slot variants. It offers passive air, active air, and liquid cooling (liquid cooling only in dual-slot variant).

Targeted Applications

- High Performance Computing
- Live Video Broadcasting
- IT Security
- Network Acceleration
- Networking Switch



Hardware

Stratix 10 FPGA
- Stratix 10 MX/NX
- 1S10MX160-F55, 1S10MX210-F55
- 1S10NX210-F55

Onboard Memory
- 8 GB HBM2
- 4x 8 GB DDR4-2400 with ECC

Interfaces
- 3x Samtec NovaRay* connectors
- 3x SODIMM sockets
- 8x QSFP28 (100Gbit/s each)
- PCIe 3.0 x16

Form Factor
- Full height, ¾ length
- Single or Dual slot
- 200 W TDP

Software

- OFS (on request)
- Intel oneAPI Base Toolkit (on request)
- Prodesign SDK including example designs, BMC firmware

Ordering Information

For ordering information, contact
sales-fpga-acceleration@prodesign-europe.com

Silicom FPGA SmartNIC N5010 Series

Provided by Silicom
View this solution on the Intel Partner Showcase or the Silicom website

The Silicom FPGA SmartNIC N5010 is a high-performance programmable PCIe server adapter based on the Stratix 10 FPGA. It is a high-performance, hardware programmable 4x 100 GbE FPGA SmartNIC enabling next-generation IA-based servers to meet the performance needs of the 4/5G Core User Plane Function/Access Gateway Function.

Targeted Applications

- Network Function Virtualization (NFV)
- Multi-Access Edge Computing (MEX)
- Cyber Security
- High Performance Computing
- Finance
- Mobile Network

Hardware

Stratix 10 FPGA
- Stratix 10 DX FPGA 2100

2x Intel Ethernet Controller E810 CAM1 (N5014)

Onboard Memory
- 8 GB HBM2
- 32 GB DDR4 with error correction code (ECC)
- 144 Mb QDR IV
- Flash

Interfaces
- PCIe 4.0 x16
- 4x QSFP28 supports 100GbE

Form Factor
- Full height, ¾ length; dual slot passive
- 225 W TDP (75W from PCIe edge connector and 150 W from auxiliary CPU/GPU connector)

Software

- OFS
- Data Plane Development Toolkit (DPDK)

Ordering Information

Part Number	Name
FB4CGG2@S10D21-D00P0	N5013
FB4CGG2@S10D21-D00P1	N5013 (with SMA connector)
FB4CGG2@S10D21-D20P0	N5014
FB4CGG2@S10D21-D20P1	N5014 (with SMA connector)

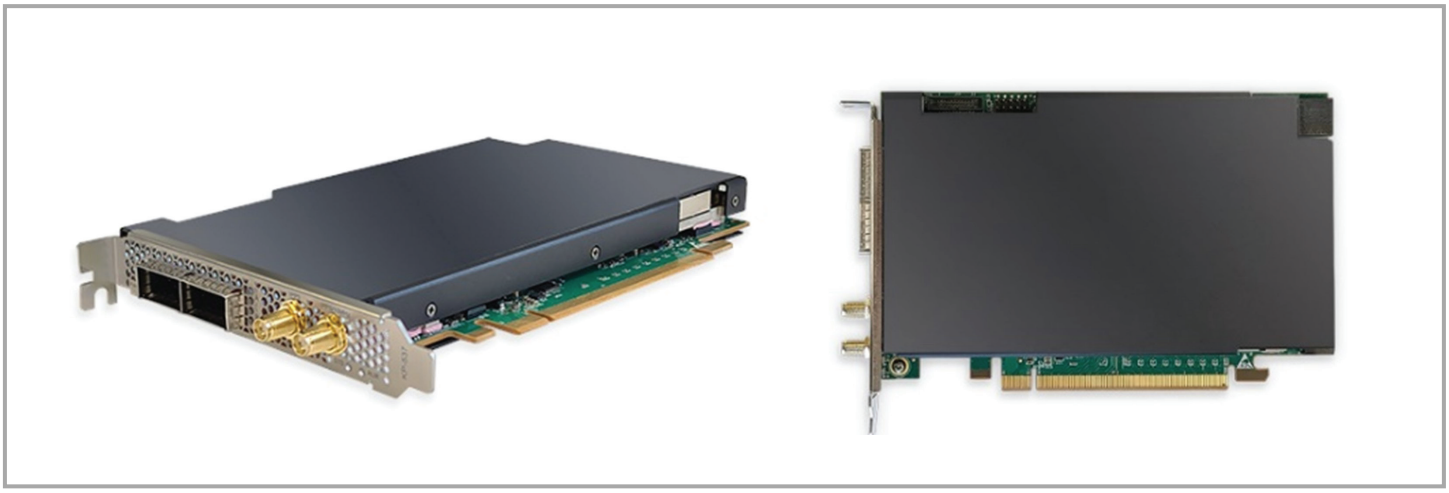
Silicom FPGA SmartNIC N6010/N6011Card

Provided by Silicom
View this solution on the Intel Partner Showcase or the Silicom website

The Silicom FPGA SmartNIC N6010 can be optimized for acceleration of communication workloads such as 4G/5G O-RAN Distribution Units. This accelerator includes an embedded Hard Processor System (HPS), quad-core Arm Cortex-A53, DDR4, 2x QSFP56 supporting up to 200GbE, and timing circuitry supporting LLS-C1, C2, and C3 timing architecture.

Targeted Applications

- 4G/5G Virtualized Radio Access Network (vRAN)
- Network Function Virtualization (NFV)
- Multi-Access Edge Computing (MEC)
- Video Transcoding
- Cyber Security
- High-Performance Computing
- Finance



Hardware

Agilex FPGA
- AGF014

1 x Internet Network Controller E810 CAM2 (N6011)

Hard Processor System
- Quad-core Arm Cortex-A53

Onboard Memory
- 8 GB DDR4 with ECC & 8 GB DDR4 without ECC
- 1 GB DDR4 for HPS
- 280 Mb flash

Interfaces
- PCIe 4.0 x16 (N6011 x8 bifurcation w/ E810)
- 2x QSFP28/56 supports 2x100GbE, 4x25 GbE, 8x 10GbE

Form Factor
- Full height, ½ length
- Max 125 W

Software

- OFS
- Intel oneAPI Base Toolkit
- DPDK/BBDev and FlexRAN
- Support for Contrail2 (CN2), vCSR, 5G vRAN, UPF

Ordering Information

Part Number	Configuration
FB2CG2@AGF14-A1P2	Silicom FPGA SmartNIC N6011 (Incl Intel E810 NIC)
FB2CG1@AGF14-A0P2	Silicom FPGA SmartNIC N6010

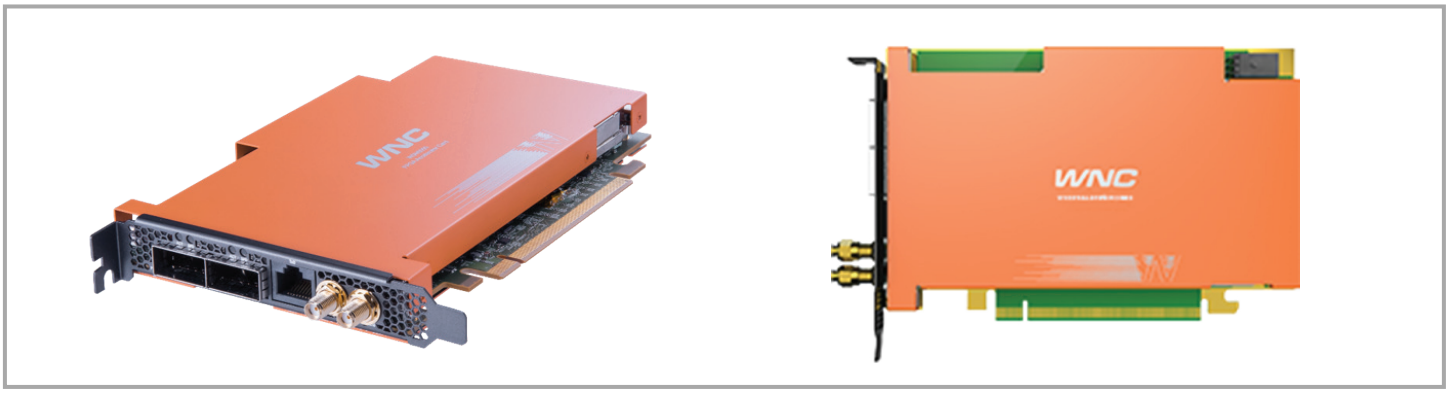
WNC FPGA SmartNIC WSN6050 Series

Provided by Wistron NeWeb Corp.
View this solution on the Intel Partner Showcase or the WNC website

The FPGA SmartNIC WSN6050 supports 5G New Radio (NR), forward error correction (FEC), and utilities FPGA to accelerate Multi-access Edge Computing (MEC). The WNC SmartNIC adopts the Precision Time Protocol (PTP) and the Synchronous Ethernet (SyncE) standards, enabling the creation of tightly synchronized fronthaul networks, critical in achieving 5G ultra-reliable low-latency communication (URLLC).

Targeted Applications

- 4G/5G vRAN
- UPF for 5G Core Network/MEC User Plane Function
- NFVi
- AI-Based Content Delivery Network
- Ultra-Low Latency for Electronic Trading
- High-Performance Computing
- Video Transcoding
- Cyber Security



Hardware

Agilex FPGA
- AGF014

Internet Network Controller E810-CAM2
- Supports 2x 100 Gbps
- PCIe 4.0 x8

Hard Processor System
- Quad-core Arm Cortex-A53

Onboard Memory
- 2GB DDR4 with ECC to HPS
- 8 GB DDR4 with ECC to fabric
- 8 GB DDR4 without ECC to fabric

Interfaces
- PCIe 4.0 x16 (x16 w/o E810, x8 x8 bifurcation w/ E810)
- SMBus
- 2x QSFP28, each slot can be configured as 1x 100 Gbps, 4x 25 Gbps, 4 x10 Gbps

Form Factor
- Full height, ½ length
- Single slot
- TDP Max 125 W (TDP is not relevant to deployment power; TDP is only used for server thermal design)

Software

- OFS
- Open Programmable Acceleration Engine (OPAE)
- Intel E810 Linux and netdev DPDK driver
- Trial version vRAN workload, bbdev DPDK driver, and FlexRAN* software patches for WSN6050
- PTP stack and PTP clock manager in HPS[1]
- National Marine Electronics Association (NMEA) standard, NMEA 0184 compatible GNSS timing synchronization software[2]

[1,2] valid for WSN6050 and WSN6051 only

Ordering Information

Part Number	Description
WSN6050	▪ E810-CAM2 ▪ 2x 8-lane PCIe 4.0 ▪ Cortex-A53 HPS ▪ 16 GB FPGA fabric DDR4 ▪ 2 GB HPS DDR4 ▪ 2x QSFP28 ▪ IEEE1588 and SyncE ▪ Samples in stock
WSN6051	▪ 16-lane PCIe 4.0 ▪ Cortex-A53 HPS ▪ 16 GB FPGA fabric DDR4 ▪ 2 GB HPS DDR4 ▪ 2x QSFP28 ▪ IEEE1588 and SyncE ▪ Samples in stock
WSN6052	▪ E810-CAM2 ▪ 2x 8-lane PCIe 4.0 ▪ 16 GB FPGA fabric DDR4 ▪ 2x QSFP28 ▪ Built to order
WSN6053	▪ 16-lane PCIe 4.0 ▪ 16 GB FPGA fabric DDR4 ▪ Built to order

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