

Postlab 3 Ruby Spring

WSVout & BS in DS, some correction

The voltage at the collector, Vort,

Vout = Vec - IcmR

Some see it is afterback by the collector

As Vin increases, the BIT eventrally reaches deep saturation is remore the cond some current begins to thou the opposite direction (prompter base to the third opposite direction (prompter base to the third) in Vort. Why the slopes are all unity, because they all overlap perfectly with the data from experiment 2; it has something to do with the emitter vesistor salue.