



CHP COMPILER

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Asynchronous is awesome

Tool infrastructure lacking

CHP great at describing behavior

PRs great at representing circuitry

Bridge that gap?



Synthesize new elements from CHP...

...Using syntax we are all familiar with...

**...that can be integrated into any toolchain
requiring PRs**



Syntax Directed Translation (Tangram)

Pre-Charge Half Buffer Design (ARCWelder)

Martin Synthesis (Us!)



Expand expressions

Expand process/operator instantiations

Expand multi bit variables/records/channels

Optimizations



Information loss in guards

Information loss in parallel branches

Loop invariant

Impossible branches

Conflicting states



Minterms

Logic Minimization

Optimizations (symmetrization, factoring...)



Assignment

```
q, r, t := x^y | z^w, a+b, a*b;
```

Conditional

```
[ a | b -> c+ [] ~a & ~b -> c- ]
```

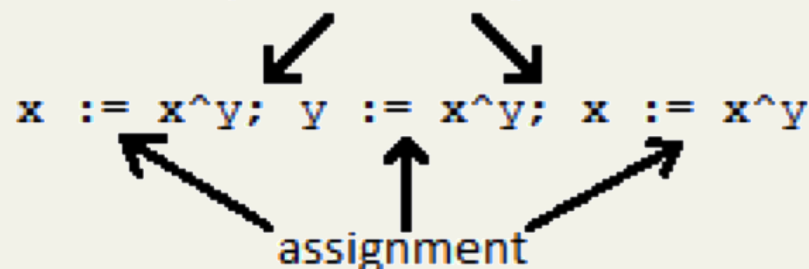
Loop

```
*[ a -> x := x + 1; a := 0  
  [] ~a -> x := x*x; a := 1  
]
```

sequential composition

$x := x^y; y := x^y; x := x^y$

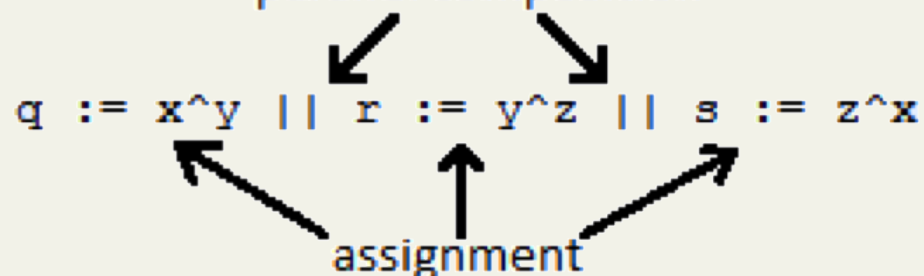
assignment



parallel composition



$q := x^y \parallel r := y^z \parallel s := z^x$

assignment






```
int<32> var;  
int var;  
mux0b2 MyMux(a, b, c, d);  
chan4p1b MyChan;
```




name  `record dualrail`
`{`
 `int<1> t;`
 `int<1> f;`
`}`
 variable list





name   input list


```
process fifo4p0b(chan4p0b l, chan4p0b r)
{
    *[l?;r!]
}
```

defining CHP 



name   input list

```
operator & (int<1> s, int<1> a, int<1> b)
{
    [   a & b -> s+
    [] ~a | ~b -> s-
    ];
}
```

defining CHP 

```
channel chan4p0b ← name
{
    int<1> r;      ← variable list
    int<1> a;

    operator!()   ← send operator
    {
        r+; [a]; r-; [~a];
    }

    operator?()   ← receive operator
    {
        [r]; a+; [~r]; a-;
    }

    operator@()   ← probe operator
    {
        r
    }
}
```



#include means libraries!

//comments are useful

**/*So are block comments
that span multiple lines!*/**

**needed to remove unne-
eded white spa-
ce to make parsing easier**



Easier to parse if syntax 1to1 with meaning

So, make 'shortcut' syntax fully explicit!

e.g. $a.r+$ means $a.r:=1$, $[a.a]$ means $[a.a \rightarrow \text{skip}]$, $*$
 $[S]$ means $*[1 \rightarrow S]$

Recursively break CHP into lists of
blocks/instructions



Record Instantiation

Process Instantiation

Copy code + search and replace

Expression Instantiation

Parse Expression + Process Instantiation



$[G1][G2]$ turns into $[G1 \& G2]$

$[G1 \rightarrow [Ga \rightarrow Sa[]Gb \rightarrow Sb]]$

turns into

$[G1 \& Ga \rightarrow Sa[]G1 \& Gb \rightarrow Sb]$

$x+; y-; z:=x;$ turns into $x,y:=1,0; z:=x;$



What gains state information?

Assignments

Guards

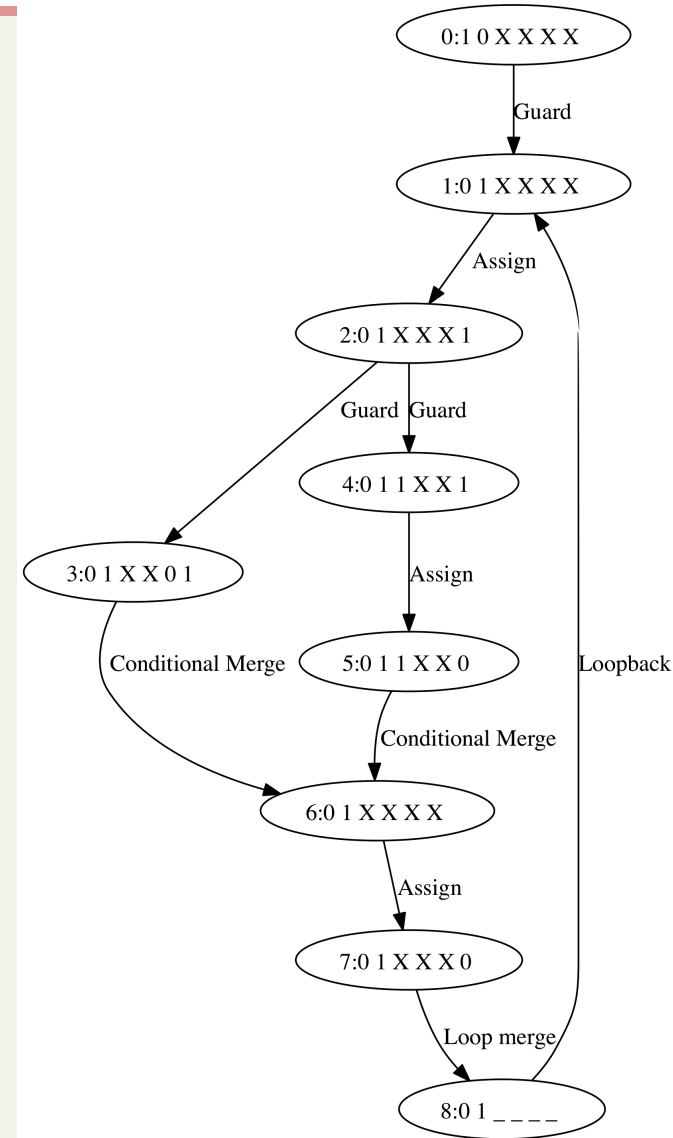
Mutual Exclusion (future)

What loses information?

Branch Merges

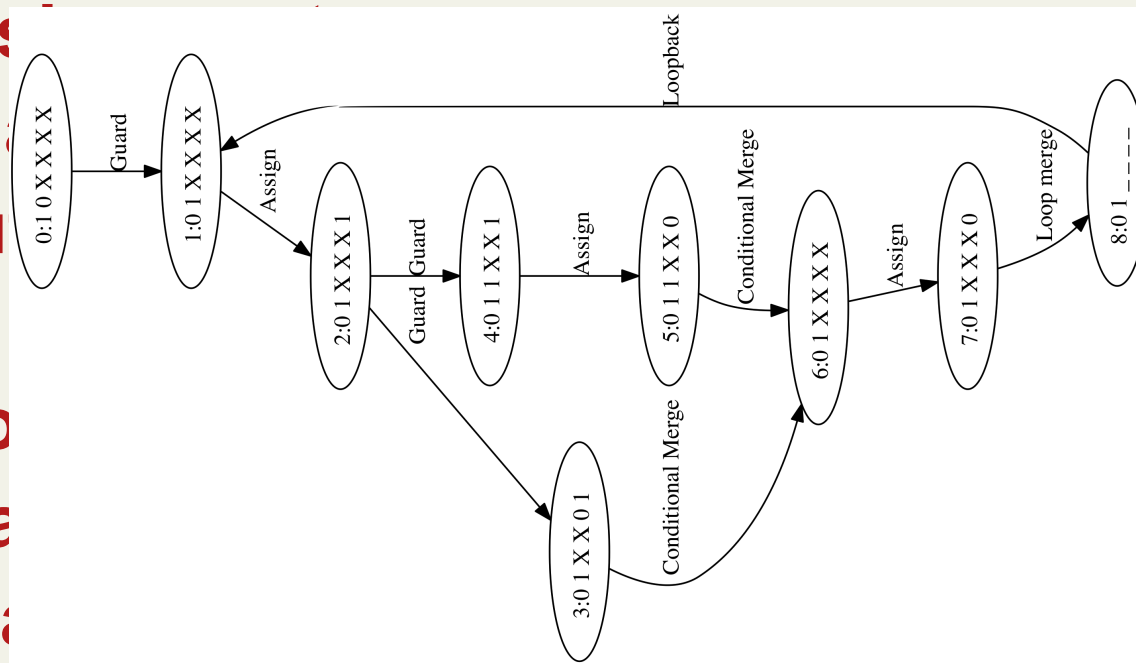
Channel Assigns

Across Parallel Branches



What gains state information?

Assign
Guard
Merge



What lo
Bra
Cha

Across Parallel Branches



Find guard with OR

Create variable whose name is the guard

Add it to the state space



Maximize the number of conflicts removed

Brute Force



For each variable/direction pair:

Find all implicant states that cause change

Build weakest 'sufficient' PR guard



Quine McCluskey reduction:

Reduce to prime implicants (think prime factors)

Find 'essential' implicants

Factor implicants:

Too long to implement?

Lots of common terms?



Handshake Reshuffling

Process Decomposition/Projection

Impossible Branch Removal

Isochronic Fork Detection



Power Analysis

Timing Analysis

Design Space Exploration

CHP to CHP Optimizations

Verification



Full integration into ACT

CHP Block

Interchangeable ACT and CHP libraries



With all the time we have already and plan to spend on this project, let's try to make it as useful as possible!



