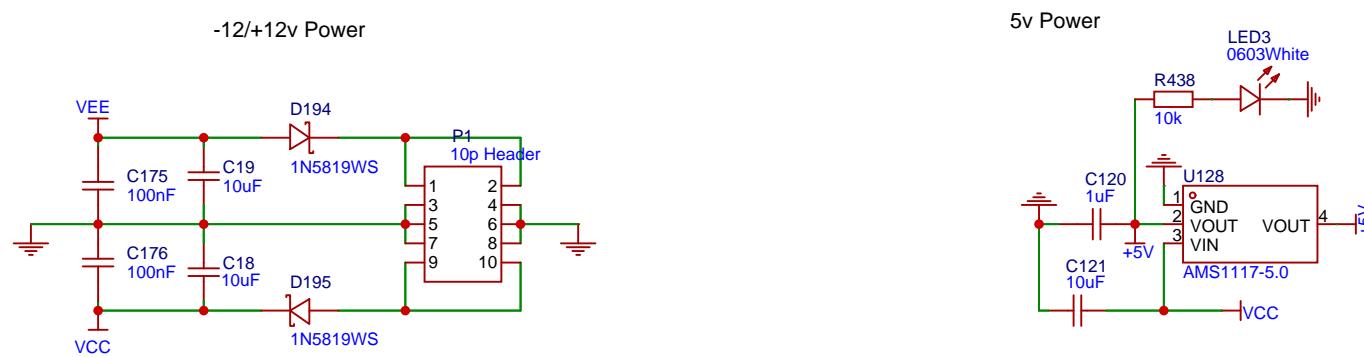
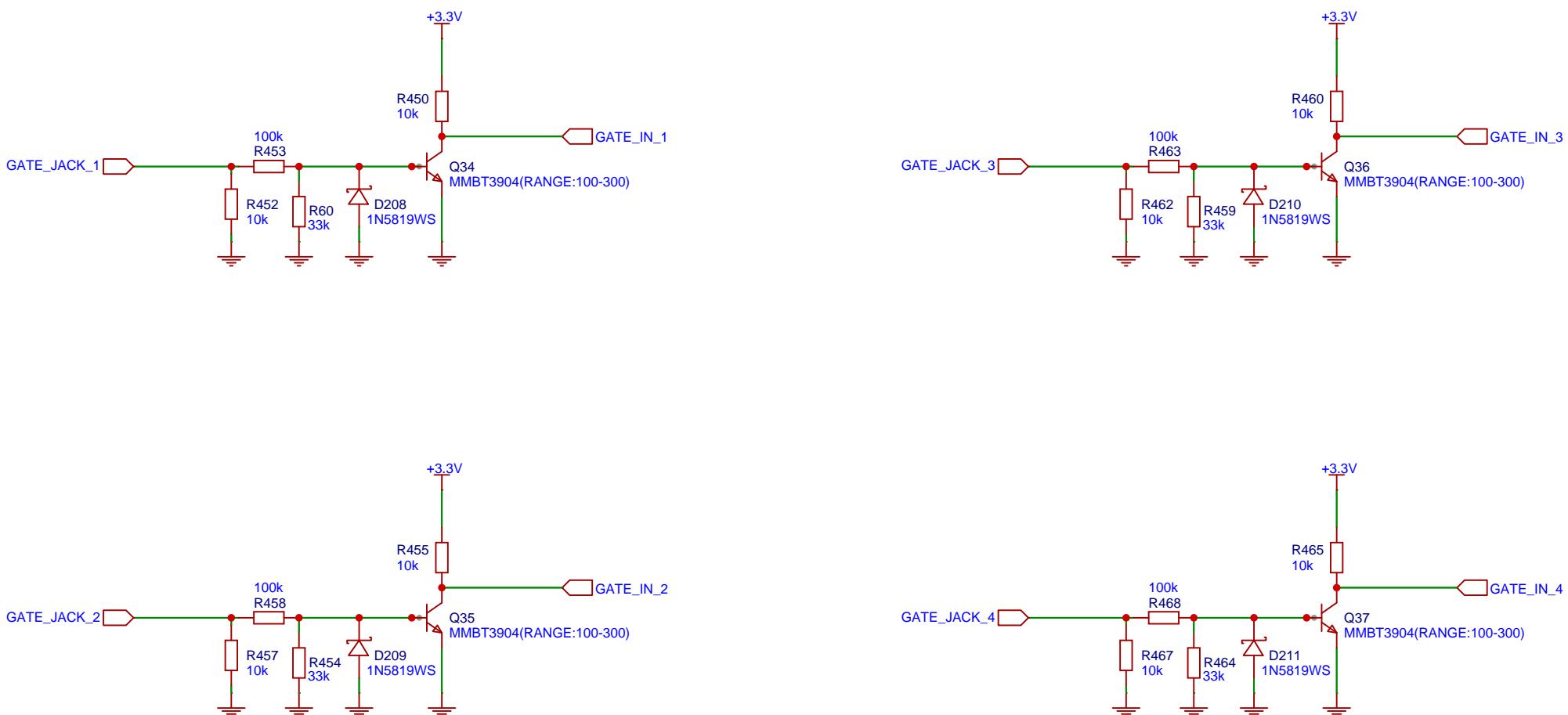


Schematic	Schematic2		Create at	2025-11-18
			Update at	2025-11-25
Board	Main board		Page	Microprocessor
Drawn		Quad ADSR 2025		
Reviewed				
		Version	Size	Page 2 Total 5
 EasyEDA		V1.0	A4	EasyEDA.com
3	4	5	6	



Schematic	Schematic2		Create at	2025-11-18
Board	Main board		Update at	2025-11-18
Drawn			Page	Power
Reviewed			Quad ADSR 2025	
	Version	Size	Page 3 Total 5	
EasyEDA		V1.0	A4	EasyEDA.com

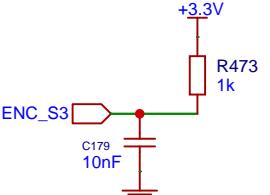


Schematic	Schematic2		Create at	2025-11-18
Board	Main board		Update at	2025-11-18
Drawn			Page	Gates in
Reviewed			Quad ADSR 2025	
	Version	Size	Page 4 Total 5	
 EasyEDA		V1.0	A4	EasyEDA.com

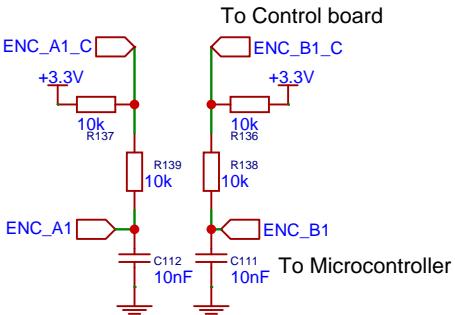
Encoder 1



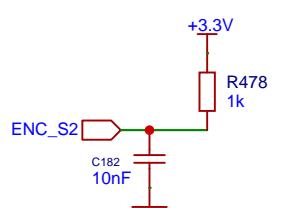
Encoder 3



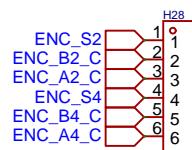
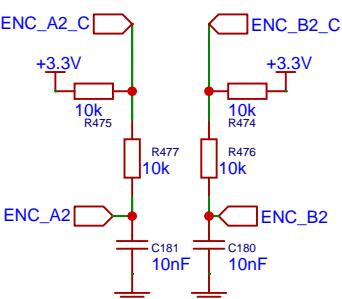
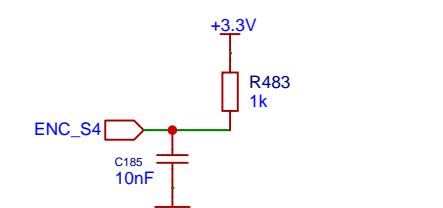
To Control board



Encoder 2



Encoder 4



Schematic	Schematic2	Create at	2025-11-18
Board	Main board	Update at	2025-11-27

Drawn		Page	UI and Headers
Reviewed			

Quad ADSR 2025

