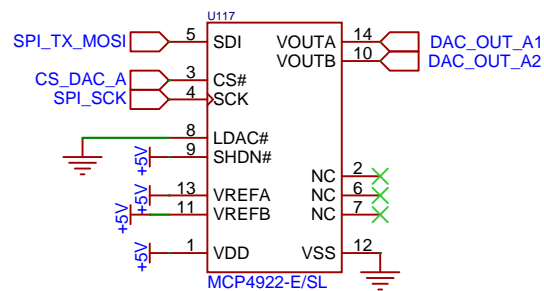
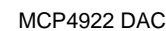
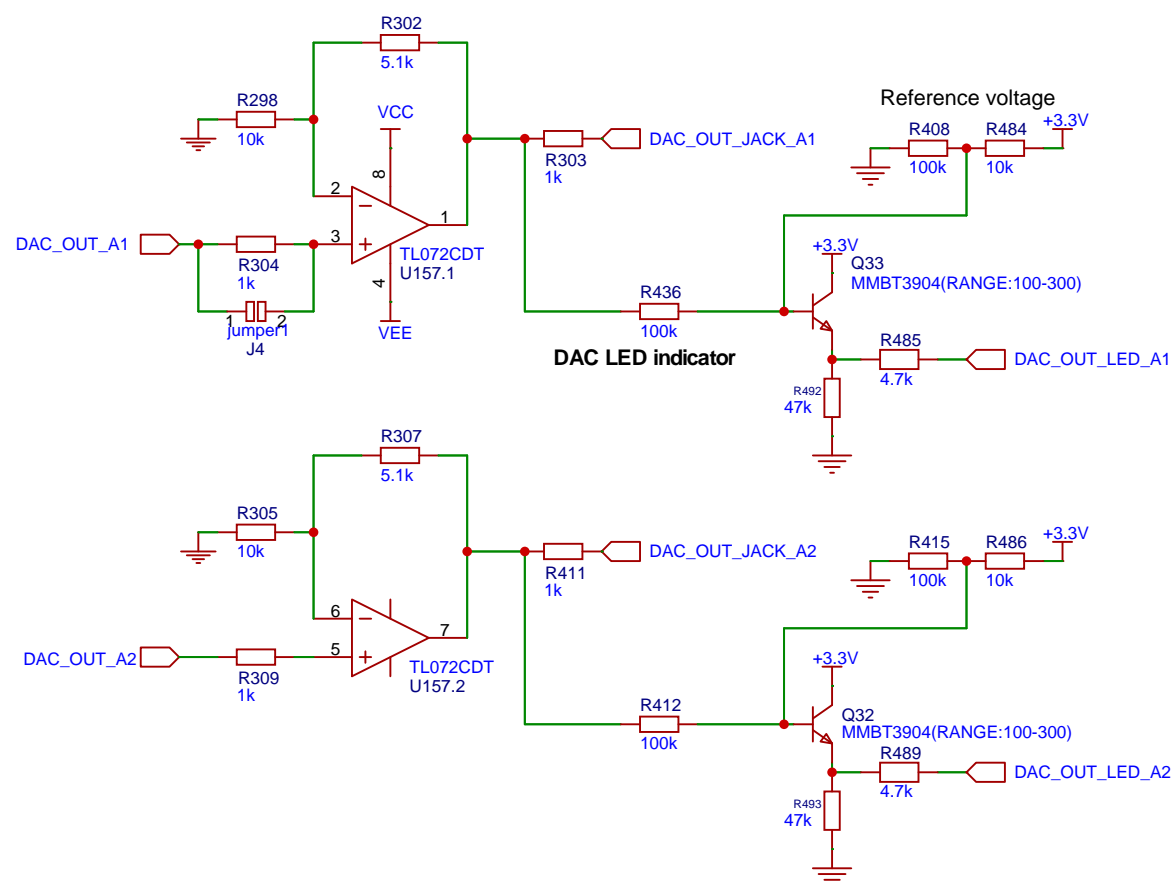
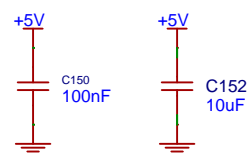


A

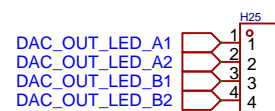
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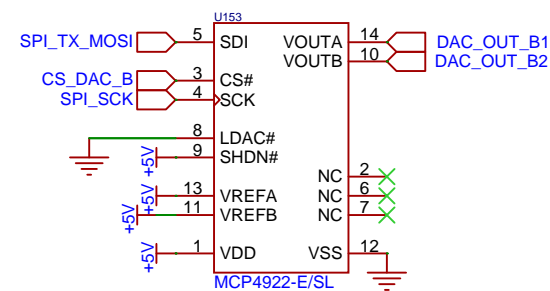
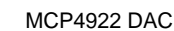
Decoupling caps



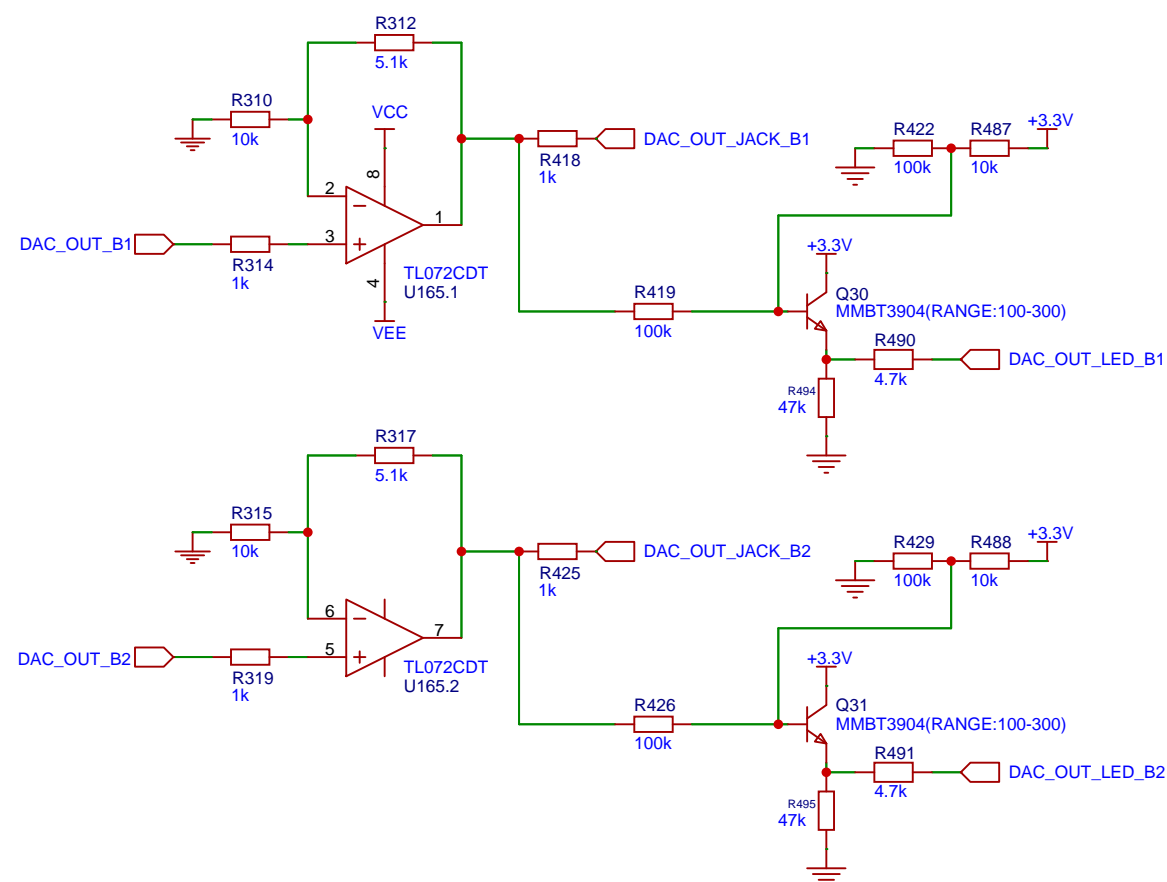
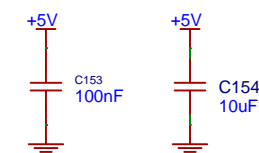
Header to control board




Jacks header is in UI and Headers

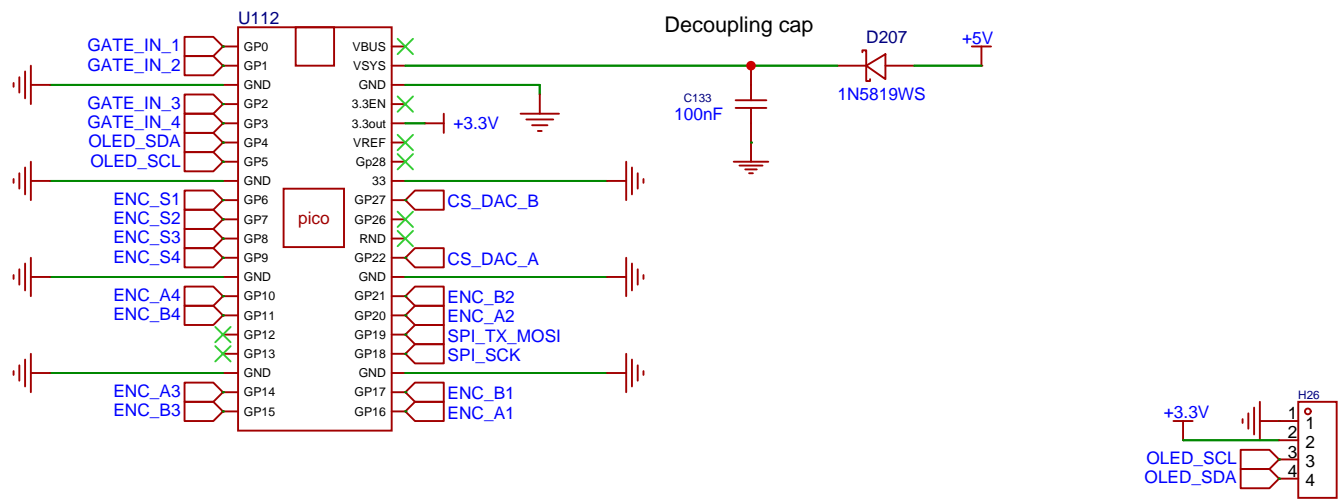


Decoupling caps

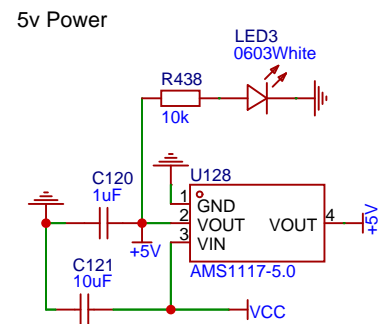
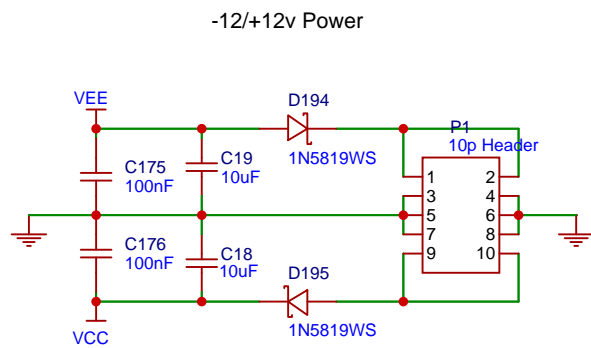



Schematic	Schematic2			Create at	2025-11-18
				Update at	2025-12-02
Board	Main board			Page	DACs
Drawn		Quad ADSR 2025			
Reviewed					
		Version	Size	Page 1 Total 5	
		V1.0	A4	EasyEDA.com	

RP2040 Microprocessor chip on Pico board



Schematic	Schematic2			Create at	2025-11-18
Board	Main board			Update at	2025-11-25
Drawn		Quad ADSR 2025			
Reviewed					
		Version	Size	Page 2 Total 5	
EasyEDA		V1.0	A4	EasyEDA.com	



Schematic	Schematic2			Create at	2025-11-18
Board	Main board			Update at	2025-11-18
Drawn		Quad ADSR 2025			
Reviewed					
		Version	Size	Page 3 Total 5	
		V1.0	A4	EasyEDA.com	

A

A

B

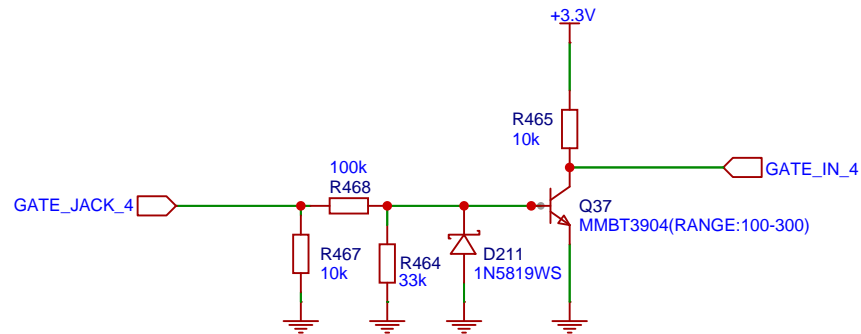
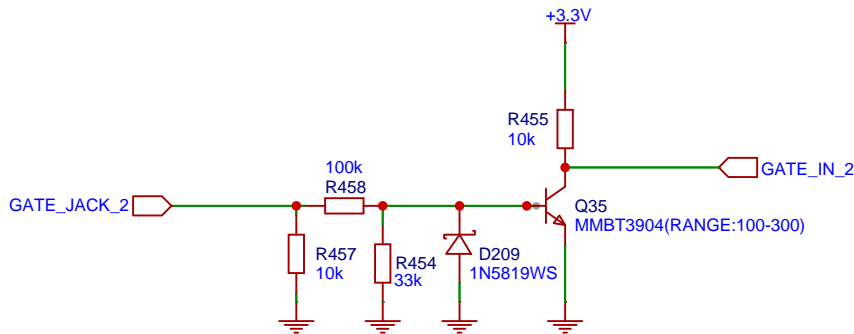
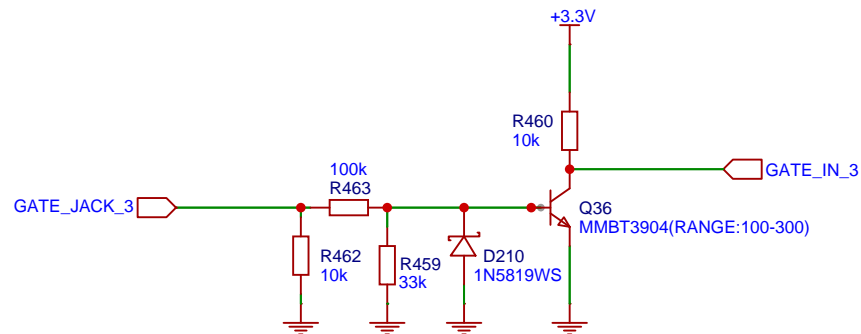
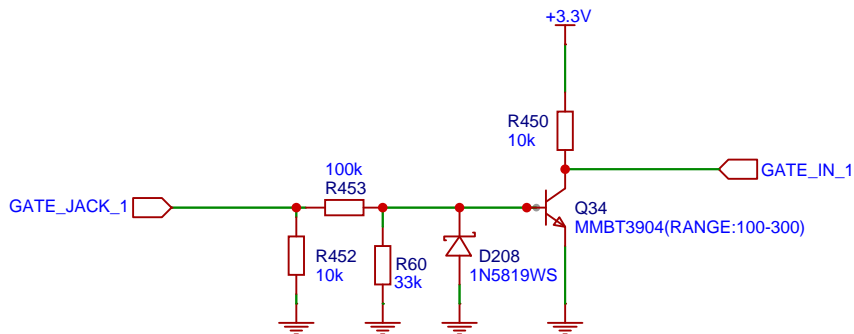
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C

C

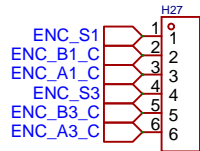
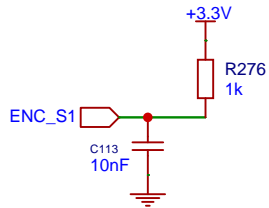
D

D

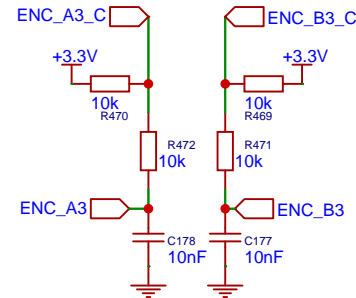
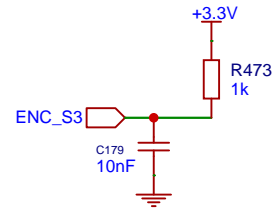


Schematic	Schematic2			Create at	2025-11-18
Board	Main board			Update at	2025-11-18
Drawn		Quad ADSR 2025			
Reviewed					
		Version	Size	Page 4 Total 5	
EasyEDA		V1.0	A4	EasyEDA.com	

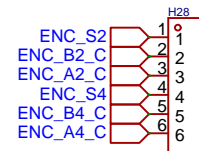
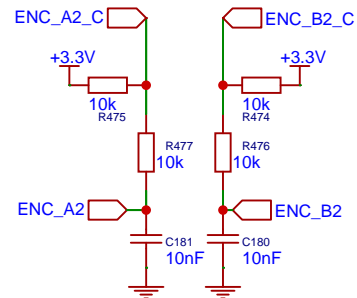
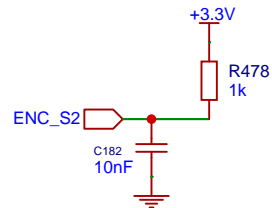
Encoder 1



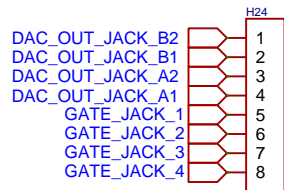
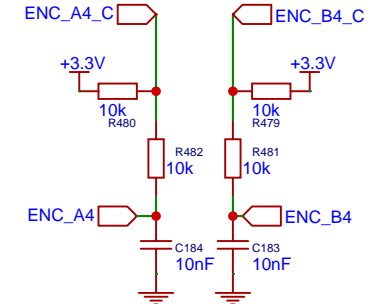
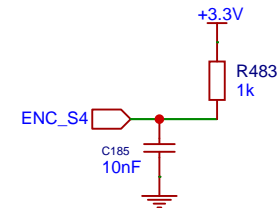
Encoder 3



Encoder 2



Encoder 4



Schematic	Schematic2		Create at	2025-11-18
Board	Main board		Update at	2025-11-27
Drawn		Quad ADSR 2025		
Reviewed				
		Version	Size	Page 5 Total 5
EasyEDA		V1.0	A4	EasyEDA.com