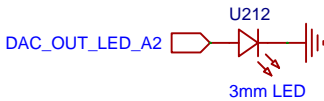
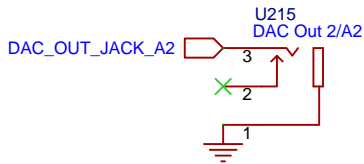
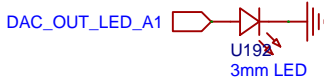
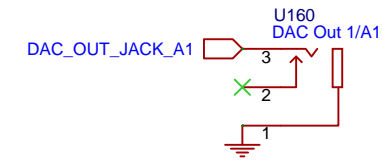
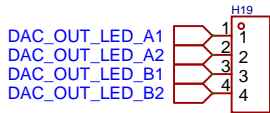


A

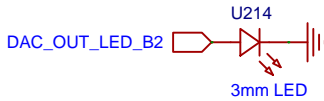
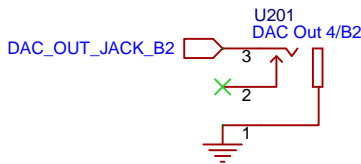
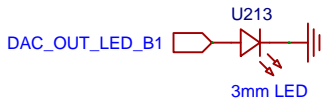
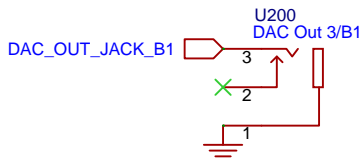
B



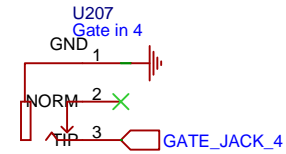
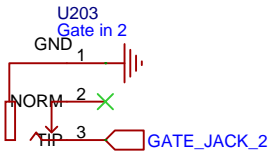
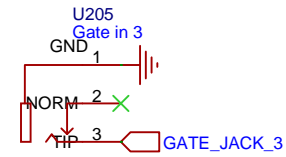
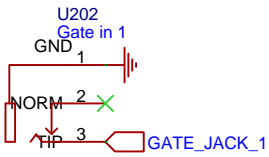
Header to main board



Jacks header is in UI and Headers

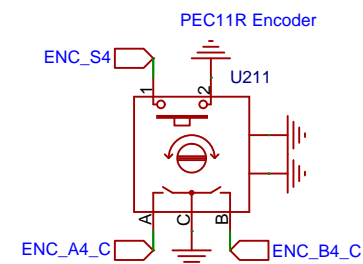
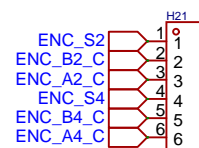
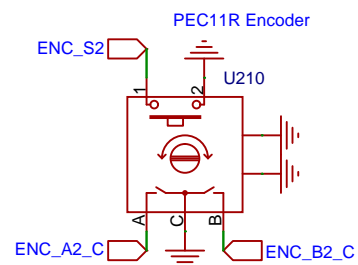
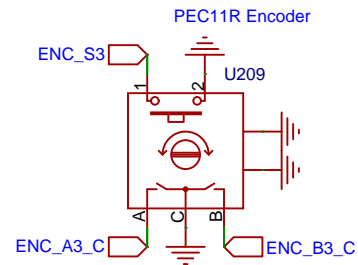
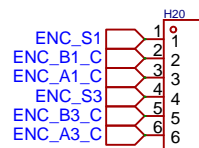
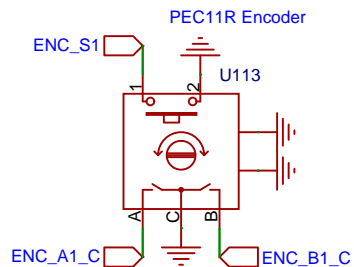


Schematic	Schematic1			Create at	2025-11-15
Board	Control board			Update at	2025-11-21
Drawn		Quad ADSR 2025			
Reviewed					
		Version	Size	Page 1 Total 3	
EasyEDA		V1.0	A4	EasyEDA.com	

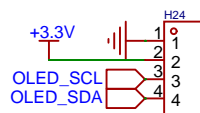
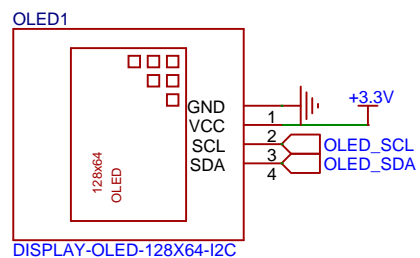


Gates header is in UI and Headers page

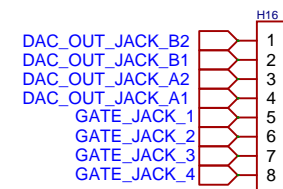
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Board	Control board			Update at	2025-11-19
Drawn		Quad ADSR 2025			
Reviewed					
		Version	Size	Page 2 Total 3	
EasyEDA		V1.0	A4	EasyEDA.com	



OLED Screen



DAC jacks and Gates together



Schematic	Schematic1			Create at	2025-11-17
Board	Control board			Update at	2025-11-27
Drawn	Quad ADSR 2025			Page	UI and Headers
Reviewed					
		Version	Size	Page 3 Total 3	
		V1.0	A4	EasyEDA.com	