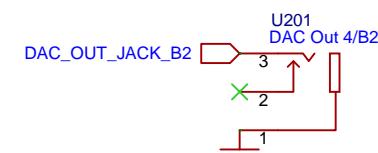
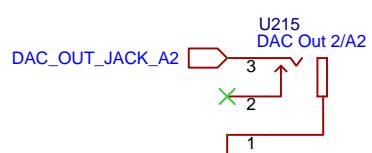
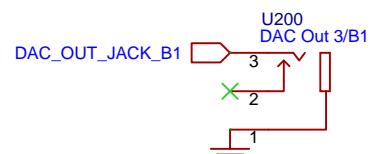
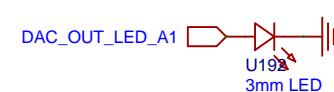
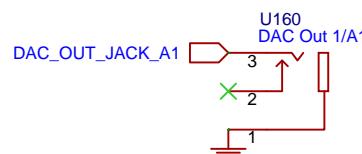
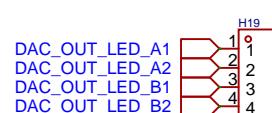


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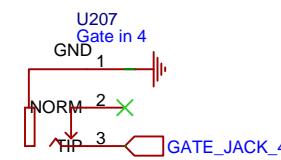
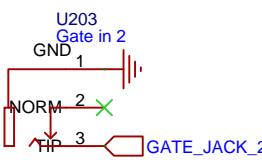
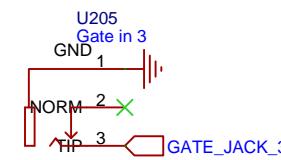
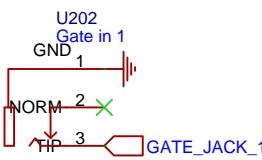
Header to main board



Jacks header is in UI and Headers

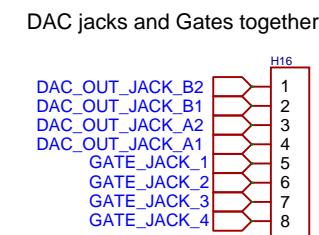
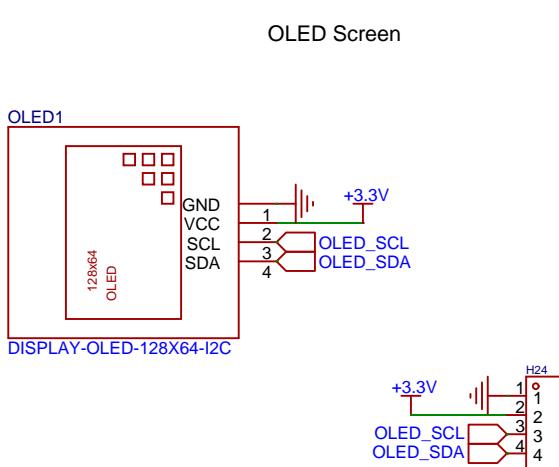
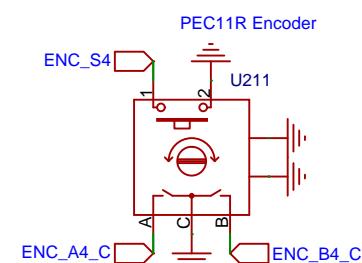
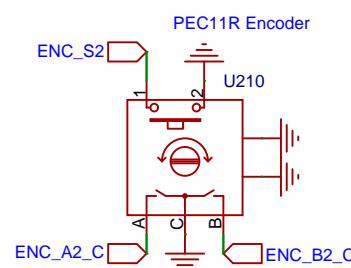
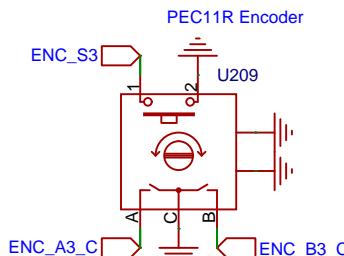
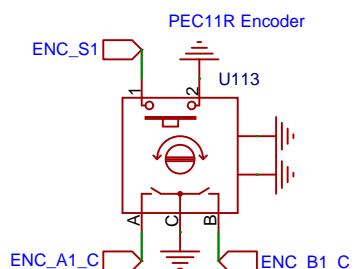
B

Schematic	Schematic1		Create at	2025-11-15
Board	Control board		Update at	2025-11-21
Drawn			Page	DACs
Reviewed			Quad ADSR 2025	
	Version	Size	Page 1 Total 3	
		V1.0	A4	EasyEDA.com



Gates header is in UI and Headers page

Schematic	Schematic1			Create at	2025-11-16
Board	Control board			Update at	2025-11-19
Drawn				Page	Gate jacks
Reviewed				Quad ADSR 2025	
	Version	Size	Page 2 Total 3		
 EasyEDA		V1.0	A4	EasyEDA.com	



Schematic	Schematic1			Create at	2025-11-17
Board	Control board			Update at	2025-11-27
Drawn				Page	UI and Headers
Quad ADSR 2025					
Reviewed				Version	Size
				V1.0	A4
				Page 3 Total 3	
				EasyEDA.com	