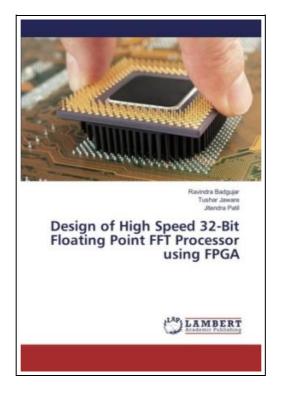
# Design of High Speed 32-Bit Floating Point FFT Processor using FPGA



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(Alfreda Bradtke)

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