Sommarstugekoll EDA234, Group 2

Names
Chalmers Kindergarten of Technology
email addresses

November 17, 2011

Abstract

Contents

1	Introduction	2
2	System Specification	2
3	System Description	2
4	Block Diagram	2
5	Block, Functionality	2
	5.1 Data Path	2
	5.2 Control Unit	2
	5.3 Functional Modules	2
	5.3.1 DTMF Module and MT8880	2
	5.3.2 Sound Module and ISD2560P	2
	5.3.3 Temperature Modul and DS18S20	2
	5.3.4 Control Functions	2
	5.3.5 Interface	2
6	State Machines	2
7	Timing Diagrams	2
8	Fault Analysis	2
9	Appendix	2
	9.1 List of Components	2
	9.2 Circuit diagram	2
	9.3 PCB Layout	2
	9.4 Program listings	2
	9.5 List of Signals	2
	9	

- 1 Introduction
- 2 System Specification
- 3 System Description
- 4 Block Diagram
- 5 Block, Functionality
- 5.1 Data Path
- 5.2 Control Unit
- 5.3 Functional Modules
- 5.3.1 DTMF Module and MT8880
- 5.3.2 Sound Module and ISD2560P
- 5.3.3 Temperature Module and DS18S20

The temperature module of the CPLD is tasked with handling the serial communication with the DS18S20 Temperature sensors via the 1-Wire buses, and outputting the read temperatures to the sound module.

A read cycle consists of three stages, Initialization, Command issuing and Reading of data. Initialization is performed by pulling the bus low for 512 s, then releasing the bus. The temperature sensor responds with a presence pulse by pulling the bus low for 106 s, confirming its presence on the bus and its operational status.

Detecting the presence pulse, the master starts to issue a ROM Command (Skip ROM, 0xCC), followed by a short recovery time and then a Function Command (Convert Temperature, 0x44). The master then resets the temperature sensor as above, then sends another pair of ROM-Function command, 0xCC and then 0xBE (Read Scratchpad).

Ideally, the DS18S20 should now be ready to transmit the data stored on its on-chip memory of 9 bytes.

- 5.3.4 Control Functions
- 5.3.5 Interface
- 6 State Machines
- 6.0.6 DS18S20 Serial Communication
- 6.0.7 MT8880
- 6.0.8 ISD2560P
- 7 Timing Diagrams
- 8 Fault Analysis
- 9 Appendix
- 9.1 List of Components
- 9.2 Circuit diagram
- 9.3 PCB Layout
- 9.4 Program listings
- 9.5 List of Signals