



1 2 3 4 5 6

A

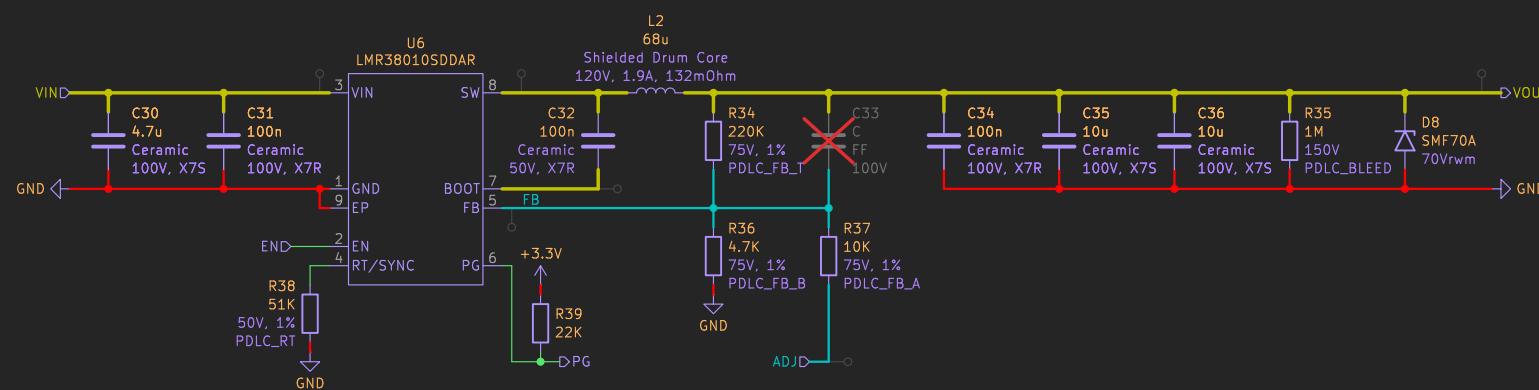
A

### PDLC OUTPUT DRIVER (VARIABLE VOLTAGE BUCK CONVERTER)

Outputs 1 A at 3 V to 63 V depending on the voltage at ADJ

EN enables output when above 1.4 volts, it must be clamped between 0 V and VIN + 0.3V even in the case where VIN = 0 V

Refer to the design document for component selection details



B

B

C

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D

D

[github.com/brown-studios/pdlc](https://github.com/brown-studios/pdlc)  
**Brown Studios LLC**

Sheet: /pdlc\_driver\_a/  
File: pdlc-output.kicad\_sch

**Title:**

Size: A4 | Date: 2026-01  
KiCad E.D.A. 9.0.6

**Rev: v2.0**  
Id: 2/3

1 2 3 4 5 6

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A

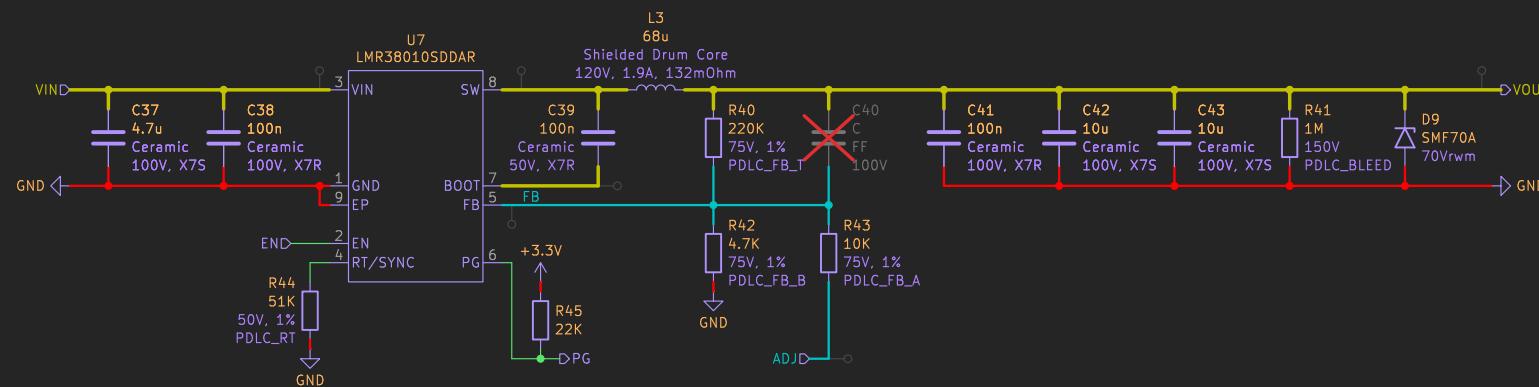
A

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Sheet: /pdlc driver b/  
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**Rev: v2.0**

Id: 3/3

1 2 3 4 5 6