

## ECE 354: Part 3

Group: ECE.354.S11-G031

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This document is concerned with the high level implementation of the specified primitives. As such, most of the low level back end is omitted for simplicity. Any kernel structures required to understand the primitive are described when the primitive is discussed.

### Design of Delayed Send

We designed `delayed_send`. The pseudo code is in Listing 1.

Listing 1: Pseudo code for design of delayed send

```
delayed_send();
```

### The KCD Process

The KCD process does stuff. Pseudo code at Listing 2.

Listing 2: Pseudo code for implementing the KCD Processes

```
Timer_Process();
```

### The CRT Process

The CRT process exists. Pseudo code here at Listing 3.

Listing 3: Pseudo code for the CRT process

```
CRT_Process();
```

### The UART I-Process

We made a UART I-Process. Pseudo code here at Listing 4.

Listing 4: Pseudo code for the UART I-Process

```
UART_Process();
```

### The Timer I-Process

We made a Timer I-Process. Pseudo code here at Listing 5.

Listing 5: Pseudo code for the UART I-Process

```
UART_Process();
```

### The Wall Clock Process

There is a clock on the wall. Pseudo code here at Listing 6.

Listing 6: Pseudo code for the wall clock process

```
wall_clock_Process();
```