

Altivar 71

User's manual

Retain for future use

Communication parameters

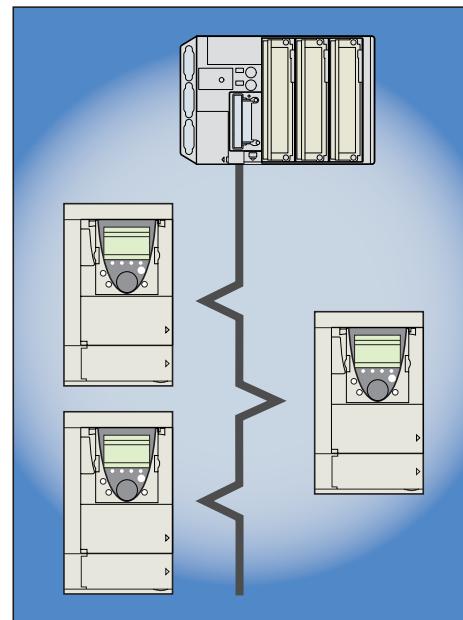


Table of Contents

Table of Contents	3
Documentation structure	6
Presentation	7
Notation	9
Description of parameters	9
Drive terminal displays	9
Profiles	10
What is a profile?	10
Functional profiles supported by the Altivar 71	11
I/O profile	12
Definition	12
Control word - run on state [2 wire] (2C)	14
Control word - run on state [3 wire] (3C)	15
Status word (ETA)	16
Example: I/O profile with positioning by sensors function	17
DSP402 profile	19
Functional description	19
DSP402 state chart	20
Description of states	21
Control word (CMD)	23
Status word (ETA)	25
Starting sequence	26
Sequence for a drive powered by the power section line supply	27
Sequence for a drive with separate control section	29
Sequence for a drive with line contactor control	32
Command/reference switching	35
Channels	35
Not separate mode	36
Separate mode	36
Switching in not separate mode	37
Switching in separate mode	37
Channel switching	38
Reference switching principle	40
Command switching principle	41
Assigning control word bits	42
Example: I/O profile with positioning by sensors function	45
Copy on switching	47
Forced local mode	48
Definition	48
Forced local mode and reference switching	49
Forced local mode and command switching	50
Priority stops	52
Priority stops on the graphic display terminal	52
I/O profile	52
DSP402 profile	53
Communication monitoring	54
Principle	54
Network monitoring criteria	54
Detailed operation	55
Reaction to a communication fault	58
Assignment of setpoints from a network	59
Setpoint parameters	59
Without PID regulator	60
With PID regulator	61
Configuration saving and switching	62
Saving the configuration	62
Restore configuration	64
Configuration switching via control word	65
Configuration switching by selection	68
Parameter set switching	70
Control parameters	74
Setpoint parameters	77
Status parameters	79
Output value parameters	87
Output values (speed)	87
Output values (torque)	87
Output values (motor)	88

Table of Contents

Reference parameters	89
References (speed)	89
References (torque)	90
Reference (regulator)	91
Measurement parameters	92
Input measurements	92
Thermal states	92
Time	93
I/O parameters	95
Fault parameters	99
Log parameters	105
Identification parameters	124
Communication parameters	126
"Controller Inside" parameters	131
DSP402 standard configuration and adjustment parameters	140
ODVA standard configuration and adjustment parameters	143
[1.1 SIMPLY START] (SIM-)	144
[1.3 SETTINGS] (SET-)	145
[1.4 MOTOR CONTROL] (drC-)	148
[1.5 INPUTS / OUTPUTS CFG] (I-O-)	166
[LI1 CONFIGURATION] (LI-)	167
[LIx CONFIGURATION] (L--)	167
[AI1 CONFIGURATION] (AI1-)	169
[AI2 CONFIGURATION] (AI2-)	170
[AI3 CONFIGURATION] (AI3-)	172
[AI4 CONFIGURATION] (AI4-)	174
[RP CONFIGURATION] (PLI-)	176
[ENCODER CONFIGURATION] (IEn-)	177
[R1 CONFIGURATION] (RI-)	179
[R2 CONFIGURATION] (R2-)	181
[R3 CONFIGURATION] (r3-)	182
[R4 CONFIGURATION] (r4-)	183
[LO1 CONFIGURATION] (LO1-)	184
[LO2 CONFIGURATION] (LO2-)	185
[LO3 CONFIGURATION] (LO3-)	186
[LO4 CONFIGURATION] (LO4-)	187
[AO1 CONFIGURATION] (AO1-)	188
[AO2 CONFIGURATION] (AO2-)	190
[AO3 CONFIGURATION] (AO3-)	191
[1.6 COMMAND] (CtL-)	192
Assignment conditions for logic inputs and control bits	192
[1.7 APPLICATION FUNCT.] (FUN-)	198
[REFERENCE SWITCH.] (rEF-)	198
[REF. OPERATIONS] (OAI-)	199
[RAMP] (rPt-)	201
[STOP CONFIGURATION] (Stt-)	205
[AUTO DC INJECTION] (AdC-)	208
[JOG] (JOG-)	209
[PRESET SPEEDS] (PSS-)	210
[+/- SPEED] (UPd-)	215
[+/-SPEED AROUND REF.] (SrE-)	216
[MEMO REFERENCE] (SPM-)	218
[FLUXING BY LI] (FLI-)	219
[LIMIT SWITCHES] (LSt-)	220
[BRAKE LOGIC CONTROL] (bLC-)	221
[EXTERNAL WEIGHT MEAS.] (ELM-)	226
[HIGH SPEED HOISTING] (HSH-)	228
[PID REGULATOR] (PId-)	230
[PID PRESET REFERENCES] (PRI-)	236
[TORQUE CONTROL] (tOr-)	238
[TORQUE LIMITATION] (tOL-)	241
[2nd CURRENT LIMIT.] (CLI-)	243
[LINE CONTACTOR COMMAND] (LLC-)	244
[OUTPUT CONTACTOR CMD] (OCC-)	245
[POSITIONING BY SENSORS] (LPO-)	247
[PARAM. SET SWITCHING] (MLP-)	250
[MULTIMOTORS/CONFIG.] (MMC-)	251
[AUTO TUNING BY LI] (tnL-)	253

Table of Contents

[TRAVERSE CONTROL] (tr0-)	254
[EVACUATION] (rFt-)	258
[1.8 FAULT MANAGEMENT] (FLt-)	260
[PTC MANAGEMENT] (PtC-)	260
[FAULT RESET] (rSt-)	261
[AUTOMATIC RESTART] (Atr-)	262
[CATCH ON THE FLY] (FLr-)	263
[MOTOR THERMAL PROT.] (tHt-)	264
[OUTPUT PHASE LOSS] (OPL-)	266
[INPUT PHASE LOSS] (IPL-)	266
[DRIVE OVERHEAT] (OHL-)	267
[THERMAL ALARM STOP] (SAt-)	268
[EXTERNAL FAULT] (EtF-)	269
[UNDERVOLTAGE MGT] (USb-)	270
[IGBT TESTS] (ilt-)	272
[4-20mA LOSS] (LFL-)	273
[FAULT INHIBITION] (InH-)	274
[COM. FAULT MANAGEMENT] (CLl-)	275
[ENCODER FAULT] (Sdd-)	276
[TORQUE OR I LIM. DETECT.] (tld-)	277
[DB RES. PROTECTION] (brP-)	278
[AUTO-TUNING FAULT] (tnF-)	279
[CARDS PAIRING] (PPI-)	279
[FALLBACK SPEED] (LFF-)	279
[RAMP DIVIDER] (FSt-)	280
[DC INJECTION] (dCI-))	281
Index of parameter codes	282
Index of parameter names	310
Index of parameter logic addresses	338
Index of parameter CANopen addresses	366
Index of parameter INTERBUS addresses	394
Index of parameter DeviceNet addresses	422

Documentation structure

Installation manual

This manual describes:

- How to assemble the drive
- How to connect the drive

Programming manual

This manual describes:

- The functions
- The parameters
- How to use the drive display terminal (integrated display terminal and graphic display terminal)

Communication Parameters Manual

This manual describes:

- The drive parameters with specific information for use via a bus or communication network
- The operating modes specific to communication (state chart)
- The interaction between communication and local control

Modbus, CANopen, Ethernet, Profibus, INTERBUS, Uni-Telway, FIPIO Modbus Plus, and DeviceNet manuals

These manuals describe:

- Assembly
- Connection to the bus or network
- Diagnostics
- Configuration of the communication-specific parameters via the integrated display terminal or the graphic display terminal

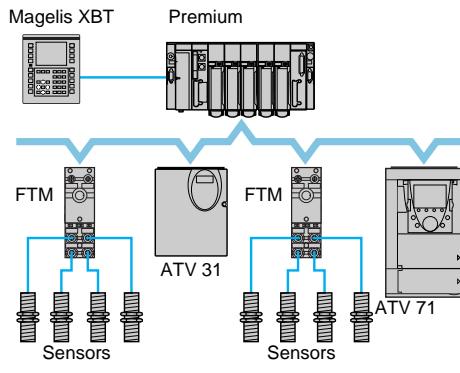
They describe the protocol communication services in detail.

Altivar 58/58F migration manual

This manual describes the differences between the Altivar 71 and the Altivar 58/58F.

It explains how to replace an Altivar 58 or 58F, including how to replace drives communicating on a bus or network.

Presentation



Example of configuration on CANopen bus

The Altivar 71 drive has been designed to meet all the configuration requirements encountered within the context of industrial communication installations.

It includes the Modbus and CANopen communication protocols as standard.

Two integrated communication ports enable direct access to the Modbus protocol:

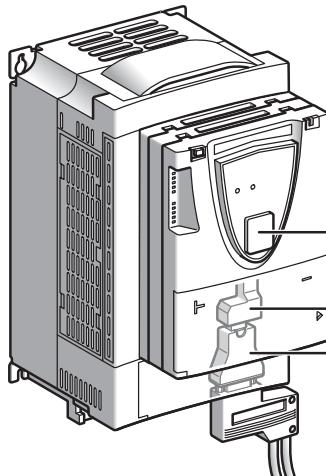
- One RJ45 Modbus connector port ②, located on the drive front panel, which is used to connect:
 - The remote graphic display terminal
 - A Magelis industrial HMI terminal
 - The PowerSuite software workshop
- One RJ45 Modbus network port ①, located on the drive's control terminals, which is dedicated to control and signaling by a PLC or other type of controller. It can also be used to connect a display terminal or the PowerSuite software workshop.

The CANopen protocol can be accessed from the Modbus network port via the CANopen adapter ③ (1).

The Altivar 71 can also be connected to other networks and industrial communication buses by using one of the communication option cards:

- Ethernet TCP/IP
- Modbus/Uni-Telway. This card provides access to additional functions, which complement those of the integrated ports: Modbus ASCII and 4-wire RS 485
- Fipio
- Modbus Plus
- Profibus DP
- DeviceNet
- INTERBUS

The control section can be powered separately, thus allowing communication (monitoring, diagnostics) to be maintained even if the power supply section fails.



The main communication functions of the Altivar 58 and Altivar 58F drives are compatible with the Altivar 71 (2):

- Connection
- Communication services
- Drive behavior (profile)
- Control and monitoring parameters
- Basic adjustment parameters

The PowerSuite software workshop supports the transfer of configurations from Altivar 58 and Altivar 58F drives to the Altivar 71.

(1) If the CANopen adapter is installed, Modbus will not be available on the network port ①.

(2) Please refer to the ATV 58(F)/ATV 71 migration manual supplied on the documentation CD-ROM.

Presentation

All the drive functions are accessible via the network:

- Control
- Monitoring
- Adjustment
- Configuration

If the "Controller Inside" programmable card is installed on the drive, its variables (%MW, etc.) can be accessed via the integrated Modbus ports or the Ethernet option card.

The speed/torque command and reference can come from different control sources:

- The I/O terminals
- The communication network
- The "Controller Inside" programmable card
- The remote graphic display terminal
- The PowerSuite software workshop (for commissioning and maintenance)

The advanced functions of the Altivar 71 enable the switching of these drive control sources to be managed in accordance with application requirements.

The assignment of periodic communication variables can be selected via:

- The network configuration software (Sycon, etc.)
- The Altivar 71's communication scanner function

With the exception of DeviceNet, regardless of network type, the Altivar 71 can be controlled:

- In accordance with the Drivecom profile (CANopen CiA DSP 402)
- In accordance with the I/O profile, whereby control is as straightforward and flexible as control via the I/O terminals

The DeviceNet card supports the ODVA standard profile.

Communication is monitored on the basis of criteria that are specific to each protocol. Regardless of protocol type, the reaction of the drive to a communication fault can be configured:

- Drive fault involving: freewheel stop, stop on ramp, fast stop or braked stop
- Stop without drive fault
- Maintain last command received
- Fallback position at preset speed
- Ignore fault

A commands from the CANopen bus is handled with the same priority as an input from the drive terminals. This enables very good response times to be achieved on the network port via the CANopen adapter.

Notation

Description of parameters

Identification

A parameter is described by means of various character strings:

- Code: 4 characters max. The code makes it possible to identify the parameter on the integrated 7-segment display terminal (Examples:**brt**, **tLIG**)
- Name: Description in plain text (used by the PowerSuite software workshop)
- Terminal name: Character string in square brackets for the graphic display terminal **[Gen. torque lim]**

Addresses

There are 4 formats for specifying parameter addresses:

- Logic address: In decimal and hexadecimal for Modbus, Ethernet TCI/IP, Fipio, Profibus DP protocols
- CANopen index: Index/subindex in hexadecimal
- Interbus-S index: Index/subindex in hexadecimal
- DeviceNet path: Class instance attribute, in hexadecimal

Read/write

- R: Read only
- R/W: Read and write
- R/WS: Read and write, but write only possible when motor is at standstill

Type

- WORD (bit register): Word where each bit represents an item of command, monitoring or configuration information.
- WORD (listing): Word where each value represents an available configuration or a state option.
- INT: Signed integer
- UINT: Unsigned integer
- DINT: Signed double integer
- UDINT: Unsigned double integer

Format

Hexadecimal values are written as follows: 16#**••••**.

Drive terminal displays

The menus that appear on the remote graphic display terminal are shown in square brackets.

Example: **[1.9 COMMUNICATION]**.

The menus that appear on the integrated 7-segment display terminal always end with a dash and appear between round brackets.

Example: **(COM-)**.

Parameter names that appear on the remote graphic display terminal are shown in square brackets.

Example: **[FALLBACK SPEED]**.

Parameter codes that appear on the integrated 7-segment display terminal are shown in round brackets.

Example: **(LFF)**.

Profiles

What is a profile?

There are 3 types of profile:

- Communication profiles
- Functional profiles
- Application profiles

Communication profiles

A communication profile describes the characteristics of the bus or network:

- Cables
- Connectors
- Electrical characteristics
- Access protocol
- Addressing system
- Periodic exchange service
- Messaging service
- ...

A communication profile is unique to a type of network (Fipio, Profibus DP, etc.) and is used by various different types of device.

Functional profiles

A functional profile describes the behavior of a type of device. It defines:

- The functions
- The parameters (name, format, unit, type, etc.)
- The periodic I/O variables
- State chart(s)
- ...

A functional profile is common to all members of a device family (variable speed drives, encoders, I/O modules, displays, etc.).

Ideally, functional profiles should be network-independent, but in reality they are not. They can feature common or similar parts. As far as speed drives are concerned, the main profiles are:

- DRIVECOM (INTERBUS)
- DSP402 (CANopen)
- PROFIDRIVE (Profibus)
- ODVA (DeviceNet)

DRIVECOM has been available since 1991.

DSP402 "Device profile for drives and motion control" represents the next stage of this standard's development and is published by Can In Automation.

Application profiles

Application profiles define in their entirety the services to be provided by the devices on a machine. Example: "CiA DSP 417-2 V 1.01 part 2 CANopen application profile for lift control systems - virtual device definitions".

Interchangeability

The aim of communication and functional profiles is to achieve interchangeability of the devices connected via the network. Although this aim is not always achieved, the profiles facilitate free competition.

Profiles

Functional profiles supported by the Altivar 71

I/O profile

Using the I/O profile simplifies PLC programming.

The I/O profile is commonly used for control of the terminals and display terminal. It is also supported by the Altivar 71 for network control.

The drive starts up as soon as the run command is sent.

The 16 bits of the control word can be assigned to a function or a terminal input.

This profile can be developed for simultaneous control of the drive via:

- The terminals
- The Modbus control word
- The CANopen control word
- The network card control word
- The "Controller Inside" control word

The I/O profile is supported by the drive itself and therefore in turn by all the communication ports (integrated Modbus, CANopen and the Ethernet, Fipio, ModbusPlus, Modbus, Uni-Telway, Profibus DP, DeviceNet, and INTERBUS communication cards).

DSP402 profile

The drive only starts up following a command sequence.

The control word is standardized.

5 bits of the control word (bits 11 to 15) can be assigned to a function or a terminal input.

The DSP402 profile is supported by the drive itself and therefore in turn by all the communication ports (integrated Modbus, CANopen and the Ethernet, Fipio, ModbusPlus, Modbus, Uni-Telway, Profibus DP, DeviceNet, and INTERBUS communication cards).

The Altivar 71 supports the DSP402 profile's "Velocity mode".

In DSP402 profile, there are 2 modes that are specific to the Altivar 71 and characterize command and reference management (see section "Command/reference switching", page [35](#)):

- Separate mode **[Separate] (SEP)**
- Not separate mode **[Not separ.] (SIM)**

ODVA (Open DeviceNet Vendor Association) profile

The drive starts up as soon as the run command is sent.

The control word is standardized.

The ODVA profile is supported by the DeviceNet communication card.

I/O profile

Definition

The behavior of the drive is identical whether via the network or via the terminals.

The I/O profile is achieved via the following configuration:

Menu	Parameter	Value
[1.6 - COMMAND] (Ctl-)	[Profile] (CHCF)	[I/O profile] (IO)

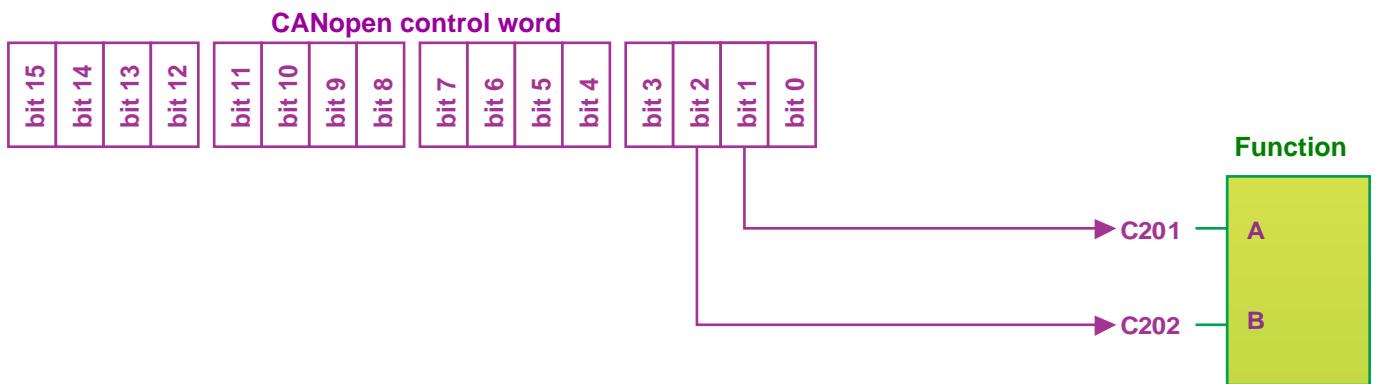
As well as to logic inputs of the terminals, drive functions can be assigned to control word bits.

A function input can be assigned to:

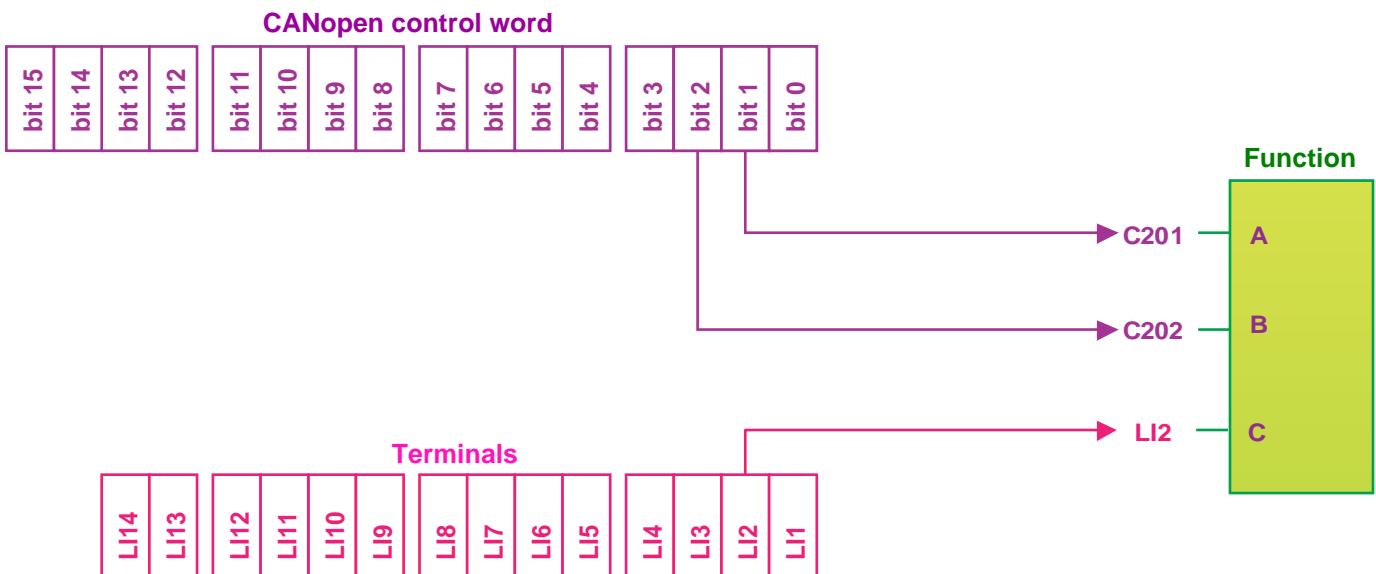
- A terminal input (LI2 to LI14)
- A Modbus control word bit (C101 to C115)
- A CANopen control word bit (C201 to C215)
- A network card control word bit (C301 to C315)
- A Controller Inside control word bit (C401 to C415)
- A switched bit (Cd00 to Cd15): See "Command/reference switching" section.

Schematic diagrams:

Fixed assignment on CANopen:

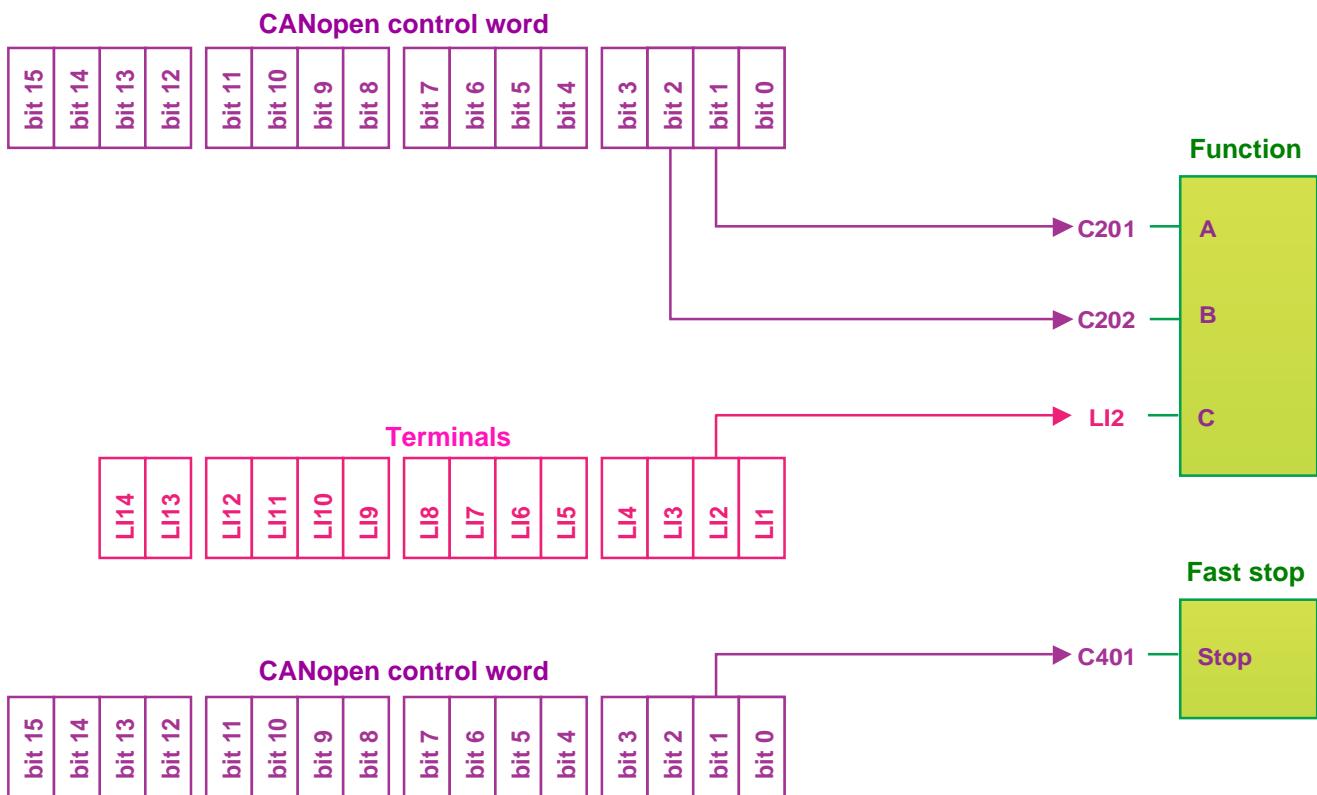


Fixed assignment to terminals and on CANopen::

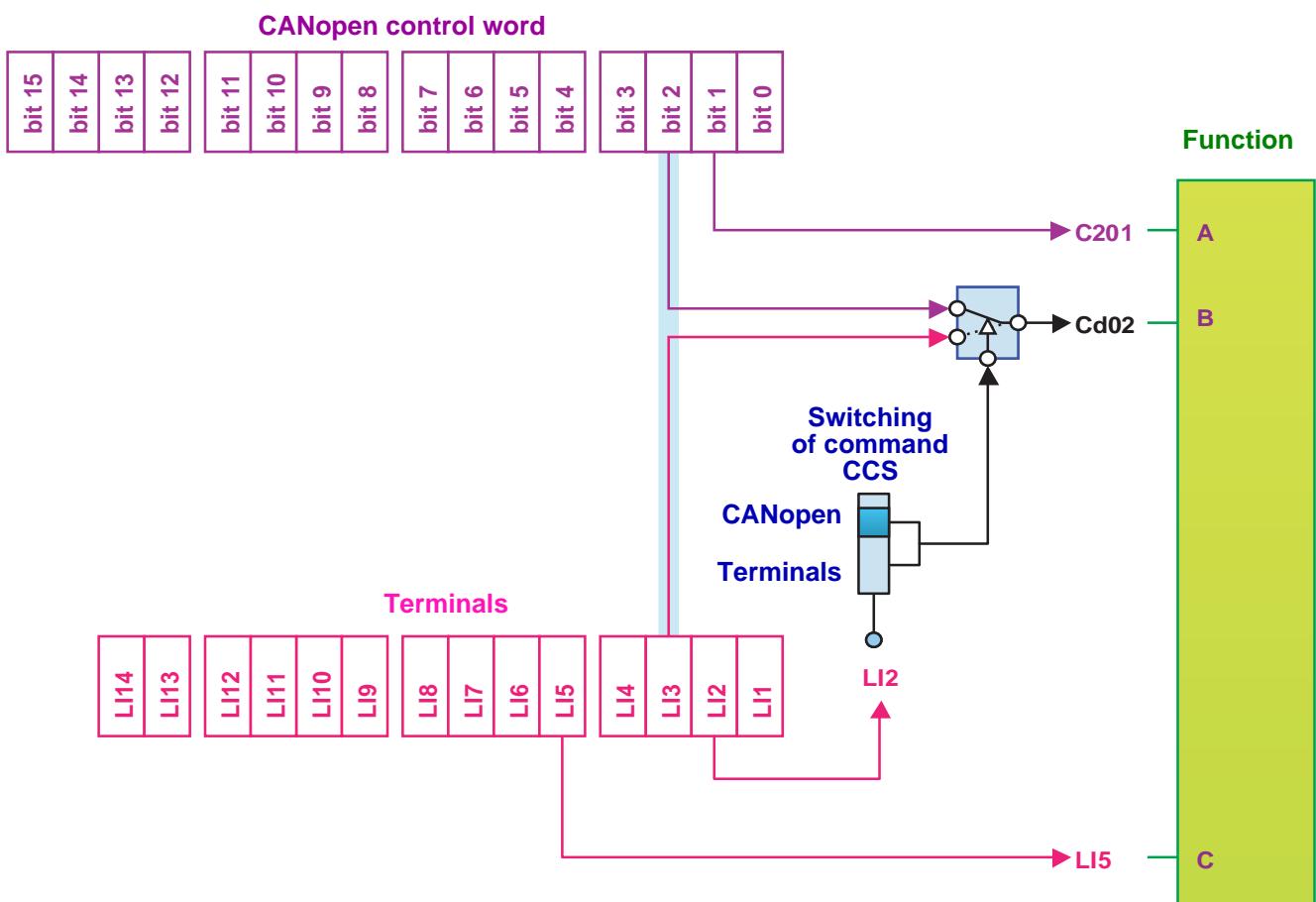


I/O profile

Fixed assignment to terminals on CANopen and on "Controller Inside" card:



Fixed assignment to terminals and on CANopen with command switching :



I/O profile

Control word - run on state [2 wire] (2C)

Please refer to the **[1.5 INPUTS / OUTPUTS CFG]** (I-O-) section of the programming manual.

The forward run command is automatically assigned to input **LI1** and to bit 0 of the control words.
This assignment cannot be modified.

The run command is active on state 1:

- Of input **LI1**, if the terminals are active
- Of bit 0 of the control word, if the network is active

Bits 1 to 15 of the control words can be assigned to drive functions.

bit 7	bit 6	bit 5	bit 4
Configurable	Configurable	Configurable	Configurable

bit 3	bit 2	bit 1	bit 0
Configurable	Configurable	Configurable	Forward

bit 15	bit 14	bit 13	bit 12
Configurable	Configurable	Configurable	Configurable

bit 11	bit 10	bit 9	bit 8
Configurable	Configurable	Configurable	Configurable

In the case of a **[2 wire] (2C)** run on state command and I/O profile, fixed assignment of a function input is possible using the following codes:

Bit	Fixed assignments						
	Drive terminals	Logic I/O card	Extended I/O card	Modbus	CANopen	Network card	"ControllerInside" card
bit 0	Forward						
bit 1	LI2	-	-	C101	C201	C301	C401
bit 2	LI3	-	-	C102	C202	C302	C402
bit 3	LI4	-	-	C103	C203	C303	C403
bit 4	LI5	-	-	C104	C204	C304	C404
bit 5	LI6	-	-	C105	C205	C305	C405
bit 6	-	LI7	-	C106	C206	C306	C406
bit 7	-	LI8	-	C107	C207	C307	C407
bit 8	-	LI9	-	C108	C208	C308	C408
bit 9	-	LI10	-	C109	C209	C309	C409
bit 10	-	-	LI11	C110	C210	C310	C410
bit 11	-	-	LI12	C111	C211	C311	C411
bit 12	-	-	LI13	C112	C212	C312	C412
bit 13	-	-	LI14	C113	C213	C313	C413
bit 14	-	-	-	C114	C214	C314	C414
bit 15	-	-	-	C115	C215	C315	C415

For example, to assign the operating direction command to bit 1 of CANopen, simply configure the **[Reverse assign.] (rrS)** parameter with the value **[C201] (C201)**.

I/O profile

Control word - run on state [3 wire] (3C)

Please refer to the [\[1.5 INPUTS / OUTPUTS CFG\] \(I-O-\)](#) section of the programming manual.

The stop command is automatically assigned to input **LI1** and to bit 0 of the control words.
This assignment cannot be modified.

This command enables running on state 1:

- Of input **LI1**, if the terminals are active
- Of bit 0 of the control word, if the network is active

The forward run command is automatically assigned to input **LI2** and to bit 1 of the control words.

This assignment cannot be modified.

The forward run command is active if the stop command is at 1 and on a rising edge (0 → 1):

- Of input **LI2**, if the terminals are active
- Of bit 1 of the control word, if the network is active

Bits 2 to 15 of the control words can be assigned to drive functions.

bit 7	bit 6	bit 5	bit 4
Configurable	Configurable	Configurable	Configurable

bit 3	bit 2	bit 1	bit 0
Configurable	Configurable	Forward	Stop

bit 15	bit 14	bit 13	bit 12
Configurable	Configurable	Configurable	Configurable

bit 11	bit 10	bit 9	bit 8
Configurable	Configurable	Configurable	Configurable

In the case of a **[3 wire] (3C)** run on state command and I/O profile, fixed assignment of a function input is possible using the following codes:

Bit	Fixed assignments						
	Drive terminals	Logic I/O card	Extended I/O card	Modbus	CANopen	Network card	"Controller Inside" card
bit 0	Run authorization (Stop)						
bit 1	Forward						
bit 2	LI3	-	-	C102	C202	C302	C402
bit 3	LI4	-	-	C103	C203	C303	C403
bit 4	LI5	-	-	C104	C204	C304	C404
bit 5	LI6	-	-	C105	C205	C305	C405
bit 6	-	LI7	-	C106	C206	C306	C406
bit 7	-	LI8	-	C107	C207	C307	C407
bit 8	-	LI9	-	C108	C208	C308	C408
bit 9	-	LI10	-	C109	C209	C309	C409
bit 10	-	-	LI11	C110	C210	C310	C410
bit 11	-	-	LI12	C111	C211	C311	C411
bit 12	-	-	LI13	C112	C212	C312	C412
bit 13	-	-	LI14	C113	C213	C313	C413
bit 14	-	-	-	C114	C214	C314	C414
bit 15	-	-	-	C115	C215	C315	C415

For example, to assign the operating direction command to bit 2 of CANopen, simply configure the **[Reverse assign.] (rrS)** parameter with the value **[C202] (C202)**.

I/O profile

Status word (ETA)

bit 7	bit 6	bit 5	bit 4	bit 3	bit 2	bit 1	bit 0
Alarm	Reserved (= 0 or 1)	Reserved (=1)	Power present	Fault	Running	Ready	Reserved (= 0 or 1)

bit 15	bit 14	bit 13	bit 12	bit 11	bit 10	bit 9	bit 8
Direction of rotation	Stop via STOP key	Reserved (=0)	Reserved (=0)	Reference outside limits	Reference reached	Command or reference via network	

I/O profile

Example: I/O profile with positioning by sensors function

Please refer to the [1.7 APPLICATION FUNCT.] (FUn-) section of the Programming Guide, under "Positioning by sensors".

In this example, a PLC is used to control the transfer of parts on a conveyor composed of transfer tables. Each table is controlled by a variable speed drive. The PLC and the drives are connected via a CANopen network.

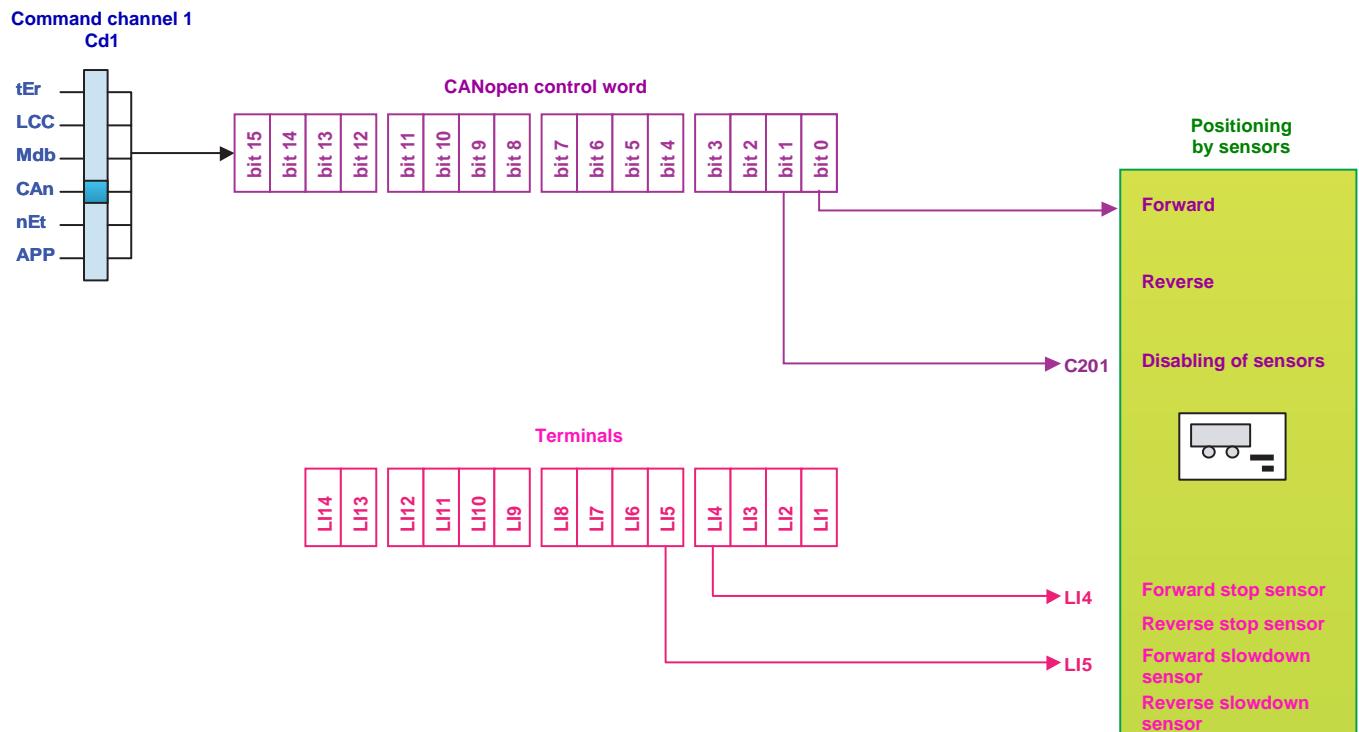
The PLC controls the operation of the installation via the CANopen bus.

The drive uses the stop sensor to inhibit transfer of the part if the next table is unavailable. In this case, the PLC enables the sensors. If the next table is free, the drive transfers the part without stopping. In this case, the PLC disables the sensors.

The stop sensor is directly connected to the drive terminals.

The slowdown sensor, which is also directly connected (to the drive) enables a more precise stop.

Configuration schematic diagram:



I/O profile

Configure the following parameters:

Parameter	Value	Comment
Type of command	On state (2 wire)	The run command is obtained by bit 0 of the CANopen control word.
Profile	I/O profile	
Reference 1 configuration	CANopen	The reference comes from the CANopen card.
Command 1 configuration	CANopen	The command comes from the CANopen card.
Assignment of stop sensor	Input LI4	
Assignment of slowdown sensor	Input LI5	
Assignment of sensor disable command	Bit 1 of CANopen control word	

Configuration via the remote graphic display terminal:

Menu	Parameter	Value
[1.5 INPUTS / OUTPUTS] (I-O-)	[2/3 wire control] (tCC)	[2 wire] (2C)
[1.6 - COMMAND] (CtL-)	[Profile] (CHCF)	[I/O profile] (IO)
	[Ref. 1 channel] (Fr1)	[CANopen] (CAn)
	[Cmd channel 1] (Cd1)	[CANopen] (CAn)
[1.7 APPLICATION FUNCT.] (FUn-) [POSITIONING BY SENSORS] (LPO-)	[Stop FW limit sw.] (SAF)	[LI4] (LI4)
	[Slowdown forward] (dAF)	[LI5] (LI5)
	[Disable limit sw.] (CLS)	[C201] (C201)

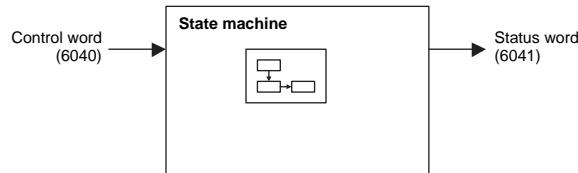
Note: The forward run command is automatically assigned to bit 0 of the CANopen control word.

DSP402 profile

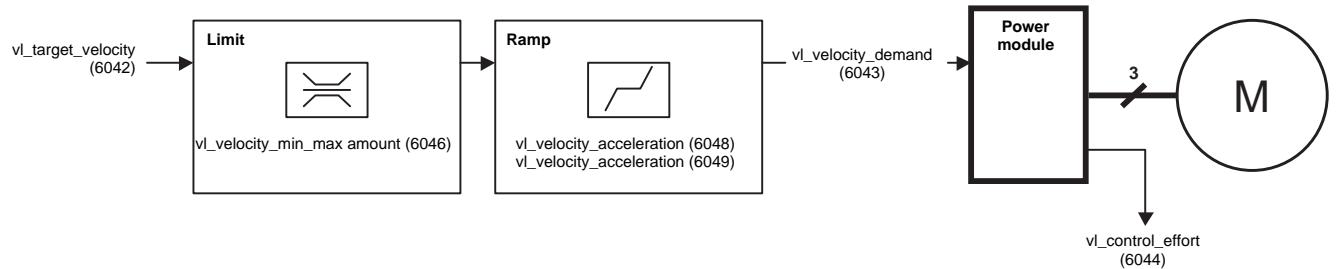
Functional description

The operation of the drive is encapsulated in 2 main functions, which are illustrated by the 2 diagrams below:

- Control diagram:

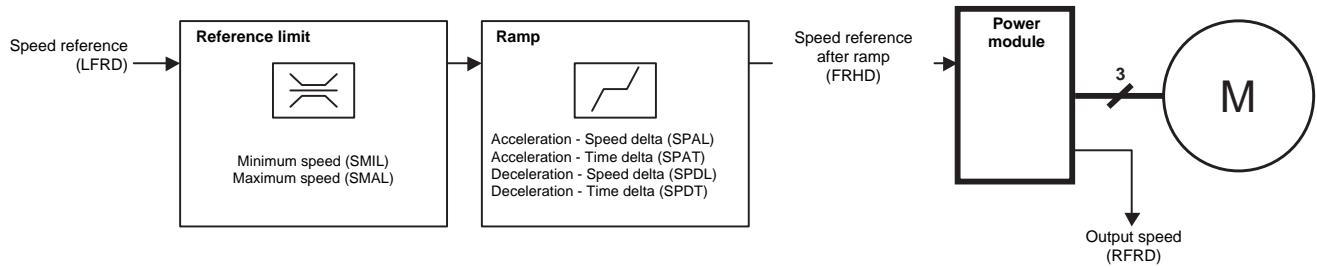
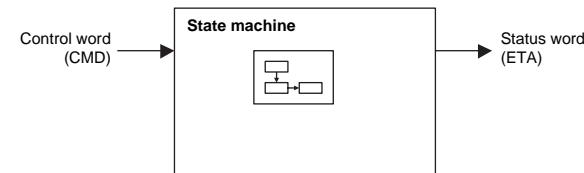


- Simplified diagram, speed reference in "Velocity" mode:



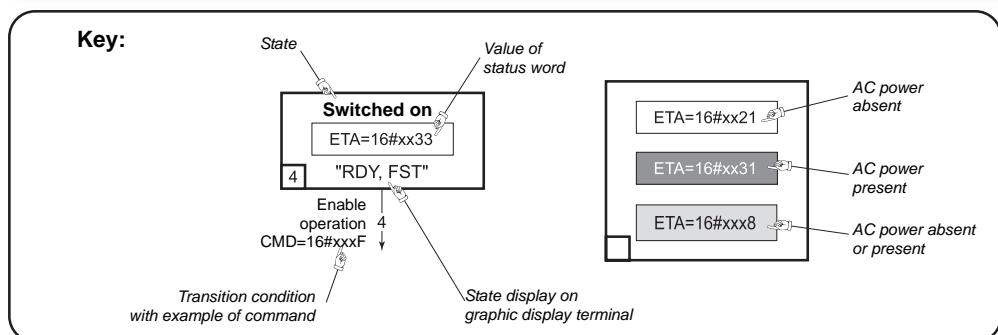
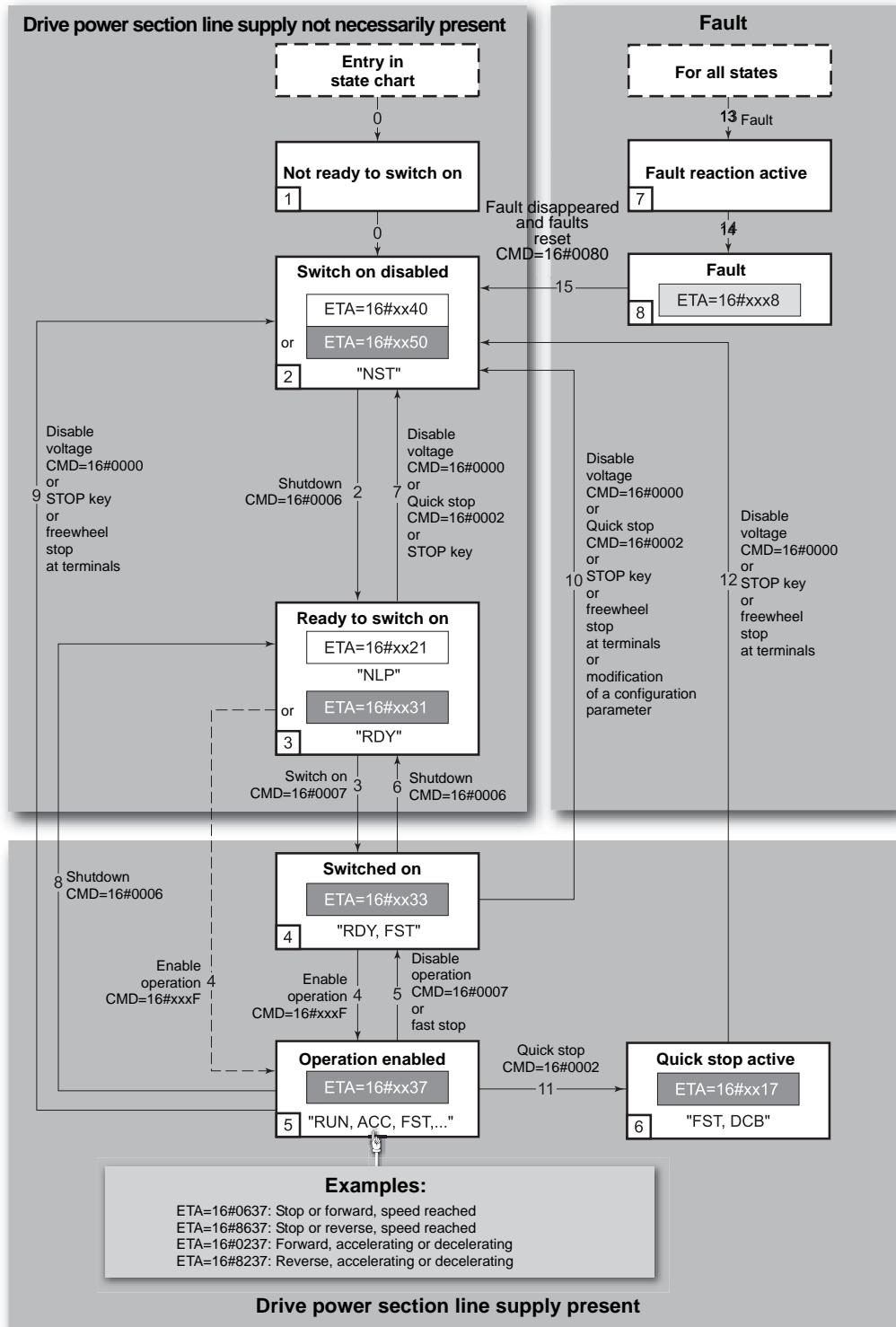
The main parameters are shown together with their DSP402 name and DSP402/Drivecom index.

These diagrams translate as follows for the Altivar system:



DSP402 profile

DSP402 state chart



Description of states

Each state represents an internal reaction by the drive.

This chart will change depending on whether the control word is sent (CMD) or an event occurs (a fault, for example).

The drive state can be identified by the value of the status word (ETA).

Not ready to switch on

Initialization starts. This is a transient state invisible to the communication bus.

Switch on disabled

The drive is inactive.

The drive is locked, no power is supplied to the motor.

For a separate control section, it is not necessary to supply AC power to the power section.

For a separate control section with line contactor, the contactor is not controlled.

The configuration and adjustment parameters can be modified.

Ready to switch on

Power section line supply pending.

For a separate control section, it is not necessary to supply AC power to the power section, but the system will expect it in order to change to "Switch on" state.

For a separate control section with line contactor, the contactor is not controlled.

The drive is locked, no power is supplied to the motor.

The configuration and adjustment parameters can be modified.

Switched on

The drive is supplied with AC power but is stationary.

For a separate control section, the power section line supply must be present.

For a separate control section with line contactor, the contactor is controlled.

The drive is locked, no power is supplied to the motor.

The power stage of the drive is ready to operate, but voltage has not yet been applied to the output.

The adjustment parameters can be modified.

Modifying a configuration parameter (motor stopped) will switch the drive back to the "Switch on disabled" state.

Operation enabled

The drive is running.

For a separate control section, the power section line supply must be present.

For a separate control section with line contactor, the contactor is controlled.

The drive is unlocked, power is supplied to the motor.

The drive functions are activated and voltage is applied to the motor terminals.

However, in the case of an open-loop drive, if the reference is zero or the "Halt" command is applied, no power is supplied to the motor and no torque is applied.

Auto-tuning (**tUn**) requires an injection of current into the motor. The drive must therefore be in the "Operation enabled" state for this command.

The adjustment parameters can be modified.

Modifying a configuration parameter (motor stopped) will switch the drive back to the "Switch on disabled" state.

DSP402 profile

Quick stop active

Emergency stop

The drive performs a fast stop, after which restarting will only be possible once the drive has changed to the "Switch on disabled" state. During fast stop, the drive is unlocked and power is supplied to the motor.

Fault reaction active

Transient state during which the drive performs an action appropriate to the type of fault.

The drive function is activated or deactivated according to the type of reaction configured in the fault management parameters used for fault management.

Fault

Drive faulty.

The drive is locked, no power is supplied to the motor.

Summary

State	Power section line supply for separate control section	Power supplied to motor	Modification of configuration parameters
Not ready to switch on	Not required	No	Yes
Switch on disabled	Not required	No	Yes
Ready to switch on	Not required	No	Yes
Switched on	Required	No	Yes, return to " 2 - Switch on disabled " state
Operation enabled	Required	Yes, apart from an open-loop drive with a zero reference or in the event of a "Halt" command for an open-loop drive.	No
Quick stop	Required	Yes, during fast stop	No
Fault reaction active	Depends on fault management configuration	Depends on fault management configuration	-
Fault	Not required	No	Yes

DSP402 profile

Control word (CMD)

bit 7	bit 6	bit 5	bit 4	bit 3	bit 2	bit 1	bit 0
Fault reset				Enable operation	Quick stop	Enable voltage	Switch on
Ack. fault	Reserved (=0)	Reserved (=0)	Reserved (=0)	Run command	Emergency stop	Authorization to supply AC power	Contactor control

bit 15	bit 14	bit 13	bit 12	bit 11	bit 10	bit 9	bit 8
Assignable	Assignable	Assignable	Assignable	By default, direction of rotation command.	Reserved (=0)	Reserved (=0)	Halt
							Halt

Command	Transition address	Final state	bit 7	bit 3	bit 2	bit 1	bit 0	Example value
			Fault reset	Enable operation	Quick stop	Enable voltage	Switch on	
Shutdown	2, 6, 8	3 - Ready to switch on	x	x	1	1	0	16#0006
Switch on	3	4 - Switched on	x	x	1	1	1	16#0007
Enable operation	4	5 - Operation enabled	x	1	1	1	1	16#000F
Disable operation	5	4 - Switched on	x	0	1	1	1	16#0007
Disable voltage	7, 9, 10, 12	2 - Switch on disabled	x	x	x	0	x	16#0000
Quick stop	11	6 - Quick stop active	x	x	0	1	x	16#0002
	7, 10	2 - Switch on disabled						
Fault reset	15	2 - Switch on disabled	0 → 1	x	x	x	x	16#0080

x: Value is of no significance for this command.

0 → 1: Command on rising edge.

DSP402 profile

"Halt" command:

The "Halt" command enables movement to be interrupted without having to leave the "5 - Operation enabled" state. The stop is performed in accordance with the **[Type of stop]** (S_{tt}) parameter.

In the case of an open-loop drive, if the "Halt" command is active, no power is supplied to the motor and no torque is applied.

In the case of a closed-loop drive, if the "Halt" command is active, power continues to be supplied to the motor and torque is applied during stopping.

Assigning control word bits

In DSP402 profile, fixed assignment of a function input is possible using the following codes:

Bit	Integrated Modbus	CANopen	Network card	"Controller Inside" card
bit 11	C111	C211	C311	C411
bit 12	C112	C212	C312	C412
bit 13	C113	C213	C313	C413
bit 14	C114	C214	C314	C414
bit 15	C115	C215	C315	C415

For example, to assign the DC injection braking to bit 13 of CANopen, simply configure the **[DC injection assign.]** (dCl) parameter with the **[C213]** (**C213**) value.

Bit 11 is assigned by default to the operating direction command **[Reverse assign.]** (rrS).

DSP402 profile

Status word (ETA)

bit 7	bit 6	bit 5	bit 4	bit 3	bit 2	bit 1	bit 0
Warning	Switch on disabled	Quick stop	Voltage enabled	Fault	Operation enabled	Switched on	Ready to switch on
Alarm	Power section line supply disabled	Emergency stop	Power section line supply present	Fault	Running	Ready	Power section line supply pending

bit 15	bit 14	bit 13	bit 12	bit 11	bit 10	bit 9	bit 8
Direction of rotation	Stop via STOP key	Reserved (=0)	Reserved (=0)	Internal limit active	Target reached	Remote	Reserved (=0)
				Reference outside limits	Reference reached	Command or reference via network	

State	bit 6	bit 5	bit 4	bit 3	bit 2	bit 1	bit 0	ETA (W3201) masked by 16#006F ⁽¹⁾
	Switch on disabled	Quick stop	Voltage enabled	Fault	Operation enabled	Switched on	Ready to switch on	
1 - Not ready to switch on	0	x	x	0	0	0	0	-
2 - Switch on disabled	1	x	x	0	0	0	0	16#0040
3 - Ready to switch on	0	1	x	0	0	0	1	16#0021
4 - Switched on	0	1	1	0	0	1	1	16#0023
5 - Operation enabled	0	1	1	0	1	1	1	16#0027
6 - Quick stop active	0	0	1	0	1	1	1	16#0007
7 - Fault reaction active	0	x	x	1	1	1	1	-
8 - Fault	0	x	x	1	0	0	0	16#0008 ⁽²⁾ or 16#0028

x: In this state, the value of the bit can be 0 or 1.

(1) This mask can be used by the PLC program to test the chart state.

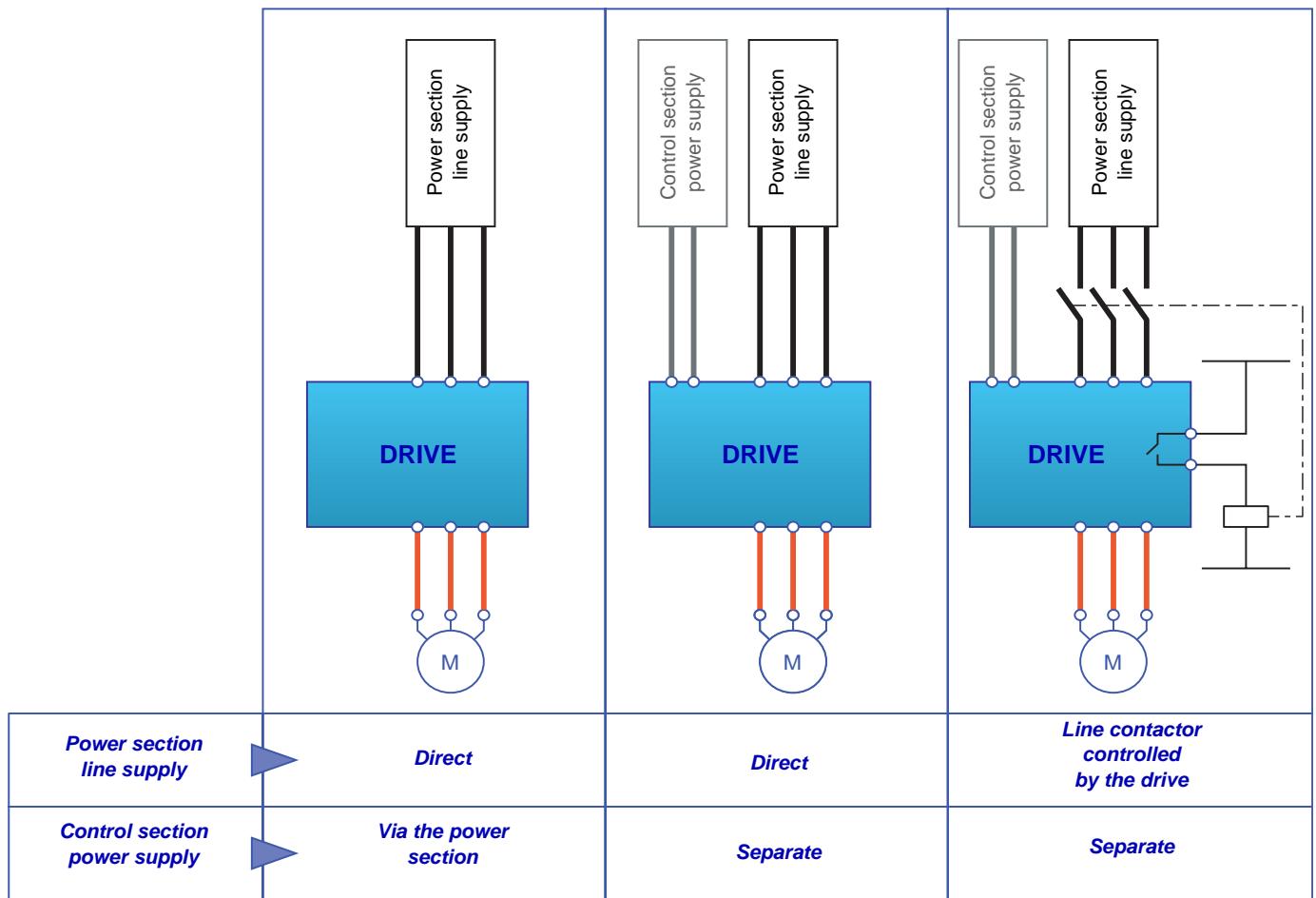
(2) Fault following state "6 - Quick stop active".

DSP402 profile

Starting sequence

The command sequence in the state chart depends on how power is being supplied to the drive.

There are three possible scenarios:

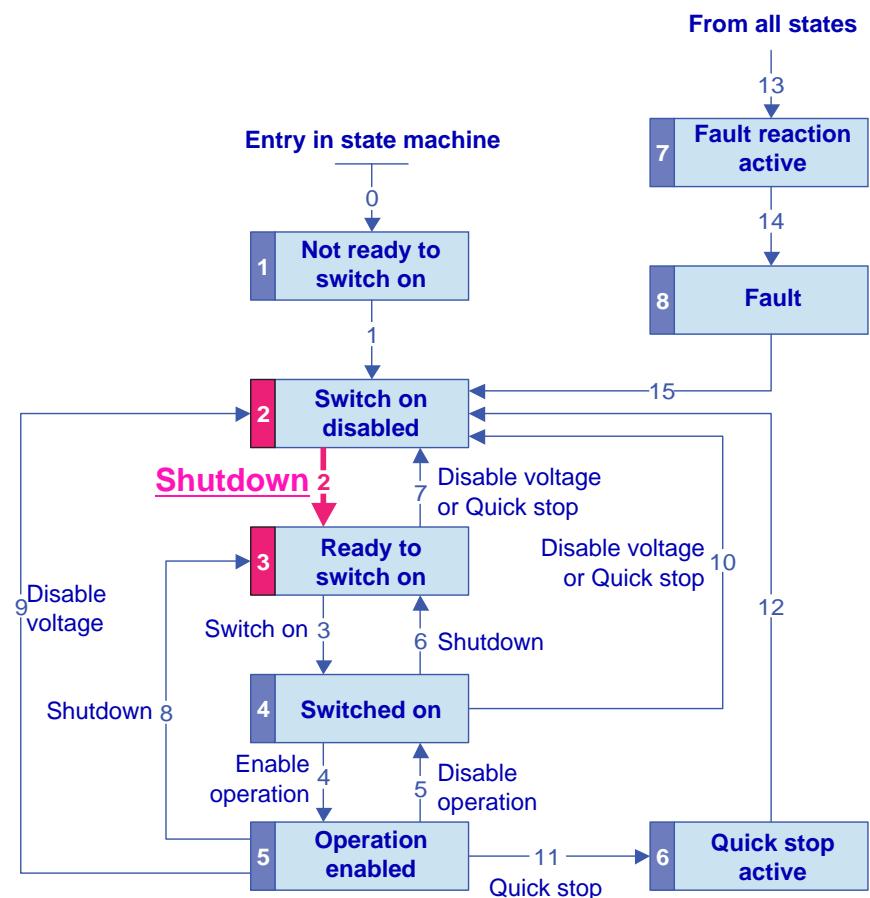
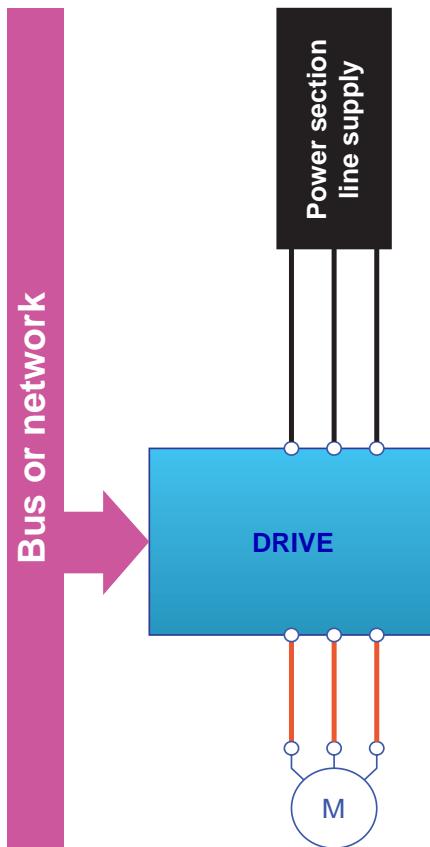


DSP402 profile

Sequence for a drive powered by the power section line supply

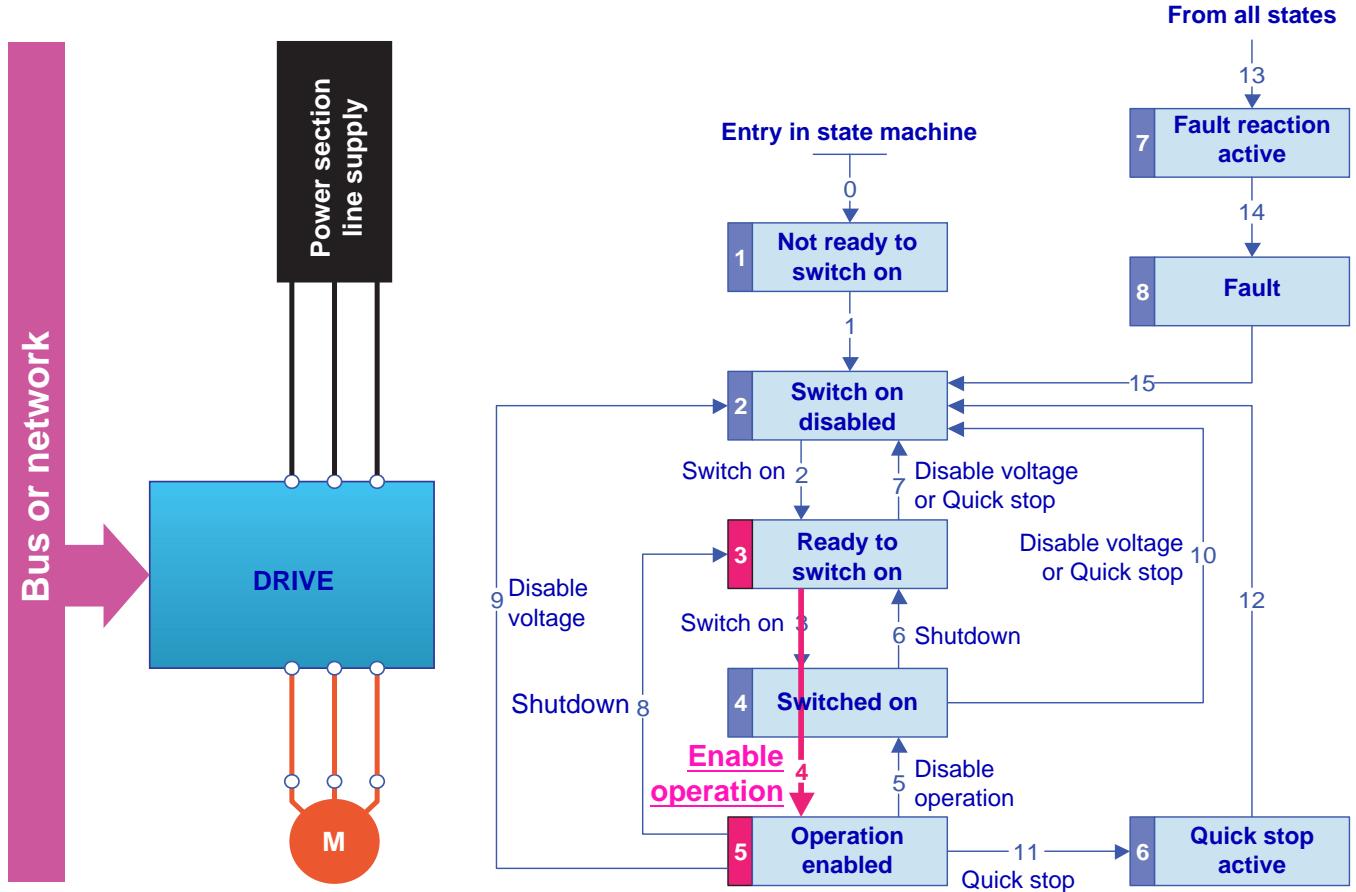
Both the power and control sections are powered by the power section line supply.
If power is supplied to the control section, it has to be supplied to the power section as well.
The following sequence must be applied:

- "2 - Shutdown" command



DSP402 profile

- Check that the drive is in the "3 - Ready to switch on" state.
- Then send the "4 - Enable operation" command.
- The motor can be controlled (send a reference not equal to zero).



DSP402 profile

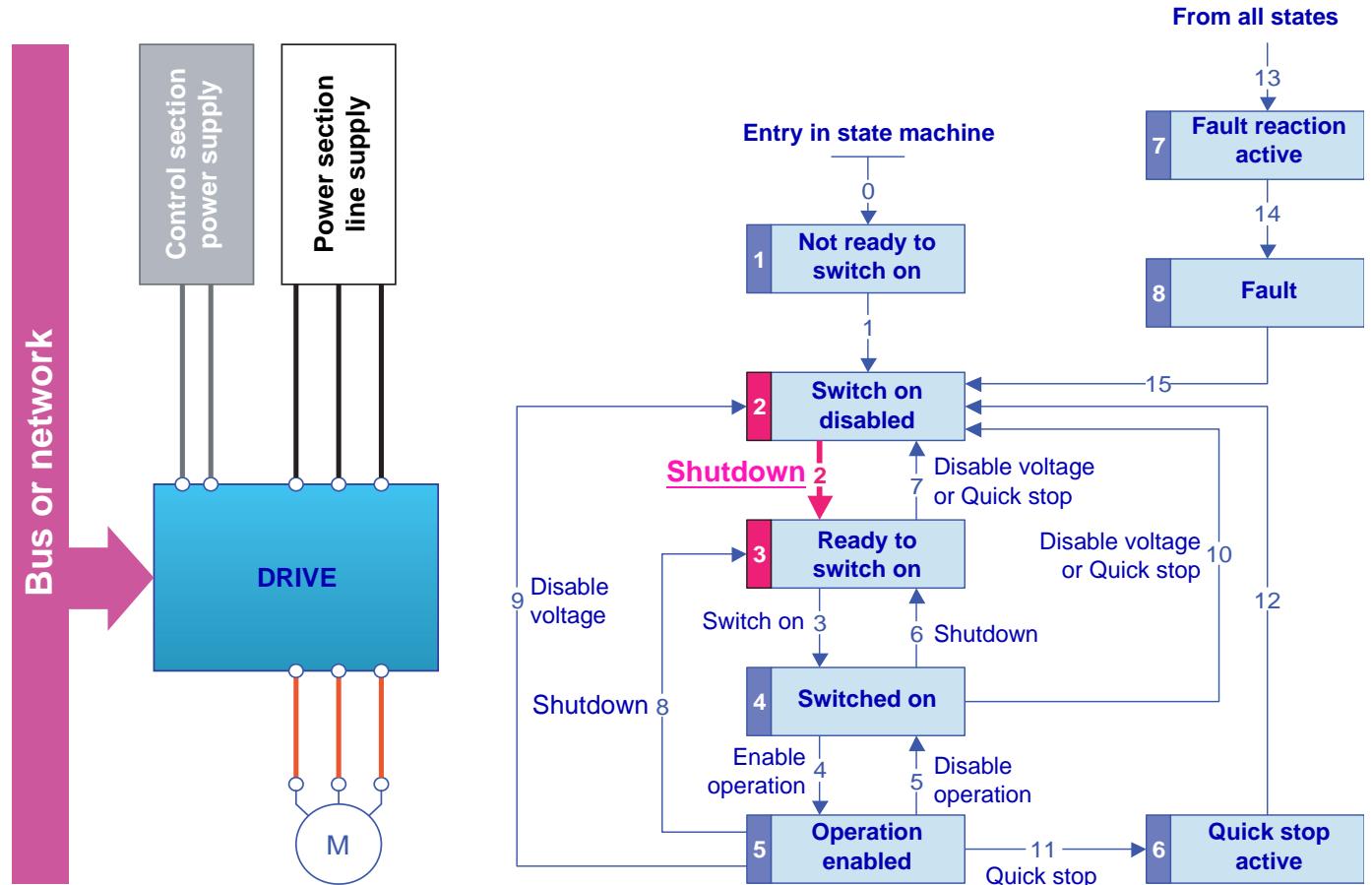
Sequence for a drive with separate control section

Power is supplied separately to the power and control sections.

If power is supplied to the control section, it does not have to be supplied to the power section as well.

The following sequence must be applied:

- The power section line supply is not necessarily present.
- "2 - Shutdown" command

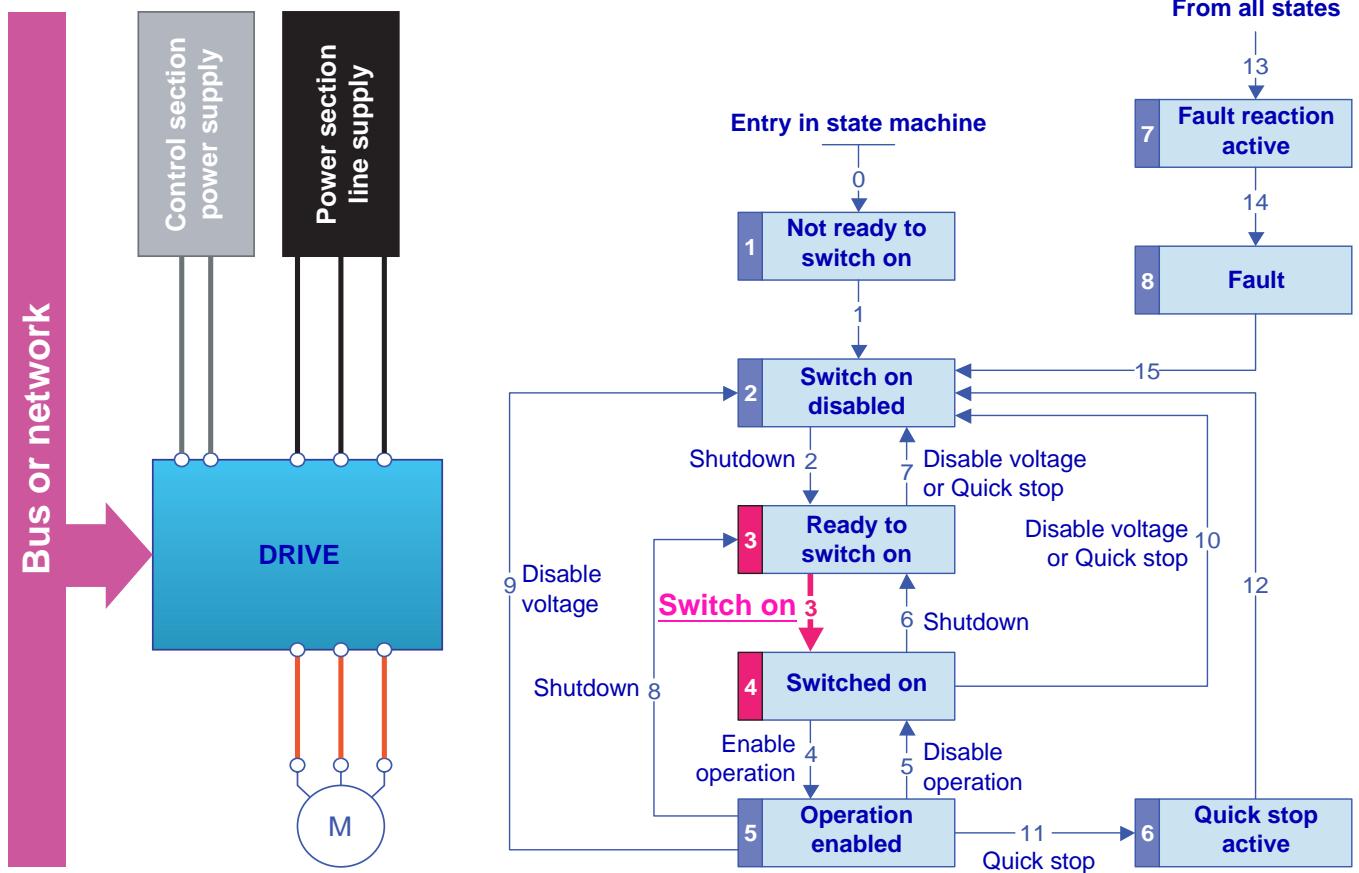


DSP402 profile

- Check that the drive is in the "3 - Ready to switch on" state.
- Check that the power section line supply is present ("Voltage enabled" of the status word).

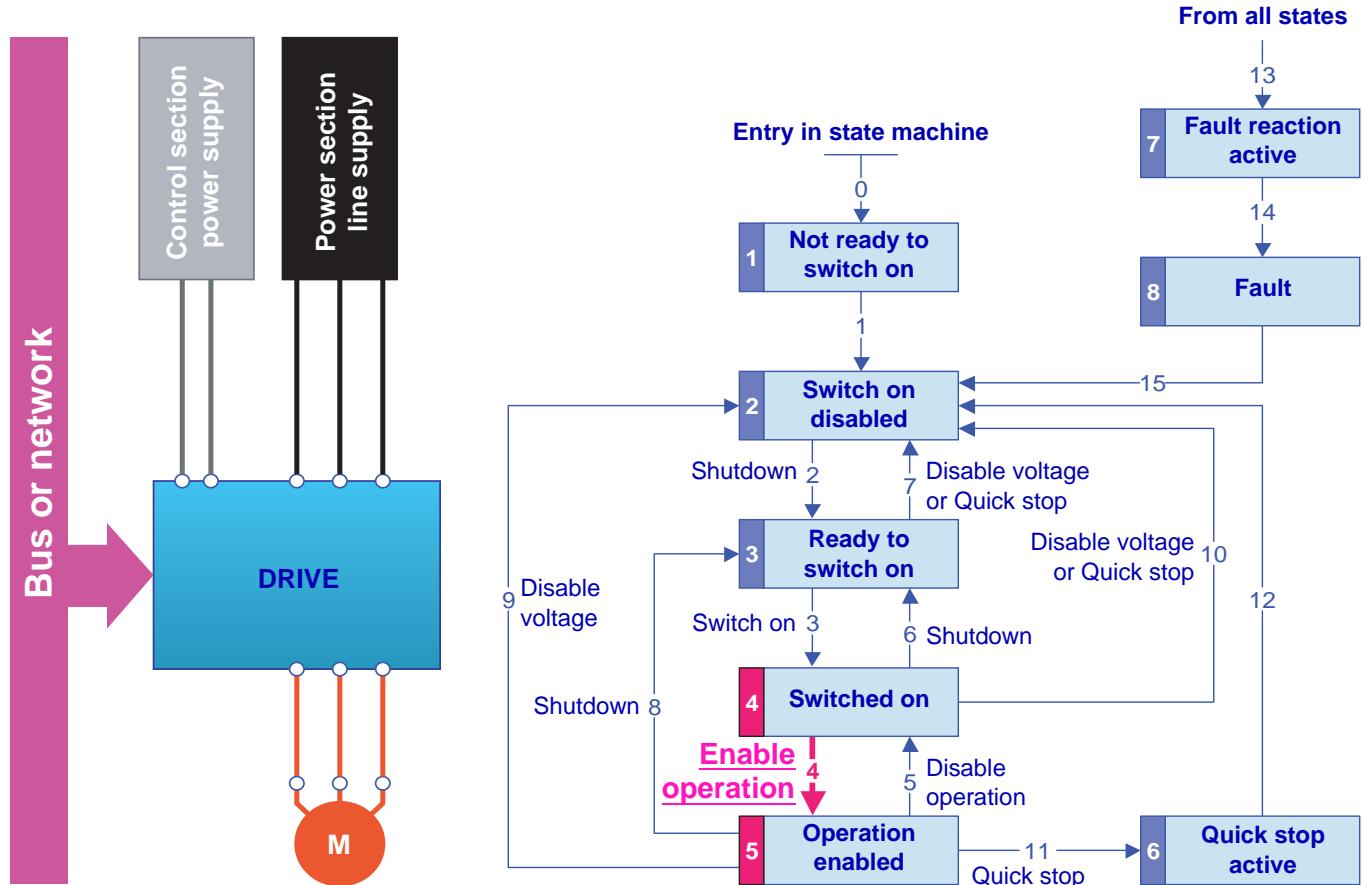
Power section line supply	Terminal display	Status word
Absent	nLP	16#●●21
Present	rdY	16#●●31

- Send the "3 - Switch on" command



DSP402 profile

- Check that the drive is in the "4 - Switched on" state.
- Then send the "4 - Enable operation" command.
- The motor can be controlled (send a reference not equal to zero).
- If the power section line supply is still not present in the "4 - Switched on" state after a time delay [Mains V. time out] (Lct), the drive will switch to fault mode (LCF).

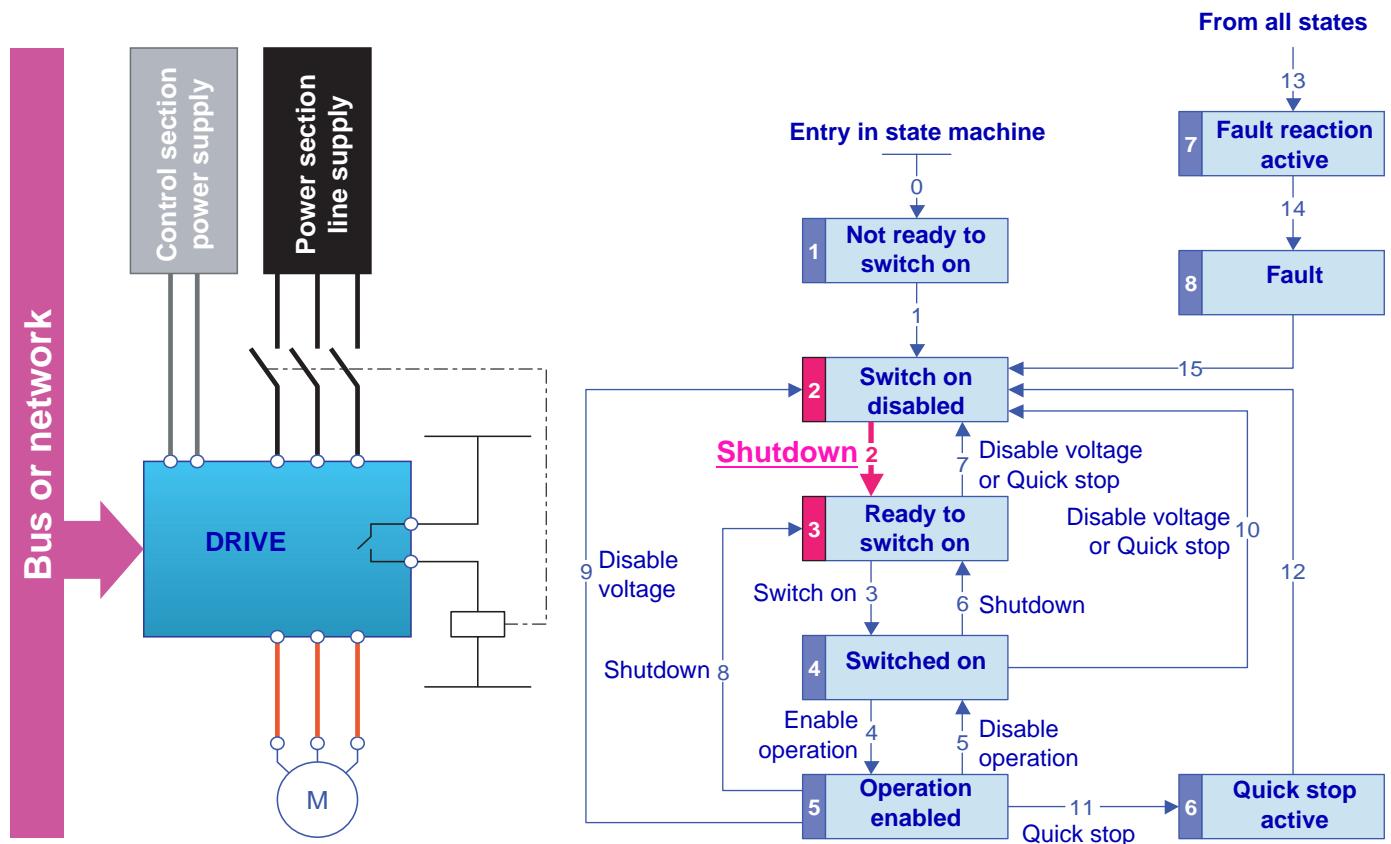


Sequence for a drive with line contactor control

Power is supplied separately to the power and control sections.

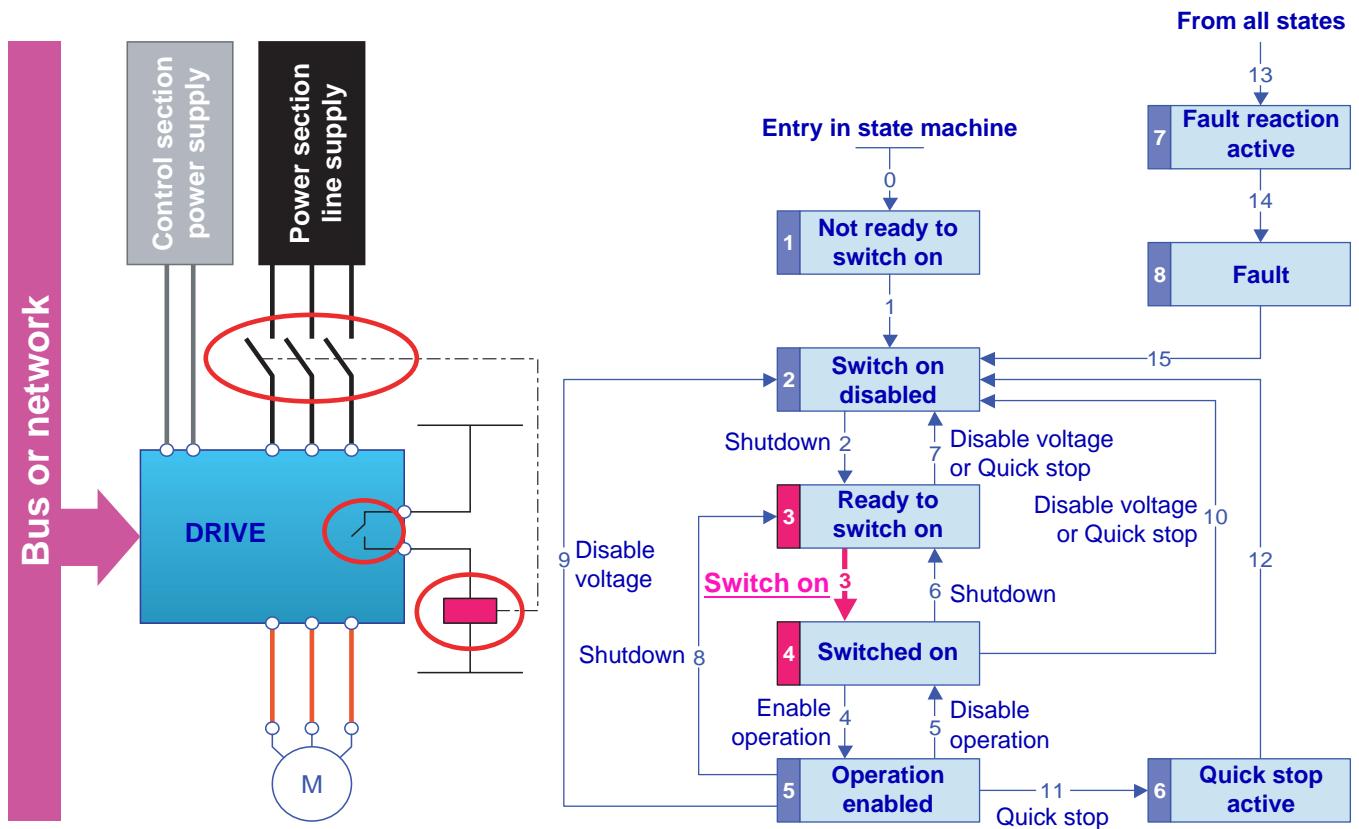
If power is supplied to the control section, it does not have to be supplied to the power section as well. The drive controls the line contactor. The following sequence must be applied:

- The power section line supply is not present as the line contactor is not being controlled.
- Send the "2 - Shutdown" command



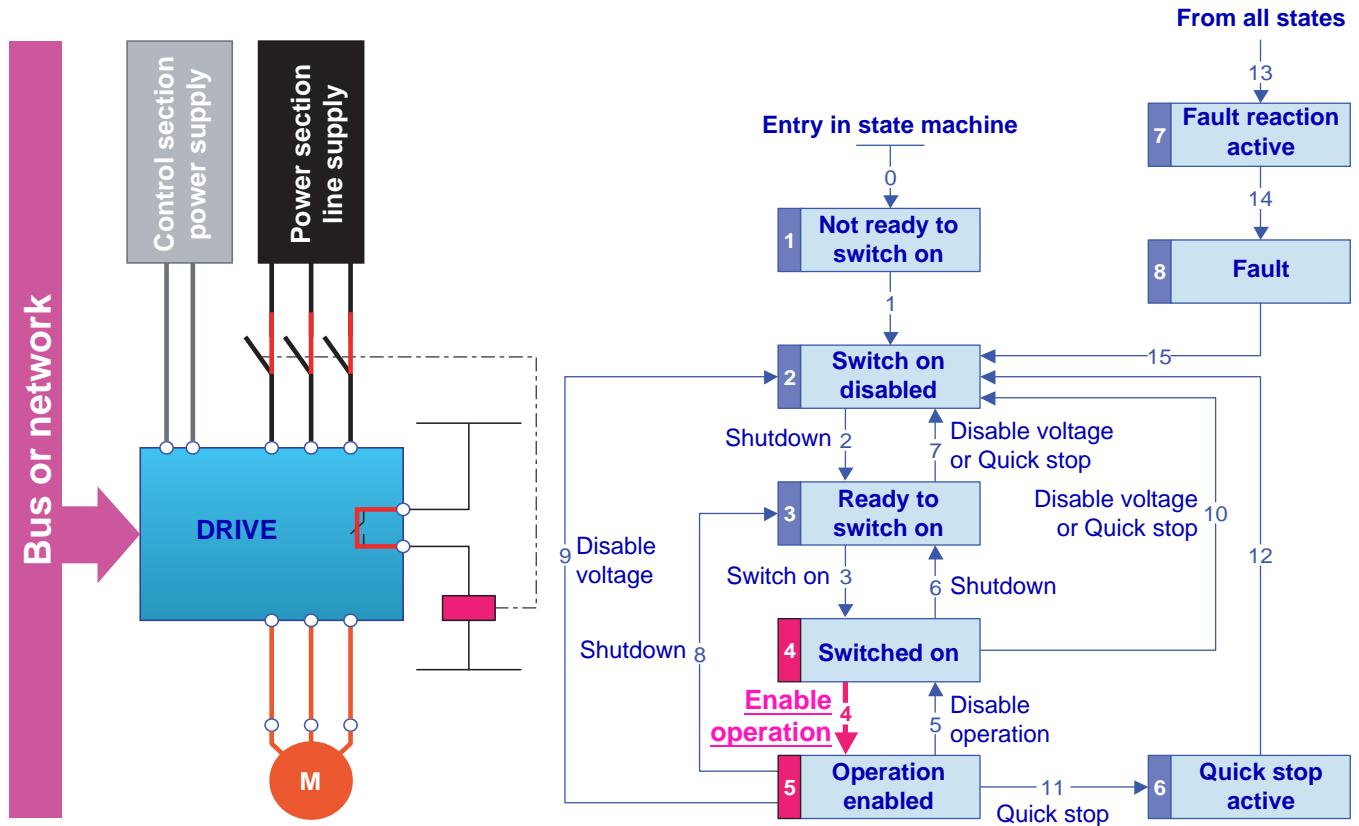
DSP402 profile

- Check that the drive is in the "3 - Ready to switch on" state
- Send the "3 - Switch on" command, which will close the line contactor and switch on the power section line supply.



DSP402 profile

- Check that the drive is in the "4 - Switched on" state
- Then send the "4 - Enable operation" command.
- The motor can be controlled (send a reference not equal to zero).
- If the power section line supply is still not present in the "4 - Switched on" state after a time delay [Mains V. time out] (Lct), the drive will switch to fault mode (LCF).



Command/reference switching

Channels

The 6 Altivar 71 channels are:

- The terminals
- The graphic display terminal
- The integrated Modbus ports
- The integrated CANopen port
- The network card
- The Controller Inside card

The Altivar 71 has 2 integrated Modbus ports. These 2 ports are physically independent of one another but together constitute a single logic channel.

The drive does not distinguish between commands and references that come from the Modbus network port and those that come from the Modbus HMI port.

With the Altivar drive, the command and reference source can be selected together or separately:

- Via configuration
- Via switching at the terminals or via a bus or communication network

Channel commands and references

All the drive's command and reference parameters are managed on a channel-by-channel basis.

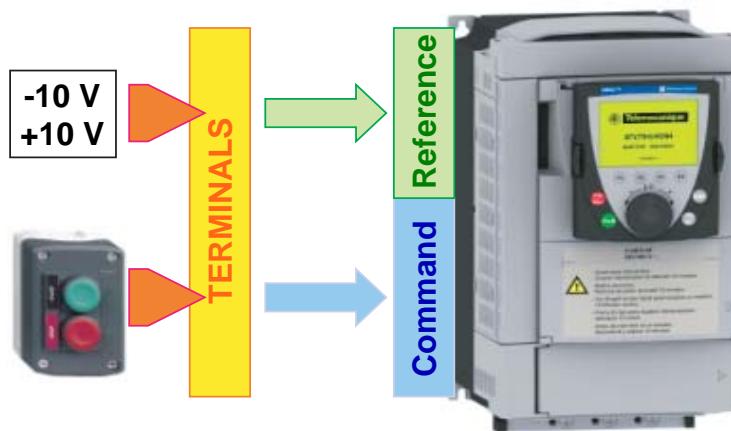
It is possible to identify the last value written for each channel and each command or reference parameter:

Parameter name	Parameter code				
	Taken into account by the drive	Modbus	CANopen	Communication card	Controller Inside
Control word	CMd	CMd1	CMd2	CMd3	CMd4
Extended control word	CMI	CMI1	CMI2	CMI3	CMI4
Speed reference (rpm)	LFrD	LFrD1	LFrD2	LFrD3	LFrD4
Frequency reference (0.1 Hz)	LFr	LFr1	LFr2	LFr3	LFr4
Torque reference	Ltr	Ltr1	Ltr2	Ltr3	Ltr4
PI regulator reference	PISp	PISp1	PISp2	PISp3	PISp4
Analog multiplier reference	MFr	MFr1	MFr2	MFr3	MFr4

Command/reference switching

Not separate mode

Command and reference come from the same channel.

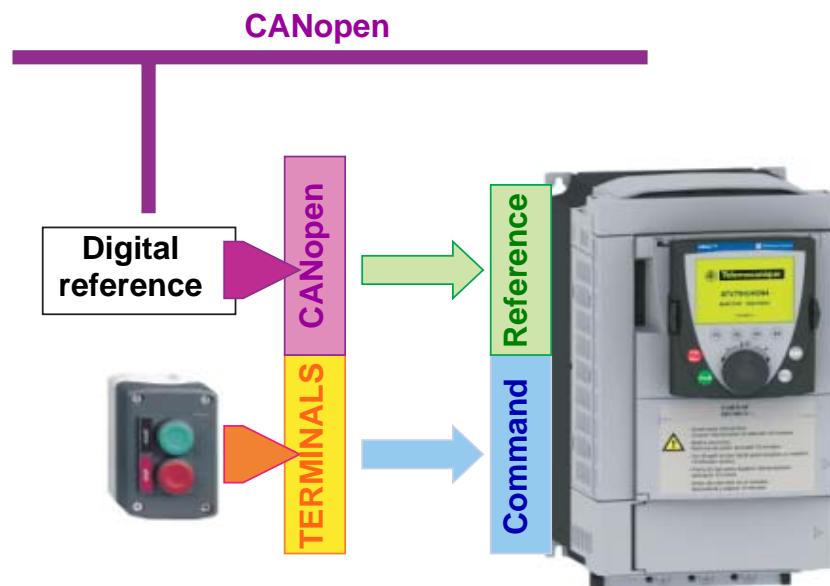


In DSP402 profile, not separate mode is configured via the terminal:

Menu	Parameter	Value
[1.6 - COMMAND] (Ctl-L)	[Profile] (CHCF)	[Not separ.] (SIM)

Separate mode

Command and reference may come from different channels.



In DSP402 profile, the separate mode is achieved via configuration with the terminal:

Menu	Parameter	Value
[1.6 - COMMAND] (Ctl-L)	[Profile] (CHCF)	[Separate] (SEP)

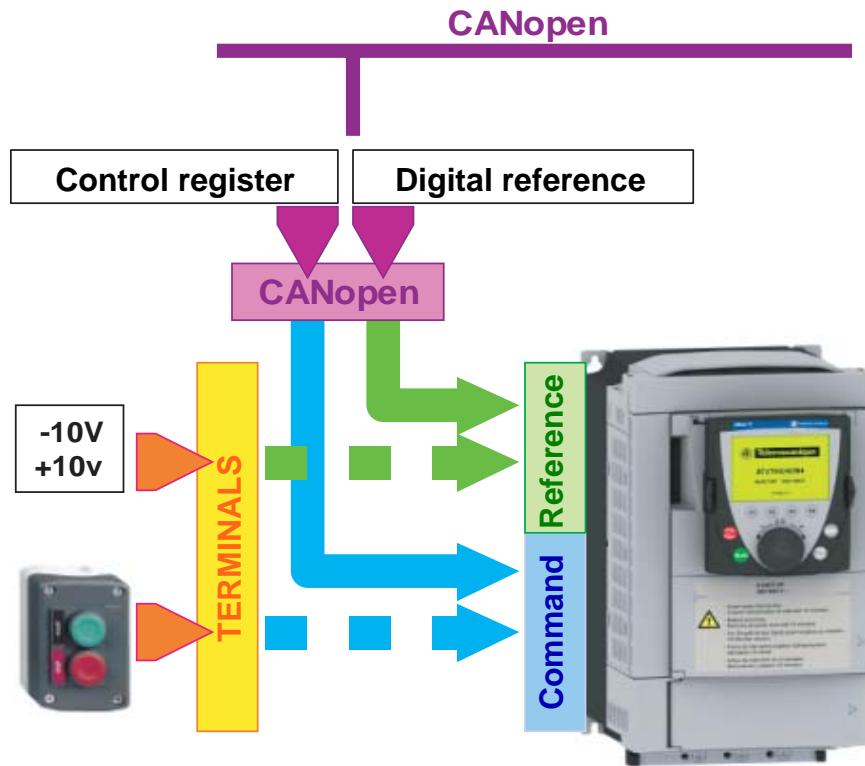
In I/O profile, the drive is automatically in separate mode.

Menu	Parameter	Value
[1.6 - COMMAND] (Ctl-L)	[Profile] (CHCF)	[I/O profile] (IO)

Command/reference switching

Switching in not separate mode

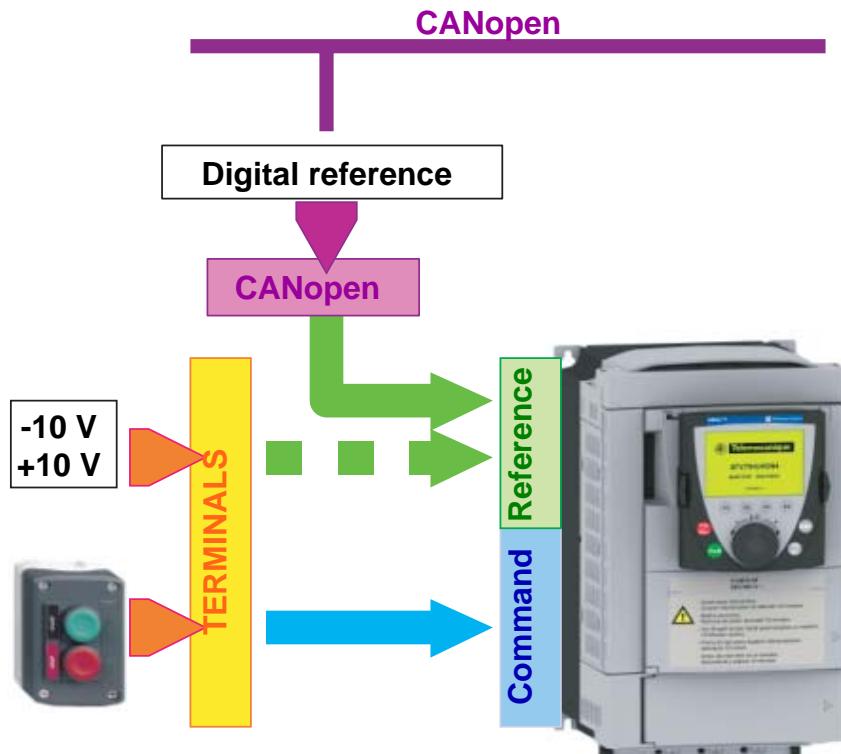
Switching takes place between 2 channels simultaneously for both reference and command.



In this example, either CANopen or the terminals supply both command and reference.

Switching in separate mode

Switching can take place between 2 channels separately for reference and command.



In this example, the command always comes from the terminals; the reference can come either from CANopen or from the terminals.

Command/reference switching

Channel switching

Reference channel configuration

Reference channel configuration enables reference sources to be predefined, which can be modified or switched subsequently via a command.

There are 3 predefined reference channels:

- Reference channel 1
- Reference channel 1B
- Reference channel 2

Reference channels 1 and 1B are used for drive application functions.

Reference channel 2 is connected directly to the reference limiting function, bypassing the application functions.

The predefined reference channels are assigned via the [Ref. 1 channel] (Fr1), [Ref. 1B channel] (Fr1b) and [Ref. 2 channel] (Fr2) configuration parameters, which can have the following values:

- [No] (nO): Not assigned
- [AI1 ref.] (AI1): Analog input AI1
- [AI2 ref.] (AI2): Analog input AI2
- [AI3 ref.] (AI3): Analog input AI3 (if extension card present)
- [AI4 ref.] (AI4): Analog input AI4 (if extension card present)
- [Pulse Input] (PI): Frequency input (if card present)
- [HMI] (LCC): Graphic display terminal
- [Modbus] (Mdb): Integrated Modbus
- [CANopen] (CAN): Integrated CANopen
- [Com. card] (nEt): Communication card (if present)
- [Prog. card] (APP): Controller Inside card (if present).
- [Encoder] (PG): Encoder input (if card present)

Note: The "+speed/-speed" function is on reference channel 2. See the programming manual for more information.

Command channel configuration

Command channel configuration enables command sources to be predefined, which can be modified or switched subsequently via a command.

There are 2 predefined command channels:

- Command channel 1
- Command channel 2

The predefined command channels are assigned via the [Cmd channel 1] (Cd1) and [Cmd channel 2] (Cd2) configuration parameters, which can have the following values:

- [Terminals] (tEr): Terminals
- [HMI] (LCC): Graphic display terminal
- [Modbus] (Mdb): Integrated Modbus
- [CANopen] (CAN): Integrated CANopen
- [Com. card] (nEt): Communication card (if present)
- [Prog. card] (APP): Controller Inside card (if present).

Command/reference switching

Switches

A channel switch is used to select predefined channels.

It can be:

- Defined via configuration
- Actuated either by an input or a network bit
- Written via a network during operation

The possible switch values are:

	Switching of function reference [Ref 1B switching] (rCb)	Switching of direct reference [Ref. 2 switching] (rFC)	Command switching [Cmd switching] (CCS)
Channel 1	Fr1	Fr1	Cd1
Channel 1B	Fr1b	-	-
Channel 2	-	Fr2	Cd2
Drive input		LI1 ... LI6	
Logic I/O card input		LI7 ... LI10	
Extended I/O card input		LI11 ... LI14	
Modbus command bit		bit 0 = C100 ... bit 15 = C115	
CANopen command bit		bit 0 = C200 ... bit 15 = C215	
Network command bit		bit 0 = C300 ... bit 15 = C315	
Controller Inside command bit		bit 0 = C400 ... bit 15 = C415	

The values Fr1, Fr1b, Fr2, Cd1 and Cd2 are either configured or written via the network during operation.

In I/O and DSP402 profiles (separate), independent switching is possible:

Type	Switching	Channel 1	Channel 2
Reference	Function reference switching [Ref 1B switching] (rCb)	Function reference 1 [Ref. 1 channel] (Fr1)	Function reference 1B [Ref. 1B channel] (Fr1b)
	Direct reference switching [Ref. 2 switching] (rFC)	Function reference 1 or 1B [Ref. 1 channel] (Fr1) [Ref. 1B channel] (Fr1b)	Direct reference 2 [Ref. 2 channel] (Fr2)
Command	Command switching [Cmd switching] (CCS)	Command 1 [Cmd channel 1] (Cd1)	Command 2 [Cmd channel 2] (Cd2)

In DSP402 profile (not separate), switching is simultaneous:

Type	Switching	Channel 1	Channel 2
Reference and command	Direct reference switching [Ref. 2 switching] (rFC)	Function reference 1 or 1B [Ref. 1 channel] (Fr1) [Ref. 1B channel] (Fr1b)	Direct reference 2 [Ref. 2 channel] (Fr2)
		Command 1 [Cmd channel 1] (Cd1)	Command 2 [Cmd channel 2] (Cd2)

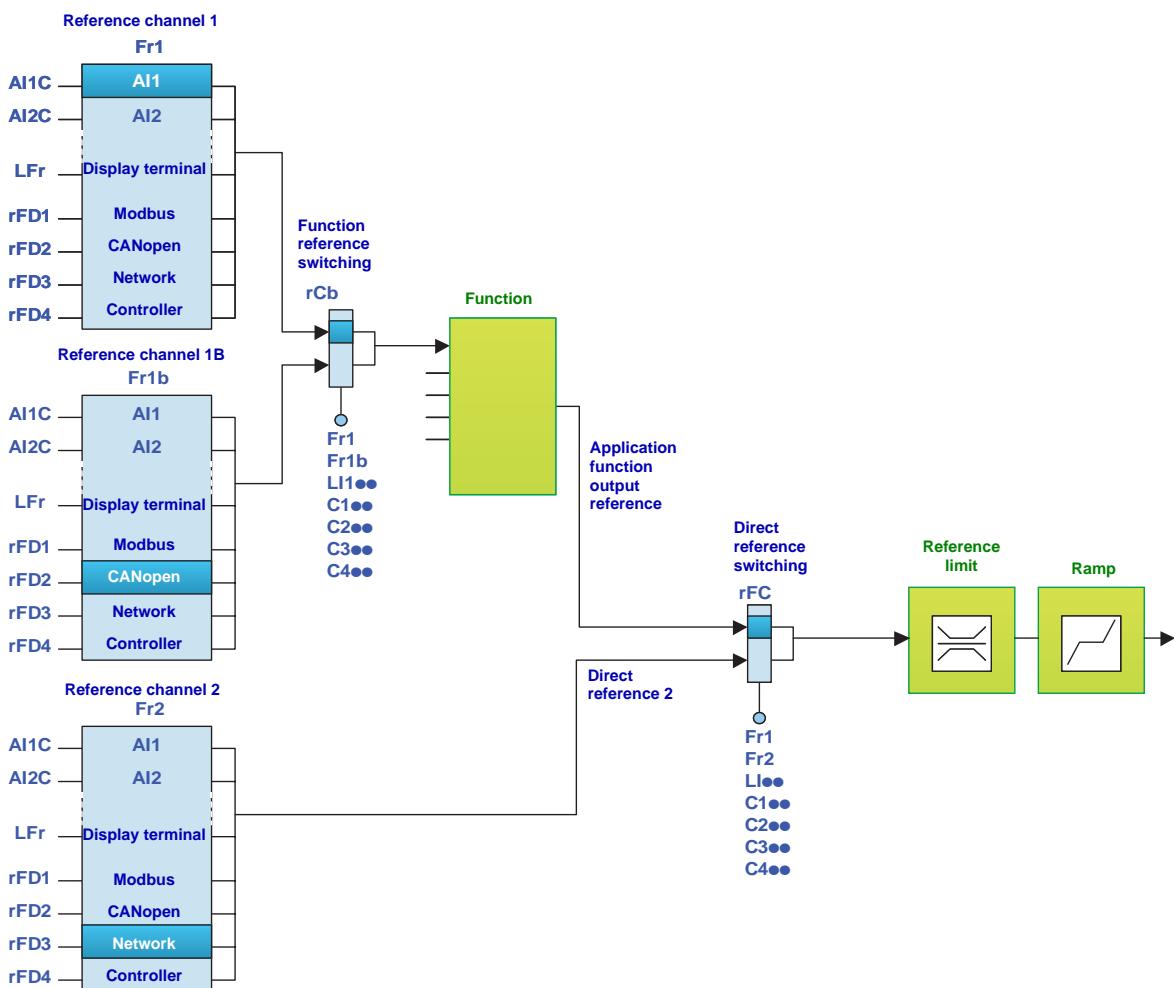
Command/reference switching

Reference switching principle

A detailed description can be found in the programming manual.

The diagram below shows reference switching as applicable to all modes:

- I/O profile
- DSP402 profile and separate mode
- DSP402 profile and not separate mode

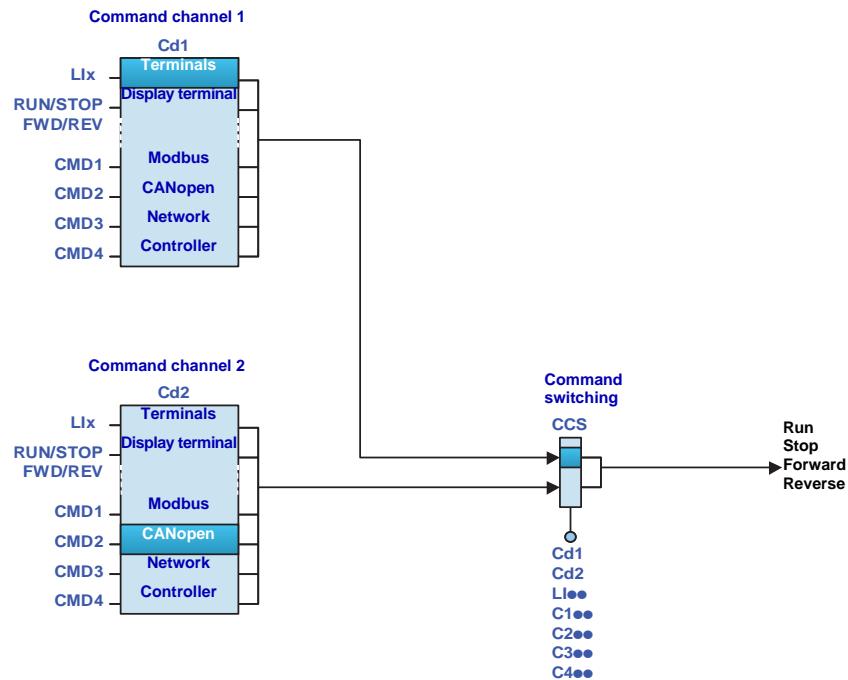


Command/reference switching

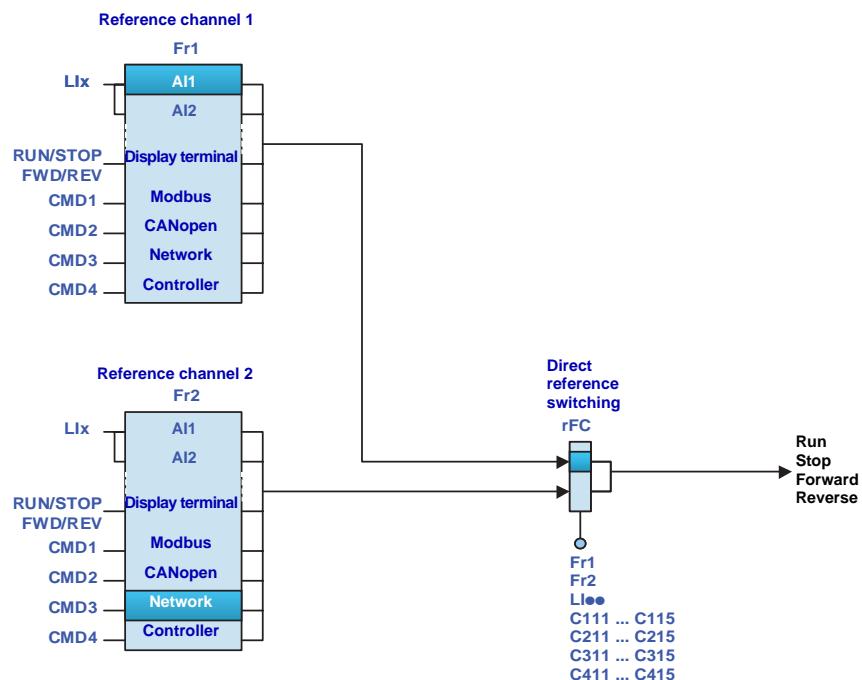
Command switching principle

A detailed description can be found in the programming manual.

I/O profile or DSP402 profile (separate mode)



DSP402 profile (not separate mode)



Command/reference switching

Assigning control word bits

I/O profile

The I/O profile is extremely flexible in terms of assigning and switching the 16 control word bits.

To switch a control word bit using:

- an input from the terminals
- or a control word bit from another communication channel

simply configure a switched assignment for the function input, instead of a fixed assignment.

Inputs and bits of the same order are switched.

Inputs LI1 to LI6 of the drive terminals can be used to switch control word bits 0 to 5.

With a logic I/O card using inputs LI7 to LI10, control word bits 6 to 9 can also be switched.

With an extended logic I/O card using inputs LI11 to LI14, control word bits 10 to 13 can also be switched.

Once an item has been assigned as switchable, it can no longer be assigned as fixed, and vice versa.

Example: Once a function input has been assigned to CD04, it cannot be assigned to LI5, C104, C204, C304 or C404.

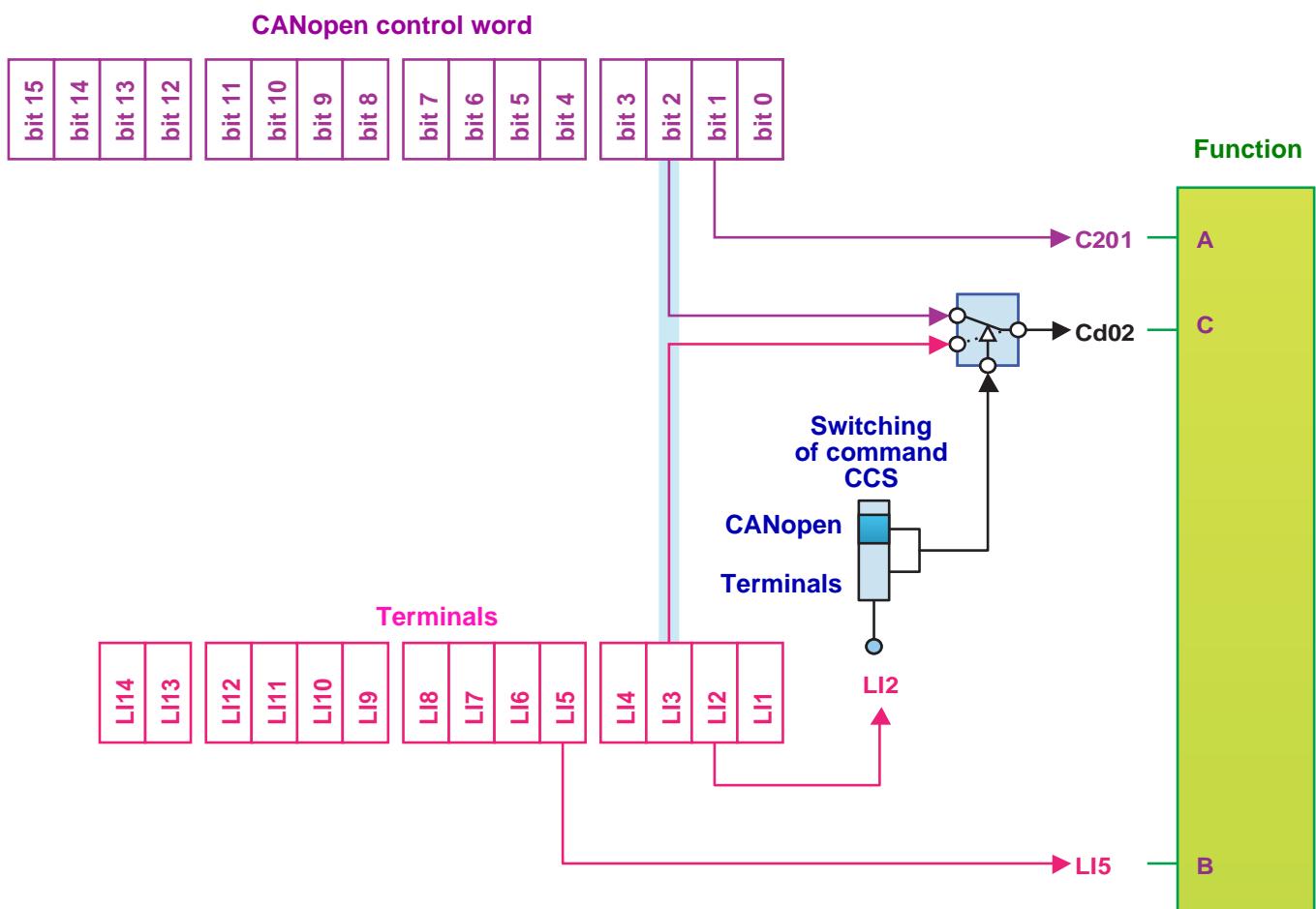
Example

Function input A is always controlled by bit 1 of the CANopen control word.

Function input B is always controlled by input LI5 on the terminals.

Depending on the value of LI2, function input C is controlled:

- Either by input LI3 on the terminals
- Or by bit 2 of the CANopen control word



Command/reference switching

The tables below show assignments on the basis of input or bit.

Run on state command [2 wire] (2C):

In all macro configurations, bit 1 is assigned by default to the operating direction command [Reverse assign.] (rrS)

Bit	Switched assignment	Fixed assignments							
		Drive terminals	Logic I/O card	Extended I/O card	Integrated Modbus	CANopen	Network card	"Controller Inside" card	
bit 0		Forward							
bit 1	Cd01	LI2	-	-	C101	C201	C301	C401	
bit 2	Cd02	LI3	-	-	C102	C202	C302	C402	
bit 3	Cd03	LI4	-	-	C103	C203	C303	C403	
bit 4	Cd04	LI5	-	-	C104	C204	C304	C404	
bit 5	Cd05	LI6	-	-	C105	C205	C305	C405	
bit 6	Cd06	-	LI7	-	C106	C206	C306	C406	
bit 7	Cd07	-	LI8	-	C107	C207	C307	C407	
bit 8	Cd08	-	LI9	-	C108	C208	C308	C408	
bit 9	Cd09	-	LI10	-	C109	C209	C309	C409	
bit 10	Cd10	-	-	LI11	C110	C210	C310	C410	
bit 11	Cd11	-	-	LI12	C111	C211	C311	C411	
bit 12	Cd12	-	-	LI13	C112	C212	C312	C412	
bit 13	Cd13	-	-	LI14	C113	C213	C313	C413	
bit 14	Cd14	-	-	-	C114	C214	C314	C414	
bit 15	Cd15	-	-	-	C115	C215	C315	C415	

Run on edge command [3 wire] (3C):

In all macro configurations, bit 2 is assigned by default to the operating direction command [Reverse assign.] (rrS)

Bit	Switched assignment	Fixed assignments							
		Drive terminals	Logic I/O card	Extended I/O card	Integrated Modbus	CANopen	Network card	"Controller Inside" card	
bit 0		Run authorization							
bit 1		Forward							
bit 2	Cd02	LI3	-	-	C102	C202	C302	C402	
bit 3	Cd03	LI4	-	-	C103	C203	C303	C403	
bit 4	Cd04	LI5	-	-	C104	C204	C304	C404	
bit 5	Cd05	LI6	-	-	C105	C205	C305	C405	
bit 6	Cd06	-	LI7	-	C106	C206	C306	C406	
bit 7	Cd07	-	LI8	-	C107	C207	C307	C407	
bit 8	Cd08	-	LI9	-	C108	C208	C308	C408	
bit 9	Cd09	-	LI10	-	C109	C209	C309	C409	
bit 10	Cd10	-	-	LI11	C110	C210	C310	C410	
bit 11	Cd11	-	-	LI12	C111	C211	C311	C411	
bit 12	Cd12	-	-	LI13	C112	C212	C312	C412	
bit 13	Cd13	-	-	LI14	C113	C213	C313	C413	
bit 14	Cd14	-	-	-	C114	C214	C314	C414	
bit 15	Cd15	-	-	-	C115	C215	C315	C415	

Command/reference switching

DSP402 profile

Control word bits of the same order are switched if the function inputs are assigned to switchable bits. Switching may be possible using LI12, LI13 or LI14 on an extended I/O card.

Once a bit has been assigned to a switchable assignment, it can no longer be assigned to a fixed assignment, and vice versa.
Example: Once a function input has been assigned to Cd04, it cannot be assigned to LI5, C104, C204, C304 or C404.

The table below shows assignments on the basis of input or bit.

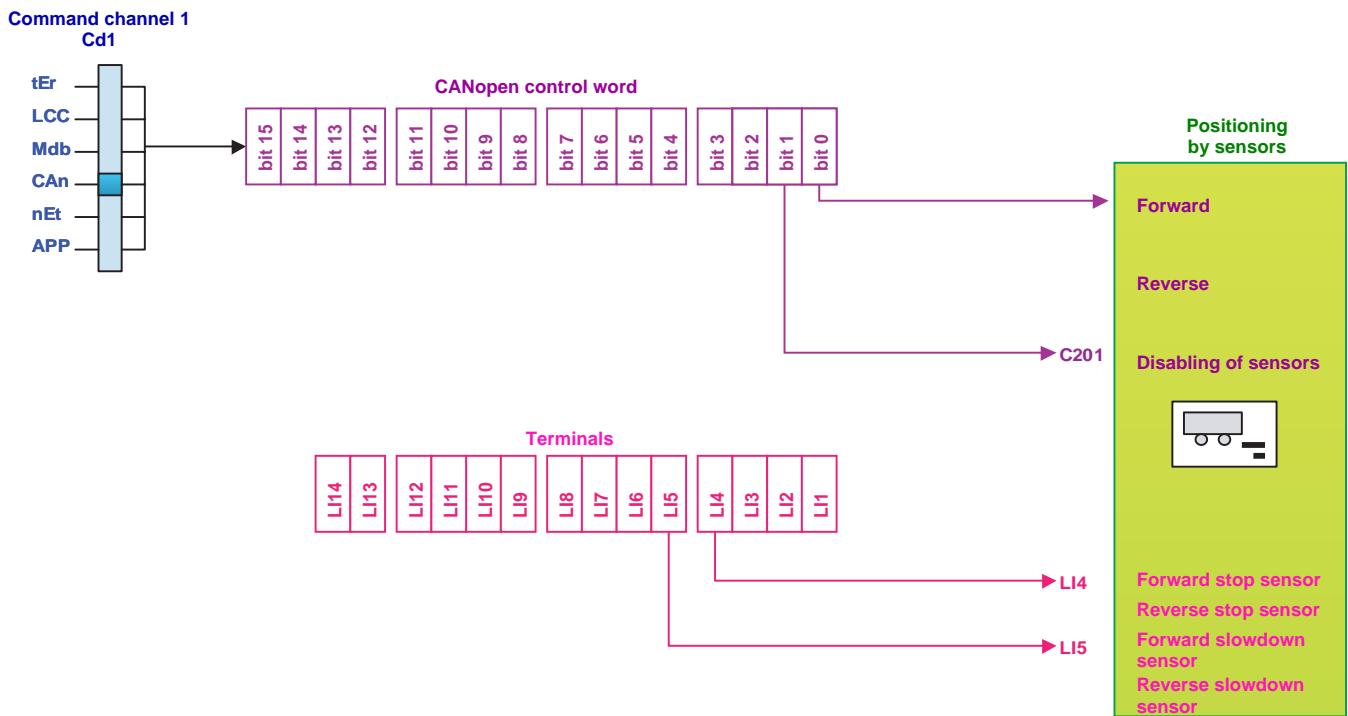
Bit	Switched assignment	Fixed assignments						
		Drive terminals	Logic I/O card	Extended I/O card	Integrated Modbus	CANopen	Network card	"Controller Inside" card
bit 0	-	LI1	-	-	-	-	-	-
bit 1	-	LI2	-	-	-	-	-	-
bit 2	-	LI3	-	-	-	-	-	-
bit 3	-	LI4	-	-	-	-	-	-
bit 4	-	LI5	-	-	-	-	-	-
bit 5	-	LI6	-	-	-	-	-	-
bit 6	-	-	LI7	-	-	-	-	-
bit 7	-	-	LI8	-	-	-	-	-
bit 8	-	-	LI9	-	-	-	-	-
bit 9	-	-	LI10	-	-	-	-	-
bit 10	-	-	-	LI11	-	-	-	-
bit 11	Cd11	-	-	LI12	C111	C211	C311	C411
bit 12	Cd12	-	-	LI13	C112	C212	C312	C412
bit 13	Cd13	-	-	LI14	C113	C213	C313	C413
bit 14	Cd14	-	-	-	C114	C214	C314	C414
bit 15	Cd15	-	-	-	C115	C215	C315	C415

Command/reference switching

Example: I/O profile with positioning by sensors function

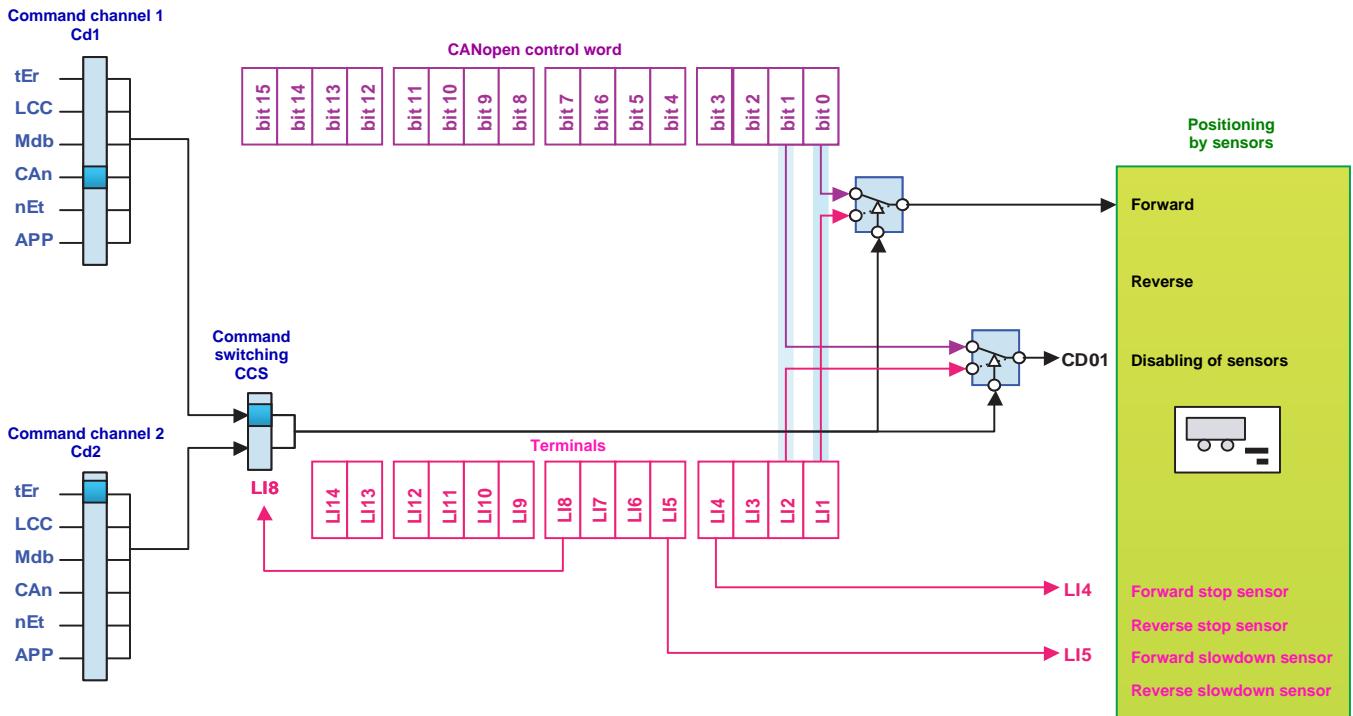
Let us return to the example given in the "I/O profile" section.

In the diagram below, the command comes from CANopen alone:



For a different application, provision must be made to enable the switching of the run commands and the disabling of the sensors and the reference at the terminals.

The new diagram will look like this:



Command/reference switching

Configure the following parameters:

Parameter	Value	Comment
Type of command	On state (2-wire)	The run command is obtained by bit 0 of the CANopen control word.
Profile	IO profile	
Reference 1 configuration	CANopen	Reference 1 comes from the CANopen card.
Command 1 configuration	CANopen	Command 1 comes from the CANopen card.
Reference 1B configuration	AI1	Reference 1B comes from analog input 1. Reference 1B has to be taken rather than reference 2, as use is to be made of the positioning function.
Command 2 configuration	Terminals	Command 2 comes from the terminals.
Assignment of stop sensor	Input LI4	The sensor inputs are not switched.
Assignment of slowdown sensor	Input LI5	
Assignment of sensor disable command	Bit 1 switched.	
Assignment of command switch	Input LI8	The LI8 inputs enable the command and reference to be switched
Assignment of application reference switch 1/1B	Input LI8	

Configuration via the graphic display terminal:

Menu/submenu	Parameter	Value
[1.5 INPUTS / OUTPUTS] (I-O-)	[2/3 wire control] (tCC)	[2 wire] (2C)
[1.6 - COMMAND] (Ctl-)	[Profile] (CHCF)	[I/O profile] (IO)
	[Ref. 1 channel] (Fr1)	[CANopen] (CAn)
	[Cmd channel 1] (Cd1)	[CANopen] (CAn)
	[Cmd channel 2] (Cd2)	[Terminals] (tEr)
	[Cmd switching] (CCS)	[LI8] (LI8)
[1.7 APPLICATION FUNCT.] (FUn-) [POSITIONING BY SENSORS] (LPO-)	[Stop FW limit sw.] (SAF)	[LI4] (LI4)
	[Slowdown forward] (dAF)	[LI5] (LI5)
	[Disable limit sw.] (CLS)	[Cd01] (Cd01)
	[Ref. 1B channel] (Fr1b)	[Ref. AI1] (AI1)
	[Ref 1B switching] (rCb)	[LI8] (LI8)

Command/reference switching

Copy on switching

When switching channels, it is possible to copy the reference or command from function channel 1 to direct channel 2.

Menu	Submenu
[1.6 COMMAND] (Ctl-)	
Parameter	Possible values
[Copy channel 1 --> 2]	No copy. [No] (nO) Copy reference [Reference] (SP) Copy command [Command] (Cd) Copy command and reference [Cmd + ref.] (ALL)

If a copy is not made, the drive stops according to the configured stop type [Type of stop] (Stt) until the first command and reference are received.

The reference before ramp (FrH) is copied unless the direct channel 2 reference is via +/- speed.

If the direct channel 2 reference is via +/- speed, the after ramp (rFr) reference is copied.

If the direct channel 2 command is via the terminals, the function channel 1 command is not copied even in pulse control (3-wire) [3 wire] (3C).

If the direct channel 2 reference is via AI1, AI2, AI3, AI4, the encoder input or frequency input, the function channel 1 reference is not copied.

Forced local mode

Definition

Forced local mode supports switching to the terminals or display terminal.

This function complements channel switching and makes it possible to make use of an existing function from the Altivar 58 range.

Forced local mode is only available in DSP402 profile, not in I/O profile.

All other communication takes priority over forced local mode.

Forced local mode can be configured via the display terminal:

Menu	Submenu
[1.9 COMMUNICATION] (COM-)	[FORCED LOCAL] (LCF-)

Parameter	Possible values
Forced local switch [Forced local assign.] (FLO)	Function inactive: [No] (nO) Assignment to a logic input (LI1 ... LI14): [LI1] (LI1) ... [LI14] (LI14) Forced local mode is active when the input is at state 1.
Forced local channel [Forced local Ref.] (FLOC)	Forced local on stop. [No] (nO) Assignment of the command to the terminals and of the reference to one of the analog inputs AI1 ... AI4 [AI2 ref.] (AI2), [AI3 ref.] (AI3), [AI4 ref.] (AI4) Assignment of the command to the terminals and of the reference to the frequency input (if card present) [Pulse Input] (PI) Assignment of the reference [HMI Frequency ref.] (LFr) and of the command (RUN/STOP/FWD/REV buttons) to the graphic display terminal [HMI] (LCC)

In "forced local" state:

- Any attempts to write the parameter via one of these channels is rejected (applies to command, reference and adjustment parameters).
- However, the parameters can be read.
- The drive does not register a communication fault.

On exiting "forced local" mode:

- The drive copies the run commands, the direction and the forced local reference to the active channel (maintained).
- Monitoring of the active command and reference channels resumes following a time delay [Time-out forc. local] (FLOt).
- Drive control only takes effect once the drive has received the reference and the command.

The time delay [Time-out forc. local] (FLOt) (default value = 10 s) can be configured via the remote graphic display terminal:

Menu	Submenu	Parameter
[1.9 COMMUNICATION] (COM-)	[FORCED LOCAL] (LCF-)	[Time-out forc. local] (FLOt)

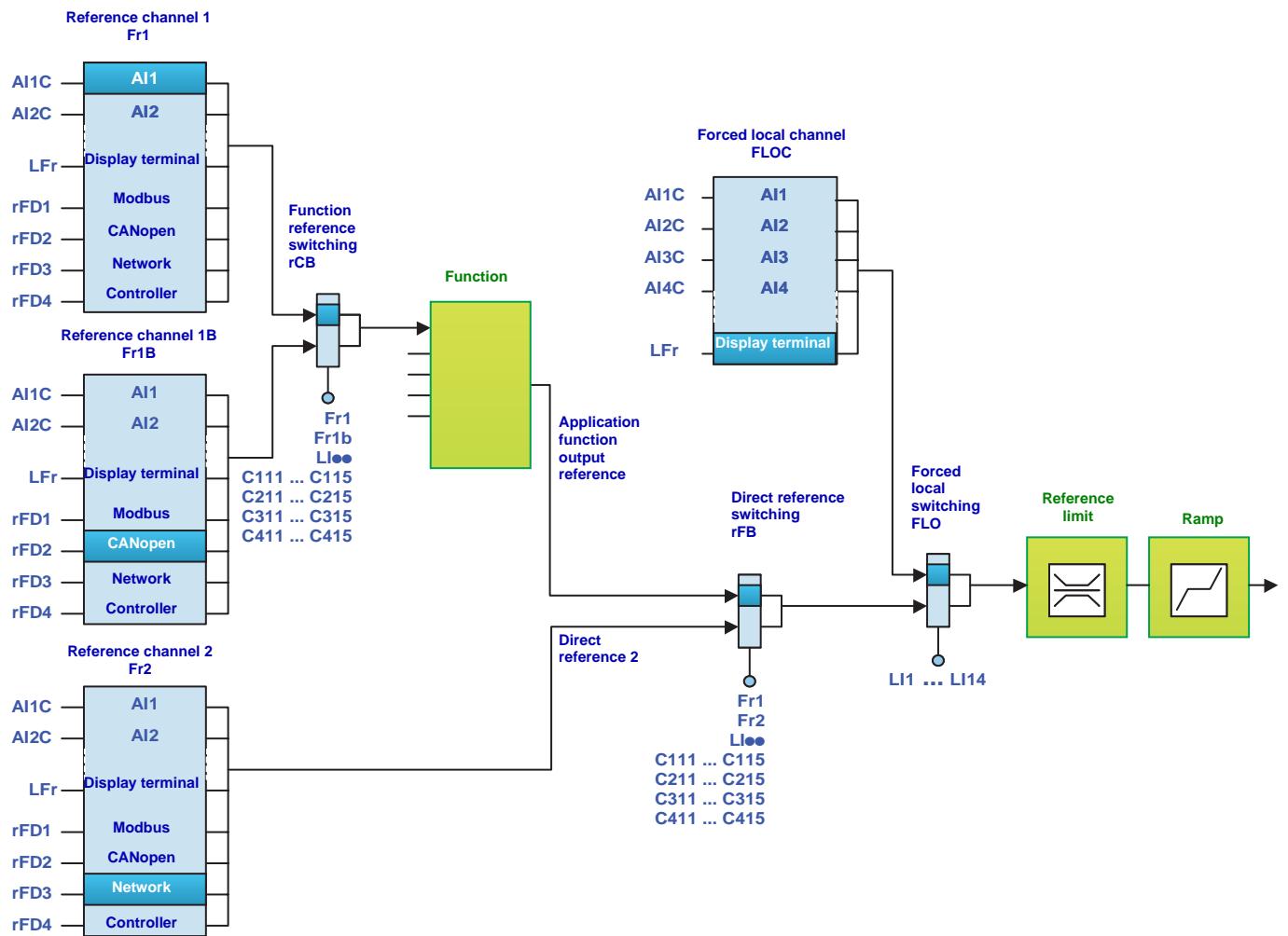
Forced local

Forced local mode and reference switching

A detailed description can be found in the programming manual.

This diagram shows reference switching as applicable to all modes:

- DSP402 profile and separate mode
- DSP402 profile and not separate mode

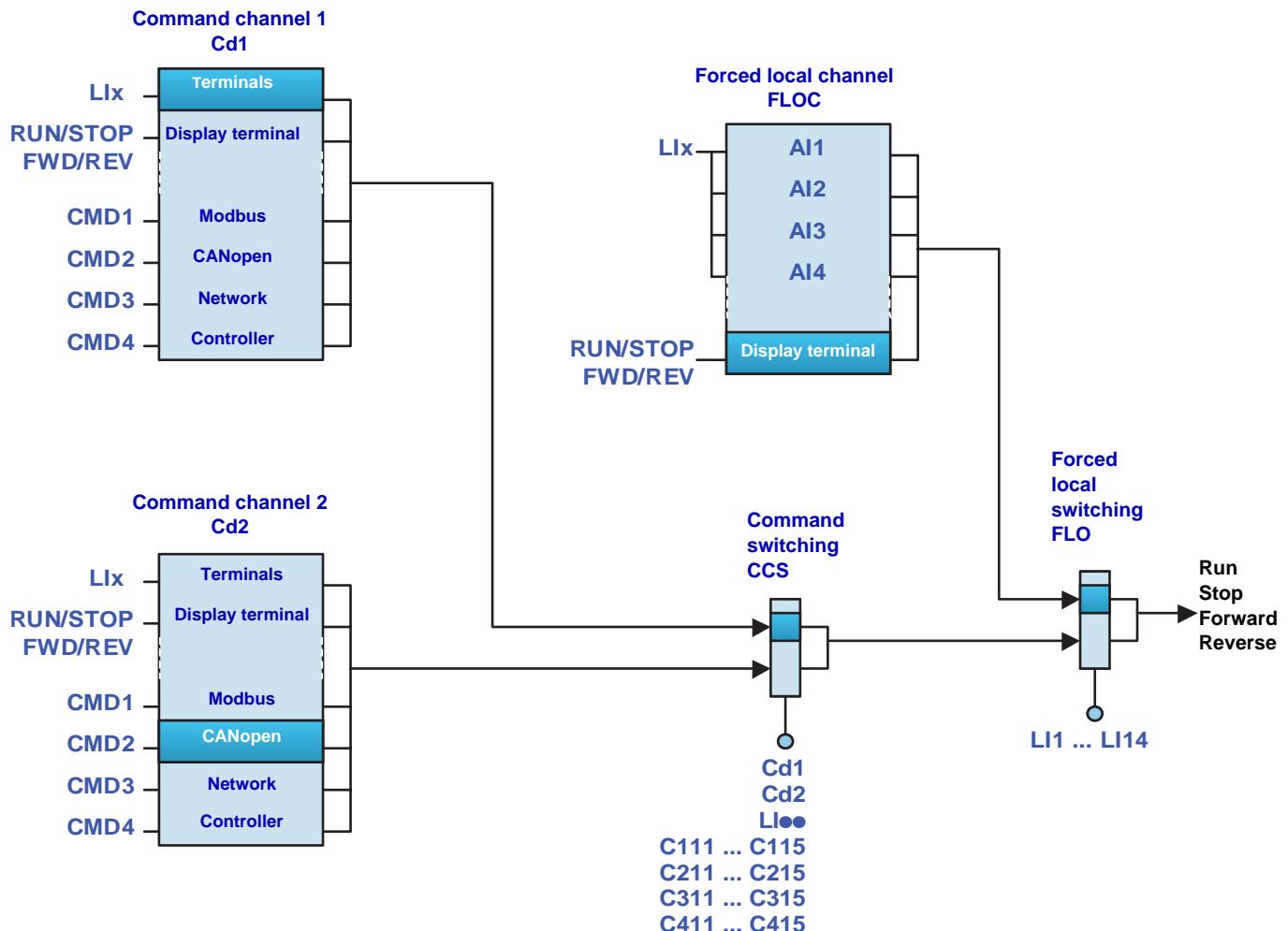


Forced local

Forced local mode and command switching

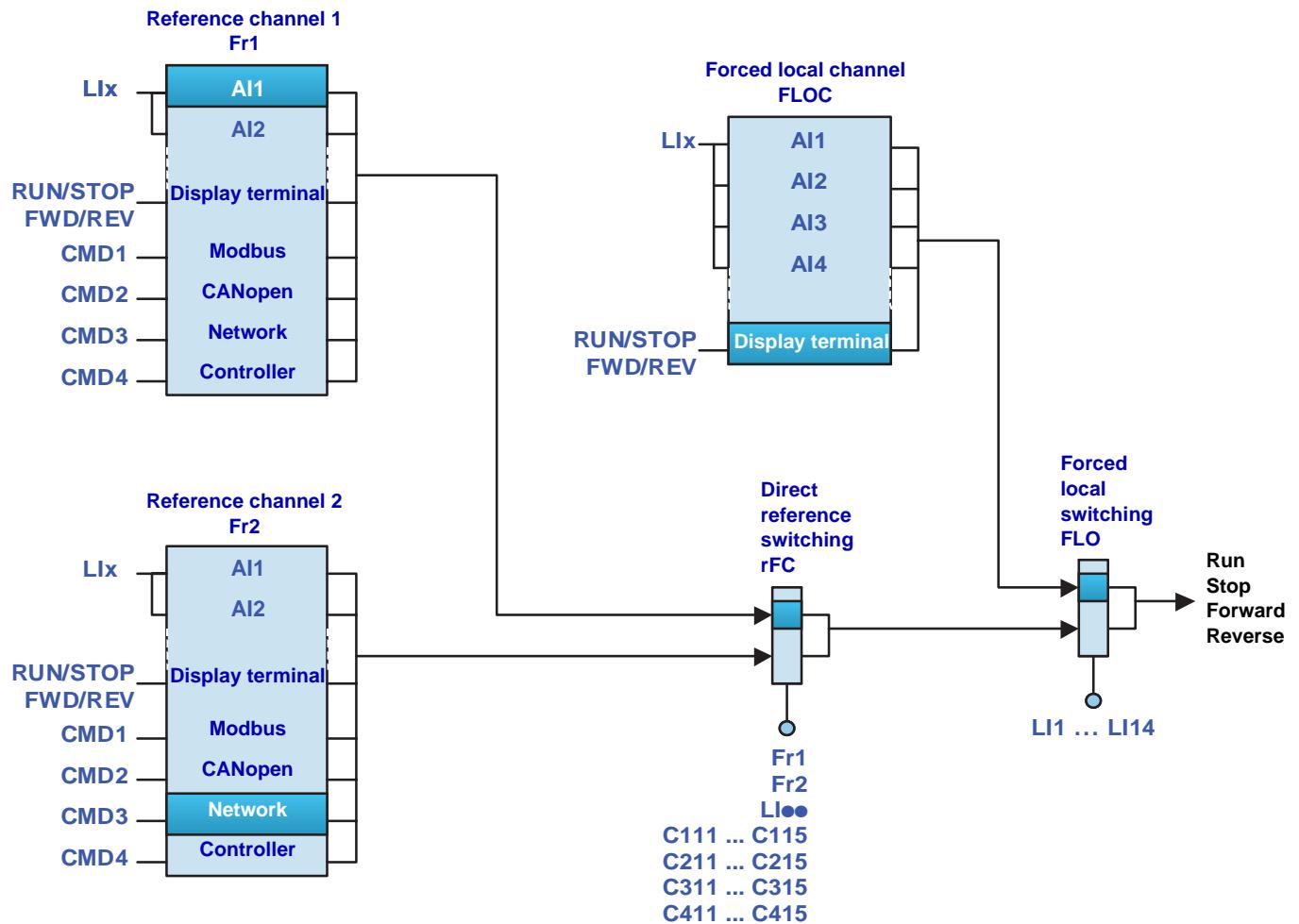
A detailed description can be found in the programming manual.

DSP402 profile (separate mode)



Forced local

DSP402 profile (not separate mode)



Priority stops

Priority stops

Priority stops on the graphic display terminal

Even if the graphic display terminal is not the active command channel, pressing the STOP key on the graphic display terminal will trigger a freewheel stop if:

- the **[Stop Key priority]** (PSt) parameter has been configured as **[Yes]** (YES) (factory setting) in the **[1.6 - COMMAND]** (CtL-) menu, via the terminal.

If the graphic display terminal is the active command channel, the STOP key will trigger a stop according to the type of stop configured with the **[Type of stop]** (Stt) parameter, regardless of the value of the **[Stop Key priority]** (PSt) parameter.

The role played by the graphic display terminal is independent of the profile.

I/O profile

In the I/O profile:

Stop and fault commands, configured as fixed assignments (**LI₀₀**, **C1₀₀**, **C2₀₀**, **C3₀₀**, **C4₀₀**) to terminal inputs or control word bits, are priority commands, even if the channel is not active.

Commands, configured as switched assignments (**Cd₀₀** ... **Cd₁₅**) are active only if the channel is active.

Fixed assignments are configured using the following values:

Channel	Run on state command [2 wire] (2C)	Run on edge command [3 wire] (3C)
Drive terminals	LI₂ ... LI₆	LI₃ ... LI₆
Logic I/O card		LI₇ ... LI₁₀
Extended I/O card		LI₁₁ ... LI₁₄
Integrated Modbus	C1₀₁ ... C1₁₅	C1₀₂ ... C1₁₅
CANopen	C2₀₁ ... C2₁₅	C2₀₂ ... C2₁₅
Communication card	C3₀₁ ... C3₁₅	C3₀₂ ... C3₁₅
"Controller Inside" card	C4₀₁ ... C4₁₅	C4₀₂ ... C4₁₅

Command	Configuration	= 0	= 1	Value for starting ⁽¹⁾
Freewheel stop	[Freewheel stop ass.] (nSt)	stop	no stop	1
Fast stop	[Fast stop assign.] (FSt)	stop	no stop	1
DC injection braking	[DC injection assign.] (dCl)	no braking	braking	0
External fault	[External fault ass.] (EtF)	no fault	fault	0

⁽¹⁾ If assignment is fixed, value required for starting, even if another channel is active.

In the case of a run on edge command, configured via **[3 wire] (3C)**:

The stop command (run enable) is assigned by default to switched bit order 0 (equivalent to **Cd₀₀**):

- It is only active on the terminals (**LI₁**) if the terminals are active.
- It is only active via the network (bit 0) if the network is active.

Priority stops

DSP402 profile

In DSP402 profile, separate or not separate mode:

Stop and fault commands configured for fixed assignment (LI_{●●}, C1_{●●}, C2_{●●}, C3_{●●}, C4_{●●}) to terminal inputs or control word bits are priority commands, even if the channel is not active.

Commands configured for switched assignment (Cd11 ... Cd15) are active only if the channel is active.

Fixed assignments are configured using the following values:

Channel	Run on state command [2 wire] (2C)	Run on edge command [3 wire] (3C)
Drive terminal	LI2 ... LI6	LI3 ... LI6
Logic I/O card		LI7 ... LI10
Extended I/O card		LI11 ... LI14
Integrated Modbus		C111 ... C115
CANopen		C211 ... C215
Communication card		C311 ... C315
"Controller Inside" card		C411 ... C415

C●11 is assigned by default to the reverse command [Reverse assign.] (rrS).

On the terminal:

Command	Configuration	= 0	= 1	Value for starting ⁽¹⁾	State reached since 5-Operation enabled
Freewheel stop	[Freewheel stop ass.] (nSt)	stop	no stop	1	2-Switch on disabled
Fast stop	[Fast stop assign.] (FSt)	stop	no stop	1	4-Switched on
DC injection braking	[DC injection assign.] (dCI)	no braking	braking	0	5-Operation enabled
External fault	[External fault ass.] (EtF)	no fault	fault	0	8-Fault

(1) If assignment is fixed, value required for starting, even if another channel is active.

In the case of a run on edge command, configured via [3 wire] (3C), the stop command (run enable) is assigned by default to LI1. This command is active even if the terminal is not the active channel.

Via the network:

Command	Configuration	= 0	= 1	Value for starting ⁽¹⁾	State reached since 5-Operation enabled
Fast stop	[Fast stop assign.] (FSt)	no stop	stop	0	4-Switched on
DC injection braking	[DC injection assign.] (dCI)	no braking	braking	0	5-Operation enabled
External fault	[External fault ass.] (EtF)	no fault	fault	0	8-Fault

(1) If assignment is fixed, value required for starting, even if another channel is active.

In DSP402 profile, it is not possible to assign the freewheel stop command [Freewheel stop ass.] (nSt) to the control word. Freewheel stop is achieved using the "5-Disable operation" or "Halt" commands with the type of stop [Type of stop] (Stt) parameter configured as [Freewheel] (nSt).

Communication monitoring

The Altivar 71 and its network ports feature mechanisms for monitoring and communication.

Principle

Following initialization (power-up), the drive checks that at least one command or reference parameter has been written for the first time by the active network.

Then, if a communication fault occurs on this network (in accordance with the protocol-specific criteria) the drive will react according to the configuration (fault, maintain, fallback, etc.).

The drive only starts once all the command and reference parameters of the active network have been written.

Network monitoring criteria

The network is monitored according to the protocol-specific criteria, which are summarized below. These criteria are described in the manual for the network concerned.

If an anomaly is detected, the port or network card sends a communication alarm.

Protocol	Criteria	Related fault ⁽¹⁾
Integrated Modbus ports	<ul style="list-style-type: none">Adjustable time-out for received requests destined for the drive.	[Modbus fault] (SLF)
Integrated CANopen port	<ul style="list-style-type: none">HeartbeatBus OffOverrunNMT state machine transition	[CANopen FAULT] (COF)
Modbus TCP/IP Ethernet card	<ul style="list-style-type: none">FDR faultIP address duplication fault	[EXTERNAL FAULT NET.] (EPF2)
	<ul style="list-style-type: none">Adjustable time-out for received control word (I/O scanning or messaging)Network overload	
Fipio card	<ul style="list-style-type: none">Non-adjustable time-out for received periodic variables destined for the drive	
Modbus Plus cards	<ul style="list-style-type: none">Adjustable time-out:<ul style="list-style-type: none">Either for received periodic variables (Peer cop) destined for the driveOr for Modbus messages destined for the drive, if no periodic variables (Peer cop) configured	[NETWORK FAULT] (CNF)
Modbus card	<ul style="list-style-type: none">Fixed time-out (10 s) for received requests destined for the drive	
Uni-Telway card	<ul style="list-style-type: none">Fixed time-out (10 s) for master polling	
Profibus DP card	<ul style="list-style-type: none">Adjustable time-out (via the network configuration software) for received periodic variables (PZD and PKW) destined for the drive	
DeviceNet cards	<ul style="list-style-type: none">Adjustable time-out:<ul style="list-style-type: none">Either for received periodic variables (Polling and COS) destined for the driveOr for network activity, if no periodic variables configured	
INTERBUS card	<ul style="list-style-type: none">Time-out for received periodic variables destined for the drive	

⁽¹⁾The fault is triggered under certain conditions.

Communication monitoring

Detailed operation

Detailed operation is complex.

It is not necessary to read this chapter except in certain situations, including:

- When several networks can be used to control the drive
- When the control variables are not sent automatically in order to optimize scan time (at the risk of triggering a communication fault)

Monitoring of parameters

- A parameter is involved in controlling the drive if it is configured as a drive function input.

Example:

If the operation on reference function is active and reference 2 has been assigned to Modbus, the Modbus reference plays a part in control.

- For each communication channel, the drive monitors all the command and reference parameters likely to be involved in controlling the drive:
 - The control word (CMD)
 - The speed reference (LFRD)
 - The frequency reference (LFR)
 - The torque reference (LTR)
 - The PID regulator reference (PISP)
 - The PID regulator feedback (AIU1)
 - The reference multiplication coefficient (MFR)
- As soon as one of these parameters has been written once to a communication channel, it is confirmed on that channel.
- The confirmation of monitored network parameters is reset:
 - In the event of a communication alarm
 - In forced local

Monitoring of communication channels

- Certain communication channels are said to be obligatory participants if they are obliged to supply one of the following parameters:
 - The control word (**CMD**) from the active command channel
 - The control word containing the command switch (bit configured on **[Cmd switching] (CCS)**)
 - The control word containing the switch for reference 1'1B (bit configured on **[Ref. 1B switching] (rCb)**)
 - The control word containing the switch for reference 1'2 (bit configured on **[Ref. 2 switching] (rFC)**)
 - The frequency or speed reference (**LFR** or **LFRD**) from the active reference channel
 - Summing frequency or speed reference (**LFR** or **LFRD**) 2 (assigned to **[Summing ref. 2] (SA2)**)
 - Summing frequency or speed reference (**LFR** or **LFRD**) 3 (assigned to **[Summing ref. 3] (SA2)**)
 - Subtracting frequency or speed reference (**LFR** or **LFRD**) 2 (assigned to **[Subtract ref. 2] (dA2)**)
 - Subtracting frequency or speed reference (**LFR** or **LFRD**) 3 (assigned to **[Subtract ref. 3] (dA3)**)
 - The torque reference (**LTR**)
 - The PID regulator reference (**PISP**)
 - The PID regulator feedback (**AIU1**)
 - The reference multiplication coefficient (**MFR**) 2 (assigned to **[Multiplier ref. 2] (MA2)**)
 - The reference multiplication coefficient (**MFR**) 3 (assigned to **[Multiplier ref. 3] (MA3)**)
- Confirming a parameter on an obligatory participant channel activates monitoring for that channel.
- If a communication alarm is sent (in accordance with the protocol criteria) by a monitored port or network card, the drive will trigger a communication fault.

The drive reacts according to the communication fault configuration (fault, maintenance, fallback, etc.)

- If a communication alarm occurs on a channel that is not being monitored, the drive will not trigger a communication fault.

Example:

A drive is powered up. It is connected to CANopen, which is the active channel.

The PLC is powered up but is not in RUN mode. Therefore, the bus is active but no parameter is sent to the drive yet.

If the CANopen bus is disconnected, a "Bus off" alarm is triggered, but not a communication fault.

Communication monitoring

- The drive does not monitor optional participant channels, which are obliged to supply:
 - A control word containing fixed assignment commands other than channel switches (example: fast stop assigned to C213)
 - A control word containing fixed assignment preset speeds
- If there is a fixed assignment between a command and the control word bit of an inactive channel, a communication alarm on this network will not trigger a communication fault.

Example:

A drive is equipped with a "Controller Inside" card and an Ethernet card.

The "Controller Inside" card controls the drive (command and reference).

One bit of the Ethernet control word is assigned to "fast stop".

If the drive is disconnected from the Ethernet network, the drive can no longer be stopped via Ethernet (however, a communication fault is not triggered).

Enabling of communication channels

- A communication channel is enabled once all the parameters involved have been confirmed.

Example:

A drive is in I/O profile with simple speed control. Modbus constitutes both the command and reference channel.

The Modbus channel will be enabled as soon as the control word and speed reference have been received.

- The drive is only able to start if all obligatory participant channels are enabled.

Example:

A drive in DSP402 profile is connected to Modbus, which is the active channel.

Unless the reference has been written at least once, it will not be possible to proceed to the "5-Operation enabled" state, even if the "4-Enable operation" command is sent.

Example:

A drive is connected to Modbus.

The terminals are both the reference and command channel.

The operation function is activated and summing input 2 is assigned to Modbus.

The drive will not start until the reference has been supplied by Modbus.

Example:

A drive is configured for switching between the terminals and CANopen.

If the command switch is assigned to the Ethernet card, startup will only be possible once the Ethernet channel is enabled.

- A communication channel is disabled:
 - In the event of a communication alarm
 - In forced local

Communication monitoring

Monitoring of inactive communication channels

- An inactive channel is one that is not currently involved in the control of the drive.
- An alarm on a port or network card does not trigger a communication fault on an inactive channel.

Example:

A drive is equipped with an Ethernet card solely for the purpose of monitoring the drive. If the drive is disconnected from the Ethernet network, no fault is triggered.

Enabling of inactive communication channels

- An inactive communication channel is enabled once all the monitored parameters assigned to it have been confirmed.

Example:

A drive is in I/O profile with simple speed control. Modbus constitutes both the command and reference channel.

The Modbus channel will be enabled as soon as the control word and speed/frequency reference have been received.

- When switching from an enabled channel to a disabled channel, the drive immediately triggers a communication fault.

Special case involving Modbus TCP/IP Ethernet card

- In the event of an FDR or IP address duplication fault, a communication fault is triggered regardless of the state of this channel (active, participant, etc.).

Communication monitoring

Reaction to a communication fault

In the event of a communication fault (on a monitored channel), the drive reacts as specified in the [1.8 - FAULT MANAGEMENT] (FLt-) menu [**COM. FAULT MANAGEMENT.J**] (CLL-) submenu by the following parameters:

- [**Modbus fault mgt**] (SLL) for integrated Modbus
- [**CANopen fault mgt**] (COL) for CANopen
- [**Network fault mgt**] (CLL) for a network card

The Modbus TCP/IP Ethernet card can also trigger an external fault (in the event of an FDR fault or IP address duplication fault), to which the drive reacts as specified in the [1.8 – FAULT MANAGEMENT] (FLt-) menu [**EXTERNAL FAULT**] (EtF-) submenu by the [**External fault mgt**] (EPL) parameter.

The drive can react in 4 possible ways:

1. Drive fault

[**Freewheel**] (YES): freewheel stop

[**Ramp stop**] (rMP): stop on ramp

[**Fast stop**] (FSt): fast stop

[**DC injection**] (dCI): DC injection stop

The fault displayed will depend on the source of the communication fault.

- [**Modbus fault**] (SLF) for integrated Modbus
- [**CANopen FAULT**] (COF) for CANopen
- [**NETWORK FAULT**] (CNF) for a network card
- [**EXTERNAL FAULT NET.J**] (EPF2) for Ethernet card FRD and IP faults.

The DSP402 state chart changes to "7 - Fault reaction active" and then to "8 - fault".

2. Reference and command maintained

[**Ignore**] (nO): fault ignored

[**Spd maint.J**] (rLS): The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed.

There is no drive fault.

If the DSP402 state chart is in "5-Operation enabled", it remains there.

3. Stop without fault

[**Per STT**] (Stt): stop according to configuration of [Type of stop] (Stt).

There is no drive fault.

If the DSP402 state chart is in "5-Operation enabled", it changes to "4-Switched on" after stopping.

4. Fallback

[**Fallback spd**] (LFF): Change to fallback speed, maintained as long as the fault persists and the run command has not been removed.

There is no drive fault.

If the DSP402 state chart is in "5-Operation enabled", it remains there.

The fallback speed can be configured in the [1.8 – FAULT MANAGEMENT] (FLt-) menu using the [**Fallback speed**] (LFF) parameter.

Caution: The drive will not start up immediately at the fallback speed. If there is a loss of communication, the drive will only run at the fallback speed if the run command was present when the communication fault occurred.

In the event of a control system being used to ensure switchover to an active safe state if there is a loss of communication: Drives that have been stopped must always be left in the run state (5 - Operation enabled) with zero reference to ensure that they change to the fallback speed.

Assignment of setpoints from a network

Setpoint parameters

The Altivar 71 supports a number of setpoint parameters, which must be selected according to the functions used in the drive.

Function used	Input to be assigned	Value	Setpoint to be sent via the network
Speed reference	[Ref.1 channel] (Fr1)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	Speed reference (LFRD)
Frequency reference	[Ref.1 channel] (Fr1)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	Frequency reference (LFR)
Sum	[Summing ref. 2] (SA2) [Summing ref. 3] (SA3)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	Speed reference (LFRD) or Frequency reference (LFR)
Subtraction	[Subtract. ref. 2] (dA2) [Subtract. ref. 3] (dA3)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	Speed reference (LFRD) or Frequency reference (LFR)
Multiplication	[Multiplier ref. 2] (MA2) [Multiplier ref. 3] (MA3)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	[Multiplying coeff.] (MFr)
PID regulator	[Ref.1 channel] (Fr1)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	PID regulator reference (PISP)
	[PID feedback ass.] (PIF)	[Network AI] (AIU1)	PID regulator feedback (AIU1)
	[AI net. channel] (AIC1)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	
Torque regulation	[Torque ref. channel] (Tr1)	[Modbus] (Mdb) or [CANopen] (CAn) or [Com. card] (nEt)	Torque reference (LTR)

Example 1:

The drive is to be controlled by sending the speed reference to the PID regulator via CANopen. No application function is used.

The following assignment must be made: [Ref.1 channel] (Fr1) = [CANopen] (CAn).

The following must be sent: Speed reference (LFRD).

Example 2:

The drive is to be controlled by sending the PID regulator reference via Modbus.

The following assignment must be made: [Ref.1 channel] (Fr1) = [Modbus] (Mdb).

The following must be sent: PID regulator reference (PISP).

Example 3:

The drive is to be controlled by sending the PID regulator reference and the feedback via the Ethernet card.

The following must be assigned:

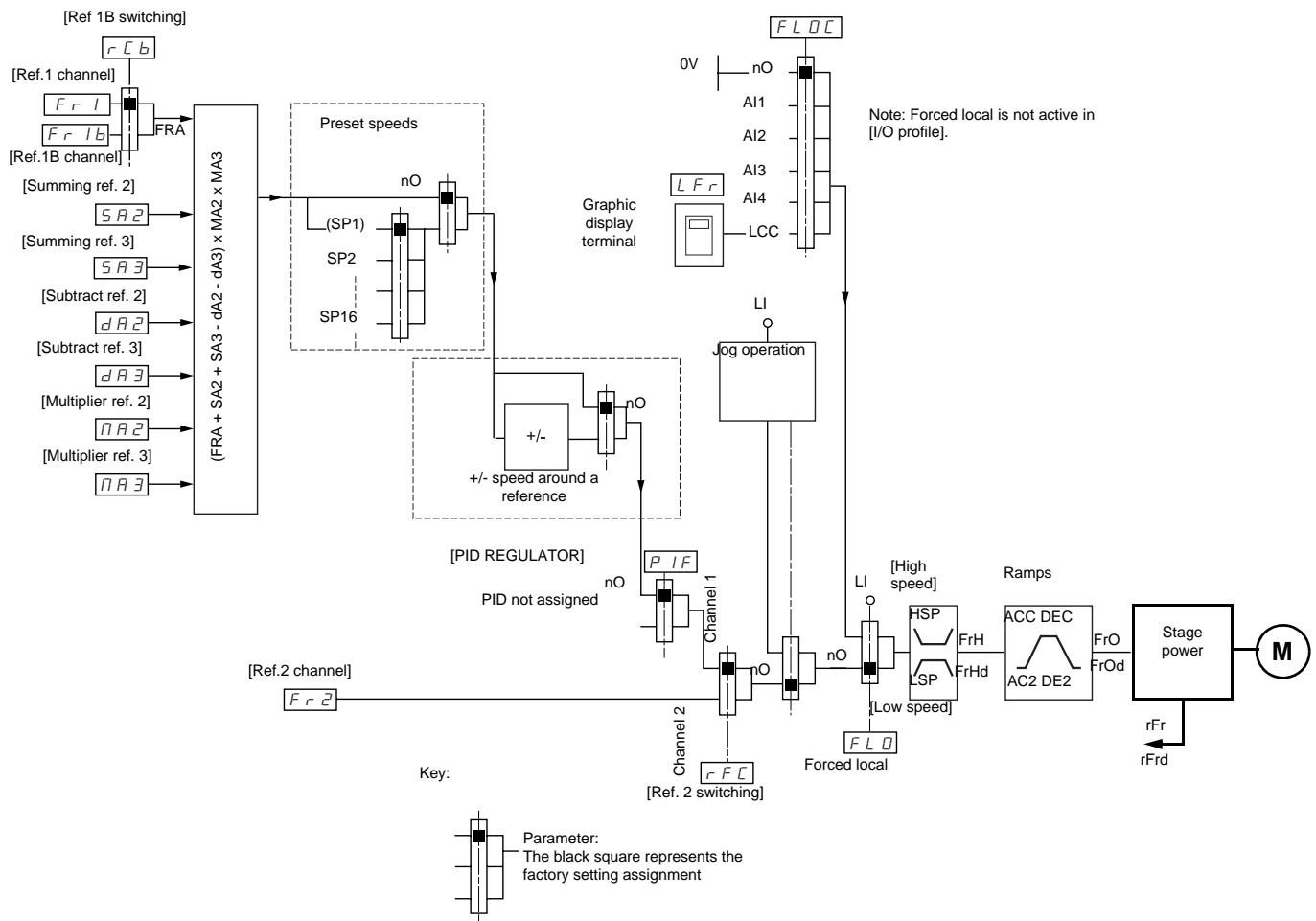
- [Ref.1 channel] (Fr1) = [Com. card] (nEt)
- [PID feedback ass.] (PIF) = [Network AI] (AIU1)
- [AI net. channel] (AIC1) = [Com. card] (nEt).

The following must be sent:

- PID regulator reference (PISP)
- PID regulator feedback (AIU1)

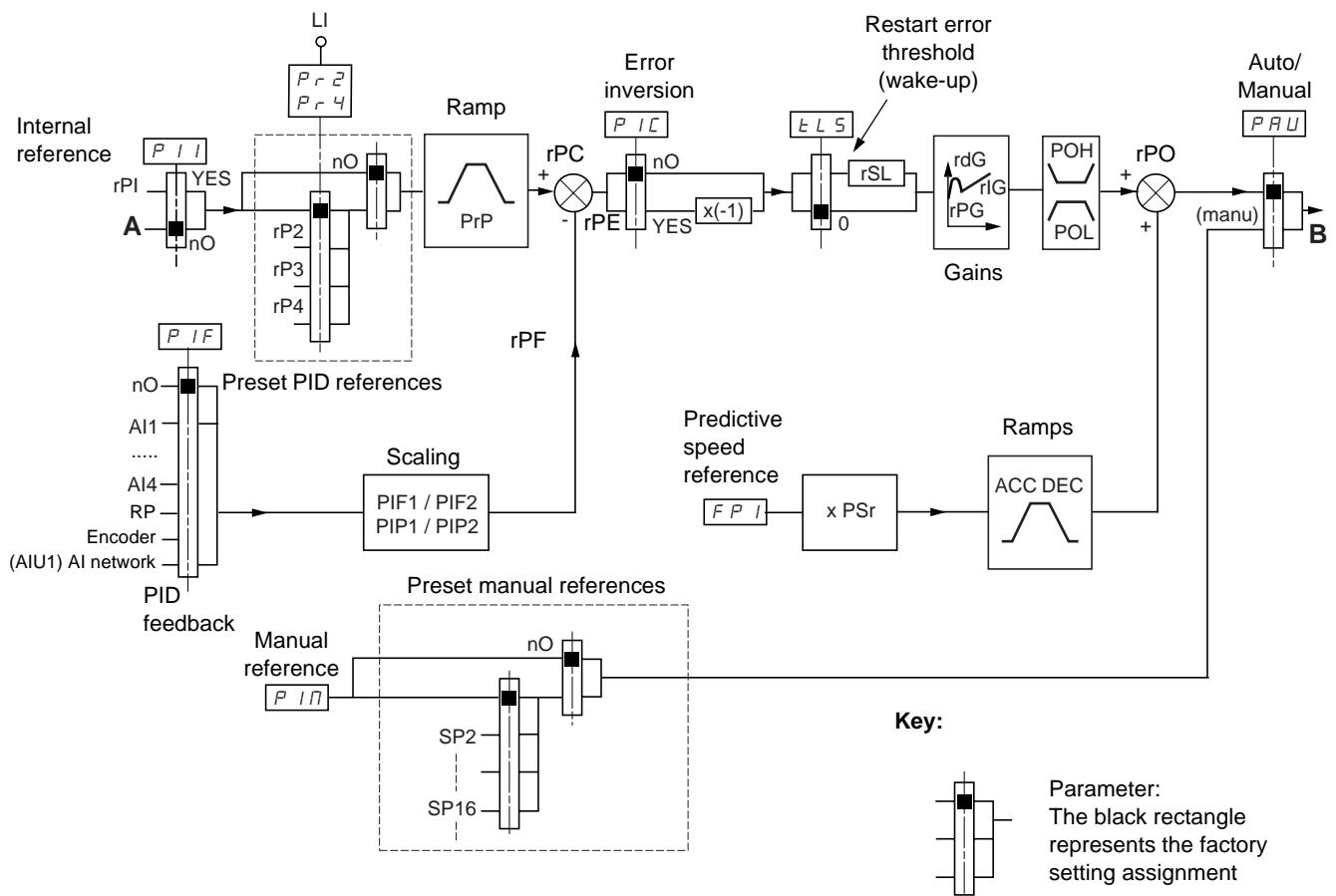
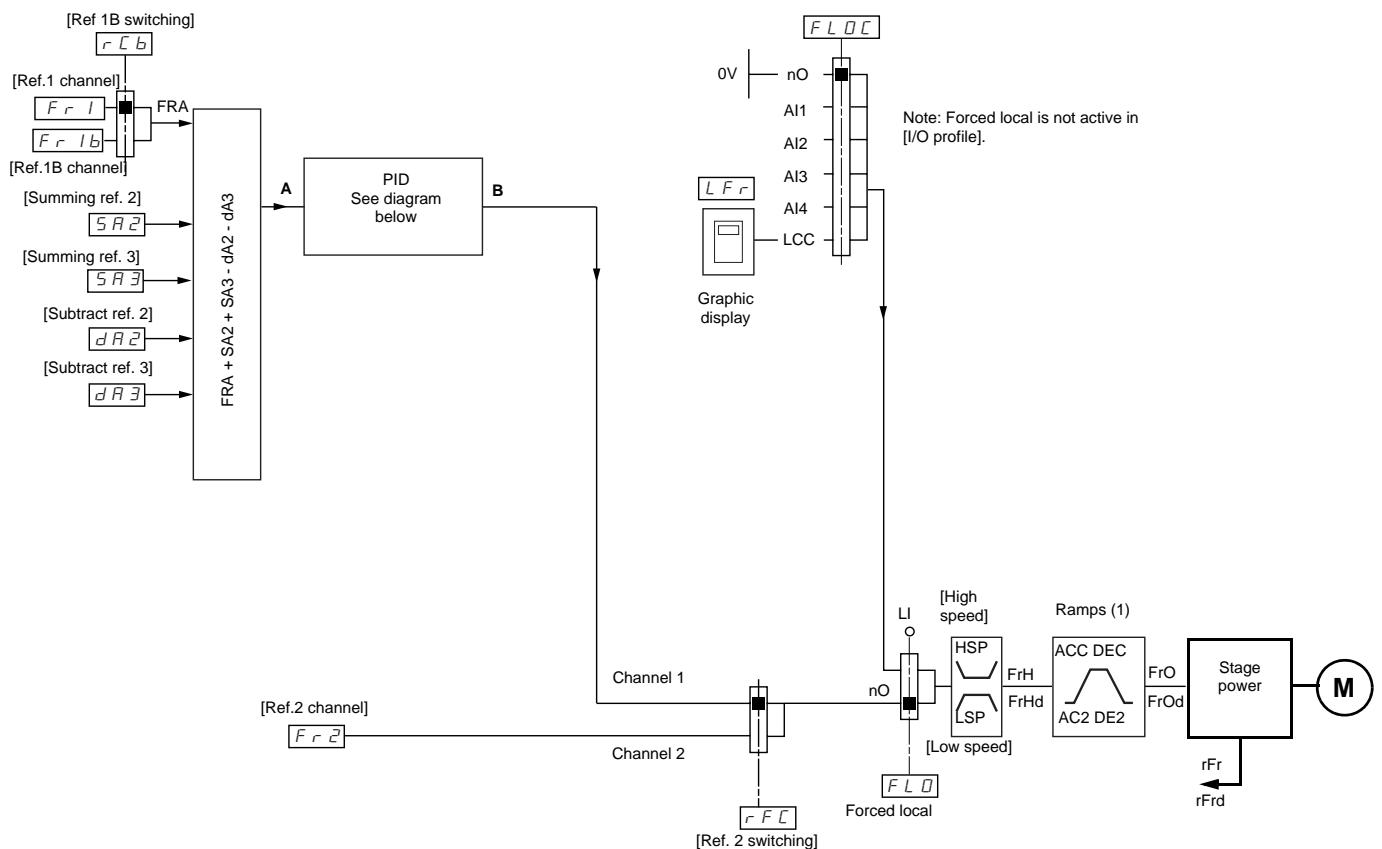
Assignment of references from a network

Without PID regulator



Assignment of references from a network

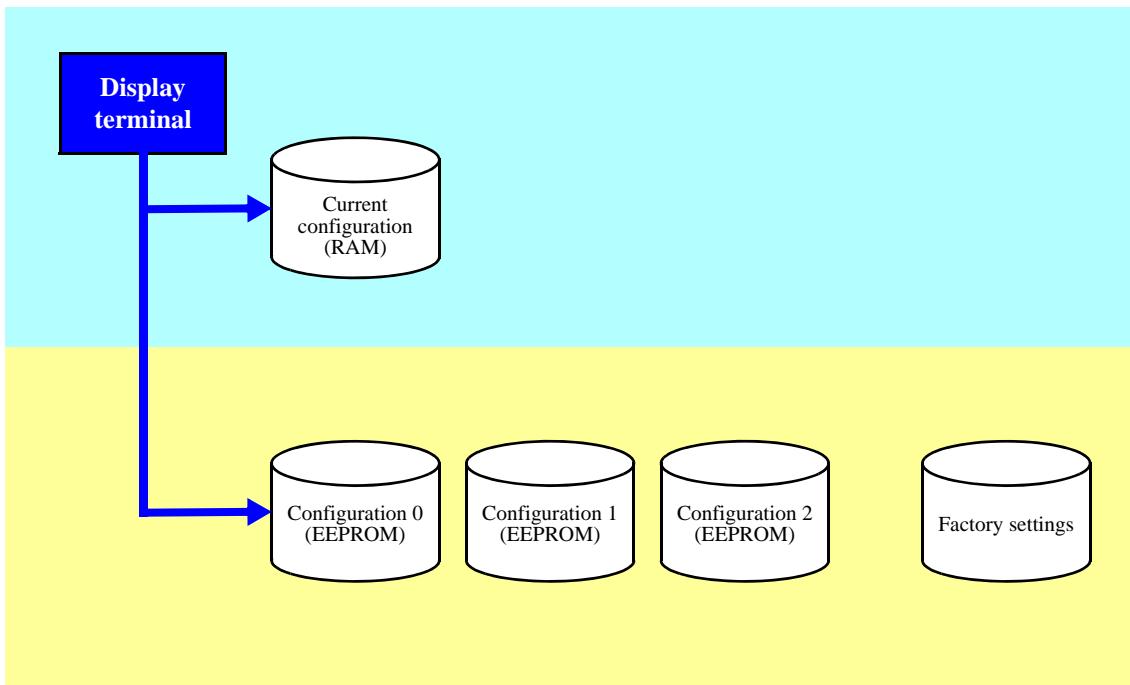
With PID regulator



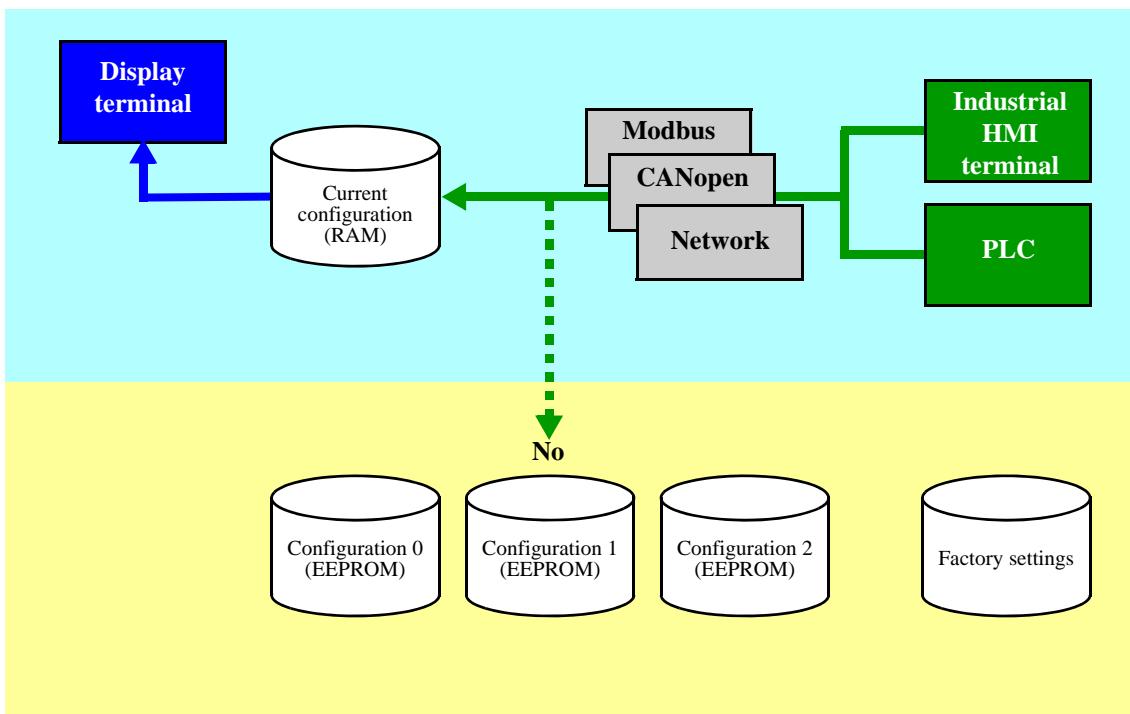
Configuration saving and switching

Saving the configuration

When a parameter is modified via the drive's integrated display terminal or graphic display terminal, this parameter is automatically saved to the EEPROM non-volatile memory.

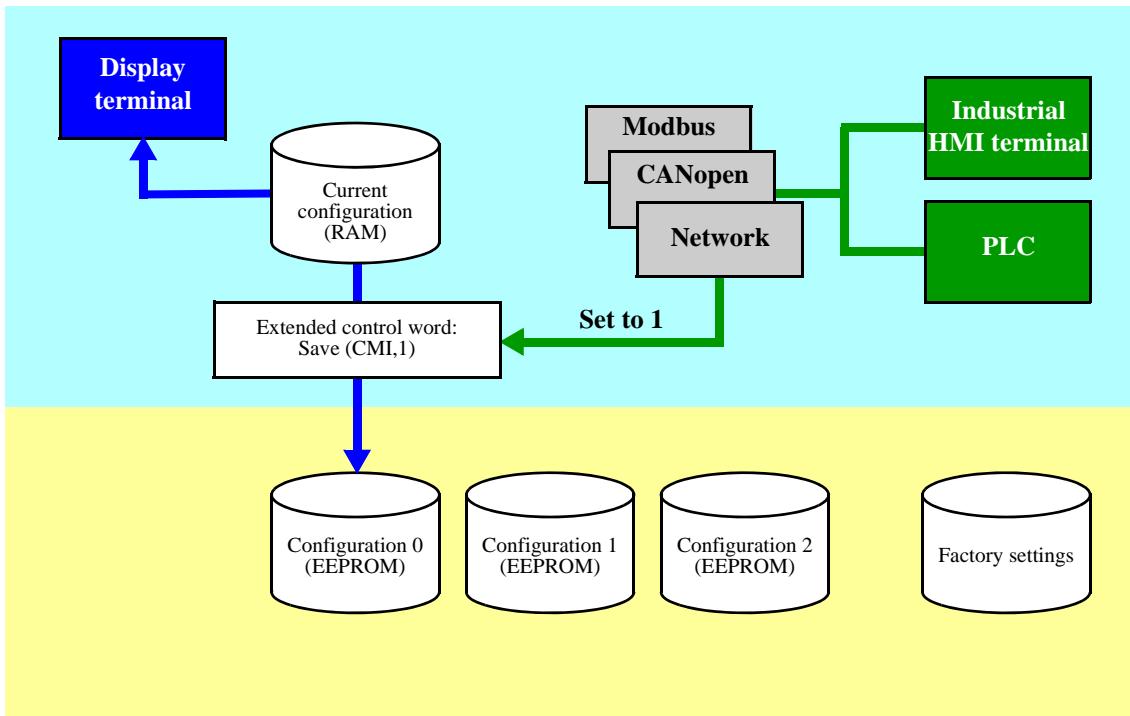


When a parameter is modified using a PLC or an HMI terminal via a network (Modbus, CANopen or a network card), this parameter is written to the current configuration in the RAM volatile memory. It is not saved to the EEPROM non-volatile memory. If the drive control voltage is disconnected, when it is reconnected, the parameter reverts to the initial value and the setting is lost.



Configuration saving and switching

To save the parameter, a save command must be executed using bit 1 of the extended control word (**(CMI)**).

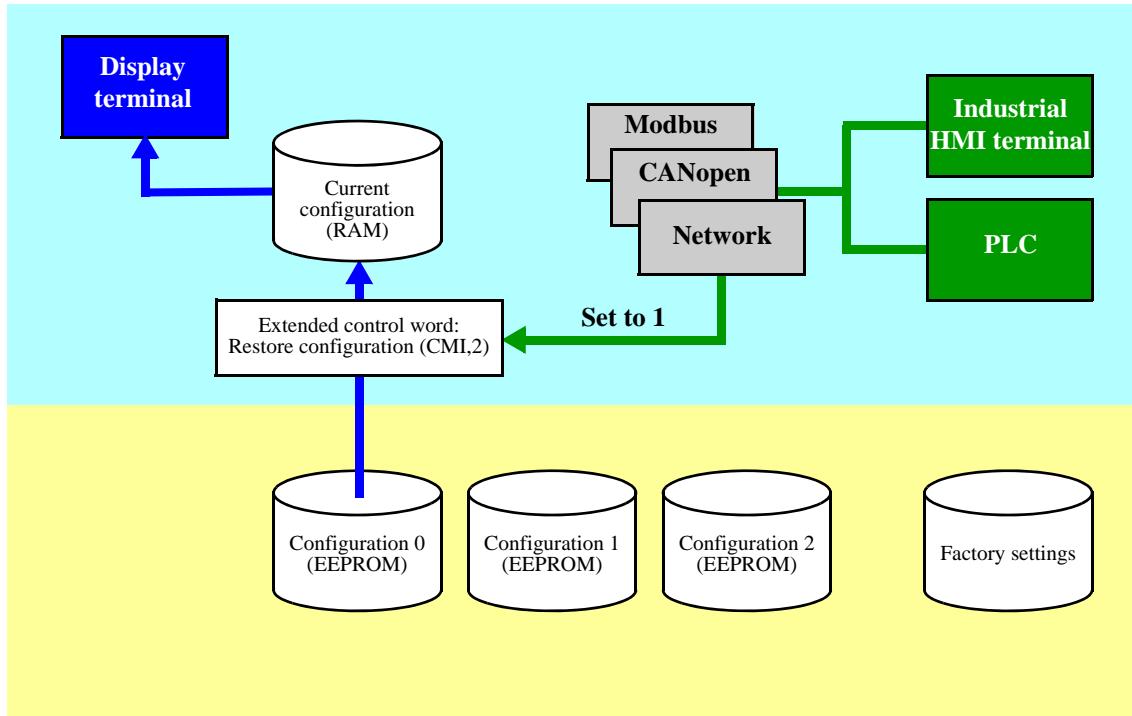


The save command is only active if the drive is stopped, and not in "5-Operation enabled" state.

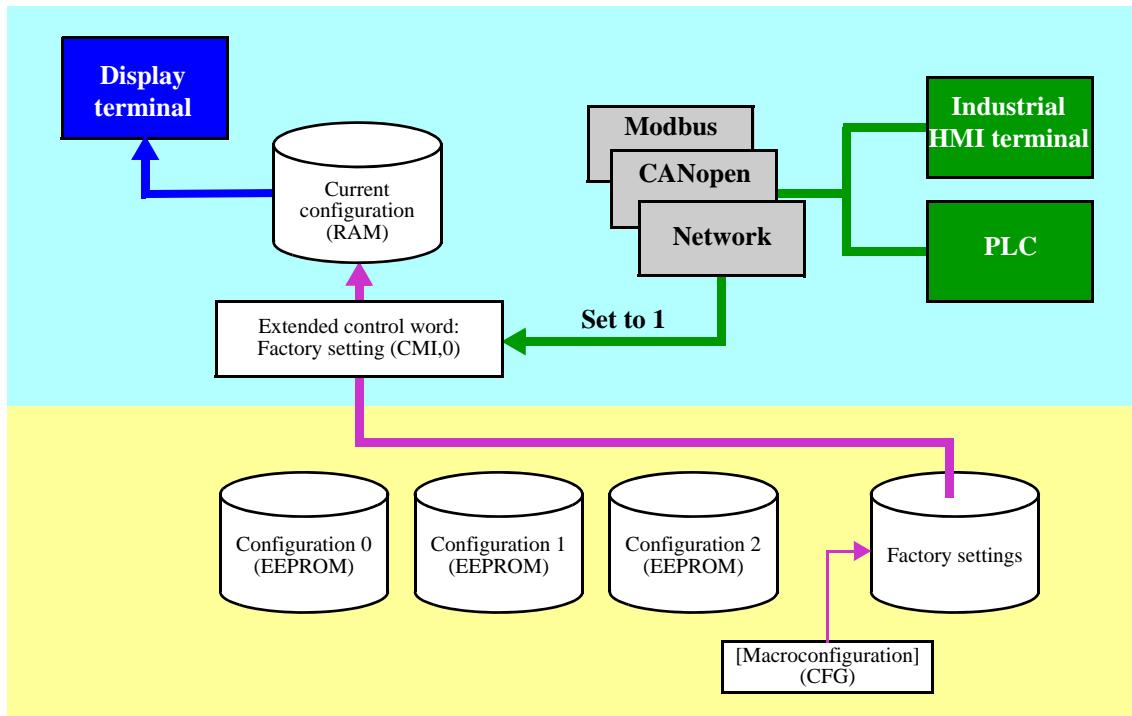
Configuration saving and switching

Restore configuration

The restore configuration command is executed using bit 2 of the extended control word (**CMI**).



The return to factory settings command is executed using bit 0 of the extended control word (**CMI**). The type of setting is determined by the active macro configuration: **[Macroconfiguration] (CFG)** parameter.

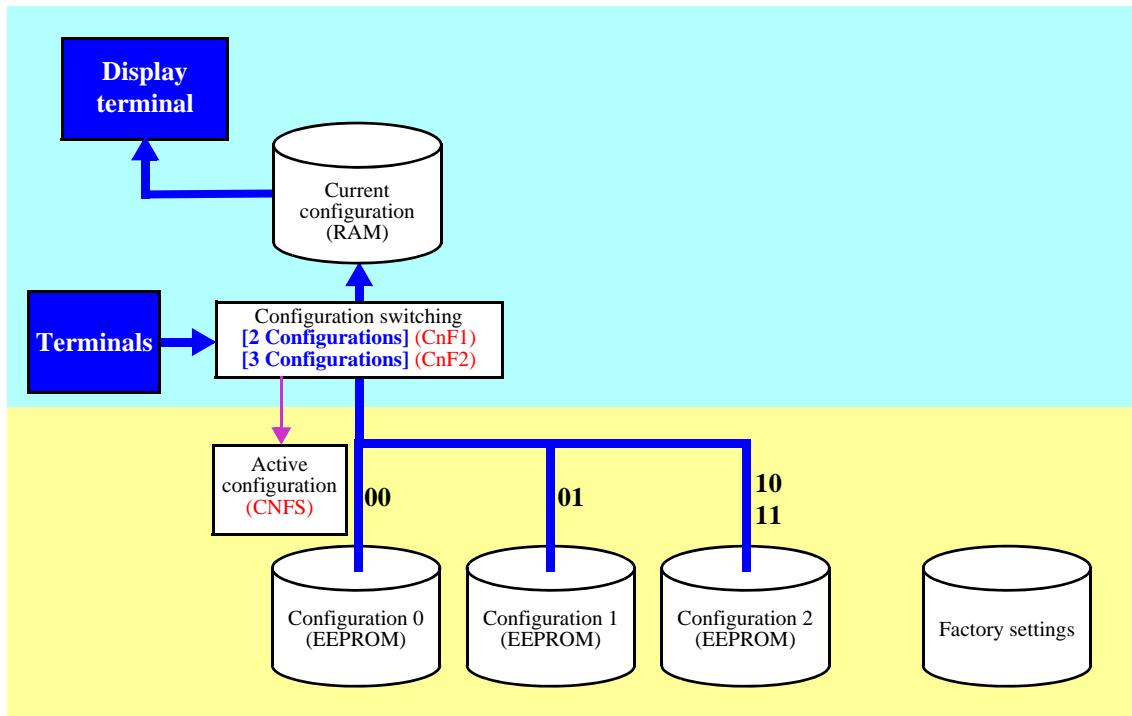


The restore command is only active if the drive is stopped, and not in "5-Operation enabled" state.

Configuration saving and switching

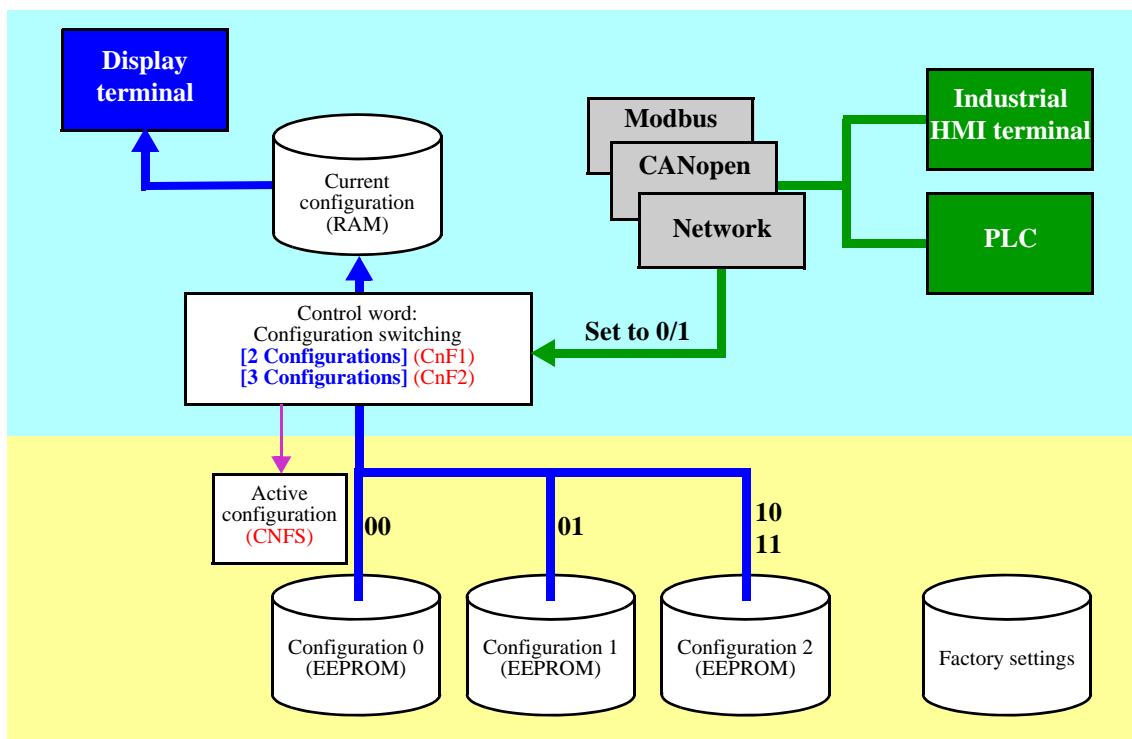
Configuration switching via control word

The configuration or motor switching function (see the programming manual) can be used via the network or via the terminals.



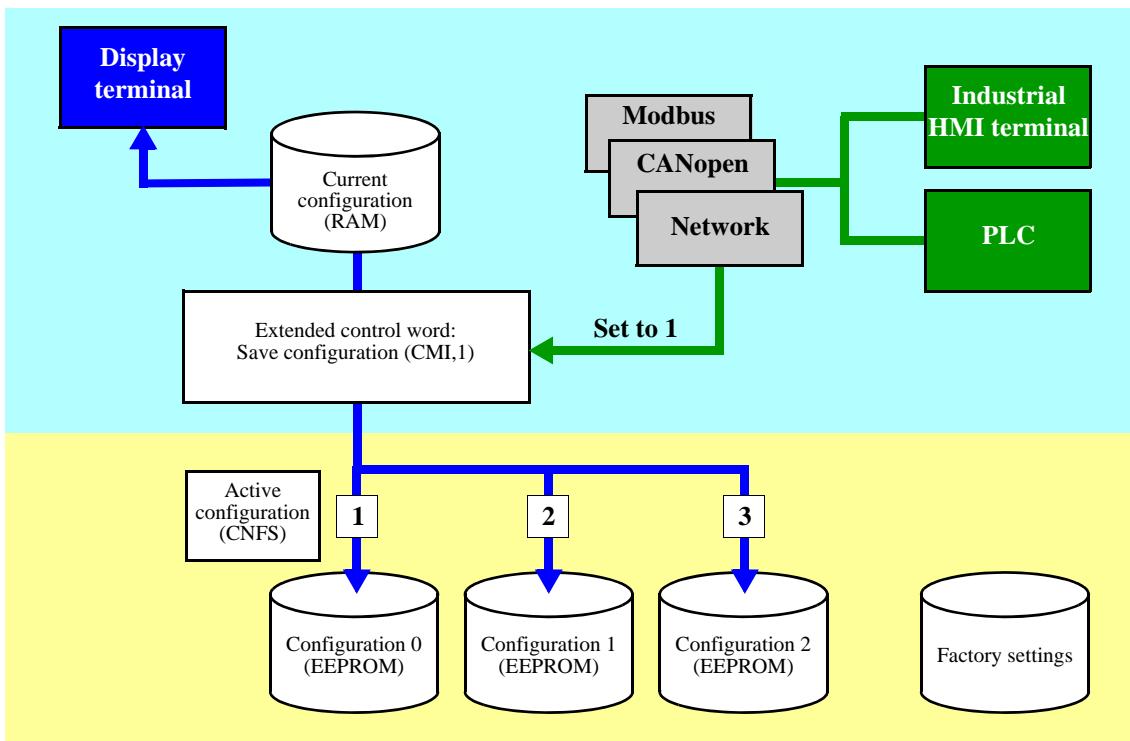
To use this function via a network, simply assign one or two bits of the control word to the motor or configuration switching command via the **[2 Configurations] (CnF1)** and **[3 Configurations] (CnF2)** parameters.

The active configuration can be read in the **[Config. active] (CNFS)** parameter.

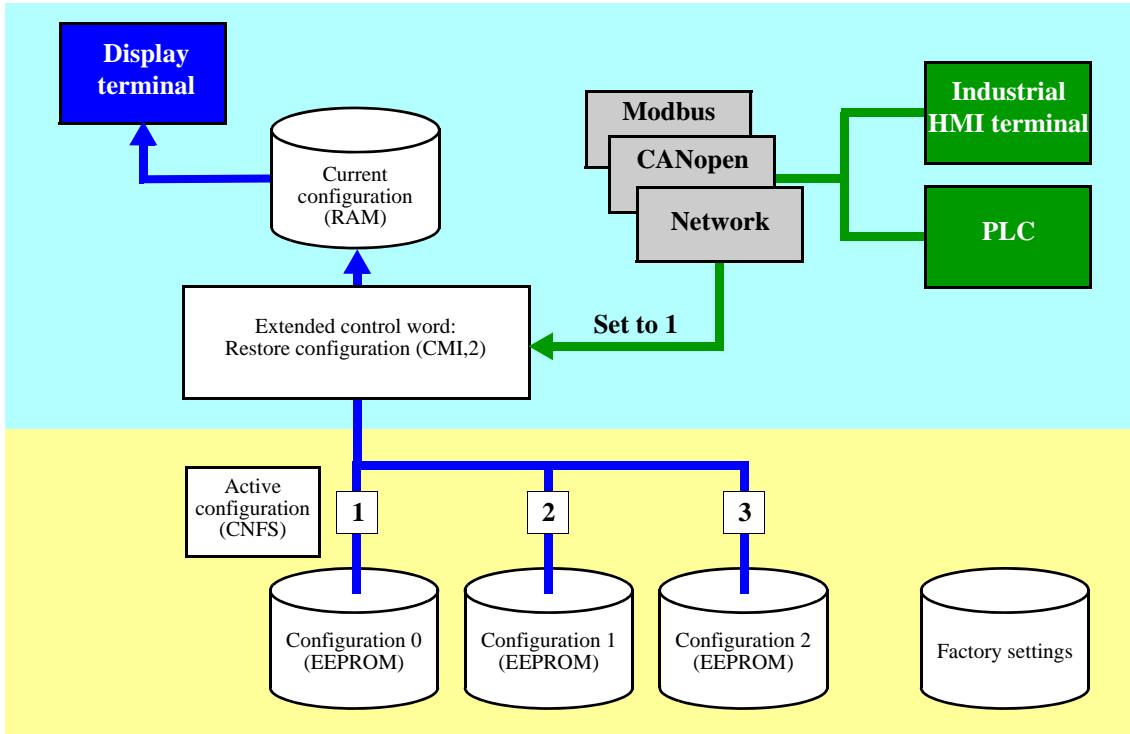


Configuration saving and switching

When the configuration or motor switching function is configured on inputs or on control word bits, to save a configuration that is already active, set bit 1 of the extended control word (**CMI**) to 1.



When the configuration or motor switching function is configured on inputs or on control word bits, bit 2 of the extended control word (**CMI**) must be set to 1.



The configuration switching commands are only active if the drive is stopped, and not in "5-Operation enabled" state.

Configuration saving and switching

Function parameters

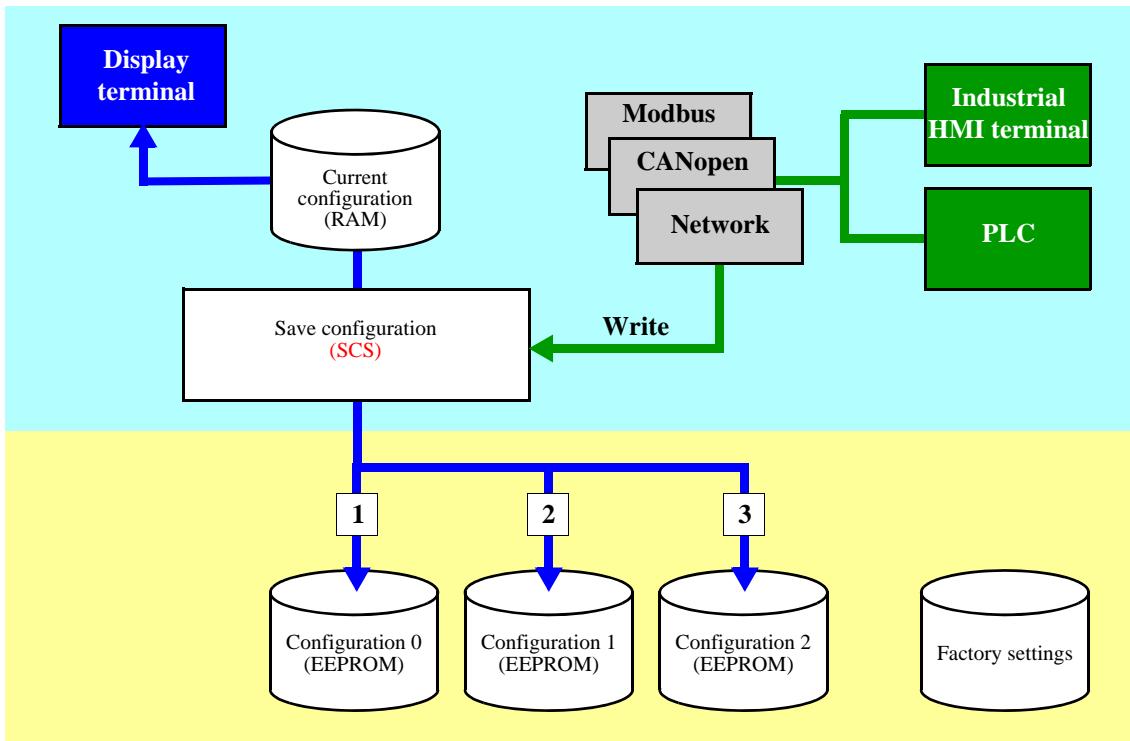
Code	Description		
CNF1	Parameter name: Assignment for 2 configurations Terminal display: [2 Configurations]		
	Logic address: 8021 = 16#1F55	Type:	WORD (listing)
	CANopen index: 2032/16	Read/write:	R/WS
	INTERBUS index: 5FBC/9C	Factory setting:	0
	DeviceNet path: 9C/01/9C		
CNF2	Parameter name: Assignment for 3 configurations Terminal display: [3 Configurations]		
	Logic address: 8022 = 16#1F56	Type:	WORD (listing)
	CANopen index: 2032/17	Read/write:	R/WS
	INTERBUS index: 5FBC/9D	Factory setting:	0
	DeviceNet path: 9C/01/9D		
CNFS	Parameter name: Active configuration Terminal display: [Config. active]		
	Logic address: 8020 = 16#1F54	Type:	WORD (listing)
	CANopen index: 2032/15	Read/write:	R
	INTERBUS index: 5FB9/CD		
	DeviceNet path: 89/01/15		
	0 = The parameter set switching function is not configured 1 = (CNF0) : Configuration no. 0 active 2 = (CNF1) : Configuration no. 1 active 3 = (CNF2) : Configuration no. 2 active		

Value of the control bit assigned by [2 Configurations] (CnF1)	0	1	0	1
Value of the control bit assigned by [3 Configurations] (CnF2)	0	0	1	1
Value of [Config. active] (CnFS)	1	2	3	3
Active configuration	0	1	2	2

Configuration saving and switching

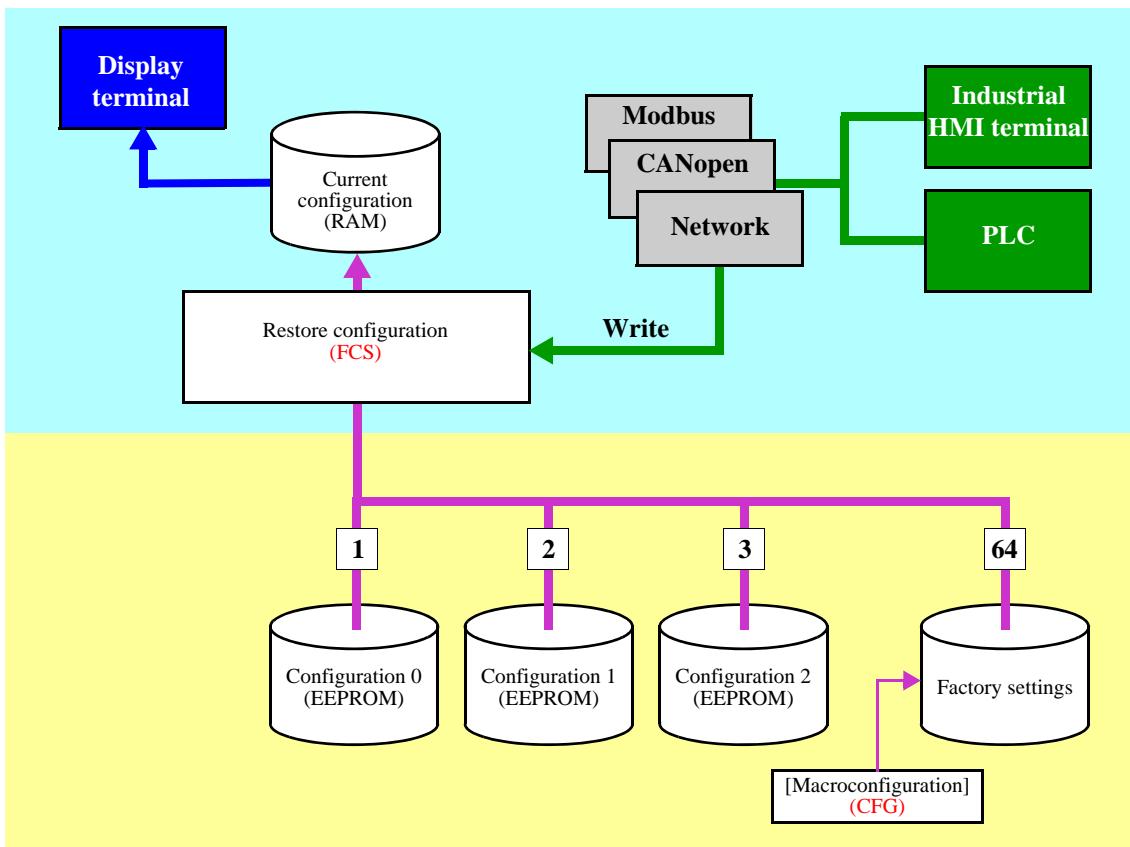
Configuration switching by selection

The current configuration can be saved in one of the 3 non-volatile configurations in EEPROM using the "Save configuration" (SCS) parameter. In this case, it is not necessary to assign a function in the control word.



One of the 3 non-volatile configurations in EEPROM can be restored to the current configuration using the "Restore configuration" (FCS) parameter.

Note: Value 64 controls the return to factory settings.



The configuration switching commands are only active if the drive is stopped, and not in "5-Operation enabled" state.

Configuration saving and switching

Function parameters

Code	Description		
SCS	Parameter name: Save configuration		
	Logic address:	8001 = 16#1F41	Type: WORD (listing)
	CANopen index:	2032/2	Read/write: R
	INTERBUS index:	5FBC/9A	
	DeviceNet path:	9C/01/9A	
	0 = No save 1 = Save to configuration no. 0 2 = Save to configuration no. 1 3 = Save to configuration no. 2		
FCS	Parameter name: Restore configuration		
	Logic address:	8002 = 16#1F42	Type: WORD (listing)
	CANopen index:	2032/3	Read/write: R
	INTERBUS index:	5FBC/9B	
	DeviceNet path:	9C/01/9B	
	0 = No restore 1 = Restore configuration no. 0 2 = Restore configuration no. 1 3 = Restore configuration no. 2 64 = Factory setting		

Parameter set switching

The parameter set switching function (see the programming manual) can be used via the network or via the terminals.

To use this function via a network, simply assign one or two bits of the control word to parameter set switching via the [2 Parameter sets] (CHA1) and [3 Parameter sets] (CHA2) parameters.

The active set can be read in the "Active parameter set" (CFPS) parameter.

The parameter sets can be written via a network.

The parameter sets can be switched with the motor running.

Function parameters

Code	Description			
CHA1	Parameter name: Assignment for 2 sets			
	Terminal display: [2 Parameter sets]			
	Logic address: 8021 = 16#1F55	Type:	WORD (listing)	
	CANopen index: 2032/16	Read/write:	R/W/S	
	INTERBUS index: 5FBC/9C	Factory setting:	0	
CHA2	Logic address: 8022 = 16#1F56	Type:	WORD (listing)	
	CANopen index: 2032/17	Read/write:	R/W/S	
	INTERBUS index: 5FBC/9D	Factory setting:	0	
	DeviceNet path: 9C/01/9C			
	Parameter name: Active parameter set			
CFPS	Logic address: 12900 = 16#3264	Type:	WORD (listing)	
	CANopen index: 2063/01	Read/write:	R	
	INTERBUS index: 5FB9/EC			
	DeviceNet path: A1/01/65			
	0 = The parameter set switching function is not configured 1 = [Set 1 active] (CFP1): Parameter set no. 1 active 2 = [Set 2 active] (CFP2): Parameter set no. 2 active 3 = [Set 3 active] (CFP3): Parameter set no. 3 active			

Value of the control bit assigned by [2 Parameter sets] (CHA1)	0	1	0	1
Value of the control bit assigned by [3 Parameter sets] (CHA2)	0	0	1	1
Value of "Active parameter set" (CFPS)	1	2	3	3
Active parameter set	1	2	3	3

Parameter set switching

Parameter sets can be loaded via the following parameters:

Address table:

No.	Code	Logic address:	CANopen index:	INTERBUS index:	DeviceNet path:
1	AD01	12911 = 16#326F	2063/C	5FBF/8C	9F/01/8C
2	AD02	12912 = 16#3270	2063/D	5FBF/8D	9F/01/8D
3	AD03	12913 = 16#3271	2063/E	5FBF/8E	9F/01/8E
4	AD04	12914 = 16#3272	2063/F	5FBF/8F	9F/01/8F
5	AD05	12915 = 16#3273	2063/10	5FBF/90	9F/01/90
6	AD06	12916 = 16#3274	2063/11	5FBF/91	9F/01/91
7	AD07	12917 = 16#3275	2063/12	5FBF/92	9F/01/92
8	AD08	12918 = 16#3276	2063/13	5FBF/93	9F/01/93
9	AD09	12919 = 16#3277	2063/14	5FBF/94	9F/01/94
10	AD10	12920 = 16#3278	2063/15	5FBF/95	9F/01/95
11	AD11	12921 = 16#3279	2063/16	5FBF/96	9F/01/96
12	AD12	12922 = 16#327A	2063/17	5FBF/97	9F/01/97
13	AD13	12923 = 16#327B	2063/18	5FBF/98	9F/01/98
14	AD14	12924 = 16#327C	2063/19	5FBF/99	9F/01/99
15	AD15	12925 = 16#327D	2063/1A	5FBF/9A	9F/01/9A

Table of values for set no. 1:

No.	Code	Logic address:	CANopen index:	INTERBUS index:	DeviceNet path:
1	S101	12931 = 16#3283	2063/20	5FBF/9B	9F/01/9B
2	S102	12932 = 16#3284	2063/21	5FBF/9C	9F/01/9C
3	S103	12933 = 16#3285	2063/22	5FBF/9D	9F/01/9D
4	S104	12934 = 16#3286	2063/23	5FBF/9E	9F/01/9E
5	S105	12935 = 16#3287	2063/24	5FBF/9F	9F/01/9F
6	S106	12936 = 16#3288	2063/25	5FBF/A0	9F/01/A0
7	S107	12937 = 16#3289	2063/26	5FBF/A1	9F/01/A1
8	S108	12938 = 16#328A	2063/27	5FBF/A2	9F/01/A2
9	S109	12939 = 16#328B	2063/28	5FBF/A3	9F/01/A3
10	S110	12940 = 16#328C	2063/29	5FBF/A4	9F/01/A4
11	S111	12941 = 16#328D	2063/2A	5FBF/A5	9F/01/A5
12	S112	12942 = 16#328E	2063/2B	5FBF/A6	9F/01/A6
13	S113	12943 = 16#328F	2063/2C	5FBF/A7	9F/01/A7
14	S114	12944 = 16#3290	2063/2D	5FBF/A8	9F/01/9F
15	S115	12945 = 16#3291	2063/2E	5FBF/A9	9F/01/A9

Parameter set switching

Table of values for parameter set no. 2:

No.	Code	Logic address:	CANopen index:	INTERBUS index:	DeviceNet path:
1	S201	12951 = 16#3297	2063/34	5FBF/AA	9F/01/AA
2	S202	12952 = 16#3298	2063/35	5FBF/AB	9F/01/AB
3	S203	12953 = 16#3299	2063/36	5FBF/AC	9F/01/AC
4	S204	12954 = 16#329A	2063/37	5FBF/AD	9F/01/AD
5	S205	12955 = 16#329B	2063/38	5FBF/AE	9F/01/AE
6	S206	12956 = 16#329C	2063/39	5FBF/AF	9F/01/AF
7	S207	12957 = 16#329D	2063/3A	5FBF/B0	9F/01/B0
8	S208	12958 = 16#329E	2063/3B	5FBF/B1	9F/01/B1
9	S209	12959 = 16#329F	2063/3C	5FBF/B2	9F/01/B2
10	S210	12960 = 16#32A0	2063/3D	5FBF/B3	9F/01/B3
11	S211	12961 = 16#32A1	2063/3E	5FBF/B4	9F/01/B4
12	S212	12962 = 16#32A2	2063/3F	5FBF/B5	9F/01/B5
13	S213	12963 = 16#32A3	2063/40	5FBF/B6	9F/01/B6
14	S214	12964 = 16#32A4	2063/41	5FBF/B7	9F/01/B7
15	S215	12965 = 16#32A5	2063/42	5FBF/B8	9F/01/B8

Table of values for set no. 3:

No.	Code	Logic address:	CANopen index:	INTERBUS index:	DeviceNet path:
1	S301	12971 = 16#32AB	2063/48	5FBF/B9	9F/01/B9
2	S302	12972 = 16#32AC	2063/49	5FBF/BA	9F/01/BA
3	S303	12973 = 16#32AD	2063/4A	5FBF/BB	9F/01/BB
4	S304	12974 = 16#32AE	2063/4B	5FBF/BC	9F/01/BC
5	S305	12975 = 16#32AF	2063/4C	5FBF/BD	9F/01/BD
6	S306	12976 = 16#32B0	2063/4D	5FBF/BE	9F/01/BE
7	S307	12977 = 16#32B1	2063/4E	5FBF/BF	9F/01/BF
8	S308	12978 = 16#32B2	2063/4F	5FBF/C0	9F/01/C0
9	S309	12979 = 16#32B3	2063/50	5FBF/C1	9F/01/C1
10	S310	12980 = 16#32B4	2063/51	5FBF/C2	9F/01/C2
11	S311	12981 = 16#32B5	2063/52	5FBF/C3	9F/01/C3
12	S312	12982 = 16#32B6	2063/53	5FBF/C4	9F/01/C4
13	S313	12983 = 16#32B7	2063/54	5FBF/C5	9F/01/C5
14	S314	12984 = 16#32B8	2063/55	5FBF/C6	9F/01/C6
15	S315	12985 = 16#32B9	2063/56	5FBF/C7	9F/01/C7

Parameter set switching

Code	Description		
VAL	Load parameter set command		
Logic address:	12901 = 16#3265	Type:	WORD (listing)
CANopen index:	2063/02	Read/write:	R
INTERBUS index:	5FB9/ED		
DeviceNet path:	A1/01/66		
	0 = Function not used or a new set of parameters has been taken into account 1 = Request to write a new set of parameters 2 = A new set of parameters is being written		
Procedure:	<ul style="list-style-type: none">• Write the addresses and values of the sets.• Set VAL to 1.• Once the new sets have been taken into account, the drive resets (VAL) to 0.		

Control parameters

Code	Description		
CMD	Parameter name: Control word Terminal display: [Cmd value] DSP402 name: controlword DRIVECOM name: Control word		
Logic address:	8601 = 16#2199	Type:	WORD (bit register)
CANopen index:	6040	Read/write:	R/W
INTERBUS index:	6040		
DeviceNet path:	B7/01/01		
Parameter conforming to DSP402 profile			
Possible values in IO profile			
On state command [2 wire] (2C) bit 0: Forward (on state) command = 0: No forward command = 1: Forward command	The assignment of bit 0 cannot be modified. It corresponds to the assignment of the terminals. It can be switched. Bit 0 (Cd00) is only active if the channel of this control word is active. Bits 1 to 15 can be assigned to commands.	On edge command [3 wire] (3C) bit 0: Stop (run authorization) = 0: Stop = 1: Run is authorized, on a forward or reverse command. bit 1: Forward (on 0 → 1 rising edge) command	The assignment of bits 0 and 1 cannot be modified. It corresponds to the assignment of the terminals. It can be switched. Bits 0 (Cd00) and 1 (Cd01) are only active if the channel of this control word is active. Bits 2 to 15 can be assigned to commands.
For example, to change the direction of operation using bit 2 of the control word of the active channel, simply configure the [Reverse assign.] (rS) parameter: <ul style="list-style-type: none">• to value [C102] (C102) ... [C402] (C402) for a fixed assignment• to value [CD02] (Cd02) for a switched assignment			
The stop commands: <ul style="list-style-type: none">• Freewheel stop [Freewheel stop ass.] (nSt)• Fast stop [Fast stop assign.] (FSt) are active at value 0, in the same way as on the terminals. = 0: Stop = 1: No stop command			
DC injection braking [DC injection assign.] (dCl) is active at value 1, in the same way as on the terminals. = 0: No braking command = 1: Braking			
If a fixed assignment is made [C101] (C101) to [C115] (C115) ... [C401] (C401) to [C415] (C415) , the freewheel stop, fast stop and DC injection braking commands are always active, even if the channel is not active. If these commands are configured as fixed assignments, the following settings must be made in order to start, even if another channel is active: <ul style="list-style-type: none">• Freewheel stop = 1• Fast stop = 1• DC injection braking = 0			
If a switched assignment is made [CD00] (Cd00) to [Cd15] (Cd15) the freewheel stop, fast stop and DC injection braking commands are only active if the channel is active.			

Control parameters

Code	Description
	<p>Possible values in DSP402 profile, separate or not separate mode:</p> <p>bit 0: "Switch on"/Contactor command bit 1: "Disable voltage"/Authorization to supply AC power bit 2: "Quick stop"/Emergency stop bit 3: "Enable operation"/Run command</p> <p>bit 4: Reserved (set to 0) bit 5: Reserved (set to 0) bit 6: Reserved (set to 0) bit 7: "Fault reset"/Fault acknowledgment active on 0 → 1 rising edge</p> <p>bit 8: "Halt"/Halt bit 9: Reserved (set to 0) bit 10: Reserved (set to 0) bit 11: Direction of rotation command = 0: Forward rotation = 1: Reverse rotation. Default value, this bit can be assigned to another command.</p> <p>bit 12: Can be assigned to a command. bit 13: Can be assigned to a command. bit 14: Can be assigned to a command. bit 15: Can be assigned to a command.</p> <p>For the description of bits 0, 1, 2, 3, 7, and 8, see the "DSP402 profile" section.</p> <p>The DSP402 standard enables the drive manufacturer to use bits 11 to 15 in a specific way. On the Altivar 71, they can be assigned to function commands. Bit 11 is assigned by default to control the direction of rotation, although it can be assigned to another command. A new assignment deletes the assignment to the direction of rotation command. Bits 12 to 15 have no default assignment. For example, to control DC injection braking using bit 12 of the Modbus control word, simply set the [DC injection assign.] (dCl) parameter to value [C212] (C212).</p> <p>The fast stop command configured by [Fast stop assign.] (FSt) is active at 1: = 0: No stop command = 1: Stop</p> <p>The DC injection command configured by [DC injection assign.] (dCl) is active at 1: = 0: No braking command = 1: Braking</p> <p>With a fixed assignment ([C1••], [C2••], [C3••] or [C4••]), the fast stop and DC injection braking commands are priority stops, even if the channel is not active. If these commands are configured as fixed assignments, the following settings must be made in order to start, even if another channel is active:</p> <ul style="list-style-type: none"> • Fast stop command = 0 • DC injection braking command = 0 <p>With a switched assignment ([Cd••]), the fast stop and DC injection braking commands are only operational if the channel is active.</p> <p>The freewheel stop [Freewheel stop ass.] (nSt) command cannot be assigned in DSP402 profile.</p>

Control parameters

Code	Description		
CMI	Parameter name: Extended control word Logic address: 8504 = 16#2138 Type: WORD (bit register) CANopen index: 2037/5 Read/write: R/W INTERBUS index: 5FB6/1E DeviceNet path: 8B/01/69		
	bit 0: Factory setting command (active at 1). bit 1: Save configuration to EEPROM non-volatile memory command (active at 1). This bit automatically changes to 0 after the request is taken into account. The command is only active if the drive is stopped, and not in "5-Operation enabled" state. Note: If CMI is a periodic network variable, the PLC program must write it to 0 after the first request is taken into account. The life of the EEPROM memory is limited to 100,000 write operations. Note: If the motor or configuration switching function is active, the configuration in the RAM is saved to the EEPROM in the configuration designated by [Config. Active] (CnFS) . bit 2: Restore configuration to EEPROM non-volatile memory command (active on 0 to 1 rising edge). This bit automatically changes to 0 after the request is taken into account. The command is only active if the drive is stopped, and not in "5-Operation enabled" state. Note: If CMI is a periodic network variable, the PLC program must write it to 0 after the first request is taken into account. This does not adversely affect the life of the EEPROM memory, but permanently copies the configuration in the memory to the current configuration. Note: If the motor or configuration switching function is active, the configuration in the EEPROM designated by [Config. Active] (CnFS) is copied to the RAM. bit 3: Reserved (= 0) bit 4: Reserved (= 0) bit 5: Reserved (= 0) bit 6: Reserved (= 0) bit 7: Reserved (= 0) bit 8: Reserved (= 0) bit 9: Definition of the frequency reference (LFr) and output frequency (rFr) unit: = 0: 0.1 Hz = 1: Standardized value 16 signed bits based on the maximum frequency. The value 32767 corresponds to [Max frequency] (tFr) . The default value of [Max frequency] (tFr) is 60 Hz, and the resolution is then approximately 0.0018 Hz. This function has no effect on the speed reference (LFrD) or the output speed (rFrD). bit 10: Fast stop command (active at 1) bit 11: DC injection braking command (active at 1) bit 12: Direction of rotation command = 0: Forward = 1: Reverse bit 13: Reserved (= 0) bit 14: Reserved (= 0) bit 15: Parameter consistency check = 0: The check is activated. Each time a parameter is written, the drive checks the relationship between the written parameter and the configuration in the drive. For example, the [High speed] (HSP) parameter must be less than the [Max frequency] (tFr) parameter. If an attempt is made to write a value greater than the [Max frequency] (tFr) parameter to the [High speed] (HSP) parameter, the write operation is accepted, but the value is limited to that of [Max frequency] (tFr) . = 1: The check is deactivated. The drive is locked in stop mode. In this drive state, the configuration can be written parameter by parameter and the drive does not modify the values that are written. 1→0: The switch from 1 to 0 triggers a calculation of the consistency of the configuration. Some parameters can be modified automatically by the drive.		

Setpoint parameters

See section "Assignment of setpoints from a network", page [59](#)

Code	Description		
LFRD	Parameter name: Speed reference DSP402 name: vI target velocity DRIVECOM name: Speed-Setpoint		
	Logic address: 8602 = 16#219A CANopen index: 6042 INTERBUS index: 6042 DeviceNet path: 2A/01/08	Type: Read/write: Unit:	INT R/W rpm
	Parameter conforming to DSP402 and ODVA profiles. Signed value.		
LFR	Parameter name: Frequency reference Terminal display: [Frequency ref.]		
	Logic address: 8502 = 16#2136 CANopen index: 2037/3 INTERBUS index: 5FB6/1C DeviceNet path: 8B/01/67	Type: Read/write: Unit:	INT R/W 0.1 Hz or standardized 16 signed bits based on the maximum frequency (TFr)
	Signed value. The unit depends on the value of bit 9 of the extended control word: = 0: 0.1 Hz = 1: Standardized value on 16 signed bits at maximum frequency. The value 32767 corresponds to [Max frequency] (tFr). The default value of [Max frequency] (tFr) is 60 Hz, and the resolution is then approximately 0.0018 Hz.		
LTR	Parameter name: Torque reference Terminal display: [HMI torque ref.] DSP402 name: Target torque DRIVECOM name: Torque-Setpoint-External		
	Logic address: 8505 = 16#2139 CANopen index: 6071 INTERBUS index: 6071 DeviceNet path: 8B/01/6A	Type: Read/write: Unit:	INT R/W 0.001 "Nominal motor torque"
	Parameter conforming to DSP402 profile Signed value. The "Nominal motor torque" is only accessible as a drive parameter. It is the result of a calculation on the other characteristics.		
LtCr	Parameter name: Torque reference (Nm)		
	Logic address: 9261 = 16#242D CANopen index: 203E/3E INTERBUS index: 5FB6/3D DeviceNet path: 2A/01/0C	Type: Read/write: Unit:	INT R/W According to INT

Setpoint parameters

Code	Description		
PISP	Parameter name: PID regulator reference Logic address: 8503 = 16#2137 CANopen index: 2037/4 INTERBUS index: 5FB6/1D DeviceNet path: 8B/01/68		
	Signed value. If the PID regulator is to be controlled via a network, this reference must be written, in accordance with the protocol: <ul style="list-style-type: none"> • Either via messaging • Or by assigning this parameter in the periodic variables 		
AIU1	Parameter name: PID regulator feedback Logic address: 5281 = 16 #14A1 CANopen index: 2016/52 INTERBUS index: 5FB9/40 DeviceNet path: 7B/01/52		
MFr	Parameter name: Multiplying coefficient Terminal display: [Multiplying coeff.] Logic address: 11831 = 16#2E37 CANopen index: 2058/20 INTERBUS index: 5FB6/3E DeviceNet path: 9C/01/20		

Status parameters

Code	Description		
ETA	Parameter name: Status word DSP402 name: Statusword DRIVECOM name: Statusword		
Logic address:	8603 = 16#219B	Type:	WORD (bit register)
CANopen index:	6041	Read/write:	R
INTERBUS index:	6041		
DeviceNet path:	71/01/02		
Parameter conforming to DSP402 profile			
Possible values in the IO profile			
Note: The value is identical in DSP402 and I/O profiles. In I/O profile, the description of the values is simplified and does not refer to the DSP402 (Drivecom) state chart.			
bit 0: Reserved (= 0 or 1) bit 1: Ready = 0: Not ready = 1: Ready bit 2: Running = 0: The drive will not start if a reference other than zero is applied. = 1: Running, if a reference other than zero is applied, the drive can start. bit 3: Fault = 0: No fault = 1: Fault			
bit 4: Power section line supply present = 0: Power section line supply absent = 1: Power section line supply present bit 5: Reserved (= 1) bit 6: Reserved (= 0 or 1) bit 7: Alarm = 0: No alarm = 1: Alarm			
bit 8: Reserved (= 0). bit 9: Command or reference via the network = 0: Command or reference via the terminals = 1: Command or reference via the network bit 10: Reference reached = 0: The reference is not reached = 1: The reference has been reached bit 11: Reference outside limits = 0: The reference is within the limits = 1: The reference is not within the limits When the drive is in speed mode, the limits are defined by the "Low speed (LSP)" and "High speed (HSP)" parameters. When the torque function is activated, refer to the description of this function (see the programming manual).			
bit 12: Reserved (= 0) bit 13: Reserved (= 0) bit 14: Stop via STOP key = 0: STOP key not pressed = 1: Stop triggered by the STOP key on the graphic display terminal bit 15: Direction of rotation = 0: Forward rotation at output = 1: Reverse rotation at output			

Status parameters

Code	Description
	<p>Possible values in DSP402 profile</p> <p>bit 0: "Ready to switch on", power section line supply pending bit 1: "Switched on", ready bit 2: "Operation enabled", running bit 3: "Fault" = 0: No fault = 1: Fault</p> <p>bit 4: "Voltage enabled", power section line supply present = 0: Power section line supply absent = 1: Power section line supply present When the drive is powered by the power section only, this bit is always at 1.</p> <p>bit 5: "Quick stop"/Emergency stop</p> <p>bit 6: "Switched on disabled", power section line supply locked.</p> <p>bit 7: "Warning", alarm = 0: No alarm = 1: Alarm</p> <p>bit 8: Reserved (= 0)</p> <p>bit 9: "Remote", command or reference via the network = 0: Command or reference via the terminals = 1: Command or reference via the network</p> <p>bit 10: "Target reached", reference reached = 0: The reference is not reached = 1: The reference has been reached When the drive is in speed mode, this is the speed reference. When the torque function is activated, refer to the description of this function (see the programming manual). When the drive stops, the reference has been reached.</p> <p>bit 11: "Internal limit active", reference outside limits = 0: The reference is within the limits = 1: The reference is not within the limits When the drive is in speed mode, the limits are defined by the "Low speed (LSP)" and "High speed (HSP)" parameters. When the torque function is activated, refer to the description of this function (see the programming manual).</p> <p>bit 12: Reserved (= 0)</p> <p>bit 13: Reserved (= 0)</p> <p>bit 14: "Stop key", STOP via stop key = 0: STOP key not pressed = 1: Stop triggered by the STOP key on the graphic display terminal</p> <p>bit 15: "Direction", direction of rotation = 0: Forward rotation at output = 1: Reverse rotation at output</p> <p>The combination of bits 0, 1, 2, 4, 5, and 6 defines the state in the DSP 402 state chart (see the DSP402 profile section).</p>

Status parameters

Code	Description		
ETI	<p>Parameter name: Extended status word 0</p> <p>Logic address: 3206 = 16#0C86</p> <p>CANopen index: 2002/7</p> <p>INTERBUS index: 5FB9/08</p> <p>DeviceNet path: 71/01/07</p>		
	bit 0: Access to the EEPROM non-volatile memory in progress		
	bit 1: = 0: No parameter consistency check		
	= 1: Parameter consistency check		
	bit 2: = 0: The drive is not in fault state or a fault is present		
	= 1: The drive is in fault state but the fault is no longer present (not reset)		
	bit 3: Reserved (= 0)		
	bit 4: Power section line supply present		
	= 0: No run command or run command cancelled by a higher priority command		
	= 1: Run command effective		
	bit 5: = 1: DC injection braking (identical to LSR4, bit 11)		
	bit 6: = 0: Drive in steady state		
	= 1: Drive in transient state		
	bit 7: = 1: Motor thermal state threshold reached for the active motor		
	bit 8: = 1: Overbraking (identical to LSR5, bit 1)		
	bit 9: = 1: Acceleration in progress (identical to LSR4, bit 13)		
	bit 10: = 1: Deceleration in progress (identical to LSR4, bit 14)		
	bit 11: = 1: Current or torque limiting in progress		
	bit 12: = 1: Fast stop in progress (identical to LSR4, bit 15)		
	bit 13: bit 13 = 0 and bit 14 = 0: Command via the terminals		
	bit 14: bit 13 = 1 and bit 14 = 0: Command via the graphic display terminal		
	bit 13 = 0 and bit 14 = 1: Command via Modbus		
	bit 13 = 1 and bit 14 = 1: Command via CANopen, the network card or the "Controller Inside" card		
	bit 15: = 0: Forward operation applied before the ramp		
	= 1: Reverse operation applied before the ramp		
LRS1	<p>Parameter name: Extended status word 1</p> <p>Logic address: 3250 = 16#0CB2</p> <p>CANopen index: 2002/33</p> <p>INTERBUS index: 5FB9/1C</p> <p>DeviceNet path: 71/01/33</p>		
	bit 0: Reserved (= 0)		
	bit 1: = 1: The drive is in fault state.		
	bit 2: = 0: The drive is locked, the motor is not powered		
	= 1: The drive is unlocked, power can be supplied to the motor (RUN state)		
	bit 3: = 1: The output contactor is controlled		
	bit 4: = 1: Frequency threshold (ftd) reached: [Freq. Th. attained] (FtA)		
	bit 5: = 1: High speed (HSP) reached: [HSP attained] (FLA)		
	bit 6: = 1: Current threshold (Ctd) reached: [Current Th. attained] (CtA)		
	bit 7: = 1: Frequency reference reached: [Frequency ref. att.] (SrA)		
	bit 8: = 1: Motor 1 thermal state threshold [Motor therm. level] (ttd) reached: [Motor th. state att.] (tSA)		
	bit 9: = 1: Brake contactor command [Brake assignment] (bLC) active		
	bit 10: = 1: PID regulator error alarm: [PID error al.] (PEE)		
	bit 11: = 1: PID regulator feedback alarm: [PID fdbk al.] (PFA)		
	bit 12: = 1: 4-20 mA alarm on analog input AI2: [AI2 4-20 mA loss] (LFF2)		
	bit 13: = 1: Second frequency threshold (ftd) reached: [Freq. Th. 2 attained] (FA2)		
	bit 14: = 1: Drive thermal state threshold [Drv therm. state al.] (tHA) reached: [Th. drv. att.] (tAd)		
	bit 15: = 1: The "traverse control" function is active		

Status parameters

Code	Description		
LRS2	Parameter name: Extended status word 2		
	Logic address: 3251 = 16#0CB3	Type:	WORD (bit register)
	CANopen index: 2002/34	Read/write:	R
	INTERBUS index: 5FB9/1D		
	DeviceNet path: 71/01/34		
	bit 0: Reserved (= 0)		
	bit 1: Reserved (= 0)		
	bit 2: Reserved (= 0)		
	bit 3: Reserved (= 0)		
	bit 4: Reserved (= 0)		
	bit 5: Reserved (= 0)		
	bit 6: Reserved (= 0)		
	bit 7: Reserved (= 0)		
	bit 8: Reserved (= 0)		
	bit 9: Reserved (= 0)		
	bit 10: Reserved (= 0)		
	bit 11: Reserved (= 0)		
	bit 12: Reserved (= 0)		
	bit 13: Reserved (= 0)		
	bit 14: Reserved (= 0)		
	bit 15: Reserved (= 0)		
LRS3	Parameter name: Extended status word 3		
	Logic address: 3252 = 16#0CB4	Type:	WORD (bit register)
	CANopen index: 2002/35	Read/write:	R
	INTERBUS index: 5FB9/1E		
	DeviceNet path: 71/01/35		
	bit 0: = 0: Reference channel 1 or 1B (Fr1) or (Fr1b) is active = 1: Reference channel 2 (Fr2) is active		
	bit 1: = 0: Command channel 1 (Cd1) is active = 1: Command channel 2 (Cd2) is active		
	bit 2: = 0: Ramp set 1 (ACC) and (dEC) = 1: Ramp set 2 (AC2) and (dE2)		
	bit 3: = 0: Current limit 1 (CLI) is active = 1: Current limit 2 (CL2) is active		
	bit 4: Reserved (= 0)		
	bit 5: = 1: Motor 2 thermal state threshold [Motor2 therm. level] (ttd2) reached: [TTh.mot2 att] (tS2)		
	bit 6: = 1: Motor 3 thermal state threshold [Motor3 therm. level] (ttd3) reached: [TTh.mot3 att] (tS3)		
	bit 7: Reserved (= 0)		
	bit 8: 24 VDC external power supply present		
	bit 9: = 1: Stop on low speed time limit function [Low speed time out] (tLS)		
	bit 10: Reserved (= 0)		
	bit 11: Reserved (= 0)		
	bit 12: Reserved (= 0)		
	bit 13: Reserved (= 0)		
	bit 14: Reserved (= 0)		
	bit 15: = 0: The output torque is positive (forward) = 1: The output torque is negative (reverse)		

Status parameters

Code	Description		
LRS4	Parameter name: Extended status word 4		
	Logic address: 3253 = 16#0CB5	Type:	WORD (bit register)
	CANopen index: 2002/36	Read/write:	R
	INTERBUS index: 5FB9/1F		
	DeviceNet path: 71/01/36		
	bit 0: = 1: Configuration 0 is active bit 1: = 1: Configuration 1 is active [Cnfg.1 act.] (CnF1) bit 2: = 1: Configuration 2 is active [Cnfg.2 act.] (CnF2) bit 3: Reserved (= 0)		
	bit 4: = 1: Parameter set 1 is active: [Set 1 active] (CFP1) bit 5: = 1: Parameter set 2 is active: [Set 2 active] (CFP2) bit 6: = 1: Parameter set 3 is active: [Set 3 active] (CFP3) bit 7: Reserved (= 0)		
	bit 8: = 0: Power section line supply present = 1: Power section line supply absent		
	bit 9: = 1: Motor "fluxing" in progress: [In motor fluxing] (FLX)		
	bit 10: = 1: The motor is "fluxed"		
	bit 11: = 1: DC injection braking (identical to ETI, bit 5)		
	bit 12: = 1: Current limiting in progress		
	bit 13: = 1: Acceleration in progress (identical to ETI, bit 9)		
	bit 14: = 1: Deceleration in progress (identical to ETI, bit 10)		
	bit 15: = 1: Fast stop in progress: [Fast stop in prog.] (FSt) (identical to ETI, bit 12)		
LRS5	Parameter name: Extended status word 5		
	Logic address: 3254 = 16#0CB6	Type:	WORD (bit register)
	CANopen index: 2002/37	Read/write:	R
	INTERBUS index: 5FB9/20		
	DeviceNet path: 71/01/37		
	bit 0: = 1: Drive DC bus loading: [DC bus loading] (dbL) bit 1: = 1: Drive braking [In braking] (brS) bit 2: = 1: The "Power removal" function is active bit 3: = 1: Automatic restart attempts in progress: [Auto restart] (AuToO)		
	bit 4: = 1: "Auto-tuning" in progress: [Auto-tuning] (tUn) bit 5: = 1: Controlled stop in progress following loss of power section line supply (CTL) bit 6: = 1: The drive cannot follow the configured deceleration ramp, deceleration automatically adapted (OBR) bit 7: = 1: Controlled output cut in progress (SOC)		
	bit 8: Reserved (= 0) bit 9: = 1: The line contactor is active bit 10: Reserved (= 0 or 1) bit 11: Reserved (= 0 or 1)		
	bit 12: Reserved (= 0 or 1) bit 13: = 1: Current present in the motor (MCP) bit 14: = 1: If the "limit switch management" [LIMIT SWITCHES] function is activated. The [Stop FW limit sw.] or [Stop RV limit sw.] stops are reached. bit 15: Reserved (= 0)		

Status parameters

Code	Description		
LRS6	Parameter name: Extended status word 6		
	Logic address: 3255 = 16#0CB7	Type:	WORD (bit register)
	CANopen index: 2002/38	Read/write:	R
	INTERBUS index: 5FB9/21		
	DeviceNet path: 71/01/38		
	<p>bit 0: = 1: Alarm group 1 is active bit 1: = 1: Alarm group 2 is active bit 2: = 1: Alarm group 3 is active bit 3: = 1: Probe 1 alarm: [PTC1 alarm] (PtC1)</p> <p>bit 4: = 1: Probe 2 alarm: [PTC2 alarm] (PtC2) bit 5: = 1: Li6 PTC probe alarm: [Li6 =PTC alarm] (PtC3) bit 6: Reserved (= 0) bit 7: = 1: External fault [External fault alarm] (EtF)</p> <p>bit 8: = 1: Undervoltage alarm [Undervoltage] (USA) bit 9: = 1: The power section line supply loss detection threshold for a controlled stop has been reached (undervoltage warning). bit 10: = 1: Slipping alarm: [Load slipping] (AnA) bit 11: = 1: Drive overheat alarm (tHA)</p> <p>bit 12: Reserved (= 0) bit 13: = 1: Speed alarm in the brake control sequence (BSA) bit 14: = 1: Brake contact alarm in the brake control sequence (BCA) bit 15: = 1: Current or torque limit alarm after time-out [Trq/I limit. time out] (StO)</p>		
	Parameter name: Extended status word 7		
	Logic address: 3256 = 16#0CB8	Type:	WORD (bit register)
	CANopen index: 2002/39	Read/write:	R
	INTERBUS index: 5FB9/22		
	DeviceNet path: 71/01/39		
	<p>bit 0: = 1: Reference channel 1 or 1B (Fr1) or (Fr1b) is active bit 1: = 1: Reference channel 2 (Fr2) is active bit 2: = 1: Command channel 1 (Cd1) is active bit 3: = 1: Command channel 2 (Cd2) is active</p> <p>bit 4: = 1: Reference channel 1B (Fr1b) is active bit 5: = 1: Spool end ("traverse control" function) bit 6: = 1: Master-slave synchronization ("traverse control" function) bit 7: = 1: Torque regulation alarm</p> <p>bit 8: = 1: IGBT thermal state alarm bit 9: = 1: Braking resistor overload alarm bit 10: = 1: Alarm sent by the "Controller Inside" card bit 11: = 1: 4-20 mA alarm on analog input AI3: [AI3 4-20 mA loss] (LFF3)</p> <p>bit 12: = 1: 4-20 mA alarm on analog input AI4: [AI4 4-20 mA loss] (LFF4) bit 13: DC bus precharging contactor controlled (DC0) bit 14: Reserved (= 0) bit 15: Reserved (= 0)</p>		

Status parameters

Code	Description		
LRS8	Parameter name: Extended status word 8 Logic address: 3257 = 16#0C89 CANopen index: 2002/3A INTERBUS index: 5FB9/23 DeviceNet path: 71/01/3A		
	bit 0: Reserved (= 0)	Type:	WORD (bit register)
	bit 1: Reserved (= 0)	Read/write:	R
	bit 2: Reserved (= 0)		
	bit 3: Reserved (= 0)		
	bit 4: Reserved (= 0)		
	bit 5: Reserved (= 0)		
	bit 6: Reserved (= 0)		
	bit 7: Reserved (= 0)		
	bit 8: Reserved (= 0)		
	bit 9: Reserved (= 0)		
	bit 10: Reserved (= 0)		
	bit 11: Reserved (= 0)		
	bit 12: Reserved (= 0)		
	bit 13: Reserved (= 0)		
	bit 14: Reserved (= 0)		
	bit 15: = 1: Drive ready(rdY)		
CRC	Parameter name: Active reference channel Logic address: 8441 = 16#20F9 CANopen index: 2036/2A INTERBUS index: 5FB9/CE DeviceNet path: 8B/01/2A		
	bit 0: = 1: The terminals are involved in the reference via an analog input.	Type:	WORD (bit register)
	bit 1: Reserved (= 0)	Read/write:	R
	bit 2: = 1: The graphic display terminal is involved in the reference.		
	bit 3: = 1: Modbus is involved in the reference.		
	bit 4: Reserved (= 0)		
	bit 5: Reserved (= 0)		
	bit 6: = 1: CANopen is involved in the reference.		
	bit 7: = 1: The terminals are involved in the +/- speed reference.		
	bit 8: Reserved (= 0)		
	bit 9: = 1: The network card is involved in the reference.		
	bit 10: = 1: The "Controller Inside" card is involved in the reference.		
	bit 11: Reserved (= 0)		
	bit 12: Reserved (= 0)		
	bit 13: Reserved (= 0)		
	bit 14: Reserved (= 0)		
	bit 15: = 1: The PowerSuite software workshop is involved in the reference.		

Status parameters

Code	Description		
CCC	Parameter name: Active command channel		
	Logic address: 8442 = 16#20FA CANopen index: 2036/2B INTERBUS index: 5FB9/CF DeviceNet path: 8B/01/2B	Type:	WORD (bit register)
	Read/write: R bit 0: = 1: The terminals are involved in control. bit 1: Reserved (= 0) bit 2: = 1: The graphic display terminal is involved in control. bit 3: = 1: Modbus is involved in control. bit 4: Reserved (= 0) bit 5: Reserved (= 0) bit 6: = 1: CANopen is involved in control. bit 7: Reserved (= 0) bit 8: Reserved (= 0) bit 9: = 1: The network card is involved in control. bit 10: = 1: The "Controller Inside" card is involved in control. bit 11: Reserved (= 0) bit 12: Reserved (= 0) bit 13: Reserved (= 0) bit 14: Reserved (= 0) bit 15: = 1: The PowerSuite software workshop is involved in control.		
CFPS	Parameter name: Active parameter set		
	Logic address: 12900 = 16#3264 CANopen index: 2063/01 INTERBUS index: 5FB9/EC DeviceNet path: A1/01/65	Type:	WORD (listing)
	Read/write: R 0: The parameter set switching function is not configured. 1 = [Set 1 active] (CFP1): Parameter set no. 1 active 2 = [Set 2 active] (CFP2): Parameter set no. 2 active 3 = [Set 3 active] (CFP3): Parameter set no. 3 active		
CNFS	Parameter name: Active configuration Terminal display: [Config. Active]		
	Logic address: 8020 = 16#1F54 CANopen index: 2032/15 INTERBUS index: 5FB9/CD DeviceNet path: 89/01/15	Type:	WORD (listing)
	Read/write: R 0: The motor or configuration switching function is not configured. 1 = (CNF0): Configuration no. 0 active 2 = (CNF1): Configuration no. 1 active 3 = (CNF2): Configuration no. 2 active		

Output value parameters

Output values (speed)

Code	Description		
rFrd	Parameter name: Output speed DSP402 name: vl control effort DRIVECOM name: Speed-Actual-Value		
	Logic address: 8604 = 16#219C CANopen index: 6044 INTERBUS index: 6044 DeviceNet path: 2A/01/07	Type: INT Read/write: R Unit: rpm	
	Parameter conforming to DSP402 and ODVA profiles. Signed value. If the drive is in open-loop mode, the speed value is estimated. If the drive is in closed-loop mode, the speed value is measured on the sensor. This parameter is linked to the "Output frequency" (rFr) parameter for which the unit is 0.1 Hz.		
rFr	Parameter name: Output frequency Terminal display: [Output frequency] Logic address: 3202 = 16#C82 CANopen index: 2002/3 INTERBUS index: 5FB9/04 DeviceNet path: 71/01/03	Type: INT Read/write: R Unit: 0.1 Hz or standardized 16 signed bits based on the maximum frequency (TFR)	
	Signed value. The unit depends on the value of bit 9 of the extended control word: = 0: 0.1 Hz. = 1: Standardized value on 16 signed bits at maximum frequency. The value 32767 corresponds to [Max frequency] (TFR) . The default value of [Max frequency] (TFR) is 60 Hz, and the resolution is then approximately 0.0018 Hz.		

Output values (torque)

Code	Description		
Otr	Parameter name: Output torque Terminal display: [Motor torque] DSP402 name: Torque actual value DRIVECOM name: Torque-Actual-Value		
	Logic address: 3205 = 16#0C85 CANopen index: 6077 INTERBUS index: 6077 DeviceNet path: 71/01/06	Type: INT Read/write: R Unit: 0.01 "Nominal motor torque"	
	Parameter conforming to DSP402 profile Signed value. The "Nominal motor torque" is only accessible as a drive parameter. It is the result of the other characteristics.		

Output value parameters

Code	Description		
Otrn	Parameter name: Output torque (Nm)		
Logic address:	3216 = 16#0C90	Type:	INT
CANopen index:	2002/11	Read/write:	R
INTERBUS index:	5FB9/10	Unit:	According to INT
DeviceNet path:	2A/01/0B		

Output values (motor)

Code	Description		
LCr	Parameter name: Motor current Terminal display: [Motor current]		
Logic address:	3204 = 16#0C84	Type:	UINT
CANopen index:	2002/5	Read/write:	R
INTERBUS index:	5FB9/06	Unit:	0.1 A
DeviceNet path:	2A/01/09		
Parameter conforming to ODVA profile			
UOP	Parameter name: Motor voltage Terminal display: [Motor voltage]		
Logic address:	3208 = 16#0C88	Type:	UINT
CANopen index:	2002/9	Read/write:	R
INTERBUS index:	5FB9/0A	Unit:	1 V
DeviceNet path:	71/01/09		
OPr	Parameter name: Motor power Terminal display: [Motor power]		
Logic address:	3211 = 16#0C8B	Type:	INT
CANopen index:	2002/C	Read/write:	R
INTERBUS index:	5FB9/0C	Unit:	1%
DeviceNet path:	71/01/0C		
AUS	Parameter name: ENA average speed Terminal display: [ENA avg speed]		
Logic address:	12102 = 16#2F46	Type:	INT
CANopen index:	205B/3	Read/write:	R
INTERBUS index:	5FB9/EA	Unit:	0.1 Hz
DeviceNet path:	9D/01/67		

Reference parameters

References (speed)

Code	Description		
FrHd	Parameter name: Speed reference before ramp Logic address: 8605 = 16#219D CANopen index: 2038/6 INTERBUS index: 5FB9/D6 DeviceNet path: 8C/01/06		
	Read/write:	Type: INT	R
	Unit:	Factory setting: rpm	
	Adjustment range:		
FrOd	Parameter name: Speed reference after ramp Terminal display: DSP402 name: vl velocity demand DRIVECOM name: Speed-Reference-Variable Logic address: 8641 = 16#21C1 CANopen index: 6043 INTERBUS index: 5FB9/D8 DeviceNet path: 8C/01/2A		
	Read/write:	Type: INT	R
	Unit:	Factory setting: rpm	
	Adjustment range:		
Parameter conforming to DSP402 profile Signed value. This parameter is linked to the "Frequency after ramp" (FRO) parameter for which the unit is 0.1 Hz.			
FrH	Parameter name: Frequency reference before ramp Terminal display: [Frequency ref.] Logic address: 3203 = 16#0C83 CANopen index: 2002/4 INTERBUS index: 5FB9/05 DeviceNet path: 71/01/04		
	Read/write:	Type: INT	R
	Unit:	0.1 Hz	
FrO	Parameter name: Frequency reference after ramp Logic address: 9021 = 16#233D CANopen index: 203C/16 INTERBUS index: 5FB9/D9 DeviceNet path: 8E/01/16		
	Read/write:	Type: INT	R
	Unit:	0.1 Hz	

Reference parameters

References (torque)

Code	Description		
trr	<p>Parameter name: Torque reference before ramp</p> <p>Terminal display: [Torque reference]</p>		
	Logic address:	9231 = 16#240F	Type: INT
	CANopen index:	203E/20	Read/write: R
	INTERBUS index:	5FB9/DB	Unit: 0.1%
	DeviceNet path:	8F/01/20	
trO	<p>Parameter name: Torque reference after ramp</p> <p>Torque demand value Torque demand value</p> <p>Torque-Command-Variable Torque-Command-Variable</p>		
	Logic address:	9232 = 16#2410	Type: INT
	CANopen index:	203E/21	Read/write: R
	INTERBUS index:	5FB9/DC	Unit: 0.001 "Nominal motor torque"
	DeviceNet path:	8F/01/21	
	<p>Parameter conforming to DSP402 profile</p> <p>Signed value.</p> <p>The "Nominal motor torque" is only accessible as a drive parameter. It is the result of the other characteristics.</p>		

Reference parameters

Reference (regulator)

See section "Assignment of setpoints from a network", page [59](#).

Code	Description		
rPC	Parameter name: PID reference after ramp Terminal display: [PID reference]		
	Logic address:	11982 = 16#2ECE	Type: UINT
	CANopen index:	2059/53	Read/write: R
	INTERBUS index:	5FB9/E7	Unit: 1
	DeviceNet path:	9C/01/B7	
rPF	Parameter name: PID regulator feedback reference Terminal display: [PID feedback]		
	Logic address:	11981 = 16#2ECD	Type: UINT
	CANopen index:	2059/52	Read/write: R
	INTERBUS index:	5FB9/E6	Unit: 1
	DeviceNet path:	9C/01/B6	
rPE	Parameter name: PID regulator discrepancy Terminal display: [PID error]		
	Logic address:	11980 = 16#2ECC	Type: INT
	CANopen index:	2059/51	Read/write: R
	INTERBUS index:	5FB9/E5	Unit: 1
	DeviceNet path:	9C/01/B5	
rPO	Parameter name: PID regulator limit output reference Terminal display: [PID Output]		
	Logic address:	11983 = 16#2ECF	Type: INT
	CANopen index:	2059/54	Read/write: R
	INTERBUS index:	5FB9/E8	Unit: 0.1 Hz
	DeviceNet path:	9C/01/B8	

Measurement parameters

Input measurements

Code	Description		
ULn	Parameter name: Power supply voltage Terminal display: [Mains voltage]		
	Logic address: 3207 = 16#0C87	Type:	UINT
	CANopen index: 2002/8	Read/write:	R
	INTERBUS index: 5FB9/09	Unit:	0.1V
	DeviceNet path: 71/01/08		
APH	Parameter name: Energy consumption Terminal display: [Consumption]		
	Logic address: 3230 = 16#0C9E	Type:	UINT
	CANopen index: 2002/1F	Read/write:	R
	INTERBUS index: 5FB9/15	Unit:	((UNT & 0x0003) == 0): 1 Wh else: (((UNT & 0x0003) == 0x0001): 1 kWh else: 1 MWh)
	DeviceNet path: 71/01/1F		

Thermal states

Code	Description		
tHd	Parameter name: Drive thermal state Terminal display: [Drv. thermal state]		
	Logic address: 3209 = 16#0C89	Type:	UINT
	CANopen index: 2002/A	Read/write:	R
	INTERBUS index: 5FB9/0B	Unit:	1%
	DeviceNet path: 71/01/0A		
tHr	Parameter name: Motor thermal state Terminal display: [Motor thermal state]		
	Logic address: 9630 = 16#259E	Type:	UINT
	CANopen index: 2042/1F	Read/write:	R
	INTERBUS index: 5FB9/DE	Unit:	1%
	DeviceNet path: 91/01/1F		
tHb	Parameter name: DBR thermal state Terminal display: [DBR thermal state]		
	Logic address: 14114 = 16#3722	Type:	UINT
	CANopen index: 206F/F	Read/write:	R
	INTERBUS index: 5FBD/7F	Unit:	1%
	DeviceNet path: A7/01/73		

Measurement parameters

Time

Code	Description		
rtH	Parameter name: Total motor operating time Terminal display: [Run time]		
	Logic address: 3231 = 16#0C9F	Type: UINT	
	CANopen index: 2002/20	Read/write: R	
	INTERBUS index: 5FB9/16	Unit:	((UNT & 0x0030) == 0): 1 s else: (((UNT & 0x0030) == 0x0010): 1 min else: 1 h}
	DeviceNet path: 71/01/20		
rtHI	Parameter name: Internal motor operating time		
	Logic address: 3232 = 16#0CA0	Type: UINT	
	CANopen index: 2002/21	Read/write: R	
	INTERBUS index: 5FB9/17	Unit:	((UNT & 0x00C0) == 0): 1 s else: (((UNT & 0x00C0) == 0x0040): 1 min else: 1 h}
	DeviceNet path: 71/01/21		
PtH	Parameter name: Total drive operating time Terminal display: [Power on time]		
	Logic address: 3233 = 16#0CA1	Type: UINT	
	CANopen index: 2002/22	Read/write: R	
	INTERBUS index: 5FB9/18	Unit:	((UNT & 0x000C) == 0): 1 s else: (((UNT & 0x000C) == 0x0004): 1 min else: 1 h}
	DeviceNet path: 71/01/22		
tAC	Parameter name: IGBT alarm time Terminal display: [IGBT alarm counter]		
	Logic address: 3235 = 16#0CA3	Type: UINT	
	CANopen index: 2002/24	Read/write: R	
	INTERBUS index: 5FB9/1A	Unit:	1 s
	DeviceNet path: 71/01/24		
EbOt	Parameter name: Current bobbin time		
	Logic address: 12209 = 16#2FB1	Type: UINT	
	CANopen index: 205C/A	Read/write: R	
	INTERBUS index: 5FB9/EB	Unit:	1 min
	DeviceNet path: 9E/01/0A		
IdM	Parameter name: Magnetizing current		
	Logic address: 9650 = 16#25B2	Type: UINT	
	CANopen index: 2042/33	Read/write: R	
	INTERBUS index: 5FB9/E0	Unit:	0.1 A
	DeviceNet path: 91/01/33		

Measurement parameters

Code	Description		
LFM	Parameter name: Leakage inductance		
	Logic address: 9660 = 16#25BC	Type: UINT	
	CANopen index: 2042/3D	Read/write: R	
	INTERBUS index: 5FB9/E1	Unit: 0.01 mH	
	DeviceNet path: 91/01/3D		
rSM	Parameter name: Asynchronous motor cold state stator resistance		
	Logic address: 9640 = 16#25A8	Type: UINT	
	CANopen index: 2042/29	Read/write: R	
	INTERBUS index: 5FB9/DF	Unit: (NCVI > "75"): 1 µOhm else: 1 mOhm	
	DeviceNet path: 91/01/29		
trM	Parameter name: Rotor time constant		
	Logic address: 9665 = 16#25C1	Type: UINT	
	CANopen index: 2042/42	Read/write: R	
	INTERBUS index: 5FB9/E2	Unit: 1 ms	
	DeviceNet path: 91/01/42		
rSMS	Parameter name: Synchronous motor cold state stator resistance		
	Logic address: 9680 = 16#25D0	Type: UINT	
	CANopen index: 2042/51	Read/write: R	
	INTERBUS index: 5FB9/E3	Unit: (NCVI > "75"): 1 µOhm else: 1 mOhm	
	DeviceNet path: 91/01/51		

I/O parameters

Code	Description		
IL1r	Parameter name: Logic input map		
	Logic address:	5202 = 16#1452	Type: WORD (bit register)
	CANopen index:	2016/3	Read/write: R
	INTERBUS index:	5FB9/28	Unit: -
	DeviceNet path:	7B/01/03	
	bit 0: Value of LI1 bit 1: Value of LI2 bit 2: Value of LI3 bit 3: Value of LI4 bit 4: Value of LI5 bit 5: Value of LI6 bit 6: Value of LI7 bit 7: Value of LI8 bit 8: Value of LI9 bit 9: Value of LI10 bit 10: Value of LI11 bit 11: Value of LI12 bit 12: Value of LI13 bit 13: Value of LI14 bit 14: Reserved (=0) bit 15: Reserved (=0)		
OL1r	Parameter name: Logic output map		
	Logic address:	5212 = 16#145C	Type: WORD (bit register)
	CANopen index:	2016/D	Read/write: R
	INTERBUS index:	5FB9/2A	Unit: -
	DeviceNet path:	7B/01/0D	
	bit 0: Value of R1 bit 1: Value of R2 bit 2: Value of R3, if a logic I/O extension card is installed. bit 3: Value of R4, if an extended I/O extension card is installed. bit 4: Reserved (=0) bit 5: Reserved (=0) bit 6: Reserved (=0) bit 7: Reserved (=0) bit 8: Value of LO1 bit 9: Value of LO2 bit 10: Value of LO3 bit 11: Value of LO4 bit 12: Reserved (=0) bit 13: Reserved (=0) bit 14: Reserved (=0) bit 15: Reserved (=0)		

I/O parameters

Code	Description		
AI1C	Parameter name: Physical image of analog input 1 Logic address: 5242 = 16#147A Type: INT CANopen index: 2016/2B Read/write: R INTERBUS index: 5FB9/33 Unit: 0.001 V DeviceNet path: 7B/01/2B		
AI1r	Parameter name: Standardized image of analog input 1 Logic address: 5232 = 16#1470 Type: INT CANopen index: 2016/21 Read/write: R INTERBUS index: 5FB9/2F Unit: 1 DeviceNet path: 7B/01/21		
AI2C	Parameter name: Physical image of analog input 2 Logic address: 5243 = 16#147B Type: INT CANopen index: 2016/2C Read/write: R INTERBUS index: 5FB9/34 Unit: 0.001 V or 0.001 mA DeviceNet path: 7B/01/2C		
	The unit is: • 0.001 V if parameter [AI2 Type] (AI2t) is configured as [Voltage] (10U) • 0.001 mA if parameter [AI2 Type] (AI2t) is configured as [Current] (0A)		
AI2r	Parameter name: Standardized image of analog input 2 Logic address: 5233 = 16#1471 Type: INT CANopen index: 2016/22 Read/write: R INTERBUS index: 5FB9/30 Unit: 1 DeviceNet path: 7B/01/22		
AI3C	Parameter name: Physical image of analog input 3 Logic address: 5244 = 16#147C Type: INT CANopen index: 2016/2D Read/write: R INTERBUS index: 5FB9/35 Unit: 0.001 mA DeviceNet path: 7B/01/2D		
AI3r	Parameter name: Standardized image of analog input 3 Logic address: 5234 = 16#1472 Type: INT CANopen index: 2016/23 Read/write: R INTERBUS index: 5FB9/31 Unit: 1 DeviceNet path: 7B/01/23		
AI4C	Parameter name: Physical image of analog input 4 Logic address: 5245 = 16#147D Type: INT CANopen index: 2016/2E Read/write: R INTERBUS index: 5FB9/36 Unit: 0.001 V or 0.001 mA DeviceNet path: 7B/01/2E		
	The unit is: • 0.001 V if parameter [AI4 Type] (AI4t) is configured as [Voltage] (10U) • 0.001 mA if parameter [AI4 Type] (AI4t) is configured as [Current] (0A)		

I/O parameters

Code	Description		
AI4r	Parameter name: Standardized image of analog input 4 Logic address: 5235 = 16#1473 Type: INT CANopen index: 2016/24 Read/write: R INTERBUS index: 5FB9/32 Unit: 1 DeviceNet path: 7B/01/24		
AO1C	Parameter name: Physical image of analog output 1 Logic address: 5271 = 16#1497 Type: INT CANopen index: 2016/48 Read/write: R/W INTERBUS index: 5FB9/3D Unit: 0.001 mA or 0.001 V DeviceNet path: 7B/01/48		
	The unit is: • 0.001 V if parameter [AO1 Type] (AO1t) is configured as [Voltage] (10U) • 0.001 mA if parameter [AO1 Type] (AO1t) is configured as [Current] (0A)		
AO1r	Parameter name: Standardized image of analog output 1 Logic address: 5261 = 16#148D Type: INT CANopen index: 2016/3E Read/write: R/W INTERBUS index: 5FB9/3A Unit: 1 DeviceNet path: 7B/01/3E		
AO2C	Parameter name: Physical image of analog output 2 Logic address: 5272 = 16#1498 Type: INT CANopen index: 2016/49 Read/write: R/W INTERBUS index: 5FB9/3E Unit: 0.001 mA or 0.001 V DeviceNet path: 7B/01/49		
	The unit is: • 0.001 V if parameter [AO2 Type] (AO2t) is configured as [Voltage] (10U) or [Voltage +/-] (n10U) • 0.001 mA if parameter [AO2 Type] (AO2t) is configured as [Current] (0A)		
AO2r	Parameter name: Standardized image of analog output 2 Logic address: 5262 = 16#148E Type: INT CANopen index: 2016/3F Read/write: R/W INTERBUS index: 5FB9/3B Unit: 1 DeviceNet path: 7B/01/3F		
AO3C	Parameter name: Physical image of analog output 3 Logic address: 5273 = 16#1499 Type: INT CANopen index: 2016/4A Read/write: R/W INTERBUS index: 5FB9/3F Unit: 0.001 mA or 0.001 V DeviceNet path: 7B/01/4A		
	The unit is: • 0.001 V if parameter [AO3 Type] (AO3t) is configured as [Voltage] (10U) or [Voltage +/-] (n10U) • 0.001 mA if parameter [AO3 Type] (AO3t) is configured as [Current] (0A)		

I/O parameters

Code	Description		
AO3r	Parameter name: Standardized image of analog output 3		
	Logic address:	5263 = 16#148F	Type: INT
	CANopen index:	2016/40	Read/write: R/W
	INTERBUS index:	5FB9/3C	Unit: 1
	DeviceNet path:	7B/01/40	
PGI	Parameter name: Encoder pulse counter		
	Logic address:	5604 = 16#15E4	Type: UINT
	CANopen index:	201A/5	Read/write: R
	INTERBUS index:	5FB3/7E	Unit: 1
	DeviceNet path:	7D/01/05	

Fault parameters

Code	Description		
Errd	Parameter name: DSP402 fault code DSP402 name: Error code DRIVECOM name: Malfunction code		
	Logic address: 8606 = 16#219E	Type:	WORD (listing)
	CANopen index: 603F	Read/write:	R
	INTERBUS index: 5FB9/D7	Unit:	-
	DeviceNet path: 8C/01/07		
Parameter conforming to DSP402 profile.			
16#0000 : No fault written to memory (nOF) 16#1000 : Capacitor precharge fault (CrF) or motor overload fault (OLF) or motor overspeed fault (SOF) 16#2310 : Overcurrent fault (OCF) 16#2320 : Impedant short-circuit fault (SCF) or power module fault (SCF) 16#2330 : Motor short-circuit fault (to ground) (SCF) 16#2340 : Motor short-circuit fault (phase to phase) (SCF) 16#3110 : Line supply overvoltage fault (OSF) 16#3120 : Line supply undervoltage fault (USF) 16#3130 : Input phase loss fault (PHF) 16#3310 : DC bus overvoltage fault (ObF) or motor phase loss fault (OPF) or motor phase loss fault - 3 phases (OPF) 16#4210 : Drive overheat fault (OHF) 16#5520 : EEPROM non-volatile memory fault (EEF) 16#6100 : Internal fault (InF) 16#6300 : Configuration (parameters) incorrect (CFF) or invalid (CFI) 16#7300 : 4-20 mA fault on AI3 (LFF) 16#7510 : Modbus communication fault (SLF) 16#8100 : CANopen communication fault (COF) 16#9000 : External fault (EPF) 16#FF00 : Auto-tuning fault (tnF) 16#FF01 : Brake control fault (bLF)			

Fault parameters

Code	Description																																																																																																										
Lft	Parameter name: Altivar fault code																																																																																																										
	Logic address: 7121 = 16#1BD1	Type: WORD (listing)																																																																																																									
	CANopen index: 2029/16	Read/write: R																																																																																																									
	INTERBUS index: 5FB3/C8	Unit: -																																																																																																									
	DeviceNet path: 84/01/7A																																																																																																										
<p>The fault remains saved in the "Altivar fault code" (Lft) parameter, even if it disappears. The "Altivar fault code" (Lft) parameter is reset after the drive is disconnected and then reconnected.</p> <table> <tbody> <tr> <td>0 :</td> <td>(nOF)</td> <td>No fault</td> </tr> <tr> <td>1 :</td> <td></td> <td>Reserved</td> </tr> <tr> <td>2 : [Control EEprom]</td> <td>(EEF1)</td> <td>Internal memory fault, control card.</td> </tr> <tr> <td>3 : [Incorrect config.]</td> <td>(CFF)</td> <td>Option card changed or removed. Control card replaced by a control card configured on a drive with a different rating.</td> </tr> <tr> <td>4 : [Invalid config.]</td> <td>(CFI)</td> <td>The current configuration is inconsistent. Invalid configuration. The configuration loaded in the drive via the bus or communication network is inconsistent.</td> </tr> <tr> <td>5 : [Modbus com.]</td> <td>(SLF1)</td> <td>Interruption in communication on the Modbus bus</td> </tr> <tr> <td>6 : [internal com. link]</td> <td>(ILF)</td> <td>Communication fault between option card and drive</td> </tr> <tr> <td>7 : [Com. network]</td> <td>(CnF)</td> <td>Communication fault on communication card</td> </tr> <tr> <td>8 : [External fit-LI/Bit]</td> <td>(EPF1)</td> <td>Fault triggered by an external device, depending on user</td> </tr> <tr> <td>9 : [Overcurrent]</td> <td>(OCF)</td> <td>Parameters in the [SETTINGS] (SET-) and [1.4 MOTOR CONTROL] (drC-) menus are not correct. Inertia or load too high. Mechanical locking.</td> </tr> <tr> <td>10 : [Precharge]</td> <td>(CrF1)</td> <td>Pre-charge relay control fault or charging resistor damaged.</td> </tr> <tr> <td>11 : [Speed fdbck loss]</td> <td>(SPF)</td> <td>Encoder feedback signal missing.</td> </tr> <tr> <td>12 : [Load slipping]</td> <td>(AnF)</td> <td>The encoder speed feedback does not match the reference.</td> </tr> <tr> <td>13 : [AI2 4-20 mA loss]</td> <td>(LFF2)</td> <td>Loss of the 4-20 mA reference on analog input AI2.</td> </tr> <tr> <td>14 : [PTC1 probe]</td> <td>(PtF1)</td> <td>PTC1 probes open or short-circuited.</td> </tr> <tr> <td>15 : [PTC1 overheat]</td> <td>(OtF1)</td> <td>Overheating of the PTC1 probes detected.</td> </tr> <tr> <td>16 : [Drive overheat]</td> <td>(OHF)</td> <td>Drive temperature too high.</td> </tr> <tr> <td>17 : [Motor overload]</td> <td>(OLF)</td> <td>Triggered by excessive motor current.</td> </tr> <tr> <td>18 : [Overbraking]</td> <td>(ObF)</td> <td>Braking too sudden or driving load.</td> </tr> <tr> <td>19 : [Mains overvoltage]</td> <td>(OSF)</td> <td>Line voltage too high. Disturbed line supply.</td> </tr> <tr> <td>20 : [1 motor phase loss]</td> <td>(OPF1)</td> <td>Loss of one phase at drive output.</td> </tr> <tr> <td>21 : [Input phase loss]</td> <td>(PHF)</td> <td>Drive incorrectly supplied or a fuse blown. Loss of one phase. 3-phase ATV71 used on a single-phase line supply. Unbalanced load.</td> </tr> <tr> <td>22 : [Undervoltage]</td> <td>(USF)</td> <td>This protection only operates with the drive on load. Line supply too low. Transient voltage dip. Damaged precharge resistor.</td> </tr> <tr> <td>23 : [Motor short-circuit]</td> <td>(SCF1)</td> <td>Short-circuit or grounding at the drive output. Significant earth leakage current at the drive output if several motors are connected in parallel.</td> </tr> <tr> <td>24 : [Overspeed]</td> <td>(SOF)</td> <td>Instability or driving load too high.</td> </tr> <tr> <td>25 : [Auto-tuning]</td> <td>(tnF)</td> <td>Special motor or motor whose power is not suitable for the drive. Motor not connected to the drive.</td> </tr> <tr> <td>26 : [Rating error]</td> <td>(InF1)</td> <td>The power card is not the one stored.</td> </tr> <tr> <td>27 : [Incompatible PB]</td> <td>(InF2)</td> <td>The power card is incompatible with the control card.</td> </tr> <tr> <td>28 : [Internal serial link]</td> <td>(InF3)</td> <td>Communication fault between the internal cards.</td> </tr> <tr> <td>29 : [Internal MFG area]</td> <td>(InF4)</td> <td>Internal data inconsistent.</td> </tr> <tr> <td>30 : [Power EEprom]</td> <td>(EEF2)</td> <td>Internal memory fault, power card.</td> </tr> <tr> <td>31 : [Impedant sh. circuit]</td> <td>(SCF2)</td> <td>Impedant short-circuit.</td> </tr> <tr> <td>32 : [Ground short circuit]</td> <td>(SCF3)</td> <td>Ground short-circuit.</td> </tr> <tr> <td>33 : [3 output phase loss]</td> <td>(OPF2)</td> <td>Motor not connected or motor power too low. Output contactor open. Instantaneous instability in the motor current.</td> </tr> <tr> <td>34 : [CANopen. com.]</td> <td>(COF)</td> <td>Interruption in communication on the CANopen bus.</td> </tr> </tbody> </table>	0 :	(nOF)	No fault	1 :		Reserved	2 : [Control EEprom]	(EEF1)	Internal memory fault, control card.	3 : [Incorrect config.]	(CFF)	Option card changed or removed. Control card replaced by a control card configured on a drive with a different rating.	4 : [Invalid config.]	(CFI)	The current configuration is inconsistent. Invalid configuration. The configuration loaded in the drive via the bus or communication network is inconsistent.	5 : [Modbus com.]	(SLF1)	Interruption in communication on the Modbus bus	6 : [internal com. link]	(ILF)	Communication fault between option card and drive	7 : [Com. network]	(CnF)	Communication fault on communication card	8 : [External fit-LI/Bit]	(EPF1)	Fault triggered by an external device, depending on user	9 : [Overcurrent]	(OCF)	Parameters in the [SETTINGS] (SET-) and [1.4 MOTOR CONTROL] (drC-) menus are not correct. Inertia or load too high. Mechanical locking.	10 : [Precharge]	(CrF1)	Pre-charge relay control fault or charging resistor damaged.	11 : [Speed fdbck loss]	(SPF)	Encoder feedback signal missing.	12 : [Load slipping]	(AnF)	The encoder speed feedback does not match the reference.	13 : [AI2 4-20 mA loss]	(LFF2)	Loss of the 4-20 mA reference on analog input AI2.	14 : [PTC1 probe]	(PtF1)	PTC1 probes open or short-circuited.	15 : [PTC1 overheat]	(OtF1)	Overheating of the PTC1 probes detected.	16 : [Drive overheat]	(OHF)	Drive temperature too high.	17 : [Motor overload]	(OLF)	Triggered by excessive motor current.	18 : [Overbraking]	(ObF)	Braking too sudden or driving load.	19 : [Mains overvoltage]	(OSF)	Line voltage too high. Disturbed line supply.	20 : [1 motor phase loss]	(OPF1)	Loss of one phase at drive output.	21 : [Input phase loss]	(PHF)	Drive incorrectly supplied or a fuse blown. Loss of one phase. 3-phase ATV71 used on a single-phase line supply. Unbalanced load.	22 : [Undervoltage]	(USF)	This protection only operates with the drive on load. Line supply too low. Transient voltage dip. Damaged precharge resistor.	23 : [Motor short-circuit]	(SCF1)	Short-circuit or grounding at the drive output. Significant earth leakage current at the drive output if several motors are connected in parallel.	24 : [Overspeed]	(SOF)	Instability or driving load too high.	25 : [Auto-tuning]	(tnF)	Special motor or motor whose power is not suitable for the drive. Motor not connected to the drive.	26 : [Rating error]	(InF1)	The power card is not the one stored.	27 : [Incompatible PB]	(InF2)	The power card is incompatible with the control card.	28 : [Internal serial link]	(InF3)	Communication fault between the internal cards.	29 : [Internal MFG area]	(InF4)	Internal data inconsistent.	30 : [Power EEprom]	(EEF2)	Internal memory fault, power card.	31 : [Impedant sh. circuit]	(SCF2)	Impedant short-circuit.	32 : [Ground short circuit]	(SCF3)	Ground short-circuit.	33 : [3 output phase loss]	(OPF2)	Motor not connected or motor power too low. Output contactor open. Instantaneous instability in the motor current.	34 : [CANopen. com.]	(COF)	Interruption in communication on the CANopen bus.		
0 :	(nOF)	No fault																																																																																																									
1 :		Reserved																																																																																																									
2 : [Control EEprom]	(EEF1)	Internal memory fault, control card.																																																																																																									
3 : [Incorrect config.]	(CFF)	Option card changed or removed. Control card replaced by a control card configured on a drive with a different rating.																																																																																																									
4 : [Invalid config.]	(CFI)	The current configuration is inconsistent. Invalid configuration. The configuration loaded in the drive via the bus or communication network is inconsistent.																																																																																																									
5 : [Modbus com.]	(SLF1)	Interruption in communication on the Modbus bus																																																																																																									
6 : [internal com. link]	(ILF)	Communication fault between option card and drive																																																																																																									
7 : [Com. network]	(CnF)	Communication fault on communication card																																																																																																									
8 : [External fit-LI/Bit]	(EPF1)	Fault triggered by an external device, depending on user																																																																																																									
9 : [Overcurrent]	(OCF)	Parameters in the [SETTINGS] (SET-) and [1.4 MOTOR CONTROL] (drC-) menus are not correct. Inertia or load too high. Mechanical locking.																																																																																																									
10 : [Precharge]	(CrF1)	Pre-charge relay control fault or charging resistor damaged.																																																																																																									
11 : [Speed fdbck loss]	(SPF)	Encoder feedback signal missing.																																																																																																									
12 : [Load slipping]	(AnF)	The encoder speed feedback does not match the reference.																																																																																																									
13 : [AI2 4-20 mA loss]	(LFF2)	Loss of the 4-20 mA reference on analog input AI2.																																																																																																									
14 : [PTC1 probe]	(PtF1)	PTC1 probes open or short-circuited.																																																																																																									
15 : [PTC1 overheat]	(OtF1)	Overheating of the PTC1 probes detected.																																																																																																									
16 : [Drive overheat]	(OHF)	Drive temperature too high.																																																																																																									
17 : [Motor overload]	(OLF)	Triggered by excessive motor current.																																																																																																									
18 : [Overbraking]	(ObF)	Braking too sudden or driving load.																																																																																																									
19 : [Mains overvoltage]	(OSF)	Line voltage too high. Disturbed line supply.																																																																																																									
20 : [1 motor phase loss]	(OPF1)	Loss of one phase at drive output.																																																																																																									
21 : [Input phase loss]	(PHF)	Drive incorrectly supplied or a fuse blown. Loss of one phase. 3-phase ATV71 used on a single-phase line supply. Unbalanced load.																																																																																																									
22 : [Undervoltage]	(USF)	This protection only operates with the drive on load. Line supply too low. Transient voltage dip. Damaged precharge resistor.																																																																																																									
23 : [Motor short-circuit]	(SCF1)	Short-circuit or grounding at the drive output. Significant earth leakage current at the drive output if several motors are connected in parallel.																																																																																																									
24 : [Overspeed]	(SOF)	Instability or driving load too high.																																																																																																									
25 : [Auto-tuning]	(tnF)	Special motor or motor whose power is not suitable for the drive. Motor not connected to the drive.																																																																																																									
26 : [Rating error]	(InF1)	The power card is not the one stored.																																																																																																									
27 : [Incompatible PB]	(InF2)	The power card is incompatible with the control card.																																																																																																									
28 : [Internal serial link]	(InF3)	Communication fault between the internal cards.																																																																																																									
29 : [Internal MFG area]	(InF4)	Internal data inconsistent.																																																																																																									
30 : [Power EEprom]	(EEF2)	Internal memory fault, power card.																																																																																																									
31 : [Impedant sh. circuit]	(SCF2)	Impedant short-circuit.																																																																																																									
32 : [Ground short circuit]	(SCF3)	Ground short-circuit.																																																																																																									
33 : [3 output phase loss]	(OPF2)	Motor not connected or motor power too low. Output contactor open. Instantaneous instability in the motor current.																																																																																																									
34 : [CANopen. com.]	(COF)	Interruption in communication on the CANopen bus.																																																																																																									

Fault parameters

Code	Description	
((LFT), continued)	35 : [Brake control] (bLF) 36 : 37 : [Internal-hard init.] (InF7) 38 : [External fault com.] (EPF2) 39 : [Application fault] (APF) 40 : [Internal-ctrl supply] (InF8) 41 : [Brake feedback] (brF) 42 : [PowerSuite com.] (SLF2) 43 : [Encoder coupling] (ECF) 44 : [Torque/current lim] (SSF) 45 : [HMI com.] (SLF3) 46 : [Power removal] (PrF) 47 : [PTC 2 probe] (PtF2) 48 : [PTC2 overheat] (OtF2) 49 : [LI6=PTC probe] (PtFL) 50 : [LI6=PTC overheat] (OtFL) 51 : [Internal- I measure] (InF9) 52 : [Internal-mains circuit] (InFA) 53 : [Internal-th. sensor] (InFb) 54 : [IGBT overheated] (tJF) 55 : [IGBT short circuit] (SCF4) 56 : [Motor short circuit] (SCF5) 57 : [Torque time-out] (SrF) 58 : [Out. contact. stuck] (FCF1) 59 : [Out. contact. open] (FCF2) 60 : [Internal-time meas.] (InFC) 61 : [AI2 input] (AI2F) 62 : [Encoder] (EnF) 63 : [Thyr. soft charge] (CrF2) 64 : [input contactor] (LCF) 65 : [DB unit sh. circuit] (bUF) 66 : 67 : [IGBT desaturation] (HdF) 68 : [Internal-option] (InF6) 69 : [internal- CPU] (InFE) 70 : [DBR overload] (bOF) 71 : [AI3 4-20 mA loss] (LFF3) 72 : [AI4 4-20 mA loss] (LFF4) 73 : [Cards pairing] (HCF)	Brake release current not reached. Brake engage frequency threshold [Brake engage freq] (bEn) only regulated when brake logic control is assigned. Reserved. Initialization of the drive is incomplete. Fault triggered by a communication network. "Controller Inside" card fault. The control supply is not correct. The brake feedback contact does not match the brake logic control. Fault communicating with PowerSuite. Break in the mechanical coupling of the encoder. Switch to torque limitation. Fault communicating with the graphic display terminal. Fault with the drive's "Power removal" safety function. PTC2 probes open or short-circuited. Overheating of the PTC2 probes detected. PTC probes on input LI6 open or short-circuited. Overheating of PTC probes detected on input LI6. The current measurements are not correct. The input stage is not operating correctly. The drive temperature sensor is not operating correctly. Drive overheated. Power component fault. Short-circuit at the drive output. Torque control function time-out. The output contactor remains closed although the opening conditions have been met. The output contactor remains open although the closing conditions have been met. Fault on the electronic time measurement component. Non-conforming signal on analog input AI2. Encoder feedback fault. DC bus charging fault (thyristors). The drive is not turned on even though [Mains V. time out] (LCt) has elapsed. Short-circuit output from braking unit. Reserved Short-circuit or grounding at the drive output. The option installed in the drive is not recognized. Internal microprocessor fault. The braking resistor is under excessive stress. Loss of the 4-20 mA reference on analog input AI3. Loss of the 4-20 mA reference on analog input AI4. The [CARDS PAIRING] (PPI-) function has been configured and a drive card has been changed.

Fault parameters

Code	Description		
CIC	<p>Parameter name: Incorrect configuration</p> <p>Logic address: 7130 = 16#1BDA Type: WORD (bit register)</p> <p>CANopen index: 2029/1F Read/write: R</p> <p>INTERBUS index: 5FB6/1A Unit: -</p> <p>DeviceNet path: 84/01/83</p> <p>bit 0: = 1: Change of rating. bit 1: Reserved (= 0 or 1). bit 2: = 1: The network card has been removed. bit 3: = 1: Saving to the EEPROM non-volatile memory is inconsistent with power on.</p> <p>bit 4: = 1: The network card has been changed. bit 5: Reserved (= 0 or 1). bit 6: = 1: The (Controller Inside) card has been removed. bit 7: = 1: The (Controller Inside) card has been changed.</p> <p>bit 8: Reserved (= 0 or 1). bit 9: = 1: An I/O card has been removed. bit 10: = 1: The I/O card has been changed. bit 11: Reserved (= 0 or 1).</p> <p>bit 12: = 1: An encoder card has been removed. bit 13: = 1: The encoder card has been changed. bit 14: Reserved (= 0 or 1). bit 15: Reserved (= 0 or 1).</p> <p>If one of these events occurs, the drive switches to [Incorrect config.] (CFF) fault mode and then automatically applies a factory setting.</p>		
APF	<p>Parameter name: "Controller Inside" card fault code</p> <p>Logic address: 7133 = 16#1BDD Type: UINT</p> <p>CANopen index: 2029/22 Read/write: R</p> <p>INTERBUS index: 5FB0/97 Unit: 1</p> <p>DeviceNet path: 84/01/86</p> <p>The fault remains saved in the parameter, even if it disappears. The parameter is reset after the drive is disconnected and then reconnected.</p> <p>Consult the "Controller Inside" card manual.</p>		
CnF	<p>Parameter name: Network card fault code</p> <p>Logic address: 7132 = 16#1BDC Type: UINT</p> <p>CANopen index: 2029/21 Read/write: R</p> <p>INTERBUS index: 5FB0/96 Unit: 1</p> <p>DeviceNet path: 84/01/85</p> <p>The fault remains saved in the parameter, even if it disappears. The parameter is reset after the drive is disconnected and then reconnected.</p> <p>The possible values of this parameter depend on the network card protocol. Consult the manual for the corresponding card.</p>		

Fault parameters

Code	Description		
ILF1	<p>Parameter name: Option card 1 fault code</p> <p>Logic address: 7134 = 16#1BDE Type: UINT</p> <p>CANopen index: 2029/23 Read/write: R</p> <p>INTERBUS index: 5FB0/98 Unit: 1</p> <p>DeviceNet path: 84/01/87 Factory setting:</p>		
	<p>The fault remains saved in the parameter, even if it disappears. The parameter is reset after the drive is disconnected and then reconnected.</p> <p>0 = No fault 1 = Loss of internal communication with the drive 2 = Hardware fault detected 3 = Error in the EEPROM non-volatile memory checksum 4 = Faulty EEPROM non-volatile memory 5 = Faulty Flash memory 6 = Faulty RAM memory 7 = Faulty NVRAM memory 8 = Faulty analog input 9 = Faulty analog output 10 = Faulty logic input 11 = Faulty logic output 101 = Unknown card 102 = Exchange problem on the drive internal bus 103 = Time-out on the drive internal bus (500 ms)</p>		
ILF2	<p>Parameter name: Option card 2 fault code</p> <p>Logic address: 7135 = 16#1BDF Type: UINT</p> <p>CANopen index: 2029/24 Read/write: R</p> <p>INTERBUS index: 5FB0/99 Unit: 1</p> <p>DeviceNet path: 84/01/88</p>		
	<p>The fault remains saved in the parameter, even if it disappears. The parameter is reset after the drive is disconnected and then reconnected.</p> <p>Same values as ILF1.</p>		
Fdrv	<p>Parameter name: Ethernet fault code</p> <p>Terminal display: [FDR fault]</p> <p>Logic address: 64233 = 16#FAE9 Type: UINT</p> <p>CANopen index: 2264/22 Read/write: R</p> <p>INTERBUS index: - Unit: -</p> <p>DeviceNet path: -</p>		
	<p>When an Ethernet fault is present, this parameter can be used to ascertain the cause of the fault. The fault code remains saved after the disappearance of the fault.</p> <p>2: The FDR configuration file is not compatible with the type of drive (e.g., incorrect drive rating). 3: Error reading the FDR configuration file on the server. 4: Error writing the FDR configuration file to the server. 5: Error writing the FDR configuration file to the drive (e.g., the drive is in forced local mode). 7: Time-out for receipt of the FDR configuration file from the server. 9: Duplication of IP address.</p>		

Fault parameters

Code	Description		
Fnb	Parameter name: Fault counter		
Logic address:	7393 = 16#1CE1	Type:	UINT
CANopen index:	202B/5E	Read/write:	R
INTERBUS index:	5FB9/CC	Unit:	-
DeviceNet path:	99/01/CC		

Log parameters

Code	Description		
dPO	Parameter name:	Fault code on last fault	
	Logic address:	7200 = 16#1C20	Type: WORD (listing)
	CANopen index:	202A/1	Read/write: R
	INTERBUS index:	5FB9/55	Unit: -
	DeviceNet path:	85/01/01	
Value of the "Alivar fault code" (Lft) parameter, written to memory when the last fault occurred.			
See the possible values of the "Alivar fault code" (Lft) parameter.			
ULP0	Parameter name:	Power supply voltage on last fault	
	Logic address:	7270 = 16#1C66	Type: UINT
	CANopen index:	202A/47	Read/write: R
	INTERBUS index:	5FB9/94	Unit: 0.1 V
	DeviceNet path:	85/01/47	
Value of the "Power supply voltage" (ULn) parameter, written to memory when the last fault occurred.			
LCP0	Parameter name:	Motor current on last fault	
	Logic address:	7240 = 16#1C48	Type: INT
	CANopen index:	202A/29	Read/write: R
	INTERBUS index:	5FB9/79	Unit: 0.1 A
	DeviceNet path:	85/01/29	
Value of the "Motor current" (LCr) parameter, written to memory when the last fault occurred.			
rFP0	Parameter name:	Output frequency on last fault	
	Logic address:	7250 = 16#1C52	Type: INT
	CANopen index:	202A/33	Read/write: R
	INTERBUS index:	5FB9/82	Unit: 0.1 Hz
	DeviceNet path:	85/01/33	
Value of the "Output frequency" (rFr) parameter, written to memory when the last fault occurred.			
tHP0	Parameter name:	Motor thermal state on last fault	
	Logic address:	7280 = 16#1C70	Type: UINT
	CANopen index:	202A/51	Read/write: R
	INTERBUS index:	5FB9/9D	Unit: 1
	DeviceNet path:	85/01/51	
EP0	Parameter name:	Status word on last fault	
	Logic address:	7210 = 16#1C2A	Type: WORD (bit register)
	CANopen index:	202A/B	Read/write: R
	INTERBUS index:	5FB9/5E	Unit: -
	DeviceNet path:	85/01/0B	
Value of the "Status word" (EtA) parameter, written to memory when the last fault occurred.			
See the possible values of the "Status word" (EtA) parameter.			

Log parameters

Code	Description		
IP0	<p>Parameter name: Extended status word 0 on last fault</p> <p>Logic address: 7220 = 16#1C34</p> <p>CANopen index: 202A/15</p> <p>INTERBUS index: 5FB9/67</p> <p>DeviceNet path: 85/01/15</p>		
	<p>Type: WORD (bit register)</p> <p>Read/write: R</p> <p>Unit: -</p> <p>Value of the "Extended status word 0" (EtI) parameter, written to memory when the last fault occurred.</p> <p>See the possible values of the "Extended status word 0" (EtI) parameter.</p>		
CMP0	<p>Parameter name: Command word on last fault</p> <p>Logic address: 7230 = 16#1C3E</p> <p>CANopen index: 202A/1F</p> <p>INTERBUS index: 5FB9/70</p> <p>DeviceNet path: 85/01/1F</p>		
	<p>Type: WORD (bit register)</p> <p>Read/write: R</p> <p>Unit: -</p> <p>Value of the "Command word" (CMd) parameter, written to memory when the last fault occurred.</p> <p>See the possible values of the "Command word" (CMd) parameter.</p>		
dCC0	<p>Parameter name: Command channel active on last fault</p> <p>Logic address: 64300 = 16#FB2C</p> <p>CANopen index: 2265/1</p> <p>INTERBUS index: 5FBA/08</p> <p>DeviceNet path: 9A/01/08</p>		
	<p>Type: WORD (listing)</p> <p>Read/write: R</p> <p>Unit: -</p> <p>0: Terminals 2: Graphic display terminal 3: Modbus 6: CANopen 9: Network card 10: "Controller Inside" card 15: PowerSuite</p>		
drC0	<p>Parameter name: Reference channel active on last fault</p> <p>Logic address: 64310 = 16#FB36</p> <p>CANopen index: 2265/B</p> <p>INTERBUS index: 5FBA/11</p> <p>DeviceNet path: 9A/01/11</p>		
	<p>Type: WORD (listing)</p> <p>Read/write: R</p> <p>Unit: -</p> <p>0: Analog input terminals 2: Graphic display terminal 3: Modbus 6: CANopen 7: +/- speed terminals 9: Network card 10: "Controller Inside" card 15: PowerSuite</p>		

Log parameters

Code	Description		
CrP0	<p>Parameter name: Channels active on last fault</p> <p>Logic address: 7290 = 16#1C7A</p> <p>CANopen index: 202A/5B</p> <p>INTERBUS index: 5FB9/A6</p> <p>DeviceNet path: 85/01/5B</p>		
	Command channel: Low order byte:		
	0: Terminals	Type:	WORD
	2: Graphic display terminal	Read/write:	R
	3: Modbus	Unit:	-
	6: CANopen		
	9: Network card		
	10: "Controller Inside" card		
	15: PowerSuite		
	Reference channel: High order byte:		
	0: Analog input terminals		
	2: Graphic display terminal		
	3: Modbus		
	6: CANopen		
	7: +/- speed terminals		
	9: Network card		
	10: "Controller Inside" card		
	15: PowerSuite		
rtP0	<p>Parameter name: Motor operating time on last fault</p> <p>Logic address: 7260 = 16#1C5C</p> <p>CANopen index: 202A/3D</p> <p>INTERBUS index: 5FB9/8B</p> <p>DeviceNet path: 85/01/3D</p>		
	Type:	UINT	
	Read/write:	R	
	Unit:	1 hour	
Md0	<p>Parameter name: Date on last fault</p> <p>Logic address: 7300 = 16#1C84</p> <p>CANopen index: 202B/1</p> <p>INTERBUS index: 5FB9/AF</p> <p>DeviceNet path: 85/01/65</p>		
	Type:	UINT	
	Read/write:	R	
	Unit:	1	
	This parameter is only significant if a "Controller Inside" card is installed in the drive.		
dM0	<p>Parameter name: Time on last fault</p> <p>Logic address: 7310 = 16#1C8E</p> <p>CANopen index: 202B/B</p> <p>INTERBUS index: 5FB9/B8</p> <p>DeviceNet path: 85/01/6F</p>		
	Type:	UINT	
	Read/write:	R	
	Unit:	1	
	This parameter is only significant if a "Controller Inside" card is installed in the drive.		
dP1	<p>Parameter name: Fault code on fault n-1</p> <p>Logic address: 7201 = 16#1C21</p> <p>CANopen index: 202A/2</p> <p>INTERBUS index: 5FB9/56</p> <p>DeviceNet path: 85/01/02</p>		
	Type:	WORD (listing)	
	Read/write:	R	
	Unit:	-	

Log parameters

Code	Description		
ULP1	Parameter name: Supply voltage on fault n-1		
	Logic address: 7271 = 16#1C67	Type: UINT	
	CANopen index: 202A/48	Read/write: R	
	INTERBUS index: 5FB9/95	Unit: 0.1 V	
	DeviceNet path: 85/01/48		
LCP1	Parameter name: Motor current on fault n-1		
	Logic address: 7241 = 16#1C49	Type: INT	
	CANopen index: 202A/2A	Read/write: R	
	INTERBUS index: 5FB9/7A	Unit: 0.1 A	
	DeviceNet path: 85/01/2A		
rFP1	Parameter name: Output frequency on fault n-1		
	Logic address: 7251 = 16#1C53	Type: INT	
	CANopen index: 202A/34	Read/write: R	
	INTERBUS index: 5FB9/83	Unit: 0.1 Hz	
	DeviceNet path: 85/01/34		
tHP1	Parameter name: Motor thermal state on fault n-1		
	Logic address: 7281 = 16#1C71	Type: UINT	
	CANopen index: 202A/52	Read/write: R	
	INTERBUS index: 5FB9/9E	Unit: 1	
	DeviceNet path: 85/01/52		
EP1	Parameter name: Status word on fault n-1		
	Logic address: 7211 = 16#1C2B	Type: WORD (bit register)	
	CANopen index: 202A/C	Read/write: R	
	INTERBUS index: 5FB9/5F	Unit: -	
	DeviceNet path: 85/01/0C		
IP1	Parameter name: Extended status word on fault n-1		
	Logic address: 7221 = 16#1C35	Type: WORD (bit register)	
	CANopen index: 202A/16	Read/write: R	
	INTERBUS index: 5FB9/68	Unit: -	
	DeviceNet path: 85/01/16		
CMP1	Parameter name: Command word on fault n-1		
	Logic address: 7231 = 16#1C3F	Type: WORD (bit register)	
	CANopen index: 202A/20	Read/write: R	
	INTERBUS index: 5FB9/71	Unit: -	
	DeviceNet path: 85/01/20		

Log parameters

Code	Description		
dCC1	Parameter name: Active command channel on fault n-1		
	Logic address:	64301 = 16#FB2D	Type: WORD (listing)
	CANopen index:	2265/2	Read/write: R
	INTERBUS index:	5FBA/09	Unit: -
	DeviceNet path:	9A/01/09	
drC1	Parameter name: Active reference channel on fault n-1		
	Logic address:	64311 = 16#FB37	Type: WORD (listing)
	CANopen index:	2265/C	Read/write: R
	INTERBUS index:	5FBA/12	Unit: -
	DeviceNet path:	9A/01/12	
CrP1	Parameter name: Active channels on fault n-1		
	Logic address:	7291 = 16#1C7B	Type: WORD
	CANopen index:	202A/5C	Read/write: R
	INTERBUS index:	5FB9/A7	Unit: -
	DeviceNet path:	85/01/5C	
rtP1	Parameter name: Motor operating time on fault n-1		
	Logic address:	7261 = 16#1C5D	Type: UINT
	CANopen index:	202A/3E	Read/write: R
	INTERBUS index:	5FB9/8C	Unit: 1 hour
	DeviceNet path:	85/01/3E	
Md1	Parameter name: Date on fault n-1		
	Logic address:	7301 = 16#1C85	Type: UINT
	CANopen index:	202B/2	Read/write: R
	INTERBUS index:	5FB9/B0	Unit: 1
	DeviceNet path:	85/01/66	
dM1	Parameter name: Time on fault n-1		
	Logic address:	7311 = 16#1C8F	Type: UINT
	CANopen index:	202B/C	Read/write: R
	INTERBUS index:	5FB9/B9	Unit: 1
	DeviceNet path:	85/01/70	
dP2	Parameter name: Fault code on fault n-2		
	Logic address:	7202 = 16#1C22	Type: WORD (listing)
	CANopen index:	202A/3	Read/write: R
	INTERBUS index:	5FB9/57	Unit: -
	DeviceNet path:	85/01/03	

Log parameters

Code	Description		
ULP2	Parameter name: Supply voltage on fault n-2		
	Logic address: 7272 = 16#1C68	Type: UINT	
	CANopen index: 202A/49	Read/write: R	
	INTERBUS index: 5FB9/96	Unit: 0.1 V	
	DeviceNet path: 85/01/49		
LCP2	Parameter name: Motor current on fault n-2		
	Logic address: 7242 = 16#1C4A	Type: INT	
	CANopen index: 202A/2B	Read/write: R	
	INTERBUS index: 5FB9/7B	Unit: 0.1 A	
	DeviceNet path: 85/01/2B		
rFP2	Parameter name: Output frequency on fault n-2		
	Logic address: 7252 = 16#1C54	Type: INT	
	CANopen index: 202A/35	Read/write: R	
	INTERBUS index: 5FB9/84	Unit: 0.1 Hz	
	DeviceNet path: 85/01/35		
tHP2	Parameter name: Motor thermal state on fault n-2		
	Logic address: 7282 = 16#1C72	Type: UINT	
	CANopen index: 202A/53	Read/write: R	
	INTERBUS index: 5FB9/9F	Unit: 1	
	DeviceNet path: 85/01/53		
EP2	Parameter name: Status word on fault n-2		
	Logic address: 7212 = 16#1C2C	Type: WORD (bit register)	
	CANopen index: 202A/D	Read/write: R	
	INTERBUS index: 5FB9/60	Unit: -	
	DeviceNet path: 85/01/0D		
IP2	Parameter name: Extended status word on fault n-2		
	Logic address: 7222 = 16#1C36	Type: WORD (bit register)	
	CANopen index: 202A/17	Read/write: R	
	INTERBUS index: 5FB9/69	Unit: -	
	DeviceNet path: 85/01/17		
CMP2	Parameter name: Command word on fault n-2		
	Logic address: 7232 = 16#1C40	Type: WORD (bit register)	
	CANopen index: 202A/21	Read/write: R	
	INTERBUS index: 5FB9/72	Unit: -	
	DeviceNet path: 85/01/21		

Log parameters

Code	Description		
dCC2	Parameter name: Active command channel on fault n-2		
	Logic address:	64302 = 16#FB2E	Type: WORD (listing)
	CANopen index:	2265/3	Read/write: R
	INTERBUS index:	5FBA/0A	Unit: -
	DeviceNet path:	9A/01/0A	
drC2	Parameter name: Active reference channel on fault n-2		
	Logic address:	64312 = 16#FB38	Type: WORD (listing)
	CANopen index:	2265/D	Read/write: R
	INTERBUS index:	5FBA/13	Unit: -
	DeviceNet path:	9A/01/13	
CrP2	Parameter name: Active channels on fault n-2		
	Logic address:	7292 = 16#1C7C	Type: WORD
	CANopen index:	202A/5D	Read/write: R
	INTERBUS index:	5FB9/A8	Unit: -
	DeviceNet path:	85/01/5D	
rtP2	Parameter name: Motor operating time on fault n-2		
	Logic address:	7262 = 16#1C5E	Type: UINT
	CANopen index:	202A/3F	Read/write: R
	INTERBUS index:	5FB9/8D	Unit: 1 hour
	DeviceNet path:	85/01/3F	
Md2	Parameter name: Date on fault n-2		
	Logic address:	7302 = 16#1C86	Type: UINT
	CANopen index:	202B/3	Read/write: R
	INTERBUS index:	5FB9/B1	Unit: 1
	DeviceNet path:	85/01/67	
dM2	Parameter name: Time on fault n-2		
	Logic address:	7312 = 16#1C90	Type: UINT
	CANopen index:	202B/D	Read/write: R
	INTERBUS index:	5FB9/BA	Unit: 1
	DeviceNet path:	85/01/71	
dP3	Parameter name: Fault code on fault n-3		
	Logic address:	7203 = 16#1C23	Type: WORD (listing)
	CANopen index:	202A/4	Read/write: R
	INTERBUS index:	5FB9/58	Unit: -
	DeviceNet path:	85/01/04	

Log parameters

Code	Description		
ULP3	Parameter name: Supply voltage on fault n-3		
	Logic address: 7273 = 16#1C69	Type: UINT	
	CANopen index: 202A/4A	Read/write: R	
	INTERBUS index: 5FB9/97	Unit: 0.1 V	
	DeviceNet path: 85/01/4A		
LCP3	Parameter name: Motor current on fault n-3		
	Logic address: 7243 = 16#1C4B	Type: INT	
	CANopen index: 202A/2C	Read/write: R	
	INTERBUS index: 5FB9/7C	Unit: 0.1 A	
	DeviceNet path: 85/01/2C		
rFP3	Parameter name: Output frequency on fault n-3		
	Logic address: 7253 = 16#1C55	Type: INT	
	CANopen index: 202A/36	Read/write: R	
	INTERBUS index: 5FB9/85	Unit: 0.1 Hz	
	DeviceNet path: 85/01/36		
tHP3	Parameter name: Motor thermal state on fault n-3		
	Logic address: 7283 = 16#1C73	Type: UINT	
	CANopen index: 202A/54	Read/write: R	
	INTERBUS index: 5FB9/A0	Unit: 1	
	DeviceNet path: 85/01/54		
EP3	Parameter name: Status word on fault n-3		
	Logic address: 7213 = 16#1C2D	Type: WORD (bit register)	
	CANopen index: 202A/E	Read/write: R	
	INTERBUS index: 5FB9/61	Unit: -	
	DeviceNet path: 85/01/0E		
IP3	Parameter name: Extended status word on fault n-3		
	Logic address: 7223 = 16#1C37	Type: WORD (bit register)	
	CANopen index: 202A/18	Read/write: R	
	INTERBUS index: 5FB9/6A	Unit: -	
	DeviceNet path: 85/01/18		
CMP3	Parameter name: Command word on fault n-3		
	Logic address: 7233 = 16#1C41	Type: WORD (bit register)	
	CANopen index: 202A/22	Read/write: R	
	INTERBUS index: 5FB9/73	Unit: -	
	DeviceNet path: 85/01/22		

Log parameters

Code	Description		
dCC3	Parameter name: Active command channel on fault n-3		
	Logic address:	64303 = 16#FB2F	Type: WORD (listing)
	CANopen index:	2265/4	Read/write: R
	INTERBUS index:	5FBA/0B	Unit: -
	DeviceNet path:	9A/01/0B	
drC3	Parameter name: Active reference channel on fault n-3		
	Logic address:	64313 = 16#FB39	Type: WORD (listing)
	CANopen index:	2265/E	Read/write: R
	INTERBUS index:	5FBA/14	Unit: -
	DeviceNet path:	9A/01/14	
CrP3	Parameter name: Active channels on fault n-3		
	Logic address:	7293 = 16#1C7D	Type: WORD
	CANopen index:	202A/5E	Read/write: R
	INTERBUS index:	5FB9/A9	Unit: -
	DeviceNet path:	85/01/5E	
rtP3	Parameter name: Motor operating time on fault n-3		
	Logic address:	7263 = 16#1C5F	Type: UINT
	CANopen index:	202A/40	Read/write: R
	INTERBUS index:	5FB9/8E	Unit: 1 hour
	DeviceNet path:	85/01/40	
Md3	Parameter name: Date on fault n-3		
	Logic address:	7303 = 16#1C87	Type: UINT
	CANopen index:	202B/4	Read/write: R
	INTERBUS index:	5FB9/B2	Unit: 1
	DeviceNet path:	85/01/68	
dM3	Parameter name: Time on fault n-3		
	Logic address:	7313 = 16#1C91	Type: UINT
	CANopen index:	202B/E	Read/write: R
	INTERBUS index:	5FB9/BB	Unit: 1
	DeviceNet path:	85/01/72	
dP4	Parameter name: Fault code on fault n-4		
	Logic address:	7204 = 16#1C24	Type: WORD (listing)
	CANopen index:	202A/5	Read/write: R
	INTERBUS index:	5FB9/59	Unit: -
	DeviceNet path:	85/01/05	

Log parameters

Code	Description		
ULP4	Parameter name: Supply voltage on fault n-4		
	Logic address: 7274 = 16#1C6A	Type: UINT	
	CANopen index: 202A/4B	Read/write: R	
	INTERBUS index: 5FB9/98	Unit: 0.1 V	
	DeviceNet path: 85/01/4B		
LCP4	Parameter name: Motor current on fault n-4		
	Logic address: 7244 = 16#1C4C	Type: INT	
	CANopen index: 202A/2D	Read/write: R	
	INTERBUS index: 5FB9/7D	Unit: 0.1 A	
	DeviceNet path: 85/01/2D		
rFP4	Parameter name: Output frequency on fault n-4		
	Logic address: 7254 = 16#1C56	Type: INT	
	CANopen index: 202A/37	Read/write: R	
	INTERBUS index: 5FB9/86	Unit: 0.1 Hz	
	DeviceNet path: 85/01/37		
tHP4	Parameter name: Motor thermal state on fault n-4		
	Logic address: 7284 = 16#1C74	Type: UINT	
	CANopen index: 202A/55	Read/write: R	
	INTERBUS index: 5FB9/A1	Unit: 1	
	85/01/55		
EP4	Parameter name: Status word on fault n-4		
	Logic address: 7214 = 16#1C2E	Type: WORD (bit register)	
	CANopen index: 202A/F	Read/write: R	
	INTERBUS index: 5FB9/62	Unit: -	
	DeviceNet path: 85/01/0F		
IP4	Parameter name: Extended status word on fault n-4		
	Logic address: 7224 = 16#1C38	Type: WORD (bit register)	
	CANopen index: 202A/19	Read/write: R	
	INTERBUS index: 5FB9/6B	Unit: -	
	DeviceNet path: 85/01/19		
CMP4	Parameter name: Command word on fault n-4		
	Logic address: 7234 = 16#1C42	Type: WORD (bit register)	
	CANopen index: 202A/23	Read/write: R	
	INTERBUS index: 5FB9/74	Unit: -	
	DeviceNet path: 85/01/23		

Log parameters

Code	Description		
dCC4	Parameter name: Active command channel on fault n-4		
	Logic address:	64304 = 16#FB30	Type: WORD (listing)
	CANopen index:	2265/5	Read/write: R
	INTERBUS index:	5FBA/0C	Unit: -
	DeviceNet path:	9A/01/0C	
drC4	Parameter name: Active reference channel on fault n-4		
	Logic address:	64314 = 16#FB3A	Type: WORD (listing)
	CANopen index:	2265/F	Read/write: R
	INTERBUS index:	5FBA/15	Unit: -
	DeviceNet path:	9A/01/15	
CrP4	Parameter name: Active channels on fault n-4		
	Logic address:	7294 = 16#1C7E	Type: WORD
	CANopen index:	202A/5F	Read/write: R
	INTERBUS index:	5FB9/AA	Unit: -
	DeviceNet path:	85/01/5F	
rtP4	Parameter name: Motor operating time on fault n-4		
	Logic address:	7264 = 16#1C60	Type: UINT
	CANopen index:	202A/41	Read/write: R
	INTERBUS index:	5FB9/8F	Unit: 1 hour
	DeviceNet path:	85/01/41	
Md4	Parameter name: Date on fault n-4		
	Logic address:	7304 = 16#1C88	Type: UINT
	CANopen index:	202B/5	Read/write: R
	INTERBUS index:	5FB9/B3	Unit: 1
	DeviceNet path:	85/01/69	
dM4	Parameter name: Time on fault n-4		
	Logic address:	7314 = 16#1C92	Type: UINT
	CANopen index:	202B/F	Read/write: R
	INTERBUS index:	5FB9/BC	Unit: 1
	DeviceNet path:	85/01/73	
dP5	Parameter name: Fault code on fault n-5		
	Logic address:	7205 = 16#1C25	Type: WORD (listing)
	CANopen index:	202A/6	Read/write: R
	INTERBUS index:	5FB9/5A	Unit: -
	DeviceNet path:	85/01/06	

Log parameters

Code	Description		
ULP5	Parameter name: Supply voltage on fault n-5		
	Logic address: 7275 = 16#1C6B	Type: UINT	
	CANopen index: 202A/4C	Read/write: R	
	INTERBUS index: 5FB9/99	Unit: 0.1 V	
	DeviceNet path: 85/01/4C		
LCP5	Parameter name: Motor current on fault n-5		
	Logic address: 7245 = 16#1C4D	Type: INT	
	CANopen index: 202A/2E	Read/write: R	
	INTERBUS index: 5FB9/7E	Unit: 0.1 A	
	DeviceNet path: 85/01/2E		
rFP5	Parameter name: Output frequency on fault n-5		
	Logic address: 7255 = 16#1C57	Type: INT	
	CANopen index: 202A/38	Read/write: R	
	INTERBUS index: 5FB9/87	Unit: 0.1 Hz	
	DeviceNet path: 85/01/38		
tHP5	Parameter name: Motor thermal state on fault n-5		
	Logic address: 7285 = 16#1C75	Type: UINT	
	CANopen index: 202A/56	Read/write: R	
	INTERBUS index: 5FB9/A2	Unit: 1	
	DeviceNet path: 85/01/56		
EP5	Parameter name: Status word on fault n-5		
	Logic address: 7215 = 16#1C2F	Type: WORD (bit register)	
	CANopen index: 202A/10	Read/write: R	
	INTERBUS index: 5FB9/63	Unit: -	
	DeviceNet path: 85/01/10		
IP5	Parameter name: Extended status word on fault n-5		
	Logic address: 7225 = 16#1C39	Type: WORD (bit register)	
	CANopen index: 202A/1A	Read/write: R	
	INTERBUS index: 5FB9/6C	Unit: -	
	DeviceNet path: 85/01/1A		
CMP5	Parameter name: Command word on fault n-5		
	Logic address: 7235 = 16#1C43	Type: WORD (bit register)	
	CANopen index: 202A/24	Read/write: R	
	INTERBUS index: 5FB9/75	Unit: -	
	DeviceNet path: 85/01/24		

Log parameters

Code	Description		
dCC5	Parameter name: Active command channel on fault n-5		
	Logic address:	64305 = 16#FB31	Type: WORD (listing)
	CANopen index:	2265/6	Read/write: R
	INTERBUS index:	5FBA/0D	Unit: -
	DeviceNet path:	9A/01/0D	
drC5	Parameter name: Active reference channel on fault n-5		
	Logic address:	64315 = 16#FB3B	Type: WORD (listing)
	CANopen index:	2265/10	Read/write: R
	INTERBUS index:	5FBA/16	Unit: -
	DeviceNet path:	9A/01/16	
CrP5	Parameter name: Active channels on fault n-5		
	Logic address:	7295 = 16#1C7F	Type: WORD
	CANopen index:	202A/60	Read/write: R
	INTERBUS index:	5FB9/AB	Unit: -
	DeviceNet path:	85/01/60	
rtP5	Parameter name: Motor operating time on fault n-5		
	Logic address:	7265 = 16#1C61	Type: UINT
	CANopen index:	202A/42	Read/write: R
	INTERBUS index:	5FB9/90	Unit: 1 hour
	DeviceNet path:	85/01/42	
Md5	Parameter name: Date on fault n-5		
	Logic address:	7305 = 16#1C89	Type: UINT
	CANopen index:	202B/6	Read/write: R
	INTERBUS index:	5FB9/B4	Unit: 1
	DeviceNet path:	85/01/6A	
dM5	Parameter name: Time on fault n-5		
	Logic address:	7315 = 16#1C93	Type: UINT
	CANopen index:	202B/10	Read/write: R
	INTERBUS index:	5FB9/BD	Unit: 1
	DeviceNet path:	85/01/74	
dP6	Parameter name: Fault code on fault n-6		
	Logic address:	7206 = 16#1C26	Type: WORD (listing)
	CANopen index:	202A/7	Read/write: R
	INTERBUS index:	5FB9/5B	Unit: -
	DeviceNet path:	85/01/07	

Log parameters

Code	Description		
ULP6	Supply voltage on fault n-6		
Logic address:	7276 = 16#1C6C	Type:	UINT
CANopen index:	202A/4D	Read/write:	R
INTERBUS index:	5FB9/9A	Unit:	0.1 V
DeviceNet path:	85/01/4D		
LCP6	Motor current on fault n-6		
Logic address:	7246 = 16#1C4E	Type:	INT
CANopen index:	202A/2F	Read/write:	R
INTERBUS index:	5FB9/7F	Unit:	0.1 A
DeviceNet path:	85/01/2F		
rFP6	Output frequency on fault n-6		
Logic address:	7256 = 16#1C58	Type:	INT
CANopen index:	202A/39	Read/write:	R
INTERBUS index:	5FB9/88	Unit:	0.1 Hz
DeviceNet path:	85/01/39		
tHP6	Motor thermal state on fault n-6		
Logic address:	7286 = 16#1C76	Type:	UINT
CANopen index:	202A/57	Read/write:	R
INTERBUS index:	5FB9/A3	Unit:	1
DeviceNet path:	85/01/57		
EP6	Status word on fault n-6		
Logic address:	7216 = 16#1C30	Type:	WORD (bit register)
CANopen index:	202A/11	Read/write:	R
INTERBUS index:	5FB9/64	Unit:	-
DeviceNet path:	85/01/11		
IP6	Extended status word on fault n-6		
Logic address:	7226 = 16#1C3A	Type:	WORD (bit register)
CANopen index:	202A/1B	Read/write:	R
INTERBUS index:	5FB9/6D	Unit:	-
DeviceNet path:	85/01/1B		
CMP6	Command word on fault n-6		
Logic address:	7236 = 16#1C44	Type:	WORD (bit register)
CANopen index:	202A/25	Read/write:	R
INTERBUS index:	5FB9/76	Unit:	-
DeviceNet path:	85/01/25		

Log parameters

Code	Description		
dCC6	Parameter name: Active command channel on fault n-6		
	Logic address:	64306 = 16#FB32	Type: WORD (listing)
	CANopen index:	2265/7	Read/write: R
	INTERBUS index:	5FBA/0E	Unit: -
	DeviceNet path:	9A/01/0E	
drC6	Parameter name: Active reference channel on fault n-6		
	Logic address:	64316 = 16#FB3C	Type: WORD (listing)
	CANopen index:	2265/11	Read/write: R
	INTERBUS index:	5FBA/17	Unit: -
	DeviceNet path:	9A/01/17	
CrP6	Parameter name: Active channels on fault n-6		
	Logic address:	7296 = 16#1C80	Type: WORD
	CANopen index:	202A/61	Read/write: R
	INTERBUS index:	5FB9/AC	Unit: -
	DeviceNet path:	85/01/61	
rtP6	Parameter name: Motor operating time on fault n-6		
	Logic address:	7266 = 16#1C62	Type: UINT
	CANopen index:	202A/43	Read/write: R
	INTERBUS index:	5FB9/91	Unit: 1 hour
	DeviceNet path:	85/01/43	
Md6	Parameter name: Date on fault n-6		
	Logic address:	7306 = 16#1C8A	Type: UINT
	CANopen index:	202B/7	Read/write: R
	INTERBUS index:	5FB9/BE	Unit: 1
	DeviceNet path:	85/01/6B	
dM6	Parameter name: Time on fault n-6		
	Logic address:	7316 = 16#1C94	Type: UINT
	CANopen index:	202B/11	Read/write: R
	INTERBUS index:	5FB9/B5	Unit: 1
	DeviceNet path:	85/01/75	
dP7	Parameter name: Fault code on fault n-7		
	Logic address:	7207 = 16#1C27	Type: WORD (listing)
	CANopen index:	202A/8	Read/write: R
	INTERBUS index:	5FB9/5C	Unit: -
	DeviceNet path:	85/01/08	

Log parameters

Code	Description		
ULP7	Parameter name: Supply voltage on fault n-7		
	Logic address: 7277 = 16#1C6D	Type: UINT	
	CANopen index: 202A/4E	Read/write: R	
	INTERBUS index: 5FB9/9B	Unit: 0.1 V	
	DeviceNet path: 85/01/4E		
LCP7	Parameter name: Motor current on fault n-7		
	Logic address: 7247 = 16#1C4F	Type: INT	
	CANopen index: 202A/30	Read/write: R	
	INTERBUS index: 5FB9/80	Unit: 0.1 A	
	DeviceNet path: 85/01/30		
rFP7	Parameter name: Output frequency on fault n-7		
	Logic address: 7257 = 16#1C59	Type: INT	
	CANopen index: 202A/3A	Read/write: R	
	INTERBUS index: 5FB9/89	Unit: 0.1 Hz	
	DeviceNet path: 85/01/3A		
tHP7	Parameter name: Motor thermal state on fault n-7		
	Logic address: 7287 = 16#1C77	Type: UINT	
	CANopen index: 202A/58	Read/write: R	
	INTERBUS index: 5FB9/A4	Unit: 1	
	85/01/58		
EP7	Parameter name: Status word on fault n-7		
	Logic address: 7217 = 16#1C31	Type: WORD (bit register)	
	CANopen index: 202A/12	Read/write: R	
	INTERBUS index: 5FB9/65	Unit: -	
	DeviceNet path: 85/01/12		
IP7	Parameter name: Extended status word on fault n-7		
	Logic address: 7227 = 16#1C3B	Type: WORD (bit register)	
	CANopen index: 202A/1C	Read/write: R	
	INTERBUS index: 5FB9/6E	Unit: -	
	DeviceNet path: 85/01/1C		
CMP7	Parameter name: Command word on fault n-7		
	Logic address: 7237 = 16#1C45	Type: WORD (bit register)	
	CANopen index: 202A/26	Read/write: R	
	INTERBUS index: 5FB9/77	Unit: -	
	DeviceNet path: 85/01/26		

Log parameters

Code	Description		
dCC7	Parameter name: Active command channel on fault n-7		
	Logic address:	64307 = 16#FB33	Type: WORD (listing)
	CANopen index:	2265/8	Read/write: R
	INTERBUS index:	5FBA/0F	Unit: -
	DeviceNet path:	9A/01/0F	
drC7	Parameter name: Active reference channel on fault n-7		
	Logic address:	64317 = 16#FB3D	Type: WORD (listing)
	CANopen index:	2265/12	Read/write: R
	INTERBUS index:	5FBA/18	Unit: -
	DeviceNet path:	9A/01/18	
CrP7	Parameter name: Active channels on fault n-7		
	Logic address:	7297 = 16#1C81	Type: WORD
	CANopen index:	202A/62	Read/write: R
	INTERBUS index:	5FB9/AD	Unit: -
	DeviceNet path:	85/01/62	
rtP7	Parameter name: Motor operating time on fault n-7		
	Logic address:	7267 = 16#1C63	Type: UINT
	CANopen index:	202A/44	Read/write: R
	INTERBUS index:	5FB9/92	Unit: 1 hour
	DeviceNet path:	85/01/44	
Md7	Parameter name: Date on fault n-7		
	Logic address:	7307 = 16#1C8B	Type: UINT
	CANopen index:	202B/8	Read/write: R
	INTERBUS index:	5FB9/B6	Unit: 1
	DeviceNet path:	85/01/6C	
dM7	Parameter name: Time on fault n-7		
	Logic address:	7317 = 16#1C95	Type: UINT
	CANopen index:	202B/12	Read/write: R
	INTERBUS index:	5FB9/BF	Unit: 1
	DeviceNet path:	85/01/76	
dP8	Parameter name: Fault code on fault n-8		
	Logic address:	7208 = 16#1C28	Type: WORD (listing)
	CANopen index:	202A/9	Read/write: R
	INTERBUS index:	5FB9/5D	Unit: -
	DeviceNet path:	85/01/09	

Log parameters

Code	Description		
ULP8	Parameter name: Supply voltage on fault n-8		
	Logic address: 7278 = 16#1C6E	Type: UINT	
	CANopen index: 202A/4F	Read/write: R	
	INTERBUS index: 5FB9/9C	Unit: 0.1 V	
	DeviceNet path: 85/01/4F		
LCP8	Parameter name: Motor current on fault n-8		
	Logic address: 7248 = 16#1C50	Type: INT	
	CANopen index: 202A/31	Read/write: R	
	INTERBUS index: 5FB9/81	Unit: 0.1 A	
	DeviceNet path: 85/01/31		
rFP8	Parameter name: Output frequency on fault n-8		
	Logic address: 7258 = 16#1C5A	Type: INT	
	CANopen index: 202A/3B	Read/write: R	
	INTERBUS index: 5FB9/8A	Unit: 0.1 Hz	
	DeviceNet path: 85/01/3B		
tHP8	Parameter name: Motor thermal state on fault n-8		
	Logic address: 7288 = 16#1C78	Type: UINT	
	CANopen index: 202A/59	Read/write: R	
	INTERBUS index: 5FB9/A5	Unit: 1	
	85/01/59		
EP8	Parameter name: Status word on fault n-8		
	Logic address: 7218 = 16#1C32	Type: WORD (bit register)	
	CANopen index: 202A/13	Read/write: R	
	INTERBUS index: 5FB9/66	Unit: -	
	DeviceNet path: 85/01/13		
IP8	Parameter name: Extended status word on fault n-8		
	Logic address: 7228 = 16#1C3C	Type: WORD (bit register)	
	CANopen index: 202A/1D	Read/write: R	
	INTERBUS index: 5FB9/6F	Unit: -	
	DeviceNet path: 85/01/1D		
CMP8	Parameter name: Command word on fault n-8		
	Logic address: 7238 = 16#1C46	Type: WORD (bit register)	
	CANopen index: 202A/27	Read/write: R	
	INTERBUS index: 5FB9/78	Unit: -	
	DeviceNet path: 85/01/27		

Log parameters

Code	Description		
dCC8	Parameter name: Active command channel on fault n-8		
	Logic address:	64308 = 16#FB34	Type: WORD (listing)
	CANopen index:	2265/9	Read/write: R
	INTERBUS index:	5FBA/10	Unit: -
	DeviceNet path:	9A/01/10	
drC8	Parameter name: Active reference channel on fault n-8		
	Logic address:	64318 = 16#FB3E	Type: WORD (listing)
	CANopen index:	2265/13	Read/write: R
	INTERBUS index:	5FBA/19	Unit: -
	DeviceNet path:	9A/01/19	
CrP8	Parameter name: Active channels on fault n-8		
	Logic address:	7298 = 16#1C82	Type: WORD
	CANopen index:	202A/63	Read/write: R
	INTERBUS index:	5FB9/AE	Unit: -
	DeviceNet path:	85/01/63	
rtP8	Parameter name: Motor operating time on fault n-8		
	Logic address:	7268 = 16#1C64	Type: UINT
	CANopen index:	202A/45	Read/write: R
	INTERBUS index:	5FB9/93	Unit: 1 hour
	DeviceNet path:	85/01/45	
Md8	Parameter name: Date on fault n-8		
	Logic address:	7308 = 16#1C8C	Type: UINT
	CANopen index:	202B/9	Read/write: R
	INTERBUS index:	5FB9/B7	Unit: 1
	DeviceNet path:	85/01/6D	
dM8	Parameter name: Time on fault n-8		
	Logic address:	7318 = 16#1C96	Type: UINT
	CANopen index:	202B/13	Read/write: R
	INTERBUS index:	5FB9/C0	Unit: 1
	DeviceNet path:	85/01/77	

Identification parameters

Code	Description		
nCV	Parameter name: Drive nominal rating		
	Logic address:	3011 = 16#0BC3	Type: WORD (listing)
	CANopen index:	2000/C	Read/write: 0
	INTERBUS index:	5FB0/02	Unit: -
	DeviceNet path:	70/01/0C	
UCAL	Parameter name: Drive line voltage		
	Logic address:	3012 = 16#0BC4	Type: WORD (listing)
	CANopen index:	2000/D	Read/write: 0
	INTERBUS index:	5FB0/03	Unit: -
	DeviceNet path:	70/01/0D	
InV	Parameter name: Rated drive current		
	Logic address:	3017 = 16#0BC9	Type: UINT
	CANopen index:	2000/12	Read/write: 0
	INTERBUS index:	5FB0/07	Unit: 0.1 A
	DeviceNet path:	70/01/12	
UdP	Parameter name: Drive software version		
	Logic address:	3302 = 16#0CE6	Type: UINT
	CANopen index:	2003/3	Read/write: 0
	INTERBUS index:	5FB0/0D	Unit: 1
	DeviceNet path:	71/01/67	
PAn0	Parameter name: Device name: characters 1 and 2		
	Logic address:	3340 = 16#0D0C	Type: UINT
	CANopen index:	2003/29	Read/write: R
	INTERBUS index:	5FB0/25	Unit:
	DeviceNet path:	71/01/8D	
	If the device is called "Ventilateur 1234", the values of the "device name" parameters are: PAn0 = 16#5665 = "Ve" PAn1 = 16#6E74 = "nt" PAn2 = 16#696C = "il" PAn3 = 16#6174 = "at" PAn4 = 16#6575 = "eu" PAn5 = 16#7220 = "r " PAn6 = 16#3132 = "12" PAn7 = 16#3334 = "34"		
PAn1	Parameter name: Device name: characters 3 and 4		
	Logic address:	3341 = 16#0D0D	Type: UINT
	CANopen index:	2003/2A	Read/write: R
	INTERBUS index:	5FB0/26	Unit:
	DeviceNet path:	71/01/8E	

Identification parameters

Code	Description		
PAn2	Parameter name: Device name: characters 5 and 6		
Logic address:	3342 = 16#0D0E	Type:	UINT
CANopen index:	2003/2B	Read/write:	R
INTERBUS index:	5FB0/27	Unit:	
DeviceNet path:	71/01/8F		
PAn3	Parameter name: Device name: characters 7 and 8		
Logic address:	3343 = 16#0D0F	Type:	UINT
CANopen index:	2003/2C	Read/write:	R
INTERBUS index:	5FB0/28	Unit:	
DeviceNet path:	71/01/90		
PAn4	Parameter name: Device name: characters 9 and 10		
Logic address:	3344 = 16#0D10	Type:	UINT
CANopen index:	2003/2D	Read/write:	R
INTERBUS index:	5FB0/29	Unit:	
DeviceNet path:	71/01/91		
PAn5	Parameter name: Device name: characters 11 and 12		
Logic address:	3345 = 16#0D11	Type:	UINT
CANopen index:	2003/2E	Read/write:	R
INTERBUS index:	5FB0/2A	Unit:	
DeviceNet path:	71/01/92		
PAn6	Parameter name: Device name: characters 13 and 14		
Logic address:	3346 = 16#0D12	Type:	UINT
CANopen index:	2003/2F	Read/write:	R
INTERBUS index:	5FB0/2B	Unit:	
DeviceNet path:	71/01/93		
PAn7	Parameter name: Device name: characters 15 and 16		
Logic address:	3347 = 16#0D13	Type:	UINT
CANopen index:	2003/30	Read/write:	R
INTERBUS index:	5FB0/2C	Unit:	
DeviceNet path:	71/01/94		

Communication parameters

Code	Description		
nC1	Parameter name: Communication scanner, value of write word 1		
	Logic address:	12761 = 16#31D9	Type: UINT
	CANopen index:	2061/3E	Read/write: 0
	INTERBUS index:	5FB6/4B	Unit: 1
	DeviceNet path:	A0/01/A2	
nC2	Parameter name: Communication scanner, value of write word 2		
	Logic address:	12762 = 16#31DA	Type: UINT
	CANopen index:	2061/3F	Read/write: 0
	INTERBUS index:	5FB6/4C	Unit: 1
	DeviceNet path:	A0/01/A3	
nC3	Parameter name: Communication scanner, value of write word 3		
	Logic address:	12763 = 16#31DB	Type: UINT
	CANopen index:	2061/40	Read/write: 0
	INTERBUS index:	5FB6/4D	Unit: 1
	DeviceNet path:	A0/01/A4	
nC4	Parameter name: Communication scanner, value of write word 4		
	Logic address:	12764 = 16#31DC	Type: UINT
	CANopen index:	2061/41	Read/write: 0
	INTERBUS index:	5FB6/4E	Unit: 1
	DeviceNet path:	A0/01/A5	
nC5	Parameter name: Communication scanner, value of write word 5		
	Logic address:	12765 = 16#31DD	Type: UINT
	CANopen index:	2061/42	Read/write: 0
	INTERBUS index:	5FB6/4F	Unit: 1
	DeviceNet path:	A0/01/A6	
nC6	Parameter name: Communication scanner, value of write word 6		
	Logic address:	12766 = 16#31DE	Type: UINT
	CANopen index:	2061/43	Read/write: 0
	INTERBUS index:	5FB6/50	Unit: 1
	DeviceNet path:	A0/01/A7	
nC7	Parameter name: Communication scanner, value of write word 7		
	Logic address:	12767 = 16#31DF	Type: UINT
	CANopen index:	2061/44	Read/write: 0
	INTERBUS index:	5FB6/51	Unit: 1
	DeviceNet path:	A0/01/A8	

Communication parameters

Code	Description		
nC8	Parameter name: Communication scanner, value of write word 8		
	Logic address:	12768 = 16#31E0	Type: UINT
	CANopen index:	2061/45	Read/write: 0
	INTERBUS index:	5FB6/52	Unit: 1
	DeviceNet path:	A0/01/A9	
nM1	Parameter name: Communication scanner, value of read word 1		
	Logic address:	12741 = 16#31C5	Type: UINT
	CANopen index:	2061/2A	Read/write: 0
	INTERBUS index:	5FB6/43	Unit: 1
	DeviceNet path:	A0/01/8E	
nM2	Parameter name: Communication scanner, value of read word 2		
	Logic address:	12742 = 16#31C6	Type: UINT
	CANopen index:	2061/2B	Read/write: 0
	INTERBUS index:	5FB6/44	Unit: 1
	DeviceNet path:	A0/01/8F	
nM3	Parameter name: Communication scanner, value of read word 3		
	Logic address:	12743 = 16#31C7	Type: UINT
	CANopen index:	2061/2C	Read/write: 0
	INTERBUS index:	5FB6/45	Unit: 1
	DeviceNet path:	A0/01/90	
nM4	Parameter name: Communication scanner, value of read word 4		
	Logic address:	12744 = 16#31C8	Type: UINT
	CANopen index:	2061/2D	Read/write: 0
	INTERBUS index:	5FB6/46	Unit: 1
	DeviceNet path:	A0/01/91	
nM5	Parameter name: Communication scanner, value of read word 5		
	Logic address:	12745 = 16#31C9	Type: UINT
	CANopen index:	2061/2E	Read/write: 0
	INTERBUS index:	5FB6/47	Unit: 1
	DeviceNet path:	A0/01/92	
nM6	Parameter name: Communication scanner, value of read word 6		
	Logic address:	12746 = 16#31CA	Type: UINT
	CANopen index:	2061/2F	Read/write: 0
	INTERBUS index:	5FB6/48	Unit: 1
	DeviceNet path:	A0/01/93	

Communication parameters

Code	Description		
nM7	Parameter name: Communication scanner, value of read word 7		
	Logic address:	12747 = 16#31CB	Type: UINT
	CANopen index:	2061/30	Read/write: 0
	INTERBUS index:	5FB6/49	Unit: 1
	DeviceNet path:	A0/01/94	
nM8	Parameter name: Communication scanner, value of read word 8		
	Logic address:	12748 = 16#31CC	Type: UINT
	CANopen index:	2061/31	Read/write: 0
	INTERBUS index:	5FB6/4A	Unit: 1
	DeviceNet path:	A0/01/95	
nCA1	Parameter name: Communication scanner, address of write word 1		
	Logic address:	12721 = 16#31B1	Type: UINT
	CANopen index:	2061/16	Read/write: 0
	INTERBUS index:	5FBD/46	Unit: 1
	DeviceNet path:	A0/01/7A	
nCA2	Parameter name: Communication scanner, address of write word 2		
	Logic address:	12722 = 16#31B2	Type: UINT
	CANopen index:	2061/17	Read/write: 0
	INTERBUS index:	5FBD/47	Unit: 1
	DeviceNet path:	A0/01/7B	
nCA3	Parameter name: Communication scanner, address of write word 3		
	Logic address:	12723 = 16#31B3	Type: UINT
	CANopen index:	2061/18	Read/write: 0
	INTERBUS index:	5FBD/48	Unit: 1
	DeviceNet path:	A0/01/7C	
nCA4	Parameter name: Communication scanner, address of write word 4		
	Logic address:	12724 = 16#31B4	Type: UINT
	CANopen index:	2061/19	Read/write: 0
	INTERBUS index:	5FBD/49	Unit: 1
	DeviceNet path:	A0/01/7D	
nCA5	Parameter name: Communication scanner, address of write word 5		
	Logic address:	12725 = 16#31B5	Type: UINT
	CANopen index:	2061/1A	Read/write: 0
	INTERBUS index:	5FBD/4A	Unit: 1
	DeviceNet path:	A0/01/7E	

Communication parameters

Code	Description		
nCA6	Parameter name: Communication scanner, address of write word 6		
	Logic address:	12726 = 16#31B6	Type: UINT
	CANopen index:	2061/1B	Read/write: 0
	INTERBUS index:	5FBD/4B	Unit: 1
	DeviceNet path:	A0/01/7F	
nCA7	Parameter name: Communication scanner, address of write word 7		
	Logic address:	12727 = 16#31B7	Type: UINT
	CANopen index:	2061/1C	Read/write: 0
	INTERBUS index:	5FBD/4C	Unit: 1
	DeviceNet path:	A0/01/80	
nCA8	Parameter name: Communication scanner, address of write word 8		
	Logic address:	12728 = 16#31B8	Type: UINT
	CANopen index:	2061/1D	Read/write: 0
	INTERBUS index:	5FBD/4D	Unit: 1
	DeviceNet path:	A0/01/81	
nMA1	Parameter name: Communication scanner, address of read word 1		
	Logic address:	12701 = 16#319D	Type: UINT
	CANopen index:	2061/2	Read/write: 0
	INTERBUS index:	5FBD/3E	Unit: 1
	DeviceNet path:	A0/01/66	
nMA2	Parameter name: Communication scanner, address of read word 2		
	Logic address:	12702 = 16#319E	Type: UINT
	CANopen index:	2061/3	Read/write: 0
	INTERBUS index:	5FBD/3F	Unit: 1
	DeviceNet path:	A0/01/67	
nMA3	Parameter name: Communication scanner, address of read word 3		
	Logic address:	12703 = 16#319F	Type: UINT
	CANopen index:	2061/4	Read/write: 0
	INTERBUS index:	5FBD/40	Unit: 1
	DeviceNet path:	A0/01/68	
nMA4	Parameter name: Communication scanner, address of read word 4		
	Logic address:	12704 = 16#31A0	Type: UINT
	CANopen index:	2061/5	Read/write: 0
	INTERBUS index:	5FBD/41	Unit: 1
	DeviceNet path:	A0/01/69	

Communication parameters

Code	Description		
nMA5	Parameter name: Communication scanner, address of read word 5		
Logic address:	12705 = 16#31A1	Type:	UINT
CANopen index:	2061/6	Read/write:	0
INTERBUS index:	5FBD/42	Unit:	1
DeviceNet path:	A0/01/6A		
nMA6	Parameter name: Communication scanner, address of read word 6		
Logic address:	12706 = 16#31A2	Type:	UINT
CANopen index:	2061/7	Read/write:	0
INTERBUS index:	5FBD/43	Unit:	1
DeviceNet path:	A0/01/6B		
nMA7	Parameter name: Communication scanner, address of read word 7		
Logic address:	12707 = 16#31A3	Type:	UINT
CANopen index:	2061/8	Read/write:	0
INTERBUS index:	5FBD/44	Unit:	1
DeviceNet path:	A0/01/6C		
nMA8	Parameter name: Communication scanner, address of read word 8		
Logic address:	12708 = 16#31A4	Type:	UINT
CANopen index:	2061/9	Read/write:	0
INTERBUS index:	5FBD/45	Unit:	1
DeviceNet path:	A0/01/6D		

"Controller Inside" parameters

Code	Description		
PIL1	Parameter name: "Controller Inside" logic input map		
	Logic address:	6901 = 16#1AF5	Type: WORDXXX
	CANopen index:	2027/2	Read/write: R
	INTERBUS index:	5FB9/49	Unit: -
	DeviceNet path:	83/01/66	
POL1	Parameter name: "Controller Inside" logic output map		
	Logic address:	6911 = 16#1AFF	Type: WORDXXX
	CANopen index:	2027/C	Read/write: R
	INTERBUS index:	5FB9/4A	Unit: -
	DeviceNet path:	83/01/70	
PAI1	Parameter name: Physical image of analog input 1		
	Logic address:	6942 = 16#1B1E	Type: UINT
	CANopen index:	2027/2B	Read/write: R
	INTERBUS index:	5FB9/4B	Unit: 0.001 mA
	DeviceNet path:	83/01/8F	
PAI2	Parameter name: Physical image of analog input 2		
	Logic address:	6943 = 16#1B1F	Type: UINT
	CANopen index:	2027/2C	Read/write: R
	INTERBUS index:	5FB9/4C	Unit: 0.001 mA
	DeviceNet path:	83/01/90	
PAO1	Parameter name: Physical image of analog output 1		
	Logic address:	6971 = 16#1B3B	Type: UINT
	CANopen index:	2027/48	Read/write: R
	INTERBUS index:	5FB9/4D	Unit: 0.001 mA
	DeviceNet path:	83/01/AC	
PAO2	Parameter name: Physical image of analog output 2		
	Logic address:	6972 = 16#1B3C	Type: UINT
	CANopen index:	2027/49	Read/write: R
	INTERBUS index:	5FB9/4E	Unit: 0.001 mA
	DeviceNet path:	83/01/AD	
Pdl	Parameter name: Drive encoder counter divisor		
	Logic address:	5610 = 16#15EA	Type: UINT
	CANopen index:	201A/B	Read/write: R
	INTERBUS index:	5FBC/76	Unit: 1
	DeviceNet path:	7D/01/0B	

"Controller Inside" parameters

Code	Description		
PUC	Parameter name: Drive encoder divided counter		
	Logic address: 5611 = 16#15EB	Type: UINT	
	CANopen index: 201A/C	Read/write: R	
	INTERBUS index: 5FB9/41	Unit: 1	
	DeviceNet path: 7D/01/0C		
dAY	Parameter name: Date		
	Logic address: 7391 = 16#1CDF	Type: UINT	
	CANopen index: 202B/5C	Read/write: R	
	INTERBUS index: 5FB9/CA	Unit: 1	
	DeviceNet path: 85/01/C0		
tIME	Parameter name: Time		
	Logic address: 7392 = 16#1CE0	Type: UINT	
	CANopen index: 202B/5D	Read/write: R	
	INTERBUS index: 5FB9/CB	Unit: 1	
	DeviceNet path: 85/01/C1		
O01	Parameter name: "Controller Inside" HMI exchange word 1		
	Logic address: 6401 = 16#1901	Type: UINT	
	CANopen index: 2022/2	Read/write: R	
	INTERBUS index: 5FB3/81	Unit: 1	
	DeviceNet path: 81/01/02		
O02	Parameter name: "Controller Inside" HMI exchange word 2		
	Logic address: 6402 = 16#1902	Type: UINT	
	CANopen index: 2022/3	Read/write: R	
	INTERBUS index: 5FB3/82	Unit: 1	
	DeviceNet path: 81/01/03		
O03	Parameter name: "Controller Inside" HMI exchange word 3		
	Logic address: 6403 = 16#1903	Type: UINT	
	CANopen index: 2022/4	Read/write: R	
	INTERBUS index: 5FB3/83	Unit: 1	
	DeviceNet path: 81/01/04		
O04	Parameter name: "Controller Inside" HMI exchange word 4		
	Logic address: 6404 = 16#1904	Type: UINT	
	CANopen index: 2022/5	Read/write: R	
	INTERBUS index: 5FB3/84	Unit: 1	
	DeviceNet path: 81/01/05		

"Controller Inside" parameters

Code	Description		
O05	Parameter name: "Controller Inside" HMI exchange word 5		
	Logic address:	6405 = 16#1905	Type: UINT
	CANopen index:	2022/6	Read/write: R
	INTERBUS index:	5FB3/85	Unit: 1
	DeviceNet path:	81/01/06	
O06	Parameter name: "Controller Inside" HMI exchange word 6		
	Logic address:	6406 = 16#1906	Type: UINT
	CANopen index:	2022/7	Read/write: R
	INTERBUS index:	5FB3/86	Unit: 1
	DeviceNet path:	81/01/07	
O07	Parameter name: "Controller Inside" HMI exchange word 7		
	Logic address:	6407 = 16#1907	Type: UINT
	CANopen index:	2022/8	Read/write: R
	INTERBUS index:	5FB3/87	Unit: 1
	DeviceNet path:	81/01/08	
O08	Parameter name: "Controller Inside" HMI exchange word 8		
	Logic address:	6408 = 16#1908	Type: UINT
	CANopen index:	2022/9	Read/write: R
	INTERBUS index:	5FB3/88	Unit: 1
	DeviceNet path:	81/01/09	
O09	Parameter name: "Controller Inside" HMI exchange word 9		
	Logic address:	6409 = 16#1909	Type: UINT
	CANopen index:	2022/A	Read/write: R
	INTERBUS index:	5FB3/89	Unit: 1
	DeviceNet path:	81/01/0A	
O10	Parameter name: "Controller Inside" HMI exchange word 10		
	Logic address:	6410 = 16#190A	Type: UINT
	CANopen index:	2022/B	Read/write: R
	INTERBUS index:	5FB3/8A	Unit: 1
	DeviceNet path:	81/01/0B	
O11	Parameter name: "Controller Inside" HMI exchange word 11		
	Logic address:	6411 = 16#190B	Type: UINT
	CANopen index:	2022/C	Read/write: R
	INTERBUS index:	5FB3/8B	Unit: 1
	DeviceNet path:	81/01/0C	

"Controller Inside" parameters

Code	Description		
O12	Parameter name: "Controller Inside" HMI exchange word 12		
	Logic address:	6412 = 16#190C	Type: UINT
	CANopen index:	2022/D	Read/write: R
	INTERBUS index:	5FB3/8C	Unit: 0.1
	DeviceNet path:	81/01/0D	
O13	Parameter name: "Controller Inside" HMI exchange word 13		
	Logic address:	6413 = 16#190D	Type: UINT
	CANopen index:	2022/E	Read/write: R
	INTERBUS index:	5FB3/8D	Unit: 0.1
	DeviceNet path:	81/01/0E	
O14	Parameter name: "Controller Inside" HMI exchange word 14		
	Logic address:	6414 = 16#190E	Type: UINT
	CANopen index:	2022/F	Read/write: R
	INTERBUS index:	5FB3/8E	Unit: 0.1
	DeviceNet path:	81/01/0F	
O15	Parameter name: "Controller Inside" HMI exchange word 15		
	Logic address:	6415 = 16#190F	Type: UINT
	CANopen index:	2022/10	Read/write: R
	INTERBUS index:	5FB3/8F	Unit: 0.1
	DeviceNet path:	81/01/10	
O16	Parameter name: "Controller Inside" HMI exchange word 16		
	Logic address:	6416 = 16#1910	Type: UINT
	CANopen index:	2022/11	Read/write: R
	INTERBUS index:	5FB3/90	Unit: 0.1
	DeviceNet path:	81/01/11	
O17	Parameter name: "Controller Inside" HMI exchange word 17		
	Logic address:	6417 = 16#1911	Type: UINT
	CANopen index:	2022/12	Read/write: R
	INTERBUS index:	5FB3/91	Unit: 0.1
	DeviceNet path:	81/01/12	
O18	Parameter name: "Controller Inside" HMI exchange word 18		
	Logic address:	6418 = 16#1912	Type: UINT
	CANopen index:	2022/13	Read/write: R
	INTERBUS index:	5FB3/92	Unit: 0.01
	DeviceNet path:	81/01/13	

"Controller Inside" parameters

Code	Description		
O19	Parameter name: "Controller Inside" HMI exchange word 19		
Logic address:	6419 = 16#1913	Type:	UINT
CANopen index:	2022/14	Read/write:	R
INTERBUS index:	5FB3/93	Unit:	0.01
DeviceNet path:	81/01/14		
O20	Parameter name: "Controller Inside" HMI exchange word 20		
Logic address:	6420 = 16#1914	Type:	UINT
CANopen index:	2022/15	Read/write:	R
INTERBUS index:	5FB3/94	Unit:	0.01
DeviceNet path:	81/01/15		
O21	Parameter name: "Controller Inside" HMI exchange word 21		
Logic address:	6421 = 16#1915	Type:	UINT
CANopen index:	2022/16	Read/write:	R
INTERBUS index:	5FB3/95	Unit:	0.1
DeviceNet path:	81/01/16		
O22	Parameter name: "Controller Inside" HMI exchange word 22		
Logic address:	6422 = 16#1916	Type:	UINT
CANopen index:	2022/17	Read/write:	R
INTERBUS index:	5FB3/96	Unit:	0.1
DeviceNet path:	81/01/17		
O23	Parameter name: "Controller Inside" HMI exchange word 23		
Logic address:	6423 = 16#1917	Type:	UINT
CANopen index:	2022/18	Read/write:	R
INTERBUS index:	5FB3/97	Unit:	0.1
DeviceNet path:	81/01/18		
O24	Parameter name: "Controller Inside" HMI exchange word 24		
Logic address:	6424 = 16#1918	Type:	UINT
CANopen index:	2022/19	Read/write:	R
INTERBUS index:	5FB3/98	Unit:	0.1
DeviceNet path:	81/01/19		
O25	Parameter name: "Controller Inside" HMI exchange word 25		
Logic address:	6425 = 16#1919	Type:	UINT
CANopen index:	2022/1A	Read/write:	R
INTERBUS index:	5FB3/99	Unit:	0.1
DeviceNet path:	81/01/1A		

"Controller Inside" parameters

Code	Description		
O26	Parameter name: "Controller Inside" HMI exchange word 26		
	Logic address:	6426 = 16#191A	Type: UINT
	CANopen index:	2022/1B	Read/write: R
	INTERBUS index:	5FB3/9A	Unit: 0.1
	DeviceNet path:	81/01/1B	
O27	Parameter name: "Controller Inside" HMI exchange word 27		
	Logic address:	6427 = 16#191B	Type: UINT
	CANopen index:	2022/1C	Read/write: R
	INTERBUS index:	5FB3/9B	Unit: 0.1
	DeviceNet path:	81/01/1C	
O28	Parameter name: "Controller Inside" HMI exchange word 28		
	Logic address:	6428 = 16#191C	Type: UINT
	CANopen index:	2022/1D	Read/write: R
	INTERBUS index:	5FB3/9C	Unit: 0.1
	DeviceNet path:	81/01/1D	
O29	Parameter name: "Controller Inside" HMI exchange word 29		
	Logic address:	6429 = 16#191D	Type: UINT
	CANopen index:	2022/1E	Read/write: R
	INTERBUS index:	5FB3/9D	Unit: 0.1
	DeviceNet path:	81/01/1E	
O30	Parameter name: "Controller Inside" HMI exchange word 30		
	Logic address:	6430 = 16#191E	Type: UINT
	CANopen index:	2022/1F	Read/write: R
	INTERBUS index:	5FB3/9E	Unit: 0.1
	DeviceNet path:	81/01/1F	
O31	Parameter name: "Controller Inside" HMI exchange word 31		
	Logic address:	6431 = 16#191F	Type: UINT
	CANopen index:	2022/20	Read/write: R
	INTERBUS index:	5FB3/9F	Unit: 0.1
	DeviceNet path:	81/01/20	
O32	Parameter name: "Controller Inside" HMI exchange word 32		
	Logic address:	6432 = 16#1920	Type: UINT
	CANopen index:	2022/21	Read/write: R
	INTERBUS index:	5FB3/A0	Unit: 0.1
	DeviceNet path:	81/01/21	

"Controller Inside" parameters

Code	Description		
O33	Parameter name: "Controller Inside" HMI exchange word 33		
	Logic address:	6433 = 16#1921	Type: UINT
	CANopen index:	2022/22	Read/write: R
	INTERBUS index:	5FB3/A1	Unit: 0.1
	DeviceNet path:	81/01/22	
O34	Parameter name: "Controller Inside" HMI exchange word 34		
	Logic address:	6434 = 16#1922	Type: UINT
	CANopen index:	2022/23	Read/write: R
	INTERBUS index:	5FB3/A2	Unit: 0.1
	DeviceNet path:	81/01/23	
O35	Parameter name: "Controller Inside" HMI exchange word 35		
	Logic address:	6435 = 16#1923	Type: UINT
	CANopen index:	2022/24	Read/write: R
	INTERBUS index:	5FB3/A3	Unit: 0.1
	DeviceNet path:	81/01/24	
O36	Parameter name: "Controller Inside" HMI exchange word 36		
	Logic address:	6436 = 16#1924	Type: UINT
	CANopen index:	2022/25	Read/write: R
	INTERBUS index:	5FB3/A4	Unit: 0.1
	DeviceNet path:	81/01/25	
O37	Parameter name: "Controller Inside" HMI exchange word 37		
	Logic address:	6437 = 16#1925	Type: UINT
	CANopen index:	2022/26	Read/write: R
	INTERBUS index:	5FB3/A5	Unit: 0.1
	DeviceNet path:	81/01/26	
O38	Parameter name: "Controller Inside" HMI exchange word 38		
	Logic address:	6438 = 16#1926	Type: UINT
	CANopen index:	2022/27	Read/write: R
	INTERBUS index:	5FB3/A6	Unit: 0.1
	DeviceNet path:	81/01/27	
O39	Parameter name: "Controller Inside" HMI exchange word 39		
	Logic address:	6439 = 16#1927	Type: UINT
	CANopen index:	2022/28	Read/write: R
	INTERBUS index:	5FB3/A7	Unit: 0.1
	DeviceNet path:	81/01/28	

"Controller Inside" parameters

Code	Description		
O40	Parameter name: "Controller Inside" HMI exchange word 40		
	Logic address:	6440 = 16#1928	Type: UINT
	CANopen index:	2022/29	Read/write: R
	INTERBUS index:	5FB3/A8	Unit: 0.1
	DeviceNet path:	81/01/29	
O41	Parameter name: "Controller Inside" HMI exchange word 41		
	Logic address:	6441 = 16#1929	Type: UINT
	CANopen index:	2022/2A	Read/write: R
	INTERBUS index:	5FB3/A9	Unit: 0.1
	DeviceNet path:	81/01/2A	
O42	Parameter name: "Controller Inside" HMI exchange word 42		
	Logic address:	6442 = 16#192A	Type: UINT
	CANopen index:	2022/2B	Read/write: R
	INTERBUS index:	5FB3/AA	Unit: 0.1
	DeviceNet path:	81/01/2B	
O43	Parameter name: "Controller Inside" HMI exchange word 43		
	Logic address:	6443 = 16#192B	Type: UINT
	CANopen index:	2022/2C	Read/write: R
	INTERBUS index:	5FB3/AB	Unit: 0.1
	DeviceNet path:	81/01/2C	
O44	Parameter name: "Controller Inside" HMI exchange word 44		
	Logic address:	6444 = 16#192C	Type: UINT
	CANopen index:	2022/2D	Read/write: R
	INTERBUS index:	5FB3/AC	Unit: 0.1
	DeviceNet path:	81/01/2D	
O45	Parameter name: "Controller Inside" HMI exchange word 45		
	Logic address:	6445 = 16#192D	Type: UINT
	CANopen index:	2022/2E	Read/write: R
	INTERBUS index:	5FB3/AD	Unit: 0.1
	DeviceNet path:	81/01/2E	
O46	Parameter name: "Controller Inside" HMI exchange word 46		
	Logic address:	6446 = 16#192E	Type: UINT
	CANopen index:	2022/2F	Read/write: R
	INTERBUS index:	5FB3/AE	Unit: 0.1
	DeviceNet path:	81/01/2F	

"Controller Inside" parameters

Code	Description		
O47	Parameter name: "Controller Inside" HMI exchange word 47		
	Logic address:	6447 = 16#192F	Type: UINT
	CANopen index:	2022/30	Read/write: R
	INTERBUS index:	5FB3/AF	Unit: 0.1
	DeviceNet path:	81/01/30	
O48	Parameter name: "Controller Inside" HMI exchange word 48		
	Logic address:	6448 = 16#1930	Type: UINT
	CANopen index:	2022/31	Read/write: R
	INTERBUS index:	5FB3/B0	Unit: 0.1
	DeviceNet path:	81/01/31	
O49	Parameter name: "Controller Inside" HMI exchange word 49		
	Logic address:	6449 = 16#1931	Type: UINT
	CANopen index:	2022/32	Read/write: R
	INTERBUS index:	5FB3/B1	Unit: 0.1
	DeviceNet path:	81/01/32	
O50	Parameter name: "Controller Inside" HMI exchange word 50		
	Logic address:	6450 = 16#1932	Type: UINT
	CANopen index:	2022/33	Read/write: R
	INTERBUS index:	5FC8/32	Unit: 0.1
	DeviceNet path:	81/01/33	

DSP402 standard configuration and adjustment parameters

Code	Description		
SPAL	Parameter name: Acceleration speed delta DSP402 name: vl velocity acceleration/delta speed DRIVECOM name: Speed-Acceleration/Delta speed		
	Logic address: 8611 = 16#21A3 CANopen index: 6048/1 INTERBUS index: 6048/1 DeviceNet path: 8C/01/0C	Type: UDINT Read/write: R/W Unit: rpm	
	Parameter conforming to DSP402 profile. Speed for calculation of acceleration ramp. 32-bit word (low order: 8611, high order: 8612). If writing registers 8611 and 8612 in succession, write register 8611 first, then register 8612. Writing this parameter also modifies: <ul style="list-style-type: none"> • [Acceleration] (ACC) • ODVA acceleration time 		
SPAt	Parameter name: Acceleration time delta DSP402 name: vl velocity acceleration/delta time DRIVECOM name: Speed-Acceleration/Delta time		
	Logic address: 8613 = 16#21A5 CANopen index: 6048/2 INTERBUS index: 6048/2 DeviceNet path: 8C/01/0E	Type: UINT Read/write: R/W Unit: 1 s	
	Parameter conforming to DSP402 profile. Time for calculation of acceleration ramp (time to go from 0 to delta speed). Writing this parameter also modifies: <ul style="list-style-type: none"> • [Acceleration] (ACC) • ODVA acceleration time 		
SPdL	Parameter name: Deceleration speed delta DSP402 name: vl velocity deceleration/delta speed DRIVECOM name: Speed-Deceleration/Delta speed		
	Logic address: 8614 = 16#21A6 CANopen index: 6049/1 INTERBUS index: 6049/1 DeviceNet path: 8C/01/0F	Type: UDINT Read/write: R/W Unit: rpm	
	Parameter conforming to DSP402 profile. Speed for calculation of deceleration ramp. 32-bit word (low order: 8614, high order: 8615). If writing registers 8614 and 8615 in succession, write register 8614 first, then register 8615. Writing this parameter also modifies: <ul style="list-style-type: none"> • [Deceleration] (DEC) • ODVA deceleration time 		

DSP402 standard configuration and adjustment parameters

Code	Description		
SPdt	Parameter name: Deceleration time delta DSP402 name: vl velocity deceleration/delta time DRIVECOM name: Speed- Deceleration/Delta time		
	Logic address: 8616 = 16#21A8	Type: UINT	
	CANopen index: 6049/2	Read/write: R/W	
	INTERBUS index: 6049/2	Unit: 1 s	
	DeviceNet path: 8C/01/11		
	Parameter conforming to DSP402 profile. Time for calculation of deceleration ramp (time to go from 0 to delta speed). Writing this parameter also modifies: • [Deceleration] (DEC) • ODVA deceleration time		
SPFn	Parameter name: Speed setpoint factor numerator DSP402 name: vl setpoint factor numerator DRIVECOM name: Setpoint-Factor-Numerator		
	Logic address: 8642 = 16#21C2	Type: INT	
	CANopen index: 604B/1	Read/write: R/W	
	INTERBUS index: 604B/1	Unit: -	
	DeviceNet path: 8C/01/2B	Factory setting: 1	Adjustment range -32768 ... -1; 1 ... 32767
	Parameter conforming to DSP402 profile.		

DSP402 standard configuration and adjustment parameters

Code	Description		
SPFd	Parameter name: Speed setpoint factor denominator	DSP402 name: vl setpoint factor denominator	DRIVECOM name: Setpoint-Factor-Denominator
Logic address:	8643 = 16#21C3	Type:	INT
CANopen index:	604B/2	Read/write:	R/W
INTERBUS index:	604B/2	Unit:	-
DeviceNet path:	8C/01/2C	Factory setting:	1
		Adjustment range	-32768 ... -1; 1 ... 32767
Parameter conforming to DSP402 profile.			
The speed setpoint factor is obtained by the)Divisor) and)Multiplier) parameters. These parameters have no unit and can take a value from -32768 to 32767, excluding value 0.			
The factor is used to modify the resolution or the direction of the setpoint. It is applied to the setpoint, and the references and output values of the speed function only.			
Reference:			
<pre> graph LR Input1[] --> DIV[DIV] Input2[] -- numerator --> DIV Input3[] -- denominator --> DIV DIV --> MUL[MUL] MUL --> Output1[] </pre>			
References and output values:			
<pre> graph LR Input1[] --> MUL[MUL] Input2[] -- numerator --> MUL Input3[] -- denominator --> MUL MUL --> DIV[DIV] DIV --> Output1[] </pre>			

ODVA standard configuration and adjustment parameters

Code	Description		
ACCd	Parameter name: ODVA acceleration time ODVA name: AccelTime Logic address: - Type: UINT CANopen index: - Read/write: R/W INTERBUS index: - Unit: ms DeviceNet path: 2A/01/12		
	Parameter conforming to ODVA profile. Speed for calculation of acceleration ramp. Writing this parameter also modifies: <ul style="list-style-type: none"> • [Acceleration] (ACC) • DSP402 "vl velocity acceleration/delta speed" (SPAL) and "vl velocity acceleration/delta time" (SPAt) Parameter only accessible via DeviceNet.		
dECd	Parameter name: ODVA deceleration time ODVA name: DecelTime Logic address: - Type: UINT CANopen index: - Read/write: R/W INTERBUS index: - Unit: ms DeviceNet path: 2A/01/13		
	Parameter conforming to ODVA profile. Speed for calculation of deceleration ramp. Writing this parameter also modifies: <ul style="list-style-type: none"> • [Deceleration] (dEC) • DSP402 "vl velocity deceleration/delta speed" (SPdL) and "vl velocity deceleration/delta time" (SPDt) Parameter only accessible via DeviceNet.		

[1.1 SIMPLY START] (SIM-)

Code	Description		
CFG	Terminal display: [Macro configuration] Logic address: 3052 = 16#BEC CANopen index: 2000/35 INTERBUS index: 5FBC/07 DeviceNet path: 70/01/35		
	Type: WORD (listing) Read/write: R/WS Factory setting: 0		
	0 = [Start/Stop] (StS): Start/stop 1 = [M. handling] (HdG): Material handling 2 = [Hoisting] (HSt): Hoisting 3 = [Gen. Use] (GEN): General use 4 = [PID regul.] (PlD): PID regulation 5 = [Network C.] (nEt): Communication bus 6 = [Mast./slave] (MSL): Master/slave		
CCFG	Terminal display: [Customized macro] Logic address: 3053 = 16#BED CANopen index: 2000/36 INTERBUS index: 5FB9/02 DeviceNet path: 70/01/36		
	Type: WORD (listing) Read/write: R		
	1 = [Yes] (YES): Read-only parameter		

[1.3 SETTINGS] (SEt-)

Code	Description		
LSP	Terminal display: [Low speed] Logic address: 3105 = 16#C21 CANopen index: 2001/6 INTERBUS index: 5FB3/76 DeviceNet path: 70/01/6A Motor frequency at min. reference		
	Read/write:	Type: UINT	
	Unit:	Unit: R/W	
	Factory setting:	0.1 Hz	0
	Adjustment range:	0 to HSP	
HSP	Terminal display: [High speed] Logic address: 3104 = 16#C20 CANopen index: 2001/5 INTERBUS index: 5FB3/75 DeviceNet path: 70/01/69 Motor frequency at max. reference		
	Read/write:	Type: UINT	
	Unit:	Unit: R/W	
	Factory setting:	0.1 Hz	According to bFr ⁽¹⁾
	Adjustment range:	LSP to 10,000	
ItH	Terminal display: [Mot. therm. current] Logic address: 9622 = 16#2596 CANopen index: 2042/17 INTERBUS index: 5FB3/DB DeviceNet path: 2A/01/0A Motor thermal protection current		
	Read/write:	Type: UINT	
	Unit:	Unit: R/W	
	Factory setting:	0.1 A	According to drive rating
	Adjustment range:	0.2 ln to 1.5 ln ⁽²⁾	
SFC	Terminal display: [K speed loop filter] Logic address: 9105 = 16#2391 CANopen index: 203D/6 INTERBUS index: 5FBF/29 DeviceNet path: 8E/01/6A Speed loop filter coefficient		
	Read/write:	Type: UINT	
	Unit:	Unit: R/W	
	Factory setting:	1	0
	Adjustment range:	0 to 100	
SPG	Terminal display: [Speed prop. gain] Logic address: 9103 = 16#238F CANopen index: 203D/4 INTERBUS index: 5FB3/D1 DeviceNet path: 8E/01/68 Speed loop proportional gain		
	Read/write:	Type: UINT	
	Unit:	Unit: R/W	
	Factory setting:	1%	40
	Adjustment range:	0 to 1000	

⁽¹⁾ According to **bFr**, see “**bFr**”, page [148](#).

If **bFr** = 0: Factory setting = 500

If **bFr** = 1: Factory setting = 600

⁽²⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.3 SETTINGS] (SEt-)

Code	Description			
Slt	Terminal display: [Speed time integral]			
	Logic address: 9104 = 16#2390	Type:	UINT	
	CANopen index: 203D/5	Read/write:	R/W	
	INTERBUS index: 5FBF/28	Unit:	1%	
	DeviceNet path: 8E/01/69	Factory setting:	100	
			Adjustment range: 1 to 1000	
Speed loop integral time constant				
CLI	Terminal display: [Current Limitation]			
	Logic address: 9201 = 16#23F1	Type:	UINT	
	CANopen index: 203E/2	Read/write:	R/W	
	INTERBUS index: 5FB3/D2	Unit:	0.1 A	
	DeviceNet path: 8F/01/02	Factory setting:	1.5 In ⁽¹⁾	
			Adjustment range: According to SFr ⁽²⁾	
Used to limit the motor current				
FLU	Terminal display: [Motor fluxing]			
	Logic address: 13902 = 16#364E	Type:	WORD (listing)	
	CANopen index: 206D/3	Read/write:	R/W	
	INTERBUS index: 5FB4/05	Factory setting:	2	
	DeviceNet path: A6/01/67			
0 = [Not cont.] (FnC) : Non-continuous mode 1 = [Continuous] (FCt) : Continuous mode 2 = [No] (FnO) : Function inactive				
tLS	Terminal display: [Low speed time out]			
	Logic address: 11701 = 16#2DB5	Type:	UINT	
	CANopen index: 2057/2	Read/write:	R/W	
	INTERBUS index: 5FB3/F8	Unit:	0.1 s	
	DeviceNet path: 9B/01/66	Factory setting:	0	
			Adjustment range: 0 to 9999	
Maximum operating time at [Low speed] (LSP) , see "LSP", page 145 .				
Ctd	Terminal display: [Current threshold]			
	Logic address: 11001 = 16#2AF9	Type:	UINT	
	CANopen index: 2050/2	Read/write:	R/W	
	INTERBUS index: 5FB3/E3	Unit:	0.1 A	
	DeviceNet path: 98/01/02	Factory setting:	In ⁽¹⁾	
			Adjustment range: 0 to 1.5 In ⁽¹⁾	
Current threshold for [I attained] (CtA) function assigned to a relay or a logic output, see page 179 .				

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

⁽²⁾ According to **SFr**, see "SFr", page [162](#)

If **SFr** ≥ 0: Adjustment range = 0 to 1.65 In ⁽¹⁾

If **SFr** < 0: Adjustment range = 0 to 1.36 In ⁽¹⁾

[1.3 SETTINGS] (SEt-)

Code	Description					
Ftd	Terminal display: [Freq. threshold]					
Logic address:	11003 = 16#2AFB	Type:	UINT			
CANopen index:	2050/4	Read/write:	R/W			
INTERBUS index:	5FB3/E5	Unit:	0.1 Hz			
DeviceNet path:	98/01/04	Factory setting:	According to bFr ⁽¹⁾			
		Adjustment range:	0 to 5000 or 10,000 according to drive rating.			
Frequency threshold for [Freq. attain.] function (FtA) assigned to a relay or a logic output.						
F2d	Terminal display: [Freq. threshold 2]					
Logic address:	11004 = 16#2AFC	Type:	UINT			
CANopen index:	2050/5	Read/write:	R/W			
INTERBUS index:	5FB3/E6	Unit:	0.1 Hz			
DeviceNet path:	98/01/05	Factory setting:	According to bFr ⁽¹⁾			
		Adjustment range:	0 to 5000 or 10,000 according to drive rating.			
Frequency threshold for the [Freq. Th. 2 attain.] function (F2A) assigned to a relay or a logic output.						

(1) According to bFr, see “bFr”, page [148](#)

If bFr = 0: Factory setting = 500

If bFr = 1: Factory setting = 600

[1.4 MOTOR CONTROL] (drC-)

Code	Description				
bFr	Terminal display: [Standard mot. freq]				
	Logic address: 3015 = 16#BC7	Type:	WORD (listing)		
	CANopen index: 2000/10	Read/write:	R/WS		
	INTERBUS index: 5FBC/05	Factory setting:	0		
	DeviceNet path: 70/01/10				
0 = [50 Hz] (50): IEC 1 = [60 Hz] (60): NEMA					
This parameter modifies the presets of the following parameters: [High speed] (HSP), see "HSP", page 145, [Freq. threshold] (Ftd), see "Ftd", page 147, [Rated motor volt.] (UnS), see below, [Rated motor freq.] (FrS), see "FrS", page 149 and [Max frequency] (tFr), see "tFr", page 149.					
nPr	Terminal display: [Rated motor power]				
	Logic address: 9613 = 16#258D	Type:	UINT		
	CANopen index: 2042/E	Read/write:	R/WS		
	INTERBUS index: 5FC2/0A	Unit:	According to drive rating and bFr ⁽¹⁾		
	DeviceNet path: 91/01/0E	Factory setting:	According to drive rating		
Rated motor power given on the nameplate.					
UnS	Terminal display: [Rated motor volt.]				
	Logic address: 9601 = 16#2581	Type:	UINT		
	CANopen index: 2042/2	Read/write:	R/WS		
	INTERBUS index: 5FB3/D3	Unit:	1 V		
	DeviceNet path: 77/01/10	Factory setting:	According to drive rating and bFr		
Adjustment range: According to drive rating ⁽²⁾					
Rated motor voltage given on the nameplate.					
nCr	Terminal display: [Rated mot. current]				
	Logic address: 9603 = 16#2583	Type:	UINT		
	CANopen index: 2042/4	Read/write:	R/WS		
	INTERBUS index: 5FB3/D5	Unit:	0.1 A		
	DeviceNet path: 91/01/04	Factory setting:	According to drive rating		
Adjustment range: 0.25 In to 1.5 In ⁽³⁾					
Rated motor current given on the nameplate.					

(1) According to drive rating and bFr

If bFr = 0 and if power > 75 kW: Unit = 1 kW

If bFr = 0 and if power ≤ 75 kW: Unit = 0.01 kW

If bFr = 1: Unit = 0.1 HP (75 W) regardless of power

(2) According to drive voltage

ATV71●●M3X: Adjustment range = 100 to 240

ATV71●●N4: Adjustment range = 200 to 480

(3) In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
FrS	Terminal display: [Rated motor freq.]		
	Logic address: 9602 = 16#2582	Type: UINT	
	CANopen index: 2042/3	Read/write: R/WS	
	INTERBUS index: 5FB3/D4	Unit: 0.1 A	
	DeviceNet path: 91/01/03	Factory setting: According to bFr ⁽¹⁾	Adjustment range: 0 to 5000 or 10,000
Rated motor frequency given on the nameplate.			
nSP	Terminal display: [Nom motor speed]		
	Logic address: 9604 = 16#2584	Type: UINT	
	CANopen index: 2042/5	Read/write: R/WS	
	INTERBUS index: 5FB3/D6	Unit: 1 rpm	
	DeviceNet path: 91/01/05	Factory setting: According to drive rating	Adjustment range: 0 to 60,000
Rated motor speed given on the nameplate.			
tFr	Terminal display: [Max frequency]		
	Logic address: 3103 = 16#C1F	Type: UINT	
	CANopen index: 2001/4	Read/write: R/WS	
	INTERBUS index: 5FB3/74	Unit: 0.1 Hz	
	DeviceNet path: 70/01/68	Factory setting: According to bFr ⁽²⁾	Adjustment range: 100 to 5000 or 10,000
tUn	Terminal display: [Auto-tuning]		
	Logic address: 9608 = 16#2588	Type: WORD (listing)	
	CANopen index: 2042/9	Read/write: R/WS	
	INTERBUS index: 5FB3/D7	Factory setting: 0	
	DeviceNet path: 91/01/09	0 = [No] (nO) Auto-tuning not performed. 1 = [Yes] (YES) : Auto-tuning is performed as soon as possible, then the parameter automatically changes to [Done] (dOnE) . 2 = [Done] (dOnE) : Use of the values given the last time auto-tuning was performed.	
AUT	Terminal display: [Automatic autotune]		
	Logic address: 9615 = 16#258F	Type: WORD (listing)	
	CANopen index: 2042/10	Read/write: R/WS	
	INTERBUS index: 5FC2/0B	Factory setting: 0	
	DeviceNet path: 91/01/10	0 = [No] (nO) Function inactive 1 = [Yes] (YES) : Auto-tuning is performed on every power-up.	

(1) According to **bFr**, see “**bFr**”, page [148](#).

If **bFr** = 0: Factory setting: 500

If **bFr** = 1: Factory setting: 600

(2) According to **bFr**, see “**bFr**”, page [148](#).

If **bFr** = 0: Factory setting: 600

If **bFr** = 1: Factory setting: 720

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
tUS	Terminal display: [Auto tuning status] Logic address: 9609 = 16#2589 CANopen index: 2042/A INTERBUS index: 5FC2/08 DeviceNet path: 91/01/0A		
	0 = [Not done] (tAb): The default stator resistance value is used to control the motor. 1 = [Pending] (PEnd): Auto-tuning has been requested but not yet performed. 2 = [In Progress] (PrOG): Auto-tuning in progress. 3 = [Failed] (FAIL): Auto-tuning has failed. 4 = [Done] (dOnE): The stator resistance measured by the auto-tuning function is used to control the motor. 5 = [Customized] (CUS): Auto-tuning has been performed, but at least one parameter set by this auto-tuning operation has subsequently been modified.		
PHr	Terminal display: [Output Ph rotation] Logic address: 13401 = 16#3459 CANopen index: 2068/2 INTERBUS index: 5FBD/5F DeviceNet path: A4/01/02		
	0 = [ABC] (AbC): Forward 1 = [ACB] (AcB): Reverse This parameter can be used to reverse the direction of rotation of the motor without reversing the wiring.		
Ctt	Terminal display: [Motor control type] Logic address: 9607 = 16#2587 CANopen index: 2042/8 INTERBUS index: 5FC2/06 DeviceNet path: 91/01/08		
	0 = [SVC V] (IUC): Open-loop voltage flux vector control 1 = [SVC I] (CUC): Open-loop current flux vector control 2 = [FVC] (FUC): Closed-loop current flux vector control for motor with incremental encoder type sensor 3 = [V/F 2pts] (UF2): Simple V/F profile without slip compensation 4 = [V/F 5pts] (UF5): 5-segment V/F profile 5 = [Sync. mot.] (SYn): For synchronous permanent magnet motors with sinusoidal electromotive force (EMF) only		
U0	Terminal display: [U0] Logic address: 12401 = 16#3071 CANopen index: 205E/2 INTERBUS index: 5FBD/2A DeviceNet path: 9F/01/02		
	Type: UINT Read/write: R/WS Unit: 1 V Factory setting: 0 Adjustment range: 0 to 600		
	V/f ratio		

[1.4 MOTOR CONTROL] (drC-)

Code	Description			
U1	Terminal display: [U1]			
	Logic address: 12403 = 16#3073	Type:	UINT	
	CANopen index: 205E/4	Read/write:	R/WS	
	INTERBUS index: 5FBD/2B	Unit:	1 V	
	DeviceNet path: 9F/01/04	Factory setting:	0	
Adjustment range: 0 to 600				
V/f ratio				
F1	Terminal display: [F1]			
	Logic address: 12404 = 16#3074	Type:	UINT	
	CANopen index: 205E/5	Read/write:	R/WS	
	INTERBUS index: 5FBD/2C	Unit:	0.1 Hz	
	DeviceNet path: 9F/01/05	Factory setting:	0	
Adjustment range: 0 to 10,000 ⁽¹⁾				
V/f ratio				
U2	Terminal display: [U2]			
	Logic address: 12405 = 16#3075	Type:	UINT	
	CANopen index: 205E/6	Read/write:	R/WS	
	INTERBUS index: 5FBD/2D	Unit:	1 V	
	DeviceNet path: 9F/01/06	Factory setting:	0	
Adjustment range: 0 to 600				
V/f ratio				
F2	Terminal display: [F2]			
	Logic address: 12406 = 16#3076	Type:	UINT	
	CANopen index: 205E/7	Read/write:	R/WS	
	INTERBUS index: 5FBD/2E	Unit:	0.1 Hz	
	DeviceNet path: 9F/01/07	Factory setting:	0	
Adjustment range: 0 to 10,000 ⁽¹⁾				
V/f ratio				
U3	Terminal display: [U3]			
	Logic address: 12407 = 16#3077	Type:	UINT	
	CANopen index: 205E/8	Read/write:	R/WS	
	INTERBUS index: 5FBD/2F	Unit:	1 V	
	DeviceNet path: 9F/01/08	Factory setting:	0	
Adjustment range: 0 to 600				
V/f ratio				

⁽¹⁾ If: **F1 ≤ F2 ≤ F3 ≤ F4 ≤ F5**.

[1.4 MOTOR CONTROL] (drC-)

Code	Description				
F3	Terminal display: [F3] Logic address: 12408 = 16#3078 CANopen index: 205E/9 INTERBUS index: 5FBD/30 DeviceNet path: 9F/01/09				
	Type:	UINT			
	Read/write:	R/WS			
	Unit:	0.1 Hz			
	Factory setting:	0			
	Adjustment range:	0 to 10,000 ⁽¹⁾			
V/f ratio					
U4	Terminal display: [U4] Logic address: 12409 = 16#3079 CANopen index: 205E/9 INTERBUS index: 5FBD/31 DeviceNet path: 9F/01/0A				
	Type:	UINT			
	Read/write:	R/WS			
	Unit:	1 V			
	Factory setting:	0			
	Adjustment range:	0 to 600			
V/f ratio					
F4	Terminal display: [F4] Logic address: 12410 = 16#307A CANopen index: 205E/B INTERBUS index: 5FBD/32 DeviceNet path: 9F/01/0B				
	Type:	UINT			
	Read/write:	R/WS			
	Unit:	0.1 Hz			
	Factory setting:	0			
	Adjustment range:	0 to 10,000 ⁽¹⁾			
V/f ratio					
U5	Terminal display: [U5] Logic address: 12411 = 16#307B CANopen index: 205E/C INTERBUS index: 5FBD/33 DeviceNet path: 9F/01/0C				
	Type:	UINT			
	Read/write:	R/WS			
	Unit:	1 V			
	Factory setting:	0			
	Adjustment range:	0 to 600			
V/f ratio. The parameter can be accessed if [Motor control type] (Ctt) = [V/F 5pts] (UF5) .					
F5	Terminal display: [F5] Logic address: 12412 = 16#307C CANopen index: 205E/D INTERBUS index: 5FBD/34 DeviceNet path: 9F/01/0D				
	Type:	UINT			
	Read/write:	R/WS			
	Unit:	0.1 Hz			
	Factory setting:	0			
	Adjustment range:	0 to 10,000 ⁽¹⁾			
V/f ratio					

⁽¹⁾ If: **F1 ≤ F2 ≤ F3 ≤ F4 ≤ F5**.

[1.4 MOTOR CONTROL] (drC-)

Code	Description			
UC2	Terminal display: [Vector Control 2pt]			
	Logic address: 14201 = 16#3779	Type:	WORD (listing)	
	CANopen index: 2070/2	Read/write:	R/WS	
	INTERBUS index: 5FBD/80	Factory setting:	0	
DeviceNet path: A8/01/02				
0 = [No] (nO) : Function inactive 1 = [Yes] (YES) : Function active				
UCP	Terminal display: [V. constant power]			
	Logic address: 14202 = 16#377A	Type:	UINT	
	CANopen index: 2070/3	Read/write:	R/WS	
	INTERBUS index: 5FBD/81	Unit:	1 V	
DeviceNet path: A8/01/03				
Factory setting: [Rated motor volt.] (UnS) Adjustment range: According to rating				
FCP	Terminal display: [Freq. Const Power]			
	Logic address: 14203 = 16#377B	Type:	UINT	
	CANopen index: 2070/4	Read/write:	R/WS	
	INTERBUS index: 5FBD/82	Unit:	0.1 Hz	
DeviceNet path: A8/01/04				
Factory setting: [Rated motor freq.] (FrS) Adjustment range: According to rating				

[1.4 MOTOR CONTROL] (drC-)

Code	Description			
nCrS	Terminal display: [Nominal I sync.]			
	Logic address: 9670 = 16#25C6	Type:	UINT	
	CANopen index: 2042/47	Read/write:	R/WS	
	INTERBUS index: 5FC2/1E	Unit:	0.1 A	
	DeviceNet path: 91/01/47	Factory setting:	According to drive rating	
Adjustment range: 0.25 In to 1.5 In ⁽¹⁾				
Rated synchronous motor current given on the nameplate.				
nSPS	Terminal display: [Nom motor spdsync]			
	Logic address: 9671 = 16#25C7	Type:	UINT	
	CANopen index: 2042/48	Read/write:	R/WS	
	INTERBUS index: 5FC2/1F	Unit:	1 rpm	
	DeviceNet path: 91/01/48	Factory setting:	According to drive rating	
Adjustment range: 0 to 60,000				
Rated synchronous motor speed given on the nameplate.				
PPnS	Terminal display: [Pole pairs]			
	Logic address: 9672 = 16#25C8	Type:	UINT	
	CANopen index: 2042/49	Read/write:	R/WS	
	INTERBUS index: 5FC2/20	Unit:	1	
	DeviceNet path: 91/01/49	Factory setting:	According to drive rating	
Adjustment range: 1 to 50				
Number of pairs of poles on the synchronous motor.				
PHS	Terminal display: [Syn. EMF constant]			
	Logic address: 9673 = 16#25C9	Type:	UINT	
	CANopen index: 2042/4A	Read/write:	R/WS	
	INTERBUS index: 5FC2/21	Unit:	0.1 V/1000 rpm	
	DeviceNet path: 91/01/4A	Factory setting:	According to drive rating	
Adjustment range: 0 to 65,535				
Synchronous motor EMF constant, in volts per 1000 rpm.				
LdS	Terminal display: [Autotune L d-axis]			
	Logic address: 9674 = 16#25CA	Type:	UINT	
	CANopen index: 2042/4B	Read/write:	R/WS	
	INTERBUS index: 5FC2/22	Unit:	0.01 mH	
	DeviceNet path: 91/01/4B	Factory setting:	According to drive rating	
Adjustment range: 0 to 65,535				
Axis "d" stator inductance in mH.				

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
LqS	Terminal display: [Autotune L q-axis] Logic address: 9675 = 16#25CB CANopen index: 2042/4C INTERBUS index: 5FC2/23 DeviceNet path: 91/01/4C Axis "q" stator inductance in mH.		
	Logic address:	Type:	UINT
	CANopen index:	Read/write:	R/WS
	INTERBUS index:	Unit:	0.01 mH
	DeviceNet path:	Factory setting:	According to drive rating
		Adjustment range:	0 to 65,535
rSAS	Terminal display: [Cust. stator R syn] Logic address: 9682 = 16#25D2 CANopen index: 2042/53 INTERBUS index: 5FC2/26 DeviceNet path: 91/01/53 Cold state stator resistance (per winding)		
	Logic address:	Type:	UINT
	CANopen index:	Read/write:	R/WS
	INTERBUS index:	Unit:	According to drive rating ⁽¹⁾
	DeviceNet path:	Factory setting:	According to drive rating
		Adjustment range:	0 to 65,535

⁽¹⁾ According to drive rating

If Power > 75 kW: Unit = 1 $\mu\Omega$

If Power \leq 75 kW: Unit = 1 m Ω

[1.4 MOTOR CONTROL] (drC-)

Code	Description			
UFr	Terminal display: [IR compensation]			
	Logic address: 9623 = 16#2597	Type:	UINT	
	CANopen index: 2042/18	Read/write:	R/W	
	INTERBUS index: 5FC2/0E	Unit:	1%	
	DeviceNet path: 91/01/18	Factory setting:	100	
Optimizes the torque at very low speed.				
SLP	Terminal display: [Slip compensation]			
	Logic address: 9625 = 16#2599	Type:	UINT	
	CANopen index: 2042/1A	Read/write:	R/W	
	INTERBUS index: 5FB3/DC	Unit:	1%	
	DeviceNet path: 91/01/1A	Factory setting:	100	
Used to adjust the slip compensation in respect of the value fixed by the rated motor speed.				

[1.4 MOTOR CONTROL] (drC-)

Code	Description			
rSM	Terminal display: [Stator R measured]			
	Logic address: 9640 = 16#25A8	Type: UINT		
	CANopen index: 2042/29	Read/write: R		
	INTERBUS index: 5FB9/DF	Unit: According to drive rating ⁽¹⁾		
	DeviceNet path: 91/01/29	Adjustment range: 0 to 65,535		
Cold stator resistance, calculated by the drive.				
IdM	Terminal display: [Idr]			
	Logic address: 9650 = 16#25B2	Type: UINT		
	CANopen index: 2042/33	Read/write: R		
	INTERBUS index: 5FB9/E0	Unit: 0.1 A		
	DeviceNet path: 91/01/33	Adjustment range: 0 to 65,535		
Magnetizing current, calculated by the drive.				
LFM	Terminal display: [Lfr]			
	Logic address: 9660 = 16#25BC	Type: UINT		
	CANopen index: 2042/3D	Read/write: R		
	INTERBUS index: 5FB9/E1	Unit: 0.01 mH		
	DeviceNet path: 91/01/3D	Adjustment range: 0 to 65,535		
Leakage inductance in mH, calculated by the drive.				
trM	Terminal display: [T2r]			
	Logic address: 9665 = 16#25C1	Type: UINT		
	CANopen index: 2042/42	Read/write: R		
	INTERBUS index: 5FB9/E2	Unit: 1 ms		
	DeviceNet path: 91/01/42	Adjustment range: 0 to 65,535		
Rotor time constant in ms, calculated by the drive.				
nSL	Terminal display: [Nominal motor slip]			
	Logic address: 9605 = 16#2585	Type: UINT		
	CANopen index: 2042/6	Read/write: R		
	INTERBUS index: 5FB9/DD	Unit: 0.1 Hz		
	DeviceNet path: 91/01/06	Adjustment range: 0 to 65,535		
Rated slip in Hz, calculated by the drive.				

⁽¹⁾ According to drive rating

If Power > 75 kW: Unit = 1 $\mu\Omega$
If Power \leq 75 kW: Unit = 1 m Ω

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
PPn	Terminal display: [Pr]		
	Logic address: 9618 = 16#2592	Type: UINT	
	CANopen index: 2042/13	Read/write: R	
	INTERBUS index: 5FB3/DA	Unit: 1	
	DeviceNet path: 91/01/13	Adjustment range: 1 to 100	
Number of pairs of poles, calculated by the drive.			
rSA	Terminal display: [R1w]		
	Logic address: 9642 = 16#25AA	Type: UINT	
	CANopen index: 2042/2B	Read/write: R/WS	
	INTERBUS index: 5FC2/11	Unit: According to drive rating ⁽¹⁾	
	DeviceNet path: 91/01/2B	Adjustment range: 0 to 65,535	
Cold state stator resistance (per winding)			
IdA	Terminal display: [Idw]		
	Logic address: 9652 = 16#25B4	Type: UINT	
	CANopen index: 2042/35	Read/write: R/WS	
	INTERBUS index: 5FC2/15	Unit: 0.1 A	
	DeviceNet path: 91/01/35	Factory setting: 0	Adjustment range: 0 to 65,535
Magnetizing current			
LfA	Terminal display: [Lfw]		
	Logic address: 9662 = 16#25BE	Type: UINT	
	CANopen index: 2042/3F	Read/write: R/WS	
	INTERBUS index: 5FC2/19	Unit: 0.01 mH	
	DeviceNet path: 91/01/3F	Factory setting: 0	Adjustment range: 0 to 65,535
Leakage inductance			
trA	Terminal display: [T2w]		
	Logic address: 9667 = 16#25C3	Type: UINT	
	CANopen index: 2042/44	Read/write: R/WS	
	INTERBUS index: 5FC2/1C	Unit: 1 ms	
	DeviceNet path: 91/01/44	Factory setting: 0	Adjustment range: 0 to 65,535
Rotor time constant			

⁽¹⁾ According to drive rating

If Power > 75 kW: Unit = 1 $\mu\Omega$

If Power \leq 75 kW: Unit = 1 m Ω

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
rSMS	Terminal display: [R1rS]		
	Logic address: 9680 = 16#25D0	Type: uint	
	CANopen index: 2042/51	Read/write: R	
	INTERBUS index: 5FB9/E3	Unit: According to drive rating ⁽¹⁾	
	DeviceNet path: 91/01/51	Adjustment range: 0 to 65,535	
	Cold state stator resistance (per winding)		
FrSS	Terminal display: [Nominal freq sync.]		
	Logic address: 9679 = 16#25CF	Type: uint	
	CANopen index: 2042/50	Read/write: R	
	INTERBUS index: 5FC2/24	Unit: 0.1 Hz	
	DeviceNet path: 91/01/50	Adjustment range: 100 to 5000	
	Rated motor frequency, calculated by the drive.		
EnS	Terminal display: [Encoder type]		
	Logic address: 5608 = 16#15E8	Type: word (listing)	
	CANopen index: 201A/A	Read/write: R/WS	
	INTERBUS index: 5FBC/75	Factory setting: 1	
	DeviceNet path: 7D/01/09		
	0 = [---] (nO): Card missing 1 = [AABB] (AAbb): For signals A, A-, B, B- 2 = [AB] (Ab): For signals A, B 3 = [A] (A): For signal A		
PGI	Terminal display: [Number of pulses]		
	Logic address: 5604 = 16#15E4	Type: uint	
	CANopen index: 201A/5	Read/write: R/WS	
	INTERBUS index: 5FB3/7E	Unit: 1	
	DeviceNet path: 7D/01/05	Factory setting: 1024	
		Adjustment range: 100 to 5000	
	Number of pulses per encoder revolution.		

⁽¹⁾ According to drive rating

If Power > 75 kW: Unit = 1 $\mu\Omega$

If Power \leq 75 kW: Unit = 1 m Ω

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
EnC	Terminal display: [Encoder check] Logic address: 5605 = 16#15E5 CANopen index: 201A/6 INTERBUS index: 5FB3/7F DeviceNet path: 7D/01/06		
	Encoder feedback check 0 = [Not done] (nO): Check not performed 1 = [Yes] (YES): Activates encoder monitoring 2 = [Done] (dOnE): Check performed successfully		
EnU	Terminal display: [Encoder usage] Logic address: 5606 = 16#15E6 CANopen index: 201A/7 INTERBUS index: 5FBC/73 DeviceNet path: 7D/01/07		
	0 = [No] (nO): Function inactive 1 = [Fdbk monit.] (SEC): The encoder provides speed feedback for monitoring only. 2 = [Spd fdk reg] (rEG): The encoder provides speed feedback for regulation and monitoring. 3 = [Speed ref.] (PGr): The encoder provides a reference.		

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
EnA	Terminal display: [ENA system] Logic address: 12101 = 16#2F45 CANopen index: 205B/2 INTERBUS index: 5FBD/23 DeviceNet path: 9D/01/66		
	0 = [No] (nO) : Function inactive 1 = [Yes] (YES) : Function active		
GPE	Terminal display: [ENA prop.gain] Logic address: 12103 = 16#2F47 CANopen index: 205B/4 INTERBUS index: 5FBF/7C DeviceNet path: 9D/01/68		
	Type: WORD (listing) Read/write: R/W Factory setting: 0 Adjustment range: 1 to 9999		
GIE	Terminal display: [ENA integral gain] Logic address: 12104 = 16#2F48 CANopen index: 205B/5 INTERBUS index: 5FBF/7D DeviceNet path: 9D/01/69		
	Type: UINT Read/write: R/W Unit: 1 Factory setting: 100 Adjustment range: 0 to 9999		
rAP	Terminal display: [Reduction ratio] Logic address: 12105 = 16#2F49 CANopen index: 205B/6 INTERBUS index: 5FBF/7E DeviceNet path: 9D/01/6A		
	Type: UINT Read/write: R/W Unit: 0.1 Factory setting: 100 Adjustment range: 100 to 9999		

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
OFI	Terminal display: [Sinus filter] Logic address: 3109 = 16#C25 CANopen index: 2001/A INTERBUS index: 5FBC/0B DeviceNet path: 70/01/6E 0 = [No] (nO) : No sinus filter 1 = [Yes] (YES) : Use of a sinus filter, to limit overvoltages on the motor and reduce the ground fault leakage current.		
SFr	Terminal display: [Switching freq.] Logic address: 3102 = 16#C1E CANopen index: 2001/3 INTERBUS index: 5FBF/02 DeviceNet path: 70/01/67	Type: WORD (listing) Read/write: R/WS Factory setting: 0	Unit: R/W 0.1 kHz Factory setting: 25 or 40 according to drive rating Adjustment range: 10 to 160
	Switching frequency		
CLI	Terminal display: [Current Limitation] Logic address: 9201 = 16#23F1 CANopen index: 203E/2 INTERBUS index: 5FB3/D2 DeviceNet path: 8F/01/02		
	Type: UINT Read/write: R/W Unit: 0.1 A Factory setting: 1.5 In ⁽¹⁾ Adjustment range: According to SFr ⁽²⁾		
	First current limitation		
nrd	Terminal display: [Noise reduction] Logic address: 3107 = 16#C23 CANopen index: 2001/8 INTERBUS index: 5FB3/78 DeviceNet path: 70/01/6C		
	Type: WORD (listing) Read/write: R/WS Factory setting: 1 0 = [No] (nO) : Fixed frequency 1 = [Yes] (YES) : Frequency with random modulation		
SUL	Terminal display: [Motor surge limit.] Logic address: 12601 = 16#3139 CANopen index: 2060/2 INTERBUS index: 5FBD/3C DeviceNet path: A0/01/02		
	Type: WORD (listing) Read/write: R/WS Factory setting: 0 This function limits motor overvoltages. 0 = [No] (nO) : Function inactive 1 = [Yes] (YES) : Function active		

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

⁽¹⁾ According to **SFr**, see above.

If **SFr** ≥ 20: Adjustment range = 0 to 1.65 In ⁽¹⁾

If **SFr** < 20: Adjustment range = 0 to 1.36 In ⁽¹⁾

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
SOP	Terminal display:	[Volt surge limit. opt]	
	Logic address:	12602 = 16#313A	Type: WORD (listing)
	CANopen index:	2060/3	Read/write: R/WS
	INTERBUS index:	5FBD/3D	Unit: 1 µs
	DeviceNet path:	A0/01/03	Factory setting: 10
Optimization parameter for transient overvoltages at the motor terminals. 6 = 6 µs 8 = 8 µs 10 = 10 µs			

[1.4 MOTOR CONTROL] (drC-)

Code	Description			
Ubr	Terminal display: [Braking level]			
	Logic address: 14101 = 16#3715	Type:	UINT	
	CANopen index: 206F/2	Read/write:	R/W	
	INTERBUS index: 5FBF/D4	Unit:	1 V	
	DeviceNet path: A7/01/66	Factory setting:	According to drive voltage ⁽¹⁾	
Adjustment range: According to drive voltage and line supply voltage				
DC bus voltage threshold above which the braking transistor cuts in to limit this voltage.				
bbA	Terminal display: [Braking balance]			
	Logic address: 14102 = 16#3716	Type:	WORD (listing)	
	CANopen index: 206F/3	Read/write:	R/WS	
	INTERBUS index: 5FBD/7B	Factory setting:	0	
	DeviceNet path: A7/01/67			
Function used to balance the braking power between the drives. 0 = [No] (nO): Function inactive 1 = [Yes] (YES): Function active				
LbA	Terminal display: [Load sharing]			
	Logic address: 14301 = 16#37DD	Type:	WORD (listing)	
	CANopen index: 2071/2	Read/write:	R/WS	
	INTERBUS index: 5FBD/83	Factory setting:	0	
	DeviceNet path: A8/01/66			
Function used to balance the torque between two motors. 0 = [No] (nO): Function inactive 1 = [Yes] (YES): Function active				
LbC	Terminal display: [Load correction]			
	Logic address: 14302 = 16#37DE	Type:	UINT	
	CANopen index: 2071/3	Read/write:	R/W	
	INTERBUS index: 5FBF/D5	Unit:	0.1 Hz	
	DeviceNet path: A8/01/67	Factory setting:	0	
Adjustment range: 0 to 10,000				
Rated correction in Hz.				

⁽¹⁾ According to drive voltage

ATV71●●●M3X: Factory setting = 395

ATV71●●●N4: Factory setting = 785

[1.4 MOTOR CONTROL] (drC-)

Code	Description		
LbC1	Terminal display: [Correction min spd]		
	Logic address: 14303 = 16#37DF	Type: UINT	
	CANopen index: 2071/4	Read/write: R/W	
	INTERBUS index: 5FBF/D6	Unit: 0.1 Hz	
	DeviceNet path: A8/01/68	Factory setting: 0	
		Adjustment range: 0 to 9999	
	Minimum speed for load correction.		
LbC2	Terminal display: [Correction max spd]		
	Logic address: 14304 = 16#37E0	Type: UINT	
	CANopen index: 2071/5	Read/write: R/W	
	INTERBUS index: 5FBF/D7	Unit: 0.1 Hz	
	DeviceNet path: A8/01/69	Factory setting: 1	
		Adjustment range: LbC1 + 1 to 10,000	
	Maximum speed for load correction.		
LbC3	Terminal display: [Torque offset]		
	Logic address: 14305 = 16#37E1	Type: UINT	
	CANopen index: 2071/6	Read/write: R/W	
	INTERBUS index: 5FBF/D8	Unit: 1%	
	DeviceNet path: A8/01/6A	Factory setting: 0	
		Adjustment range: 0 to 300	
	Minimum torque for load correction.		
LbF	Terminal display: [Sharing filter]		
	Logic address: 14306 = 16#37E2	Type: UINT	
	CANopen index: 2071/7	Read/write: R/W	
	INTERBUS index: 5FBF/D9	Unit: 1 ms	
	DeviceNet path: A8/01/6B	Factory setting: 100	
		Adjustment range: 100 to 20,000	
	Load correction filtering.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description		
tCC	Terminal display: [2/3 wire control] Logic address: 11101 = 16#2B5D CANopen index: 2051/2 INTERBUS index: 5FB3/E8 DeviceNet path: 98/01/66 0 = [2 wire] (2C): 2-wire control 1 = [3 wire] (3C): 3-wire control		
tCt	Terminal display: [2 wire type] Logic address: 11102 = 16#2B5E CANopen index: 2051/3 INTERBUS index: 5FB3/E9 DeviceNet path: 98/01/67 0 = [Level] (LEL): State 0 or 1 is taken into account for run or stop. 1 = [Transition] (trn): A change of state (transition or edge) is necessary to initiate operation, in order to prevent accidental restarts after a break in the power supply. 2 = [Fwd priority] (PFO): State 0 or 1 is taken into account for run or stop, but the "forward" input always takes priority over the "reverse" input.		
rrS	Terminal display: [Reverse assign.] Logic address: 11105 = 16#2B61 CANopen index: 2051/6 INTERBUS index: 5FBC/FE DeviceNet path: 98/01/6A Assignment of the reverse direction command: 0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192 . ACC and dEC are enabled when the assigned input or bit is at 0. AC2 and dE2 are enabled when the assigned input or bit is at 1.		

(1) According to tCC, see "tCC", page [166](#).

If tCC = 0: Factory setting = **[LI2]** (LI2)

If tCC = 1: Factory setting = **[LI3]** (LI3)

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[LI1 CONFIGURATION] (L1-)

Code	Description				
L1d	Terminal display:	[LI1 On Delay]			
	Logic address:	4001 = 16#FA1	Type:	UINT	
	CANopen index:	200A/2	Read/write:	R/W	
	INTERBUS index:	5FBC/0E	Unit:	1 ms	
	DeviceNet path:	75/01/02	Factory setting:	0	
This parameter is used to take account of the change of the logic input to state 1 with a delay that can be adjusted between 0 and 200 milliseconds, in order to filter out possible interference. The change to state 0 is taken into account without delay.					

[LIx CONFIGURATION] (L--)

All the logic inputs available on the drive are processed as in the example for LI1 above, up to LI6, LI10 or LI14, depending on whether or not option cards have been inserted.

Tables indicating addresses for each of the **Lxd** parameters appear below.

Code	L1d	L2d	L3d	L4d	L5d
Terminal display	[LI1 On Delay]	[LI2 On Delay]	[LI3 On Delay]	[LI4 On Delay]	[LI5 On Delay]
Logic address:	4001 = 16#FA1	4002 = 16#FA2	4003 = 16#FA3	4004 = 16#FA4	4005 = 16#FA5
CANopen index:	200A/2	200A/3	200A/4	200A/5	200A/6
INTERBUS index:	5FBC/0E	5FBC/0F	5FBC/10	5FBC/11	5FBC/12
DeviceNet path:	75/01/02	75/01/03	75/01/04	75/01/05	75/01/06

Code	L6d	L7d	L8d	L9d	L10d
Terminal display	[LI6 On Delay]	[LI7 On Delay]	[LI8 On Delay]	[LI9 On Delay]	[LI10 On Delay]
Logic address:	4006 = 16#FA6	4007 = 16#FA7	4008 = 16#FA8	4009 = 16#FA9	4010 = 16#FAA
CANopen index:	200A/7	200A/8	200A/9	200A/A	200A/B
INTERBUS index:	5FBC/13	5FBC/14	5FBC/15	5FBC/16	5FBC/17
DeviceNet path:	75/01/07	75/01/08	75/01/09	75/01/0A	75/01/0B

Code	L11d	L12d	L13d	L14d
Terminal display	[LI11 On Delay]	[LI12 On Delay]	[LI13 On Delay]	[LI14 On Delay]
Logic address:	4011 = 16#FAB	4012 = 16#FAC	4013 = 16#FAD	4014 = 16#FAE
CANopen index:	200A/C	200A/D	200A/E	200A/F
INTERBUS index:	5FBC/18	5FBC/19	5FBC/1A	5FBC/1B
DeviceNet path:	75/01/0C	75/01/0D	75/01/0E	75/01/0F

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description		
bSP	Terminal display: [Reference template] Logic address: 3106 = 16#C22 CANopen index: 2001/7 INTERBUS index: 5FB3/77 DeviceNet path: 70/01/6B		
	This parameter defines how the speed reference is taken into account, regardless of the reference channel. 0 = [Standard] (bSd) : At zero reference the frequency = LSP. 1 = [Pedestal] (bLS) : At reference = 0 to LSP the frequency = LSP. 2 = [Deadband] (bnS) : At reference = 0 to LSP the frequency = 0. 4 = [Deadband 0] (bnS0) : This operation is the same as [Standard] (bSd) , except that in the following cases at zero reference, the frequency = 0: - The signal is less than [min value] , which is greater than 0 (example 1 V on a 2 - 10 V input) - The signal is greater than [min value] , which is greater than [max value] (example 11 V on a 10 - 0 V input)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
	If the input range is configured as "bidirectional", operation remains identical to [Standard] (bSd) .		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AI1 CONFIGURATION] (AI1-)

Code	Description		
AI1t	Terminal display: [AI1 Type]		
	Logic address:	4402 = 16#1132	Type: WORD (listing)
	CANopen index:	200E/3	Read/write: R
	INTERBUS index:	5FBC/34	Factory setting: 1
	DeviceNet path:	77/01/03	
	1 = [Voltage] (10U): Voltage input		
UIL1	Terminal display: [AI1 min value]		
	Logic address:	4412 = 16#113C	Type: UINT
	CANopen index:	200E/D	Read/write: R/WS
	INTERBUS index:	5FBC/38	Unit: 0.1 V
	DeviceNet path:	77/01/0D	Factory setting: 0
			Adjustment range: 0 to 100
UIH1	Terminal display: [AI1 max value]		
	Logic address:	4422 = 16#1146	Type: UINT
	CANopen index:	200E/17	Read/write: R/WS
	INTERBUS index:	5FBC/3B	Unit: 0.1 V
	DeviceNet path:	77/01/17	Factory setting: 100
			Adjustment range: 0 to 100
AI1F	Terminal display: [AI1 filter]		
	Logic address:	4452 = 16#1164	Type: UINT
	CANopen index:	200E/35	Read/write: R/WS
	INTERBUS index:	5FBC/44	Unit: 0.01 s
	DeviceNet path:	77/01/35	Factory setting: 0
			Adjustment range: 0 to 1000
	Interference filtering		
AI1E	Terminal display: [AI1 Interm. point X]		
	Logic address:	4462 = 16#116E	Type: UINT
	CANopen index:	200E/3F	Read/write: R/WS
	INTERBUS index:	5FBC/48	Unit: 1%
	DeviceNet path:	77/01/3F	Factory setting: 0
			Adjustment range: 0 to 100
	Input delinearization point coordinate.		
AI1S	Terminal display: [AI1 Interm. point Y]		
	Logic address:	4472 = 16#1178	Type: UINT
	CANopen index:	200E/49	Read/write: R/WS
	INTERBUS index:	5FBC/4C	Unit: 1%
	DeviceNet path:	77/01/49	Factory setting: 0
			Adjustment range: 0 to 100
	Output delinearization point coordinate (frequency reference).		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AI2 CONFIGURATION] (AI2-)

Code	Description			
AI2t	Terminal display: [AI2 Type]			
	Logic address: 4403 = 16#1133	Type:	WORD (listing)	
	CANopen index: 200E/4	Read/write:	R/WS	
	INTERBUS index: 5FBC/35	Factory setting:	2	
DeviceNet path: 77/01/04				
1 = [Voltage] (10U): Voltage input 2 = [Current] (0A): Current input				
CrL2	Terminal display: [AI2 min value]			
	Logic address: 4433 = 16#1151	Type:	UINT	
	CANopen index: 200E/22	Read/write:	R/WS	
	INTERBUS index: 5FBC/3E	Unit:	0.1 mA	
	DeviceNet path: 77/01/22	Factory setting:	0	
Adjustment range: 0 to 200				
UIL2	Terminal display: [AI2 min value]			
	Logic address: 4413 = 16#113D	Type:	UINT	
	CANopen index: 200E/E	Read/write:	R/WS	
	INTERBUS index: 5FBC/39	Unit:	0.1 V	
	DeviceNet path: 77/01/0E	Factory setting:	0	
Adjustment range: 0 to 100				
CrH2	Terminal display: [AI2 max value]			
	Logic address: 4443 = 16#115B	Type:	UINT	
	CANopen index: 200E/2C	Read/write:	R/WS	
	INTERBUS index: 5FBC/41	Unit:	0.1 mA	
	DeviceNet path: 77/01/2C	Factory setting:	200	
Adjustment range: 0 to 200				
UIH2	Terminal display: [AI2 max value]			
	Logic address: 4423 = 16#1147	Type:	UINT	
	CANopen index: 200E/18	Read/write:	R/WS	
	INTERBUS index: 5FBC/3C	Unit:	0.1 V	
	DeviceNet path: 77/01/18	Factory setting:	0	
Adjustment range: 0 to 100				
AI2F	Terminal display: [AI2 filter]			
	Logic address: 4453 = 16#1165	Type:	UINT	
	CANopen index: 200E/36	Read/write:	R/WS	
	INTERBUS index: 5FBC/45	Unit:	0.01 s	
	DeviceNet path: 77/01/36	Factory setting:	0	
Adjustment range: 0 to 1000				
Interference filtering				

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description		
AI2L	Terminal display: [AI2 range] Logic address: 4483 = 16#1183 CANopen index: 200E/54 INTERBUS index: 5FBC/50 DeviceNet path: 77/01/54		
	0 = [0 - 100%] (POS): Unidirectional input 1 = [+/- 100%] (nEG): Bidirectional input		
AI2E	Terminal display: [AI2 Interm. point X] Logic address: 4463 = 16#116F CANopen index: 200E/40 INTERBUS index: 5FBC/49 DeviceNet path: 77/01/40		
	Input delinearization point coordinate.		
AI2S	Terminal display: [AI2 Interm. point Y] Logic address: 4473 = 16#1179 CANopen index: 200E/4A INTERBUS index: 5FBC/4D DeviceNet path: 77/01/4A		
	Output delinearization point coordinate (frequency reference).		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AI3 CONFIGURATION] (AI3-)

Code	Description		
AI3t	Terminal display: [AI3 Type]		
	Logic address:	4404 = 16#1134	Type: WORD (listing)
	CANopen index:	200E/5	Read/write: R
	INTERBUS index:	5FBC/36	Factory setting: 2
	DeviceNet path:	77/01/05	
	2 = [Current] (0A): Current input		
CrL3	Terminal display: [AI3 min value]		
	Logic address:	4434 = 16#1152	Type: UINT
	CANopen index:	200E/23	Read/write: R/WS
	INTERBUS index:	5FBC/3F	Unit: 0.1 mA
	DeviceNet path:	77/01/23	Factory setting: 0
			Adjustment range: 0 to 200
CrH3	Terminal display: [AI3 max value]		
	Logic address:	4444 = 16#115C	Type: UINT
	CANopen index:	200E/2D	Read/write: R/WS
	INTERBUS index:	5FBC/42	Unit: 0.1 mA
	DeviceNet path:	77/01/2D	Factory setting: 200
			Adjustment range: 0 to 200
AI3F	Terminal display: [AI3 filter]		
	Logic address:	4454 = 16#1166	Type: UINT
	CANopen index:	200E/37	Read/write: R/WS
	INTERBUS index:	5FBC/46	Unit: 0.01 s
	DeviceNet path:	77/01/37	Factory setting: 0
			Adjustment range: 0 to 1000
	Interference filtering		
AI3L	Terminal display: [AI3 range]		
	Logic address:	4484 = 16#1184	Type: WORD (listing)
	CANopen index:	200E/55	Read/write: R/WS
	INTERBUS index:	5FBC/51	Factory setting: 0
	DeviceNet path:	77/01/55	
	0 = [0 - 100%] (POS): Unidirectional input 1 = [+/- 100%] (nEG): Bidirectional input		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description		
AI3E	Terminal display:	[AI3 Interm. point X]	
	Logic address:	4464 = 16#1170	Type: UINT
	CANopen index:	200E/41	Read/write: R/WS
	INTERBUS index:	5FBC/4A	Unit: 1%
	DeviceNet path:	77/01/4A	Factory setting: 0 Adjustment range: 0 to 100
Input delinearization point coordinate.			
AI3S	Terminal display:	[AI3 Interm. point Y]	
	Logic address:	4474 = 16#117A	Type: UINT
	CANopen index:	200E/4B	Read/write: R/WS
	INTERBUS index:	5FBC/4E	Unit: 1%
	DeviceNet path:	77/01/4B	Factory setting: 0 Adjustment range: 0 to 100
Output delinearization point coordinate (frequency reference).			

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AI4 CONFIGURATION] (AI4-)

Code	Description			
AI4t	Terminal display: [AI4 Type]			
	Logic address: 4405 = 16#1135	Type:	WORD (listing)	
	CANopen index: 200E/6	Read/write:	R/WS	
	INTERBUS index: 5FBC/37	Factory setting:	2	
DeviceNet path: 77/01/06				
1 = [Voltage] (10U): Voltage input 2 = [Current] (0A): Current input				
CrL4	Terminal display: [AI4 min value]			
	Logic address: 4435 = 16#1153	Type:	UINT	
	CANopen index: 200E/24	Read/write:	R/WS	
	INTERBUS index: 5FBC/40	Unit:	0.1 mA	
	DeviceNet path: 77/01/24	Factory setting:	0	
Adjustment range: 0 to 200				
UIL4	Terminal display: [AI4 min value]			
	Logic address: 4415 = 16#113D	Type:	UINT	
	CANopen index: 200E/10	Read/write:	R/WS	
	INTERBUS index: 5FBC/3A	Unit:	0.1 V	
	DeviceNet path: 77/01/10	Factory setting:	0	
Adjustment range: 0 to 100				
CrH4	Terminal display: [AI4 max value]			
	Logic address: 4445 = 16#115D	Type:	UINT	
	CANopen index: 200E/2E	Read/write:	R/WS	
	INTERBUS index: 5FBC/43	Unit:	0.1 mA	
	DeviceNet path: 77/01/2E	Factory setting:	200	
Adjustment range: 0 to 200				
UIH4	Terminal display: [AI4 max value]			
	Logic address: 4425 = 16#1149	Type:	UINT	
	CANopen index: 200E/1A	Read/write:	R/WS	
	INTERBUS index: 5FBC/3D	Unit:	0.1 V	
	DeviceNet path: 77/01/1A	Factory setting:	0	
Adjustment range: 0 to 100				
AI4F	Terminal display: [AI4 filter]			
	Logic address: 4455 = 16#1167	Type:	UINT	
	CANopen index: 200E/38	Read/write:	R/WS	
	INTERBUS index: 5FBC/47	Unit:	0.01 s	
	DeviceNet path: 77/01/38	Factory setting:	0	
Adjustment range: 0 to 1000				
Interference filtering				

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description			
AI4L	Terminal display: [AI4 range]			
	Logic address: 4485 = 16#1185	Type:	WORD (listing)	
	CANopen index: 200E/56	Read/write:	R/WS	
	INTERBUS index: 5FBC/52	Factory setting:	0	
	DeviceNet path: 77/01/56			
0 = [0 - 100%] (POS): Unidirectional input 1 = [+/- 100%] (nEG): Bidirectional input				
AI4E	Terminal display: [AI4 Interm. point X]			
	Logic address: 4465 = 16#1171	Type:	UINT	
	CANopen index: 200E/42	Read/write:	R/WS	
	INTERBUS index: 5FBC/4B	Unit:	1%	
	DeviceNet path: 77/01/42	Factory setting:	0	
Input delinearization point coordinate.				
AI4S	Terminal display: [AI4 Interm. point Y]			
	Logic address: 4475 = 16#117B	Type:	UINT	
	CANopen index: 200E/4C	Read/write:	R/WS	
	INTERBUS index: 5FBC/4F	Unit:	1%	
	DeviceNet path: 77/01/4C	Factory setting:	0	
Output delinearization point coordinate (frequency reference).				

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[RP CONFIGURATION] (PLI-)

Code	Description			
PIL	Terminal display: [RP min value]			
	Logic address: 13302 = 16#33F6	Type:	INT	
	CANopen index: 2067/3	Read/write:	R/WS	
	INTERBUS index: 5FBD/59	Unit:	0.01 kHz	
	DeviceNet path: A3/01/67	Factory setting:	0	
Frequency corresponding to the minimum speed				
PFr	Terminal display: [RP max value]			
	Logic address: 13303 = 16#33F7	Type:	UINT	
	CANopen index: 2067/4	Read/write:	R/WS	
	INTERBUS index: 5FBD/5A	Unit:	0.01 kHz	
	DeviceNet path: A3/01/68	Factory setting:	3000	
Frequency corresponding to the maximum speed				
PFI	Terminal display: [RP filter]			
	Logic address: 13304 = 16#33F8	Type:	UINT	
	CANopen index: 2067/5	Read/write:	R/WS	
	INTERBUS index: 5FBD/5B	Unit:	1 ms	
	DeviceNet path: A3/01/69	Factory setting:	0	
Interference filtering				

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[ENCODER CONFIGURATION] (IEn-)

Code	Description		
EnS	Terminal display: [Encoder type] Logic address: 5608 = 16#15E8 CANopen index: 201A/9 INTERBUS index: 5FBC/75 DeviceNet path: 7D/01/09		
	To be configured in accordance with the type of encoder used. 0 = [NO] (nO): Encoder card missing 1 = [AABB] (AAAb): For signals A, A-, B, B- 2 = [AB] (Ab): For signals A, B 3 = [A] (A): For signal A		
EnC	Terminal display: [Encoder check] Logic address: 5605 = 16#15E5 CANopen index: 201A/6 INTERBUS index: 5FB3/7F DeviceNet path: 7D/01/06		
	Encoder feedback check 0 = [Not done] (nO): Check not performed 1 = [Yes] (YES): Activates encoder monitoring 2 = [Done] (dOnE): Check performed successfully		
EnU	Terminal display: [Encoder usage] Logic address: 5606 = 16#15E6 CANopen index: 201A/7 INTERBUS index: 5FBC/73 DeviceNet path: 7D/01/07		
	0 = [No] (nO): Function inactive. In this case, the other parameters cannot be accessed. 1 = [Fdbk monit.] (SEC): The encoder provides speed feedback for monitoring only. 2 = [Spd fdk reg.] (rEG): The encoder provides speed feedback for regulation and monitoring. This configuration is automatic if the drive has been configured for closed-loop operation, and is only possible in this type of operation. 3 = [Speed ref.] (PGr): The encoder provides a reference.		
PGI	Terminal display: [Number of pulses] Logic address: 5604 = 16#15E4 CANopen index: 201A/5 INTERBUS index: 5FB3/7E DeviceNet path: 7D/01/05		
	Adjustment range: 100 to 5000 Number of pulses per encoder revolution.		
PGA	Terminal display: [Reference type] Logic address: 13301 = 16#33F5 CANopen index: 2067/2 INTERBUS index: 5FBD/58 DeviceNet path: A3/01/66		
	0 = [Encoder] (EnC): Use of an encoder 1 = [Freq. gen.] (PtG): Use of a frequency generator (unsigned reference)		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description			
EIL	Terminal display:	[Freq. min value]		
	Logic address:	13310 = 16#33FE	Type:	INT
	CANopen index:	2067/B	Read/write:	R/WS
	INTERBUS index:	5FBD/5C	Unit:	0.01 kHz
	DeviceNet path:	A3/01/6F	Factory setting:	0
			Adjustment range:	-30,000 to 30,000
Frequency corresponding to the minimum speed				
EFr	Terminal display:	[Freq. max value]		
	Logic address:	13311 = 16#33FF	Type:	UINT
	CANopen index:	2067/C	Read/write:	R/WS
	INTERBUS index:	5FBD/5D	Unit:	0.01 kHz
	DeviceNet path:	A3/01/70	Factory setting:	30,000
			Adjustment range:	0 to 30,000
Frequency corresponding to the maximum speed				
EFI	Terminal display:	[Freq. signal filter]		
	Logic address:	13312 = 16#3400	Type:	UINT
	CANopen index:	2067/D	Read/write:	R/WS
	INTERBUS index:	5FBD/5E	Unit:	1 ms
	DeviceNet path:	A3/01/71	Factory setting:	0
			Adjustment range:	0 to 1000
Interference filtering				

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[R1 CONFIGURATION] (RI-)

Code	Description		
r1	Terminal display: [R1 Assignment]		
	Logic address: 5001 = 16#1389	Type: WORD (listing)	
	CANopen index: 2014/2	Read/write: R/W/S	
	INTERBUS index: 5FB3/7B	Factory setting: 1	
	DeviceNet path: 7A/01/02		
	0 = [No] (nO): Not assigned 1 = [No drive flt] (FLt): Drive not faulty (relay normally energized, and de-energized if there is a fault) 2 = [Drv running] (rUn): Drive running 4 = [Freq. attain.] (FtA): Frequency reached 5 = [HSP attain.] (FLA): High speed reached 6 = [I attained] (CtA): Current threshold reached 7 = [Freq.ref.att] (SrA): Frequency reference reached 8 = [Th.mot. att.] (tSA): Motor 1 thermal state reached 10 = [PID error al] (PEE): PID error alarm 11 = [PID fdbk al.] (PFA): PID feedback alarm 12 = [AI2 Al. 4-20] (AP2): Alarm indicating absence of 4-20 mA signal on input AI2 13 = [Freq. 2 attained] (F2A): Frequency 2 reached 14 = [Th.drv.att.] (tAd): Drive thermal state reached 37 = [Th.mot2 att] (tS2): Motor 2 thermal state reached 38 = [Th.mot3 att] (tS3): Motor 3 thermal state reached 47 = [Neg Torque] (AtS): Negative torque (braking) 48 = [Cnfg.0 act.] (CnF0): Configuration 0 active 49 = [Cnfg.1 act.] (CnF1): Configuration 1 active 50 = [Cnfg.2 act.] (CnF2): Configuration 2 active 52 = [Set 1 active] (CFP1): Parameter set 1 active 53 = [Set 2 active] (CFP2): Parameter set 2 active 54 = [Set 3 active] (CFP3): Parameter set 3 active 64 = [DC charged] (dbl): DC bus loading 65 = [In braking] (brS): Drive braking 66 = [P. removed] (PRM): Drive locked by "Power removal" input 78 = [Limit sw.att] (LSA): Limit switch reached 79 = [I present] (MCP): Motor current present 80 = [Alarm Grp 1] (AG1): Alarm group 1 81 = [Alarm Grp 2] (AG2): Alarm group 2 82 = [Alarm Grp 3] (AG3): Alarm group 3 83 = [PTC1 alarm] (P1A): Probe alarm motor 1 84 = [PTC2 alarm] (P2A): Probe alarm motor 2 85 = [LI6=PTC alarm] (PLA): Probe alarm motor 3 87 = [Ext. fault al] (EFA): External fault alarm 88 = [Under V. al.] (USA): Undervoltage alarm 89 = [Uvolt warn] (UPA): Undervoltage warning 90 = [slipping al.] (AnA): Slipping alarm 91 = [AI. °C drv] (tHA): Drive overheating 93 = [Load mvt al] (bSA): Braking speed alarm 94 = [Brk cont. al] (bCA): Brake contact alarm 95 = [Lim T/l att.] (SSA): Torque limit alarm 103 = [Torque Control al.] (rtA): Torque control alarm 104 = [IGBT al.] (tJA): IGBT alarm 105 = [Brake R. al.] (bOA): Braking resistor temperature alarm 106 = [Option al.] (APA): Alarm generated by an option card 107 = [AI3 Al. 4-20] (AP3): Alarm indicating absence of 4-20 mA signal on input AI3 108 = [AI4 Al. 4-20] (AP4): Alarm indicating absence of 4-20 mA signal on input AI4 127 = [Ready] (rdY): Drive ready		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description			
r1d	Terminal display: [R1 Delay time]			
	Logic address: 4241 = 16#1091	Type:	UINT	
	CANopen index: 200C/2A	Read/write:	R/WS	
	INTERBUS index: 5FBC/2C	Unit:	1 ms	
	DeviceNet path: 76/01/2A	Factory setting:	0	
The change in state only takes effect once the configured time has elapsed, when the information becomes true.				
r1s	Terminal display: [R1 Active at]			
	Logic address: 4201 = 16#1069	Type:	WORD (listing)	
	CANopen index: 200C/2	Read/write:	R/WS	
	INTERBUS index: 5FBC/1C	Factory setting:	0	
	DeviceNet path: 76/01/02			
Configuration of the operating logic: 0 = [1] (POS) : State 1 when the information is true 1 = [0] (nEG) : State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive flt] (FLt) assignment.				
r1H	Terminal display: [R1 Holding time]			
	Logic address: 4221 = 16#107D	Type:	UINT	
	CANopen index: 200C/16	Read/write:	R/WS	
	INTERBUS index: 5FBC/24	Unit:	1 ms	
	DeviceNet path: 76/01/16	Factory setting:	0	
The change in state only takes effect once the configured time has elapsed, when the information becomes false.				

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[R2 CONFIGURATION] (R2-)

Code	Description		
r2	Terminal display: [R2 Assignment] Logic address: 5002 = 16#138A CANopen index: 2014/3 INTERBUS index: 5FB3/7C DeviceNet path: 7A/01/03		
	Identical to r1, see "r1", page 179 , with the addition of (read-only as these selections can only be configured in the [APPLICATION FUNCT.] (Fun-) menu): 9 = [Brk control] (bLC): Brake contactor control 3 = [Output cont] (OCC): Output contactor control 73 = [Input cont] (LLC): Line contactor control 101 = [Spool end] (EbO) : Spool end (traverse control function) 102 = [Sync wobbl.] (tSY): "Counter wobble" synchronization		
r2d	Terminal display: [R2 Delay time] Logic address: 4242 = 16#1092 CANopen index: 200C/2B INTERBUS index: 5FBC/2D DeviceNet path: 76/01/2B	Type:	UINT
		Read/write:	R/W/S
		Unit:	1 ms
		Factory setting:	0
		Adjustment range:	0 to 9999
	The delay time cannot be set for the [No drive flt] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
r2s	Terminal display: [R2 Active at] Logic address: 4202 = 16#106A CANopen index: 200C/3 INTERBUS index: 5FBC/1D DeviceNet path: 76/01/03	Type:	WORD (listing)
		Read/write:	R/W/S
		Factory setting:	0
	Configuration of the operating logic: 0 = [1] (POS) : State 1 when the information is true 1 = [0] (nEG) : State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive flt] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
r2H	Terminal display: [R2 Holding time] Logic address: 4222 = 16#107E CANopen index: 200C/17 INTERBUS index: 5FBC/25 DeviceNet path: 76/01/17	Type:	UINT
		Read/write:	R/W/S
		Unit:	1 ms
		Factory setting:	0
		Adjustment range:	0 to 9999
	The holding time cannot be set for the [No drive flt] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[R3 CONFIGURATION] (r3-)

Code	Description		
r3	Terminal display: [R3 Assignment]		
	Logic address:	5003 = 16#138B	Type: WORD (listing)
	CANopen index:	2014/4	Read/write: R/W/S
	INTERBUS index:	5FBC/67	Factory setting: 0
	DeviceNet path:	7A/01/04	
	Identical to r2, see "r2", page 181 .		
r3d	Terminal display: [R3 Delay time]		
	Logic address:	4243 = 16#1093	Type: UINT
	CANopen index:	200C/2C	Read/write: R/W/S
	INTERBUS index:	5FBC/2E	Unit: 1 ms
	DeviceNet path:	76/01/2C	Factory setting: 0
			Adjustment range: 0 to 9999
	The delay time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
r3s	Terminal display: [R3 Active at]		
	Logic address:	4203 = 16#106B	Type: WORD (listing)
	CANopen index:	200C/4	Read/write: R/W/S
	INTERBUS index:	5FBC/1E	Factory setting: 0
	DeviceNet path:	76/01/04	
	Configuration of the operating logic: 0 = [1] (POS) : State 1 when the information is true 1 = [0] (nEG) : State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
r3H	Terminal display: [R3 Holding time]		
	Logic address:	4223 = 16#107F	Type: UINT
	CANopen index:	200C/18	Read/write: R/W/S
	INTERBUS index:	5FBC/26	Unit: 1 ms
	DeviceNet path:	76/01/18	Factory setting: 0
			Adjustment range: 0 to 9999
	The holding time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[R4 CONFIGURATION] (r4-)

Code	Description		
r4	Terminal display: [R4 Assignment]		
	Logic address:	5004 = 16#138C	Type: WORD (listing)
	CANopen index:	2014/5	Read/write: R/W/S
	INTERBUS index:	5FBC/68	Factory setting: 0
	DeviceNet path:	7A/01/05	
	Identical to r2, see "r2", page 181 .		
r4d	Terminal display: [R4 Delay time]		
	Logic address:	4244 = 16#1094	Type: UINT
	CANopen index:	200C/2D	Read/write: R/W/S
	INTERBUS index:	5FBC/2F	Unit: 1 ms
	DeviceNet path:	76/01/2D	Factory setting: 0
			Adjustment range: 0 to 9999
	The delay time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
r4s	Terminal display: [R4 Active at]		
	Logic address:	4204 = 16#106C	Type: WORD (listing)
	CANopen index:	200C/5	Read/write: R/W/S
	INTERBUS index:	5FBC/1F	Factory setting: 0
	DeviceNet path:	76/01/05	
	Configuration of the operating logic: 0 = [1] (POS): State 1 when the information is true 1 = [0] (nEG): State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
r4H	Terminal display: [R4 Holding time]		
	Logic address:	4224 = 16#1080	Type: UINT
	CANopen index:	200C/19	Read/write: R/W/S
	INTERBUS index:	5FBC/27	Unit: 1 ms
	DeviceNet path:	76/01/19	Factory setting: 0
			Adjustment range: 0 to 9999
	The holding time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[LO1 CONFIGURATION] (LO1-)

Code	Description		
LO1	Terminal display: [LO1 assignment]		
	Logic address:	5009 = 16#1391	Type: WORD (listing)
	CANopen index:	2014/A	Read/write: R/W/S
	INTERBUS index:	5FB3/7D	Factory setting: 0
	DeviceNet path:	7A/01/0A	
	Identical to r2, see "r2", page 181 .		
LO1d	Terminal display: [LO1 delay time]		
	Logic address:	4249 = 16#1099	Type: UINT
	CANopen index:	200C/32	Read/write: R/W/S
	INTERBUS index:	5FBC/30	Unit: 1 ms
	DeviceNet path:	76/01/32	Factory setting: 0
			Adjustment range: 0 to 9999
	The delay time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
LO1S	Terminal display: [LO1 active at]		
	Logic address:	4209 = 16#1071	Type: WORD (listing)
	CANopen index:	200C/2D	Read/write: R/W/S
	INTERBUS index:	5FBC/20	Factory setting: 0
	DeviceNet path:	76/01/0A	
	Configuration of the operating logic: 0 = [1] (POS): State 1 when the information is true 1 = [0] (nEG): State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
LO1H	Terminal display: [LO1 holding time]		
	Logic address:	4229 = 16#1085	Type: UINT
	CANopen index:	200C/1E	Read/write: R/W/S
	INTERBUS index:	5FBC/28	Unit: 1 ms
	DeviceNet path:	76/01/1E	Factory setting: 0
			Adjustment range: 0 to 9999
	The holding time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[LO2 CONFIGURATION] (LO2-)

Code	Description		
LO2	Terminal display: [LO2 assignment]		
	Logic address:	5010 = 16#1392	Type: WORD (listing)
	CANopen index:	2014/B	Read/write: R/W/S
	INTERBUS index:	5FBC/6A	Factory setting: 0
	DeviceNet path:	7A/01/0B	
	Identical to r2, see "r2", page 181 .		
LO2d	Terminal display: [LO2 delay time]		
	Logic address:	4250 = 16#109A	Type: UINT
	CANopen index:	200C/33	Read/write: R/W/S
	INTERBUS index:	5FBC/31	Unit: 1 ms
	DeviceNet path:	76/01/33	Factory setting: 0
			Adjustment range: 0 to 9999
	The delay time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
LO2S	Terminal display: [LO2 active at]		
	Logic address:	4210 = 16#1072	Type: WORD (listing)
	CANopen index:	200C/B	Read/write: R/W/S
	INTERBUS index:	5FBC/21	Factory setting: 0
	DeviceNet path:	76/01/0B	
	Configuration of the operating logic: 0 = [1] (POS) : State 1 when the information is true 1 = [0] (nEG) : State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
LO2H	Terminal display: [LO2 holding time]		
	Logic address:	4230 = 16#1086	Type: UINT
	CANopen index:	200C/1F	Read/write: R/W/S
	INTERBUS index:	5FBC/29	Unit: 1 ms
	DeviceNet path:	76/01/1F	Factory setting: 0
			Adjustment range: 0 to 9999
	The holding time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[LO3 CONFIGURATION] (LO3-)

Code	Description		
LO3	Terminal display: [LO3 assignment]		
	Logic address:	5011 = 16#1393	Type: WORD (listing)
	CANopen index:	2014/C	Read/write: R/W/S
	INTERBUS index:	5FBC/6B	Factory setting: 0
	DeviceNet path:	7A/01/0C	
	Identical to r2, see "r2", page 181 .		
LO3d	Terminal display: [LO3 delay time]		
	Logic address:	4251 = 16#109B	Type: UINT
	CANopen index:	200C/34	Read/write: R/W/S
	INTERBUS index:	5FBC/32	Unit: 1 ms
	DeviceNet path:	76/01/34	Factory setting: 0
			Adjustment range: 0 to 9999
	The delay time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
LO3S	Terminal display: [LO3 active at]		
	Logic address:	4211 = 16#1073	Type: WORD (listing)
	CANopen index:	200C/C	Read/write: R/W/S
	INTERBUS index:	5FBC/22	Factory setting: 0
	DeviceNet path:	76/01/0C	
	Configuration of the operating logic: 0 = [1] (POS): State 1 when the information is true 1 = [0] (nEG): State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
LO3H	Terminal display: [LO3 holding time]		
	Logic address:	4231 = 16#1087	Type: UINT
	CANopen index:	200C/20	Read/write: R/W/S
	INTERBUS index:	5FBC/2A	Unit: 1 ms
	DeviceNet path:	76/01/20	Factory setting: 0
			Adjustment range: 0 to 9999
	The holding time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[LO4 CONFIGURATION] (LO4-)

Code	Description		
LO4	Terminal display: [LO4 assignment]		
	Logic address:	5012 = 16#1394	Type: WORD (listing)
	CANopen index:	2014/D	Read/write: R/W/S
	INTERBUS index:	5FBC/6C	Factory setting: 0
	DeviceNet path:	7A/01/0D	
	Identical to r2, see "r2", page 181 .		
LO4d	Terminal display: [LO4 delay time]		
	Logic address:	4252 = 16#109C	Type: UINT
	CANopen index:	200C/35	Read/write: R/W/S
	INTERBUS index:	5FBC/33	Unit: 1 ms
	DeviceNet path:	76/01/35	Factory setting: 0
			Adjustment range: 0 to 9999
	The delay time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		
LO4S	Terminal display: [LO4 active at]		
	Logic address:	4212 = 16#1074	Type: WORD (listing)
	CANopen index:	200C/D	Read/write: R/W/S
	INTERBUS index:	5FBC/23	Factory setting: 0
	DeviceNet path:	76/01/0D	
	Configuration of the operating logic: 0 = [1] (POS): State 1 when the information is true 1 = [0] (nEG): State 0 when the information is true Configuration [1] (1) cannot be modified for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments.		
LO4H	Terminal display: [LO4 holding time]		
	Logic address:	4232 = 16#1088	Type: UINT
	CANopen index:	200C/21	Read/write: R/W/S
	INTERBUS index:	5FBC/2B	Unit: 1 ms
	DeviceNet path:	76/01/21	Factory setting: 0
			Adjustment range: 0 to 9999
	The holding time cannot be set for the [No drive fit] (FLt), [Brk control] (bLC) and [Input cont.] (LCC) assignments and remains at 0.		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AO1 CONFIGURATION] (AO1-)

Code	Description		
AO1	Terminal display: [AO1 assignment]		
	Logic address: 5021 = 16#139D	Type: WORD (listing)	
	CANopen index: 2014/16	Read/write: R/W/S	
	INTERBUS index: 5FBC/6D	Factory setting: 130	
	DeviceNet path: 7A/01/16		
	0 = [No] (nO): Not assigned 129 = [I motor] (OCr): Current in the motor, between 0 and 2 ln 130 = [Motor freq.] (OFr): Output frequency, between 0 and [Max frequency] (tFr) 131 = [Ramp out.] (OrP): Between 0 and [Max frequency] (tFr) 132 = [Motor torq.] (trq): Motor torque, between 0 and 3 times the rated motor torque 133 = [Sign. torque] (Stq): Signed motor torque, between -3 and +3 times the rated motor torque 134 = [sign ramp] (OrS): Signed ramp output, between - [Max frequency] (tFr) and + [Max frequency] (tFr) 135 = [PID ref.] (OPS): PID regulator reference between [Min PID reference] (PIP1) and [Max PID reference] (PIP2) 136 = [PID feedback] (OPF): PID regulator reference between [Min PID reference] (PIP1) and [Max PID reference] (PIP2) 137 = [PID error] (OPE): PID regulator error between - 5% and + 5% of ([Max PID feedback] (PIF2) - [Min PID feedback] (PIF1)) 138 = [PID output] (OPI): PID regulator output between [Low speed] (LSP) and [High speed] (HSP) 139 = [Mot. power] (OPr): Motor power, between 0 and 2.5 times [Rated motor power] (nPr) 140 = [Mot thermal] (tHr): Motor thermal state, between 0 and 200% of the rated thermal state 141 = [Drv thermal] (tHd): Drive thermal state, between 0 and 200% of the rated thermal state 173 = [Sign. o/p frq.] (OFS): Signed output frequency, between - [Max frequency] (tFr) and + [Max frequency] (tFr) 174 = [Mot therm2] (tHr2): Thermal state of motor 2, between 0 and 200% of the rated thermal state 175 = [Mot therm3] (tHr3): Thermal state of motor 3, between 0 and 200% of the rated thermal state 177 = [Uns.TrqRef] (Utr): Torque reference, between 0 and 3 times the rated motor torque 178 = [Sign trq ref.] (Str): Signed torque reference, between -3 and +3 times the rated motor torque 179 = [Torque lim.] (tqL): Torque limit, between 0 and 3 times the rated motor torque 180 = [Motor volt.] (UOP): Voltage applied to the motor, between 0 and [Rated motor volt.] (UnS)		
AO1t	Terminal display: [AO1 Type]		
	Logic address: 4601 = 16#11F9	Type: WORD (listing)	
	CANopen index: 2010/2	Read/write: R/W/S	
	INTERBUS index: 5FBC/53	Factory setting: 2	
	DeviceNet path: 78/01/02		
	1 = [Voltage] (10U): Voltage output 2 = [Current] (0A): Current output		
AOL1	Terminal display: [AO1 min Output]		
	Logic address: 4641 = 16#1221	Type: UINT	
	CANopen index: 2010/2A	Read/write: R/W/S	
	INTERBUS index: 5FBC/5F	Unit: 0.1 mA	
	DeviceNet path: 78/01/2A	Factory setting: 0	
		Adjustment range: 0 to 200	
AOH1	Terminal display: [AO1 max Output]		
	Logic address: 4651 = 16#122B	Type: UINT	
	CANopen index: 2010/34	Read/write: R/W/S	
	INTERBUS index: 5FBC/62	Unit: 0.1 mA	
	DeviceNet path: 78/01/34	Factory setting: 200	
		Adjustment range: 0 to 200	

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

Code	Description		
UOL1	Terminal display: [AO1 min Output]		
Logic address:	4621 = 16#120D	Type:	UINT
CANopen index:	2010/16	Read/write:	R/WS
INTERBUS index:	5FBC/59	Unit:	0.1 V
DeviceNet path:	78/01/16	Factory setting:	0
		Adjustment range:	0 to 100
UOH1	Terminal display: [AO1 max Output]		
Logic address:	4631 = 16#1217	Type:	UINT
CANopen index:	2010/20	Read/write:	R/WS
INTERBUS index:	5FBC/5C	Unit:	0.1 V
DeviceNet path:	78/01/20	Factory setting:	100
		Adjustment range:	0 to 100
AO1F	Terminal display: [AO1 Filter]		
Logic address:	4611 = 16#1203	Type:	UINT
CANopen index:	2010/C	Read/write:	R/WS
INTERBUS index:	5FBC/56	Unit:	0.01 s
DeviceNet path:	78/01/0C	Factory setting:	0
		Adjustment range:	0 to 1000
Interference filtering			

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AO2 CONFIGURATION] (AO2-)

Code	Description		
AO2	Terminal display: [AO2 assignment]		
	Logic address:	5022 = 16#139E	Type: WORD (listing)
	CANopen index:	2014/17	Read/write: R/WS
	INTERBUS index:	5FBC/6E	Factory setting: 129
	DeviceNet path:	7A/01/17	
	Same assignments as AO1, see "AO1", page 188 .		
AO2t	Terminal display: [AO2 Type]		
	Logic address:	4602 = 16#11FA	Type: WORD (listing)
	CANopen index:	2010/3	Read/write: R/WS
	INTERBUS index:	5FBC/54	Factory setting: 2
	DeviceNet path:	78/01/03	
	1 = [Voltage] (10U) : Voltage output 2 = [Current] (0A) : Current output 5 = [Voltage +/-] (n10U) : Bipolar voltage output		
AOL2	Terminal display: [AO2 min Output]		
	Logic address:	4642 = 16#1222	Type: UINT
	CANopen index:	2010/2B	Read/write: R/WS
	INTERBUS index:	5FBC/60	Unit: 0.1 mA
	DeviceNet path:	78/01/2B	Factory setting: 0
			Adjustment range: 0 to 200
AOH2	Terminal display: [AO2 max Output]		
	Logic address:	4652 = 16#122C	Type: UINT
	CANopen index:	2010/35	Read/write: R/WS
	INTERBUS index:	5FBC/63	Unit: 0.1 mA
	DeviceNet path:	78/01/35	Factory setting: 200
			Adjustment range: 0 to 200
UOL2	Terminal display: [AO2 min Output]		
	Logic address:	4622 = 16#120E	Type: UINT
	CANopen index:	2010/17	Read/write: R/WS
	INTERBUS index:	5FBC/5A	Unit: 0.1 V
	DeviceNet path:	78/01/17	Factory setting: 0
			Adjustment range: 0 to 100
UOH2	Terminal display: [AO2 max Output]		
	Logic address:	4632 = 16#1218	Type: UINT
	CANopen index:	2010/21	Read/write: R/WS
	INTERBUS index:	5FBC/5D	Unit: 0.1 V
	DeviceNet path:	78/01/21	Factory setting: 100
			Adjustment range: 0 to 100
AO2F	Terminal display: [AO2 Filter]		
	Logic address:	4612 = 16#1204	Type: UINT
	CANopen index:	2010/D	Read/write: R/WS
	INTERBUS index:	5FBC/57	Unit: 0.01 s
	DeviceNet path:	78/01/0D	Factory setting: 0
			Adjustment range: 0 to 1000
	Interference filtering		

[1.5 INPUTS / OUTPUTS CFG] (I-O-)

[AO3 CONFIGURATION] (AO3-)

Code	Description		
AO3	Terminal display: [AO3 assignment]		
	Logic address:	5023 = 16#139F	Type: WORD (listing)
	CANopen index:	2014/18	Read/write: R/WS
	INTERBUS index:	5FBC/6F	Factory setting: According to CFG (1)
	DeviceNet path:	7A/01/18	
	Same assignments as AO1, see "AO1", page 188 .		
AO3t	Terminal display: [AO3 Type]		
	Logic address:	4603 = 16#11FB	Type: WORD (listing)
	CANopen index:	2010/4	Read/write: R/WS
	INTERBUS index:	5FBC/55	Factory setting: 2
	DeviceNet path:	78/01/04	
	1 = [Voltage] (10U) : Voltage output 2 = [Current] (0A) : Current output 5 = [Voltage +/-] (n10U) : Bipolar voltage output		
AOL3	Terminal display: [AO3 min Output]		
	Logic address:	4643 = 16#1223	Type: UINT
	CANopen index:	2010/2C	Read/write: R/WS
	INTERBUS index:	5FBC/61	Unit: 0.1 mA
	DeviceNet path:	78/01/2C	Factory setting: 0
			Adjustment range: 0 to 200
AOH3	Terminal display: [AO3 max Output]		
	Logic address:	4653 = 16#122D	Type: UINT
	CANopen index:	2010/36	Read/write: R/WS
	INTERBUS index:	5FBC/64	Unit: 0.1 mA
	DeviceNet path:	78/01/36	Factory setting: 200
			Adjustment range: 0 to 200
UOL3	Terminal display: [AO3 min Output]		
	Logic address:	4623 = 16#120F	Type: UINT
	CANopen index:	2010/18	Read/write: R/WS
	INTERBUS index:	5FBC/5B	Unit: 0.1 V
	DeviceNet path:	78/01/18	Factory setting: 0
			Adjustment range: 0 to 100
UOH3	Terminal display: [AO3 max Output]		
	Logic address:	4633 = 16#1219	Type: UINT
	CANopen index:	2010/22	Read/write: R/WS
	INTERBUS index:	5FBC/5E	Unit: 0.1 V
	DeviceNet path:	78/01/22	Factory setting: 100
			Adjustment range: 0 to 100
AO3F	Terminal display: [AO3 Filter]		
	Logic address:	4613 = 16#1205	Type: UINT
	CANopen index:	2010/E	Read/write: R/WS
	INTERBUS index:	5FBC/58	Unit: 0.01 s
	DeviceNet path:	78/01/0E	Factory setting: 0
			Adjustment range: 0 to 1000
	Interference filtering		

(1) According to **CFG**, see "CFG", page [144](#).

If **CFG** = 1 or **CFG** = 2, factory setting = 133

If **CFG** = 4, factory setting = 138

If **CFG** = 6, factory setting = 130

[1.6 COMMAND] (CtL-)

Assignment conditions for logic inputs and control bits

The following elements are available for every command or function that can be assigned to a logic input or a control bit:

129 = [LI1] (LI1)	Drive with or without option	193 = [C201] (C201)	With integrated CANopen in [I/O profile] (IO) configuration
130 = [LI2] (LI2)		194 = [C202] (C202)	
131 = [LI3] (LI3)		195 = [C203] (C203)	
132 = [LI4] (LI4)		196 = [C204] (C204)	
133 = [LI5] (LI5)		197 = [C205] (C205)	
134 = [LI6] (LI6)		198 = [C206] (C206)	
135 = [LI7] (LI7)	With VW3A3201 logic I/O card	199 = [C207] (C207)	With integrated CANopen in [I/O profile] (IO) configuration
136 = [LI8] (LI8)		200 = [C208] (C208)	
137 = [LI9] (LI9)		201 = [C209] (C209)	
138 = [LI10] (LI10)		202 = [C210] (C210)	
139 = [LI11] (LI11)	With VW3A3202 extended I/O card	203 = [C211] (C211)	With integrated CANopen regardless of the configuration
140 = [LI12] (LI12)		204 = [C212] (C212)	
141 = [LI13] (LI13)		205 = [C213] (C213)	
142 = [LI14] (LI14)		206 = [C214] (C214)	
160 = [CD00] (Cd00)	In [I/O profile] (IO) configuration	207 = [C215] (C215)	With a communication card in [I/O profile] (IO) configuration
161 = [CD01] (Cd01)		209 = [C301] (C301)	
162 = [CD02] (Cd02)		210 = [C302] (C302)	
163 = [CD03] (Cd03)		211 = [C303] (C303)	
164 = [CD04] (Cd04)		212 = [C304] (C304)	
165 = [CD05] (Cd05)		213 = [C305] (C305)	
166 = [CD06] (Cd06)		214 = [C306] (C306)	
167 = [CD07] (Cd07)		215 = [C307] (C307)	
168 = [CD08] (Cd08)		216 = [C308] (C308)	
169 = [CD09] (Cd09)		217 = [C309] (C309)	
170 = [CD10] (Cd10)		218 = [C310] (C310)	
171 = [CD11] (Cd11)		219 = [C311] (C311)	With a communication card regardless of the configuration
172 = [CD12] (Cd12)		220 = [C312] (C312)	
173 = [CD13] (Cd13)		221 = [C313] (C313)	
174 = [CD14] (Cd14)		222 = [C314] (C314)	
175 = [CD15] (Cd15)		223 = [C315] (C315)	
177 = [C101] (C101)	With integrated Modbus in [I/O profile] (IO) configuration.	225 = [C401] (C401)	With Controller Inside card in [I/O profile] (IO) configuration
178 = [C102] (C102)		226 = [C402] (C402)	
179 = [C103] (C103)		227 = [C403] (C403)	
180 = [C104] (C104)		228 = [C404] (C404)	
181 = [C105] (C105)		229 = [C405] (C405)	
182 = [C106] (C106)		230 = [C406] (C406)	
183 = [C107] (C107)		231 = [C407] (C407)	
184 = [C108] (C108)		232 = [C408] (C408)	
185 = [C109] (C109)		233 = [C409] (C409)	
186 = [C110] (C110)		234 = [C410] (C410)	
187 = [C111] (C111)		235 = [C411] (C411)	
188 = [C112] (C112)		236 = [C412] (C412)	
189 = [C113] (C113)		237 = [C413] (C413)	
190 = [C114] (C114)		238 = [C414] (C414)	
191 = [C115] (C115)		239 = [C415] (C415)	

[1.6 COMMAND] (CtL-)

Code	Description		
Fr1	Terminal display: [Ref.1 channel] Logic address: 8413 = 16#20DD CANopen index: 2036/E INTERBUS index: 5FBC/A4 DeviceNet path: 8B/01/0E		
	1 = [AI1] (AI1) : Analog input 2 = [AI2] (AI2) : Analog input 3 = [AI3] (AI3) : Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4) : Analog input, if VW3A3202 extension card has been inserted 163 = [HMI] (LCC) : Graphic display terminal 164 = [Modbus] (Mdb) : Integrated Modbus 167 = [CANopen] (CAN) : Integrated CANopen 169 = [Com. card] (nEt) : Communication card (if inserted) 170 = [Prog. card] (APP) : Controller Inside card (if inserted) 181 = [RP] (PI) : Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG) : Encoder input, if encoder card has been inserted		
rIn	Terminal display: [RV Inhibition] Logic address: 3108 = 16#C24 CANopen index: 2001/9 INTERBUS index: 5FB3/79 DeviceNet path: 70/01/6D		
	0 = [No] (nO) 1 = [Yes] (YES) Inhibition of movement in reverse direction, does not apply to direction requests sent by logic inputs.		
PSt	Terminal display: [Stop Key priority] Logic address: 64002 = 16#FA02 CANopen index: 2262/3 INTERBUS index: 5FBD/88 DeviceNet path: 94/01/06		
	0 = [No] (nO) 1 = [Yes] (YES) : Gives priority to the STOP key on the graphic display terminal when the graphic display terminal is not enabled as the command channel.		
CHCF	Terminal display: [Profile] Logic address: 8401 = 16#20D1 CANopen index: 2036/2 INTERBUS index: 5FBC/9F DeviceNet path: 8B/01/02		
	0 = [8 serie] (SE8) : ATV58 interchangeability (see migration manual) 1 = [Not separ.] (SIM) : Reference and command, not separate 2 = [Separate] (SEP) : Separate reference and command 3 = [I/O profile] (IO) : I/O profile		

(1) According to **CFG**, see "CFG", page [144](#).

If **CFG** = 5: Factory setting = 164

Otherwise, factory setting = 1

[1.6 COMMAND] (CtL-)

Code	Description		
CCS	Terminal display: [Cmd switching] Logic address: 8421 = 16#20D1 CANopen index: 2036/16 INTERBUS index: 5FBC/A7 DeviceNet path: 8B/01/16		
	0 = [No] (nO) : Not assigned 98 = [ch1 active] (Cd1) : No switching, [Cmd channel 1] (Cd1) active 99 = [ch2 active] (Cd2) : No switching, [Cmd channel 2] (Cd2) active 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192 .		
	<ul style="list-style-type: none"> • If the assigned input or bit is at 0, channel [Cmd channel 1] (Cd1) is active. • If the assigned input or bit is at 1, channel [Cmd channel 2] (Cd2) is active. 		
Cd1	Terminal display: [Cmd channel 1] Logic address: 8423 = 16#20E7 CANopen index: 2036/18 INTERBUS index: 5FBC/A8 DeviceNet path: 8B/01/18		
	1 = [Terminals] (tEr) : Terminals 3 = [HMI] (LCC) : Graphic display terminal 10 = [Modbus] (Mdb) : Integrated Modbus 20 = [CANopen] (CAn) : Integrated CANopen 30 = [Com. card] (nEt) : Communication card (if inserted) 31 = [Prog. card] (APP) : Controller Inside card (if inserted)		
Cd2	Terminal display: [Cmd channel 2] Logic address: 8424 = 16#20E8 CANopen index: 2036/19 INTERBUS index: 5FBC/A9 DeviceNet path: 8B/01/19		
	1 = [Terminals] (tEr) : Terminals 3 = [HMI] (LCC) : Graphic display terminal 10 = [Modbus] (Mdb) : Integrated Modbus 20 = [CANopen] (CAn) : Integrated CANopen 30 = [Com. card] (nEt) : Communication card (if inserted) 31 = [Prog. card] (APP) : Controller Inside card (if inserted)		

[1.6 COMMAND] (CtL-)

Code	Description		
rFC	Terminal display: [Ref. 2 switching]		
	Logic address: 8411 = 16#20DB	Type: WORD (listing)	
	CANopen index: 2036/C	Read/write: R/WS	
	INTERBUS index: 5FBC/A2	Factory setting: According to CFG and tCC ⁽¹⁾	
	DeviceNet path: 8B/01/0C		
	0 = [No] (nO) : Not assigned 96 = [ch1 active] (Fr1) : No switching, [Ref.1 channel] (Fr1) active 97 = [ch2 active] (Fr2) : No switching, [Ref.2 channel] (Fr2) active 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
	• If the assigned input or bit is at 0, [Ref.1 channel] (Fr1) is active. • If the assigned input or bit is at 1, channel [Ref.2 channel] (Fr2) is active.		
Fr2	Terminal display: [Ref.2 channel]		
	Logic address: 8414 = 16#20DE	Type: WORD (listing)	
	CANopen index: 2036/F	Read/write: R/WS	
	INTERBUS index: 5FBC/A5	Factory setting: According to CFG ⁽²⁾	
	DeviceNet path: 8B/01/0F		
	0 = [No] (nO) : Not assigned 1 = [AI1] (AI1) : Analog input 2 = [AI2] (AI2) : Analog input 3 = [AI3] (AI3) : Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4) : Analog input, if VW3A3202 extension card has been inserted 160 = [+/- speed] (UPdt) : +/- speed command 163 = [HMI] (LCC) : Graphic display terminal 164 = [Modbus] (Mdb) : Integrated Modbus 167 = [CANopen] (CAn) : Integrated CANopen 169 = [Com. card] (nEt) : Communication card (if inserted) 170 = [Prog. card] (APP) : Controller Inside card (if inserted) 181 = [RP] (PI) : Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG) : Encoder input, if encoder card has been inserted		
COP	Terminal display: [Copy channel 1 <-> 2]		
	Logic address: 8402 = 16#20D2	Type: WORD (listing)	
	CANopen index: 2036/3	Read/write: R/WS	
	INTERBUS index: 5FBC/A0	Factory setting: 0	
	DeviceNet path: 8B/01/03		
	0 = [No] (nO) : No copy 1 = [Reference] (SP) : Copy reference 2 = [Command] (Cd) : Copy command 3 = [Cmd + ref.] (ALL) : Copy command and reference		

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 5 and **tCC** = 0, factory setting = **[LI3] (LI3)**

If **CFG** = 5 and **tCC** = 1, factory setting = **[LI4] (LI4)**

Otherwise, factory setting = 96

⁽²⁾ According to **CFG**, see "CFG", page [144](#).

If **CFG** = 5, factory setting = 1

Otherwise, factory setting = 0

[1.6 COMMAND] (CtL-)

Code	Description		
FN1	Terminal display: [F1 key assignment] Logic address: 13501 = 16#34BD CANopen index: 2069/2 INTERBUS index: 5FBD/60 DeviceNet path: A4/01/66		
	0 = [No] : Not assigned 180 = [Jog] : JOG operation 181 = [Preset spd2] : Second preset speed 182 = [Preset spd3] : Third preset speed 183 = [PID ref. 2] : Second preset PID reference 184 = [PID ref. 3] : Third preset PID reference 185 = [+Speed] : + speed 186 = [-Speed] : - speed 187 = [T/K] : Command via the display terminal: Takes priority over [Cmd switching] (CCS) and [Ref. 2 switching] (rFC)		
FN2	Terminal display: [F2 key assignment] Logic address: 13502 = 16#34BE CANopen index: 2069/3 INTERBUS index: 5FBD/61 DeviceNet path: A4/01/67		
	0 = [No] : Not assigned 180 = [Jog] : JOG operation 181 = [Preset spd2] : Second preset speed 182 = [Preset spd3] : Third preset speed 183 = [PID ref. 2] : Second preset PID reference 184 = [PID ref. 3] : Third preset PID reference 185 = [+Speed] : + speed 186 = [-Speed] : - speed 187 = [T/K] : Command via the display terminal: Takes priority over [Cmd switching] (CCS) and [Ref. 2 switching] (rFC)		
FN3	Terminal display: [F3 key assignment] Logic address: 13503 = 16#34BF CANopen index: 2069/4 INTERBUS index: 5FBD/62 DeviceNet path: A4/01/68		
	0 = [No] : Not assigned 180 = [Jog] : JOG operation 181 = [Preset spd2] : Second preset speed 182 = [Preset spd3] : Third preset speed 183 = [PID ref. 2] : Second preset PID reference 184 = [PID ref. 3] : Third preset PID reference 185 = [+Speed] : + speed 186 = [-Speed] : - speed 187 = [T/K] : Command via the display terminal: Takes priority over [Cmd switching] (CCS) and [Ref. 2 switching] (rFC)		

[1.6 COMMAND] (CtL-)

Code	Description		
FN4	Terminal display: [F4 key assignment]		
	Logic address: 13504 = 16#34C0	Type:	WORD (listing)
	CANopen index: 2069/5	Read/write:	R/WS
	INTERBUS index: 5FBD/63	Factory setting:	0
	DeviceNet path: A4/01/69		
	0 = [No] : Not assigned 180 = [Jog] : JOG operation 181 = [Preset spd2] : Second preset speed 182 = [Preset spd3] : Third preset speed 183 = [PID ref. 2] : Second preset PID reference 184 = [PID ref. 3] : Third preset PID reference 185 = [+Speed] : + speed 186 = [-Speed] : - speed 187 = [T/K] : Command via the display terminal: Takes priority over [Cmd switching] (CCS) and [Ref. 2 switching] (rFC)		
bMP	Terminal display: [HMI cmd.]		
	Logic address: 13529 = 16#34D9	Type:	WORD (listing)
	CANopen index: 2069/1E	Read/write:	R/WS
	INTERBUS index: 5FBD/6C	Factory setting:	0
	DeviceNet path: A4/01/82		
	0 = [Stop] : Command and reference from previous channel are canceled and the drive comes to a standstill 1 = [Bumpless] : Command and reference from previous channel are copied		

[1.7 APPLICATION FUNCT.] (FUn-)

[REFERENCE SWITCH.] (rEF-)

Code	Description		
rCb	Terminal display: [Ref 1B switching] Logic address: 8412 = 16#20DC CANopen index: 2036/D INTERBUS index: 5FBC/A3 DeviceNet path: 8B/01/0D		
	0 = [No] (nO): Not assigned 96 = [ch1 active] (Fr1): No switching, [Ref.1 channel] (Fr1) active 100 = [ch1B active] (Fr1b): No switching, [Ref.1B channel] (Fr1b) active 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	96
Fr1b	Terminal display: [Ref.1B channel] Logic address: 8415 = 16#20DF CANopen index: 2036/10 INTERBUS index: 5FBC/A6 DeviceNet path: 8B/01/10		
	0 = [No] (nO): Not assigned 1 = [AI1] (AI1): Analog input 2 = [AI2] (AI2): Analog input 3 = [AI3] (AI3): Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4): Analog input, if VW3A3202 extension card has been inserted 163 = [HMI] (LCC): Graphic display terminal 164 = [Modbus] (Mdb): Integrated Modbus 167 = [CANopen] (CAN): Integrated CANopen 169 = [Com. card] (nEt): Communication card (if inserted) 170 = [Prog. card] (APP): Controller Inside card (if inserted) 181 = [RP] (PI): Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG): Encoder input, if encoder card has been inserted	Type:	Word (listing)
		Read/write:	R/WS
		Factory setting:	0

[1.7 APPLICATION FUNCT.] (FUn-)

[REF. OPERATIONS] (OAI-)

Code	Description		
SA2	Terminal display: [Summing ref. 2] Logic address: 11801 = 16#2E19 CANopen index: 2058/2 INTERBUS index: 5FBD/13 DeviceNet path: 9C/01/02		
	Selection of a reference to be added to [Ref.1 channel] (Fr1) or [Ref.1B channel] (Fr1b). 0 = [No] (nO) : No source assigned 1 = [AI1] (AI1) : Analog input 2 = [AI2] (AI2) : Analog input 3 = [AI3] (AI3) : Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4) : Analog input, if VW3A3202 extension card has been inserted 163 = [HMI] (LCC) : Graphic display terminal 164 = [Modbus] (Mdb) : Integrated Modbus 167 = [CANopen] (CAn) : Integrated CANopen 169 = [Com. card] (nEt) : Communication card (if inserted) 170 = [Prog. card] (APP) : Controller Inside card (if inserted) 181 = [RP] (PI) : Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG) : Encoder input, if encoder card has been inserted		
SA3	Terminal display: [Summing ref. 3] Logic address: 11802 = 16#2E1A CANopen index: 2058/3 INTERBUS index: 5FBD/14 DeviceNet path: 9C/01/03		
	Selection of a reference to be added to [Ref.1 channel] (Fr1) or [Ref.1B channel] (Fr1b). Possible assignments are identical to [Summing ref. 2] (SA2) above.		
dA2	Terminal display: [Subtract ref. 2] Logic address: 11811 = 16#2E23 CANopen index: 2058/C INTERBUS index: 5FBD/15 DeviceNet path: 9C/01/0C		
	Selection of a reference to be subtracted from [Ref.1 channel] (Fr1) or [Ref.1B channel] (Fr1b). Possible assignments are identical to [Summing ref. 2] (SA2) above.		
dA3	Terminal display: [Subtract ref. 3] Logic address: 11812 = 16#2E24 CANopen index: 2058/D INTERBUS index: 5FBD/16 DeviceNet path: 9C/01/0D		
	Selection of a reference to be subtracted from [Ref.1 channel] (Fr1) or [Ref.1B channel] (Fr1b). Possible assignments are identical to [Summing ref. 2] (SA2) above.		

(1) According to **CFG**, see "CFG", page 144.

If **CFG** = 1: Factory setting = 2

If **CFG** = 3: Factory setting = 2

Otherwise, factory setting = 0

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
MA2	Terminal display: [Multiplier ref. 2] Logic address: 11821 = 16#2E2D CANopen index: 2058/16 INTERBUS index: 5FBD/17 DeviceNet path: 9C/01/16		
	Selection of a multiplier reference for [Ref.1 channel] (Fr1) or [Ref.1B channel] (Fr1b). Possible assignments are identical to [Summing ref. 2] (SA2), see "SA2", page 199 .		
MA3	Terminal display: [Multiplier ref. 3] Logic address: 11822 = 16#2E2E CANopen index: 2058/17 INTERBUS index: 5FBD/18 DeviceNet path: 9C/01/17		
	Selection of a multiplier reference for [Ref.1 channel] (Fr1) or [Ref.1B channel] (Fr1b). Possible assignments are identical to [Summing ref. 2] (SA2), see "SA2", page 199 .		

[1.7 APPLICATION FUNCT.] (FUn-)

[RAMP] (rPt-)

Code	Description		
rPt	Terminal display: [Ramp type]		
	Logic address: 9004 = 16#232C	Type:	WORD (listing)
	CANopen index: 203C/5	Read/write:	R/W
	INTERBUS index: 5FB3/CB	Factory setting:	0
DeviceNet path: 8E/01/05			
0 = [Linear] (LIn) 1 = [S ramp] (S) 2 = [U ramp] (U) 3 = [Customized] (CUS)			
Inr	Terminal display: [Ramp increment]		
	Logic address: 9020 = 16#233C	Type:	WORD (listing)
	CANopen index: 203C/15	Read/write:	R/W
	INTERBUS index: 5FBF/26	Factory setting:	1
DeviceNet path: 8E/01/15			
0 = [0.01] (0.01): Ramp up to 99.99 seconds 1 = [0.1] (0.1): Ramp up to 999.9 seconds 2 = [1] (1): Ramp up to 6000 seconds			
ACC	Terminal display: [Acceleration]		
	Logic address: 9001 = 16#2329	Type:	UINT
	CANopen index: 203C/2	Read/write:	R/W
	INTERBUS index: 5FBF/1E	Unit:	According to Inr (1)
DeviceNet path: 8E/01/02			
Factory setting: 30 Adjustment range: According to Inr (1)			
Time to accelerate from 0 to the [Rated motor freq.] (FrS) , see "FrS", page 149 .			
dEC	Terminal display: [Deceleration]		
	Logic address: 9002 = 16#232A	Type:	UINT
	CANopen index: 203C/3	Read/write:	R/W
	INTERBUS index: 5FBF/1F	Unit:	According to Inr (1)
DeviceNet path: 8E/01/03			
Factory setting: 30 Adjustment range: According to Inr (1)			
Time to decelerate from the [Rated motor freq.] (FrS) to 0, see "FrS", page 149 .			

(1) According to Inr, see "Inr", page [201](#).

If Inr = 0: Unit = 0.01 s, adjustment range = 1 to 9999

If Inr = 1: Unit = 0.1 s, adjustment range = 1 to 9999

If Inr = 2: Unit = 1 s, adjustment range = 1 to 6000

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
tA1	Terminal display: [Begin Acc round]			
	Logic address: 9005 = 16#232D	Type:	UINT	
	CANopen index: 203C/6	Read/write:	R/W	
	INTERBUS index: 5FB3/CC	Unit:	1%	
	DeviceNet path: 8E/01/06	Factory setting:	10	
			Adjustment range: 0 to 100	
Rounding of start of acceleration ramp as a % of the [Acceleration] (ACC) or [Acceleration 2] (AC2) ramp time.				
tA2	Terminal display: [End Acc round]			
	Logic address: 9006 = 16#232E	Type:	UINT	
	CANopen index: 203C/7	Read/write:	R/W	
	INTERBUS index: 5FB3/CD	Unit:	1%	
	DeviceNet path: 8E/01/07	Factory setting:	10	
			Adjustment range: 0 to (100 - tA1)	
Rounding of end of acceleration ramp as a % of the [Acceleration] (ACC) or [Acceleration 2] (AC2) ramp time.				
tA3	Terminal display: [Begin Dec round]			
	Logic address: 9007 = 16#232F	Type:	UINT	
	CANopen index: 203C/8	Read/write:	R/W	
	INTERBUS index: 5FB3/CE	Unit:	1%	
	DeviceNet path: 8E/01/08	Factory setting:	10	
			Adjustment range: 0 to 100	
Rounding of start of deceleration ramp as a % of the [Deceleration] (dEC) or [Deceleration 2] (dE2) ramp time.				
tA4	Terminal display: [End Dec round]			
	Logic address: 9008 = 16#2330	Type:	UINT	
	CANopen index: 203C/9	Read/write:	R/W	
	INTERBUS index: 5FB3/CF	Unit:	1%	
	DeviceNet path: 8E/01/09	Factory setting:	10	
			Adjustment range: 0 to (100 - tA3)	
Rounding of end of deceleration ramp as a % of the [Deceleration] (dEC) or [Deceleration 2] (dE2) ramp time.				
Fr	Terminal display: [Ramp 2 threshold]			
	Logic address: 9011 = 16#2333	Type:	UINT	
	CANopen index: 203C/C	Read/write:	R/W/S	
	INTERBUS index: 5FB3/D0	Unit:	0.1 Hz	
	DeviceNet path: 8E/01/0C	Factory setting:	0	
			Adjustment range: 0 to [Max frequency] (tFr)	
Ramp switching threshold				

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
rPS	Terminal display: [Ramp switching]		
	Logic address: 9010 = 16#2332	Type:	WORD (listing)
	CANopen index: 203C/B	Read/write:	R/WS
	INTERBUS index: 5FBC/B3	Factory setting:	0
	DeviceNet path: 8E/01/0B		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
	• ACC and dEC are enabled when the assigned input or bit is at 0. • AC2 and dE2 are enabled when the assigned input or bit is at 1.		
AC2	Terminal display: [Acceleration 2]		
	Logic address: 9012 = 16#2334	Type:	UINT
	CANopen index: 203C/D	Read/write:	R/W
	INTERBUS index: 5FBF/24	Unit:	According to Inr (1)
	DeviceNet path: 8E/01/0D	Factory setting:	50
		Adjustment range:	According to Inr (1)
	Time to accelerate from 0 to the [Rated motor freq.] (FrS) .		
dE2	Terminal display: [Deceleration 2]		
	Logic address: 9013 = 16#2335	Type:	UINT
	CANopen index: 203C/E	Read/write:	R/W
	INTERBUS index: 5FBF/25	Unit:	According to Inr (1)
	DeviceNet path: 8E/01/0E	Factory setting:	50
		Adjustment range:	According to Inr (1)
	Time to decelerate from the [Rated motor freq.] (FrS) to 0.		

(1) According to [Inr](#), see "Inr", page [201](#).

If [Inr](#) = 0: Unit = 0.01 s, adjustment range = 1 to 9999

If [Inr](#) = 1: Unit = 0.1 s, adjustment range = 1 to 9999

If [Inr](#) = 2: Unit = 1 s, adjustment range = 1 to 6000

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
brA	Terminal display: [Dec ramp adapt.]		
	Logic address: 9003 = 16#232B	Type:	WORD (listing)
	CANopen index: 203C/4	Read/write:	R/WS
	INTERBUS index: 5FB3/CA	Factory setting:	0
	DeviceNet path: 8E/01/04		
<p>Activating this function automatically adapts the deceleration ramp, if this has been set at too low a value for the inertia of the load.</p> <p>0 = [No] (nO): Function inactive 1 = [Yes] (YES): Function active, for applications that do not require strong deceleration.</p> <p>The following selections appear depending on the rating of the drive. They enable stronger deceleration to be obtained than with [Yes] (YES):</p> <ul style="list-style-type: none"> 2 = [High torq. A] (dYnA) 3 = [High torq. B] (dYnb) 4 = [High torq. C] (dYnC) 			

[1.7 APPLICATION FUNCT.] (FUn-)

[STOP CONFIGURATION] (Stt-)

Code	Description		
Stt	Terminal display: [Stop type] Logic address: 11201 = 16#2BC1 CANopen index: 2052/2 INTERBUS index: 5FB3/EB DeviceNet path: 99/01/02		
	Stop mode on disappearance of the run command or appearance of a stop command. 0 = [Ramp stop] (rMP): On ramp 1 = [Fast stop] (FSt): Fast stop 2 = [Freewheel] (nSt): Freewheel stop 3 = [DC injection] (dCl): DC injection stop		
nSt	Terminal display: [Freewheel stop ass.] Logic address: 11202 = 16#2BC2 CANopen index: 2052/3 INTERBUS index: 5FBD/02 DeviceNet path: 99/01/03		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192 .		
FSt	Terminal display: [Fast stop assign.] Logic address: 11204 = 16#2BC4 CANopen index: 2052/5 INTERBUS index: 5FBD/04 DeviceNet path: 99/01/05		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192 .		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
dCF	Terminal display: [Ramp divider]		
	Logic address: 11230 = 16#2BDE	Type: UINT	
	CANopen index: 2052/1F	Read/write: R/W	
	INTERBUS index: 5FB3/ED	Unit: 1	
	DeviceNet path: 99/01/1F	Factory setting: 4	Adjustment range: 0 to 10
The ramp that is enabled (dEC or dE2) is then divided by this coefficient when stop requests are sent.			
dCI	Terminal display: [DC injection assign.]		
	Logic address: 11203 = 16#2BC3	Type: WORD (listing)	
	CANopen index: 2052/4	Read/write: R/WS	
	INTERBUS index: 5FBD/03	Factory setting: 0	
	DeviceNet path: 99/01/04		
0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)			
See: "Assignment conditions for logic inputs and control bits", page 192 .			
IdC	Terminal display: [DC inject. level 1]		
	Logic address: 11210 = 16#2BCA	Type: UINT	
	CANopen index: 2052/B	Read/write: R/W	
	INTERBUS index: 5FB3/EC	Unit: 0.1 A	
	DeviceNet path: 99/01/0B	Factory setting: 0.64 In⁽¹⁾	Adjustment range: 0.1 In to 1.41 In⁽¹⁾
Level of DC injection braking current activated via logic input or selected as stop mode.			
tdI	Terminal display: [DC injection time 1]		
	Logic address: 11213 = 16#2BCD	Type: UINT	
	CANopen index: 2052/E	Read/write: R/W	
	INTERBUS index: 5FBF/55	Unit: 0.1 s	
	DeviceNet path: 99/01/0E	Factory setting: 5	Adjustment range: 1 to 300
Maximum current injection time [DC inject. level 1] (IdC)			

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
IdC2	Terminal display: [DC inject. level 2] Logic address: 11212 = 16#2BCC CANopen index: 2052/D INTERBUS index: 5FBF/54 DeviceNet path: 99/01/0D		
	Type:	UINT	
	Read/write:	R/W	
	Unit:	0.1 A	
	Factory setting:	0.5 In ⁽¹⁾	
	Adjustment range:	0.1 In to 1.41 In ⁽¹⁾	
	Injection current activated by logic input or selected as stop mode, once period of time [DC injection time 1] (tdl) has elapsed.		
tdC	Terminal display: [DC injection time 2] Logic address: 11211 = 16#2BCB CANopen index: 2052/C INTERBUS index: 5FBF/53 DeviceNet path: 99/01/0C		
	Type:	UINT	
	Read/write:	R/W	
	Unit:	0.1 s	
	Factory setting:	5	
	Adjustment range:	1 to 300	
	Maximum current injection time [DC inject. level 2] (IdC2)		

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

[AUTO DC INJECTION] (AdC-)

Code	Description		
AdC	Terminal display: [Auto DC injection] Logic address: 10401 = 16#28A1 CANopen index: 204A/2 INTERBUS index: 5FB3/E2 DeviceNet path: 95/01/02		
	0 = [No] (nO) : No injection 1 = [Yes] (YES) : Adjustable injection time 2 = [Continuous] (Ct) : Continuous standstill injection		
SdC1	Terminal display: [Auto DC inj. level 1] Logic address: 10403 = 16#28A3 CANopen index: 204A/4 INTERBUS index: 5FBF/45 DeviceNet path: 95/01/04		
	Type: UINT Read/write: R/W Unit: 0.1 A Factory setting: 0.7 In⁽¹⁾ Adjustment range: 0 to 1.2 In⁽¹⁾		
	Level of standstill DC injection current.		
tdC1	Terminal display: [Auto DC inj. time 1] Logic address: 10402 = 16#28A2 CANopen index: 204A/3 INTERBUS index: 5FBF/44 DeviceNet path: 95/01/03		
	Type: UINT Read/write: R/W Unit: 0.1 s Factory setting: 5 Adjustment range: 1 to 300		
	Standstill injection time.		
SdC2	Terminal display: [Auto DC inj. level 2] Logic address: 10405 = 16#28A5 CANopen index: 204A/6 INTERBUS index: 5FBF/47 DeviceNet path: 95/01/06		
	Type: UINT Read/write: R/W Unit: 0.1 A Factory setting: 0.5 In⁽¹⁾ Adjustment range: 0 to 1.2 In⁽¹⁾		
	2 nd level of standstill DC injection current.		
tdC2	Terminal display: [Auto DC inj. time 2] Logic address: 10404 = 16#28A4 CANopen index: 204A/5 INTERBUS index: 5FBF/46 DeviceNet path: 95/01/05		
	Type: UINT Read/write: R/W Unit: 0.1 s Factory setting: 0 Adjustment range: 0 to 300		
	2 nd standstill injection time.		

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

[JOG] (JOG-)

Code	Description		
JOG	Terminal display: [JOG] Logic address: 11110 = 16#2B66 CANopen index: 2051/B INTERBUS index: 5FBC/FF DeviceNet path: 98/01/6F		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	According to CFG and tCC ⁽¹⁾
	See: "Assignment conditions for logic inputs and control bits", page 192 . The function is active when the assigned input or bit is at 1.		
JGF	Terminal display: [Jog frequency] Logic address: 11111 = 16#2B67 CANopen index: 2051/C INTERBUS index: 5FBF/50 DeviceNet path: 98/01/70		
	Type:	UINT	
	Read/write:	R/W	
	Unit:	0.1 Hz	
	Factory setting:	100	
	Adjustment range:	0 to 100	
	Reference in jog operation		
JGT	Terminal display: [Jog delay] Logic address: 11112 = 16#2B68 CANopen index: 2051/D INTERBUS index: 5FB3/EA DeviceNet path: 98/01/71		
	Type:	UINT	
	Read/write:	R/W	
	Unit:	0.1 s	
	Factory setting:	5	
	Adjustment range:	0 to 20	
	Anti-repeat delay between 2 consecutive jog operations.		

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 3 and **tCC** = 0: Factory setting = **[LI3]** (LI3)

If **CFG** = 3 and **tCC** = 1: Factory setting = **[LI4]** (LI4)

Otherwise, factory setting = 0.

[1.7 APPLICATION FUNCT.] (FUn-)

[PRESET SPEEDS] (PSS-)

Code	Description		
PS2	Terminal display: [2 preset speeds]		
	Logic address: 11401 = 16#2C89	Type:	WORD (listing)
	CANopen index: 2054/2	Read/write:	R/WS
	INTERBUS index: 5FBD/06	Factory setting:	According to CFG and tCC ⁽¹⁾
	DeviceNet path: 9A/01/02		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
PS4	Terminal display: [4 preset speeds]		
	Logic address: 11402 = 16#2C8A	Type:	WORD (listing)
	CANopen index: 2054/3	Read/write:	R/WS
	INTERBUS index: 5FBD/07	Factory setting:	According to CFG and tCC ⁽²⁾
	DeviceNet path: 9D/01/07		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 . To obtain 4 speeds you must also configure 2 speeds.		

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 1 and **tCC** = 0: Factory setting = **[LI3]** (LI3)

If **CFG** = 1 and **tCC** = 1: Factory setting = **[LI4]** (LI4)

Otherwise, factory setting = 0.

⁽²⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 1 and **tCC** = 0: Factory setting = **[LI4]** (LI4)

If **CFG** = 1 and **tCC** = 1: Factory setting = **[LI5]** (LI5)

Otherwise, factory setting = 0.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
PS8	Terminal display: [8 preset speeds]		
	Logic address: 11403 = 16#2C8B	Type:	WORD (listing)
	CANopen index: 2054/4	Read/write:	R/WS
	INTERBUS index: 5FBD/08	Factory setting:	According to CFG and tCC ⁽¹⁾
	DeviceNet path: 9A/01/04		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 . To obtain 8 speeds you must also configure 2 and 4 speeds.		
PS16	Terminal display: [16 preset speeds]		
	Logic address: 11404 = 16#2C8C	Type:	WORD (listing)
	CANopen index: 2054/5	Read/write:	R/WS
	INTERBUS index: 5FBD/09	Factory setting:	0
	DeviceNet path: 9A/01/05		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 . To obtain 16 speeds you must also configure 2, 4 and 8 speeds.		
SP2	Terminal display: [Preset speed 2]		
	Logic address: 11410 = 16#2C92	Type:	UINT
	CANopen index: 2054/B	Read/write:	R/W
	INTERBUS index: 5FB3/F0	Unit:	0.1 Hz
	DeviceNet path: 9A/01/0B	Factory setting:	100
		Adjustment range:	0 to 5000 or 10,000 according to drive rating

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 1 and **tCC** = 0: Factory setting = **[LI5] (LI5)**

If **CFG** = 1 and **tCC** = 1: Factory setting = **[LI6] (LI6)**

Otherwise, factory setting = 0.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
SP3	Terminal display: [Preset speed 3]		
	Logic address: 11411 = 16#2C93	Type: uint	
	CANopen index: 2054/C	Read/write:	R/W
	INTERBUS index: 5FB3/F1	Unit:	0.1 Hz
	DeviceNet path: 9A/01/0C	Factory setting:	150
		Adjustment range:	0 to 5000 or 10,000 according to drive rating
SP4	Terminal display: [Preset speed 4]		
	Logic address: 11412 = 16#2C94	Type: uint	
	CANopen index: 2054/D	Read/write:	R/W
	INTERBUS index: 5FB3/F2	Unit:	0.1 Hz
	DeviceNet path: 9A/01/0D	Factory setting:	200
		Adjustment range:	0 to 5000 or 10,000 according to drive rating
SP5	Terminal display: [Preset speed 5]		
	Logic address: 11413 = 16#2C95	Type: uint	
	CANopen index: 2054/E	Read/write:	R/W
	INTERBUS index: 5FB3/F3	Unit:	0.1 Hz
	DeviceNet path: 9A/01/0E	Factory setting:	250
		Adjustment range:	0 to 5000 or 10,000 according to drive rating
SP6	Terminal display: [Preset speed 6]		
	Logic address: 11414 = 16#2C96	Type: uint	
	CANopen index: 2054/F	Read/write:	R/W
	INTERBUS index: 5FB3/F4	Unit:	0.1 Hz
	DeviceNet path: 9A/01/0F	Factory setting:	300
		Adjustment range:	0 to 5000 or 10,000 according to drive rating
SP7	Terminal display: [Preset speed 7]		
	Logic address: 11415 = 16#2C97	Type: uint	
	CANopen index: 2054/10	Read/write:	R/W
	INTERBUS index: 5FB3/F5	Unit:	0.1 Hz
	DeviceNet path: 9A/01/10	Factory setting:	350
		Adjustment range:	0 to 5000 or 10,000 according to drive rating

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
SP8	Terminal display: [Preset speed 8]		
	Logic address:	11416 = 16#2C98	Type: UINT
	CANopen index:	2054/11	Read/write: R/W
	INTERBUS index:	5FB3/F6	Unit: 0.1 Hz
	DeviceNet path:	9A/01/11	Factory setting: 400
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP9	Terminal display: [Preset speed 9]		
	Logic address:	11417 = 16#2C99	Type: UINT
	CANopen index:	2054/12	Read/write: R/W
	INTERBUS index:	5FBF/5F	Unit: 0.1 Hz
	DeviceNet path:	9A/01/12	Factory setting: 450
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP10	Terminal display: [Preset speed 10]		
	Logic address:	11418 = 16#2C9A	Type: UINT
	CANopen index:	2054/13	Read/write: R/W
	INTERBUS index:	5FBF/60	Unit: 0.1 Hz
	DeviceNet path:	9A/01/13	Factory setting: 500
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP11	Terminal display: [Preset speed 11]		
	Logic address:	11419 = 16#2C9B	Type: UINT
	CANopen index:	2054/14	Read/write: R/W
	INTERBUS index:	5FBF/61	Unit: 0.1 Hz
	DeviceNet path:	9A/01/14	Factory setting: 550
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP12	Terminal display: [Preset speed 12]		
	Logic address:	11420 = 16#2C9C	Type: UINT
	CANopen index:	2054/15	Read/write: R/W
	INTERBUS index:	5FBF/62	Unit: 0.1 Hz
	DeviceNet path:	9A/01/15	Factory setting: 600
			Adjustment range: 0 to 5000 or 10,000 according to drive rating

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
SP13	Terminal display: [Preset speed 13]		
	Logic address:	11421 = 16#2C9D	Type: UINT
	CANopen index:	2054/16	Read/write: R/W
	INTERBUS index:	9F/01/63	Unit: 0.1 Hz
	DeviceNet path:	9A/01/16	Factory setting: 700
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP14	Terminal display: [Preset speed 14]		
	Logic address:	11422 = 16#2C9E	Type: UINT
	CANopen index:	2054/17	Read/write: R/W
	INTERBUS index:	5FBF/64	Unit: 0.1 Hz
	DeviceNet path:	9A/01/17	Factory setting: 800
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP15	Terminal display: [Preset speed 15]		
	Logic address:	11423 = 16#2C9F	Type: UINT
	CANopen index:	2054/18	Read/write: R/W
	INTERBUS index:	5FBF/65	Unit: 0.1 Hz
	DeviceNet path:	9A/01/18	Factory setting: 900
			Adjustment range: 0 to 5000 or 10,000 according to drive rating
SP16	Terminal display: [Preset speed 16]		
	Logic address:	11424 = 16#2CA0	Type: UINT
	CANopen index:	2054/19	Read/write: R/W
	INTERBUS index:	5FBF/66	Unit: 0.1 Hz
	DeviceNet path:	9A/01/19	Factory setting: 1000
			Adjustment range: 0 to 5000 or 10,000 according to drive rating

[1.7 APPLICATION FUNCT.] (FUn-)

[+/- SPEED] (UPd-)

Code	Description		
USP	Terminal display: [+ speed assignment] Logic address: 11501 = 16#2CED CANopen index: 2055/2 INTERBUS index: 5FBD/0A DeviceNet path: 9A/01/66		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
	See: "Assignment conditions for logic inputs and control bits", page 192 . Function active if the assigned input or bit is at 1.		
dSP	Terminal display: [-Speed assignment] Logic address: 11502 = 16#2CEE CANopen index: 2055/3 INTERBUS index: 5FBD/0B DeviceNet path: 9A/01/67		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
	See: "Assignment conditions for logic inputs and control bits", page 192 . Function active if the assigned input or bit is at 1.		
Str	Terminal display: [Reference saved] Logic address: 11503 = 16#2CEF CANopen index: 2055/4 INTERBUS index: 5FBD/0C DeviceNet path: 9A/01/68		
	This parameter can be used to save the reference. Therefore, the next time the drive starts up, the speed reference is the last reference saved. 0 = [No] (nO) : No save (the next time the drive starts up, the speed reference is [Low speed] (LSP) , see "LSP", page 145 1 = [RAM] (rAM) : Save to RAM 2 = [EEprom] (EEP) : Save to EEPROM		

[1.7 APPLICATION FUNCT.] (FUn-)

[+/-SPEED AROUND REF.] (SrE-)

Code	Description		
USI	Terminal display: [+ speed assignment] Logic address: 11520 = 16#2D00 CANopen index: 2055/15 INTERBUS index: 5FBD/0E DeviceNet path: 9D/01/0E		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
dSI	Terminal display: [-Speed assignment] Logic address: 11521 = 16#2D01 CANopen index: 2055/16 INTERBUS index: 5FBD/0F DeviceNet path: 9A/01/7A		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
SrP	Terminal display: [+/-Speed limitation] Logic address: 11505 = 16#2CF1 CANopen index: 2055/6 INTERBUS index: 5FB3/F7 DeviceNet path: 9A/01/6A		
		Type:	UINT
		Read/write:	R/W
		Unit:	1%
		Factory setting:	10
		Adjustment range:	0 to 50
	This parameter limits the variation range with +/- speed as a % of the reference. The ramps used in this function are [Acceleration 2] (AC2) and [Deceleration 2] (dE2) .		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
AC2	Terminal display:	[Acceleration 2]		
	Logic address:	9012 = 16#2334	Type:	UINT
	CANopen index:	203C/D	Read/write:	R/WS
	INTERBUS index:	5FBF/24	Unit:	According to Inr (1)
	DeviceNet path:	8E/01/0D	Factory setting:	50
			Adjustment range:	According to Inr (1)
Time to accelerate from 0 to the [Rated motor freq.] (FrS) .				
dE2	Terminal display:	[Deceleration 2]		
	Logic address:	9013 = 16#2335	Type:	UINT
	CANopen index:	203C/E	Read/write:	R/WS
	INTERBUS index:	5FBF/25	Unit:	According to Inr (1)
	DeviceNet path:	8E/01/0E	Factory setting:	50
			Adjustment range:	According to Inr (1)
Time to decelerate from the [Rated motor freq.] (FrS) to 0.				

(1) According to **Inr**, see "Inr", page [201](#).

If **Inr** = 0: Unit = 0.01 s, adjustment range = 1 to 9999

If **Inr** = 1: Unit = 0.1 s, adjustment range = 1 to 9999

If **Inr** = 2: Unit = 1 s, adjustment range = 1 to 6000

[1.7 APPLICATION FUNCT.] (FUn-)

[MEMO REFERENCE] (SPM-)

Code	Description		
SPM	Terminal display: [Ref. memo ass.]		
	Logic address: 8491 = 16#212B	Type:	WORD (listing)
	CANopen index: 2036/5C	Read/write:	R/WS
	INTERBUS index: 5FBC/AD	Factory setting:	0
	DeviceNet path: 8B/01/5C		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14): Assignment to a logic input. Function active if the assigned input is at 1.		

[1.7 APPLICATION FUNCT.] (FUn-)

[FLUXING BY LI] (FLI-)

Code	Description		
FLU	Terminal display: [Motor fluxing] Logic address: 13902 = 16#364E CANopen index: 206D/3 INTERBUS index: 5FB4/05 DeviceNet path: A6/01/67		
	0 = [Not cont.] (FnC) : Non-continuous mode 1 = [Continuous] (FCt) : Continuous mode 2 = [No] (FnO) : Function inactive		
FLI	Terminal display: [Fluxing assignment] Logic address: 13901 = 16#364D CANopen index: 206D/2 INTERBUS index: 5FBD/79 DeviceNet path: A6/01/66		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type: Read/write: Factory setting:	WORD (listing) R/W 2 WORD (listing) R/WS 0
	See: "Assignment conditions for logic inputs and control bits", page 192 .		

[1.7 APPLICATION FUNCT.] (FUn-)

[LIMIT SWITCHES] (LSt-)

Code	Description		
LAF	Terminal display: [Stop FW limit sw.] Logic address: 11601 = 16#2D51 CANopen index: 2056/2 INTERBUS index: 5FBD/10 DeviceNet path: 9B/01/02		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
LAr	Terminal display: [Stop RV limit sw.] Logic address: 11602 = 16#2D52 CANopen index: 2056/3 INTERBUS index: 5FBD/11 DeviceNet path: 9B/01/03		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
LAS	Terminal display: [Stop type] Logic address: 11603 = 16#2D53 CANopen index: 2056/4 INTERBUS index: 5FBD/12 DeviceNet path: 9B/01/04		
	0 = [Ramp stop] (rMP) 1 = [Fast stop] (FSt) 2 = [Freewheel] (NSt)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	2

[1.7 APPLICATION FUNCT.] (FUn-)

[BRAKE LOGIC CONTROL] (bLC-)

Code	Description		
bLC	Terminal display: [Brake assignment] Logic address: 10001 = 16#2711 CANopen index: 2046/2 INTERBUS index: 5FBC/F1 DeviceNet path: 93/01/02		
	0 = [No] (nO): Function not assigned 2 = [R2] (r2): Relay (selection extended to R3 or R4 if one or two I/O cards have been inserted) 3 = [R3] (r3) 4 = [R4] (r4) 17 = [LO1] (LO1): Logic output (if one or two I/O cards have been inserted, LO1 to LO2 or LO4 can be selected) 18 = [LO2] (LO2) 19 = [LO3] (LO3) 20 = [LO4] (LO4)		
bSt	Terminal display: [Movement type] Logic address: 10008 = 16#2718 CANopen index: 2046/9 INTERBUS index: 5FBF/3B DeviceNet path: 93/01/09		
	0 = [Traveling] (HOr): Resistive-load movement 1 = [Hoisting] (UEr): Driving-load movement If [Weight sensor ass.] (PES) page 226 is not [No] (nO), [Movement type] (bSt) is forced to 1.		
bCI	Terminal display: [Brake contact] Logic address: 10009 = 16#2719 CANopen index: 2046/A INTERBUS index: 5FBC/F2 DeviceNet path: 93/01/0A		
	If the brake has a monitoring contact (closed for released brake). 0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192.		
bIP	Terminal display: [Brake impulse] Logic address: 10007 = 16#2717 CANopen index: 2046/8 INTERBUS index: 5FB3/E0 DeviceNet path: 93/01/08		
	0 = [No] (nO): The motor torque is given in the required operating direction, at current lbr. 1 = [Yes] (YES): The motor torque is always given in the Forward direction, at current lbr. 2 = [2 IBr] (2lbr): The torque is in the required direction, at current lbr for Forward and lrd for Reverse. If the [Movement type] (bSt) is [Traveling] (HOr), [Brake impulse] (bIP) is forced to 0.		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
Ibr	Terminal display: [Brake release I FW]			
	Logic address: 10006 = 16#2716	Type:	UINT	
	CANopen index: 2046/7	Read/write:	R/W	
	INTERBUS index: 5FB3/DF	Unit:	0.1 A	
	DeviceNet path: 93/01/07	Factory setting:	0	
			Adjustment range: 0 to 1.36 In ⁽¹⁾	
Brake release current threshold for ascending or forward movement				
Ird	Terminal display: [Brake release I Rev]			
	Logic address: 10011 = 16#271B	Type:	UINT	
	CANopen index: 2046/C	Read/write:	R/W	
	INTERBUS index: 5FBF/3D	Unit:	0.1 A	
	DeviceNet path: 93/01/0C	Factory setting:	0	
			Adjustment range: 0 to 1.36 In ⁽¹⁾	
Brake release current threshold for descending or reverse movement				
brt	Terminal display: [Brake Release time]			
	Logic address: 10004 = 16#2714	Type:	UINT	
	CANopen index: 2046/5	Read/write:	R/W	
	INTERBUS index: 5FB3/DD	Unit:	0.01 s	
	DeviceNet path: 93/01/05	Factory setting:	0	
			Adjustment range: 0 to 500	
Brake release time delay				
blr	Terminal display: [Brake release freq]			
	Logic address: 10012 = 16#271C	Type:	INT	
	CANopen index: 2046/D	Read/write:	R/W	
	INTERBUS index: 5FB3/E1	Unit:	0.1 Hz	
	DeviceNet path: 93/01/0D	Factory setting:	-1	
			Adjustment range: -1 to 100	
Brake release frequency threshold -1 = [Auto] (AUT0) : The drive adopts a value equal to the rated slip of the motor.				
bEn	Terminal display: [Brake engage freq]			
	Logic address: 10003 = 16#2713	Type:	INT	
	CANopen index: 2046/4	Read/write:	R/W	
	INTERBUS index: 5FBF/36	Unit:	0.1 Hz	
	DeviceNet path: 93/01/04	Factory setting:	-1	
			Adjustment range: -1 to 100	
Brake engage frequency threshold -1 = [Auto] (AUT0) : The drive adopts a value equal to the rated slip of the motor.				

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
tbE	Terminal display: [Brake engage delay] Logic address: 10010 = 16#271A Type: UINT CANopen index: 2046/B Read/write: R/W INTERBUS index: 5FBF/3C Unit: 0.01 s DeviceNet path: 93/01/0B Factory setting: 0 Adjustment range: 0 to 500		
	Time delay before request to engage brake		
bEt	Terminal display: [Brake engage time] Logic address: 10005 = 16#2715 Type: UINT CANopen index: 2046/6 Read/write: R/W INTERBUS index: 5FB3/DE Unit: 0.01 s DeviceNet path: 93/01/06 Factory setting: 0 Adjustment range: 0 to 500		
	Brake engage time		
SdC1	Terminal display: [Auto DC inj. level 1] Logic address: 10403 = 16#28A3 Type: UINT CANopen index: 204A/4 Read/write: R/W INTERBUS index: 5FBF/45 Unit: 0.1 A DeviceNet path: 95/01/04 Factory setting: 0.7 In ⁽¹⁾ Adjustment range: 0 to 1.2 In ⁽¹⁾		
	Level of standstill DC injection current.		
bEd	Terminal display: [Engage at reversal] Logic address: 10020 = 16#2724 Type: WORD (listing) CANopen index: 2046/15 Read/write: R/W INTERBUS index: 5FBF/41 Factory setting: 0		
	0 = [No] (nO): The brake does not engage. 1 = [Yes] (YES): The brake engages.		
JdC	Terminal display: [Jump at reversal] Logic address: 10013 = 16#271D Type: INT CANopen index: 2046/E Read/write: R/W INTERBUS index: 5FBF/3F Unit: 0.1 Hz DeviceNet path: 93/01/0E Factory setting: -1 Adjustment range: -1 to 100		
	-1 = [Auto] (AUtO): The drive adopts a value equal to the rated slip of the motor.		

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
ttr	Terminal display: [Time to restart]		
	Logic address: 10022 = 16#2726	Type: UINT	
	CANopen index: 2046/17	Read/write: R/W	
	INTERBUS index: 5FBF/42	Unit: 0.01 s	
	DeviceNet path: 93/01/17	Factory setting: 0	Adjustment range: 0 to 500
Time between the end of a brake engage sequence and the start of a brake release sequence			
brH0 ⁽¹⁾	Terminal display: [BRH b0]		
	Logic address: 10050 = 16# 2742 (bit 0)	Type: (1)	
	CANopen index: 2046/33 (bit 0)	Read/write: R/WS	
	INTERBUS index: 5FBC/F3 (bit 0)	Factory setting: 0	
	DeviceNet path: 93/01/33 (bit 0)		
0 = [0] (0) : The engage/release sequence is executed in full. 1 = [1] (1) : The brake is released immediately.			
brH1 ⁽¹⁾	Terminal display: [BRH b1]		
	Logic address: 10050 = 16# 2742 (bit 1)	Type: (1)	
	CANopen index: 2046/33 (bit 1)	Read/write: R/WS	
	INTERBUS index: 5FBC/F3 (bit 1)	Factory setting: 0	
	DeviceNet path: 93/01/33 (bit 1)		
0 = [0] (0) : The brake contact in steady state fault is active (fault if the contact is open during operation). 1 = [1] (1) : The brake contact in steady state fault is inactive.			
brH2 ⁽¹⁾	Terminal display: [BRH b2]		
	Logic address: 10050 = 16# 2742 (bit 2)	Type: (1)	
	CANopen index: 2046/33 (bit 2)	Read/write: R/WS	
	INTERBUS index: 5FBC/F3 (bit 2)	Factory setting: 0	
	DeviceNet path: 93/01/33 (bit 2)		
0 = [0] (0) : The brake contact is not taken into account. 1 = [1] (1) : The brake contact is taken into account.			
brH3 ⁽¹⁾	Terminal display: [BRH b3]		
	Logic address: 10050 = 16# 2742 (bit 3)	Type: (1)	
	CANopen index: 2046/33 (bit 3)	Read/write: R/WS	
	INTERBUS index: 5FBC/F3 (bit 3)	Factory setting: 0	
	DeviceNet path: 93/01/33 (bit 3)		
0 = [0] (0) : During the brake engage sequence, the brake contact must be open before the end of [Brake engage time] (bEt) , otherwise the drive will lock with a brF brake contact fault. 1 = [1] (1) : During the brake engage sequence, the brake contact must be open before the end of [Brake engage time] (bEt) , otherwise a bCA brake contact alarm will be triggered and zero speed maintained.			

⁽¹⁾ Parameters **[BRH_b0]** (**brH0**) to **[BRH_b4]** (**brH4**) are the first 5 bits of the **[BRH]** (**brH**) word. They must be read or written together on this word.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
brH4 ⁽¹⁾	Terminal display: [BRH b4]		
	Logic address: 10050 = 16# 2742 (bit 4)	Type:	⁽¹⁾
	CANopen index: 2046/33 (bit 4)	Read/write:	R/WS
	INTERBUS index: 5FBC/F3 (bit 4)	Factory setting:	0
	DeviceNet path: 93/01/33 (bit 4)		
0 = [0] (0) : No action in the event of a movement for which no command has been given. 1 = [1] (1) : If a movement occurs for which no command has been given, the drive switches to zero speed regulation, with no brake release command, and a bSA alarm is triggered.			
brr	Terminal display: [Current ramp time]		
	Logic address: 10015 = 16#271F	Type:	UINT
	CANopen index: 2046/10	Read/write:	R/W
	INTERBUS index: 5FBF/40	Unit:	0.01 s
	DeviceNet path: 93/01/10	Factory setting:	0
Adjustment range: 0 to 500			
Torque current ramp time for a current variation equal to [Brake release I FW] (lbr).			

⁽¹⁾ Parameters **[BRH_b0]** (brH0) to **[BRH_b4]** (brH4) are the first 5 bits of the **[BRH]** (brH) word. They must be read or written together on this word.

[1.7 APPLICATION FUNCT.] (FUn-)

[EXTERNAL WEIGHT MEAS.] (ELM-)

Code	Description		
PES	Terminal display: [Weight sensor ass.]		
	Logic address: 10070 = 16#2756	Type:	WORD (listing)
	CANopen index: 2046/47	Read/write:	R/W/S
	INTERBUS index: 5FBC/F4	Factory setting:	0
	DeviceNet path: 93/01/47		
	0 = [No] (nO): Function inactive 1 = [AI1] (AI1): Analog input 2 = [AI2] (AI2): Analog input 3 = [AI3] (AI3): Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4): Analog input, if VW3A3202 extension card has been inserted 181 = [RP] (PI): Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG): Encoder input, if encoder card has been inserted		
LP1	Terminal display: [Point 1 X]		
	Logic address: 10071 = 16#2757	Type:	UINT
	CANopen index: 2046/48	Read/write:	R/W/S
	INTERBUS index: 5FBC/F5	Unit:	0.01%
	DeviceNet path: 93/01/48	Factory setting:	0
	Adjustment range: 0 to LP2 - 1		
	0 to 99.99% of signal on assigned input.		
CP1	Terminal display: [Point 1 Y]		
	Logic address: 10072 = 16#2758	Type:	INT
	CANopen index: 2046/49	Read/write:	R/W/S
	INTERBUS index: 5FBC/F6	Unit:	0.1 A
	DeviceNet path: 93/01/49	Factory setting:	- In ⁽¹⁾
	Adjustment range: - 1.36 In to 1.36 In ⁽¹⁾		
	Current corresponding to load [Point 1 X] (LP1)		
LP2	Terminal display: [Point 2 X]		
	Logic address: 10073 = 16#2759	Type:	UINT
	CANopen index: 2046/4A	Read/write:	R/W/S
	INTERBUS index: 5FBC/F7	Unit:	0.01%
	DeviceNet path: 93/01/4A	Factory setting:	5000
	Adjustment range: LP1 + 1 to 10,000		
	0.01 to 100% of signal on assigned input.		

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
CP2	Terminal display:	[Point 2Y]	
	Logic address:	10074 = 16#275A	Type: INT
	CANopen index:	2046/4B	Read/write: R/WS
	INTERBUS index:	5FBC/F8	Unit: 0.1 A
	DeviceNet path:	93/01/4B	Factory setting: 0 Adjustment range: - 1.36 In to 1.36 In ⁽¹⁾
Current corresponding to load [Point 2 X] (LP2)			
IbrA	Terminal display:	[Ibr 4-20 mA loss]	
	Logic address:	10075 = 16#275B	Type: UINT
	CANopen index:	2046/4C	Read/write: R/W
	INTERBUS index:	5FBC/F9	Unit: 0.1 A
	DeviceNet path:	93/01/4C	Factory setting: 0 Adjustment range: 0 to 1.36 In ⁽¹⁾
Brake release current in the event of the loss of the weight sensor information.			

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

[1.7 APPLICATION FUNCT.] (FUn-)

[HIGH SPEED HOISTING] (HSH-)

Code	Description		
HSO	Terminal display: [High speed hoisting]		
	Logic address:	12301 = 16#300D	Type: WORD (listing)
	CANopen index:	205D/2	Read/write: R/W
	INTERBUS index:	5FB/29	Factory setting: 0
DeviceNet path: 9E/01/66			
0 = [No] (nO): Function inactive 1 = [Speed ref] (SSO): "Speed reference" mode 2 = [I Limit] (CSO): "Current limitation" mode			
COF	Terminal display: [Motor speed coeff.]		
	Logic address:	12303 = 16#300F	Type: UINT
	CANopen index:	205D/4	Read/write: R/W
	INTERBUS index:	5FB/88	Unit: 1%
	DeviceNet path:	9E/01/68	Factory setting: 100 Adjustment range: 0 to 100
Speed reduction coefficient calculated by the drive for Ascending direction.			
COr	Terminal display: [Gen. speed coeff]		
	Logic address:	12304 = 16#3010	Type: UINT
	CANopen index:	205D/5	Read/write: R/W
	INTERBUS index:	5FB/89	Unit: 1%
	DeviceNet path:	9E/01/69	Factory setting: 50 Adjustment range: 0 to 100
Speed reduction coefficient calculated by the drive for Descending direction.			
tOS	Terminal display: [Load measuring tm.]		
	Logic address:	12307 = 16#3013	Type: UINT
	CANopen index:	205D/8	Read/write: R/W
	INTERBUS index:	5FB/8C	Unit: 0.01 s
	DeviceNet path:	9E/01/6C	Factory setting: 50 Adjustment range: 10 to 6500
Duration of speed step for measurement.			
OSP	Terminal display: [Measurement spd]		
	Logic address:	12305 = 16#3011	Type: UINT
	CANopen index:	205D/6	Read/write: R/W
	INTERBUS index:	5FB/8A	Unit: 0.1 Hz
	DeviceNet path:	9E/01/6A	Factory setting: 400 Adjustment range: 0 to FrS
Speed stabilized for measurement.			

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
CLO	Terminal display:	[High speed I Limit]		
	Logic address:	12302 = 16#300E	Type:	UINT
	CANopen index:	205D/3	Read/write:	R/W
	INTERBUS index:	5FBF/87	Unit:	0.1 A
	DeviceNet path:	9E/01/67	Factory setting:	In ⁽¹⁾
				Adjustment range: According to SFr ⁽²⁾
Current limitation at high speed.				
SCL	Terminal display:	[I Limit. frequency]		
	Logic address:	12306 = 16#3012	Type:	UINT
	CANopen index:	205D/7	Read/write:	R/W
	INTERBUS index:	5FBF/8B	Unit:	0.1 Hz
	DeviceNet path:	9E/01/6B	Factory setting:	400
				Adjustment range: 0 to 5000 or 10,000 according to drive rating
Frequency threshold, above which the high-speed limitation current is active.				

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

⁽²⁾ According to **SFr**, see “SFr”, page [162](#).
 If **SFr** ≥ 20: Adjustment range = 0 to 1.65 In ⁽¹⁾
 If **SFr** < 20: Adjustment range = 0 to 1.36 In ⁽¹⁾

[1.7 APPLICATION FUNCT.] (FUn-)

[PID REGULATOR] (PId-)

Code	Description		
PIF	Terminal display: [PID feedback ass.] Logic address: 11901 = 16#2E7D CANopen index: 2059/2 INTERBUS index: 5FBD/19 DeviceNet path: 9C/01/66		
	Type: WORD (listing) Read/write: R/W Factory setting: According to CFG ⁽¹⁾		
	0 = [No] (nO): Function inactive 1 = [AI1] (AI1): Analog input 2 = [AI2] (AI2): Analog input 3 = [AI3] (AI3): Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4): Analog input, if VW3A3202 extension card has been inserted 181 = [RP] (PI): Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG): Encoder input, if encoder card has been inserted 183 = [Network AI] (AIU1): Feedback via communication bus		
AIC1	Terminal display: [AI net. channel] Logic address: 5282 = 16#14A2 CANopen index: 2016/53 INTERBUS index: 5FBC/70 DeviceNet path: 7B/01/53		
	Type: WORD (listing) Read/write: R/W Factory setting: 0		
	0 = [No] (nO): Not assigned 164 = [Modbus] (Mdb): Integrated Modbus 167 = [CANopen] (CAn): Integrated CANopen 168 = [Com. card] (nEt): Communication card (if inserted) 169 = [Prog. card] (APP): Controller Inside card (if inserted)		
PIF1	Terminal display: [Min PID feedback] Logic address: 11904 = 16#2E80 CANopen index: 2059/5 INTERBUS index: 5FBF/6A DeviceNet path: 9C/01/69		
	Type: UINT Read/write: R/W Unit: 1 Factory setting: 100 Adjustment range: 0 to PIF2		
	Value for minimum feedback		
PIF2	Terminal display: [Max PID feedback] Logic address: 11905 = 16#2E81 CANopen index: 2059/6 INTERBUS index: 5FBF/6B DeviceNet path: 9C/01/6A		
	Type: UINT Read/write: R/W Unit: 1 Factory setting: 1000 Adjustment range: PIF1 to 32,767		
	Value for maximum feedback		

⁽¹⁾ According to **CFG**, see “CFG”, page [144](#).

If **CFG** = 4: Factory setting = 2

Otherwise, factory setting = 0

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
PIP1	Terminal display: [Min PID reference]			
	Logic address: 11906 = 16#2E82	Type:	UINT	
	CANopen index: 2059/7	Read/write:	R/W	
	INTERBUS index: 5FBF/6C	Unit:	1	
	DeviceNet path: 9C/01/6B	Factory setting:	150	
Adjustment range: PIP1 to PIP2				
Minimum process value				
PIP2	Terminal display: [Max PID reference]			
	Logic address: 11907 = 16#2E83	Type:	UINT	
	CANopen index: 2059/8	Read/write:	R/W	
	INTERBUS index: 5FBF/6D	Unit:	1	
	DeviceNet path: 9C/01/6C	Factory setting:	900	
Adjustment range: PIP1 to PIF2				
Maximum process value				
PII	Terminal display: [Act. internal PID ref.]			
	Logic address: 11908 = 16#2E84	Type:	WORD (listing)	
	CANopen index: 2059/9	Read/write:	R/W	
	INTERBUS index: 5FBD/1A	Factory setting:	0	
	DeviceNet path: 9C/01/6D			
0 = [No] (nO) : The PID regulator reference is set via Fr1 or Fr1b. 1 = [Yes] (YES) : The PID regulator reference is internal via parameter rPI.				
RPI	Terminal display: [Internal PID ref.]			
	Logic address: 11920 = 16#2E90	Type:	UINT	
	CANopen index: 2059/15	Read/write:	R/W	
	INTERBUS index: 5FBF/6E	Unit:	1	
	DeviceNet path: 9C/01/79	Factory setting:	150	
Adjustment range: PIP1 to PIP2				
Internal PID regulator reference				
RPG	Terminal display: [PID prop. gain]			
	Logic address: 11941 = 16#2EA5	Type:	UINT	
	CANopen index: 2059/2A	Read/write:	R/W	
	INTERBUS index: 5FB3/FA	Unit:	0.01	
	DeviceNet path: 9C/01/8E	Factory setting:	100	
Adjustment range: 1 to 10,000				
Proportional gain				

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
rIG	Terminal display: [PID integral gain]			
	Logic address: 11942 = 16#2EA6	Type:	UINT	
	CANopen index: 2059/2B	Read/write:	R/W	
	INTERBUS index: 5FB3/FB	Unit:	0.01	
	DeviceNet path: 9C/01/8F	Factory setting:	100	
Adjustment range: 1 to 10,000				
Integral gain				
rdG	Terminal display: [PID derivative gain]			
	Logic address: 11943 = 16#2EA7	Type:	UINT	
	CANopen index: 2059/2C	Read/write:	R/W	
	INTERBUS index: 5FB3/FC	Unit:	0.01	
	DeviceNet path: 9C/01/90	Factory setting:	0	
Adjustment range: 0 to 10,000				
Derivative gain				
PrP	Terminal display: [PID ramp]			
	Logic address: 11984 = 16#2ED0	Type:	UINT	
	CANopen index: 2059/55	Read/write:	R/W	
	INTERBUS index: 5FBF/7B	Unit:	0.1 s	
	DeviceNet path: 9C/01/B9	Factory setting:	0	
Adjustment range: 0 to 999				
PID acceleration/deceleration ramp				
PIC	Terminal display: [PID correct. reverse]			
	Logic address: 11940 = 16#2EA4	Type:	WORD (listing)	
	CANopen index: 2059/29	Read/write:	R/WS	
	INTERBUS index: 5FB3/F9	Factory setting:	0	
	DeviceNet path: 9C/01/8D			
Reversal of the direction of correction 0 = [No] (nO) 1 = [Yes] (YES)				
POL	Terminal display: [Min PID output]			
	Logic address: 11952 = 16#2EB0	Type:	INT	
	CANopen index: 2059/35	Read/write:	R/W	
	INTERBUS index: 5FBF/76	Unit:	0.1 Hz	
	DeviceNet path: 9C/01/99	Factory setting:	0	
Adjustment range: -5000 to 5000 or -10,000 to 10,000, according to rating				
Minimum value of regulator output in Hz				

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description				
POH	Terminal display: [Max PID output] Logic address: 11953 = 16#2EB1 CANopen index: 2059/36 INTERBUS index: 5FBF/77 DeviceNet path: 9C/01/9A				
	Type:	INT			
	Read/write:	R/W			
	Unit:	0.1 Hz			
	Factory setting:	600			
	Adjustment range:	0 to 5000 or 0 to 10,000, according to rating			
	Maximum value of regulator output in Hz				
PAL	Terminal display: [Min fbk alarm] Logic address: 11961 = 16#2EB9 CANopen index: 2059/3E INTERBUS index: 5FB3/FE DeviceNet path: 9C/01/A2				
	Type:	UINT			
	Read/write:	R/W			
	Unit:	1			
	Factory setting:	100			
	Adjustment range:	PIF1 to PIF2			
	Minimum monitoring threshold for regulator feedback				
PAH	Terminal display: [Max fbk alarm] Logic address: 11962 = 16#2EBA CANopen index: 2059/3F INTERBUS index: 5FB3/FF DeviceNet path: 9C/01/A3				
	Type:	UINT			
	Read/write:	R/W			
	Unit:	1			
	Factory setting:	1000			
	Adjustment range:	PIF1 to PIF2			
	Maximum monitoring threshold for regulator feedback				
PER	Terminal display: [PID error Alarm] Logic address: 11963 = 16#2EBB CANopen index: 2059/40 INTERBUS index: 5FB4/01 DeviceNet path: 9C/01/A4				
	Type:	UINT			
	Read/write:	R/W			
	Unit:	1			
	Factory setting:	100			
	Adjustment range:	0 to 65,535			
	Regulator error monitoring threshold				

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
PIS	Terminal display: [PID integral reset]		
	Logic address: 11944 = 16#2EA8	Type:	WORD (listing)
	CANopen index: 2059/2D	Read/write:	R/WS
	INTERBUS index: 5FBD/1E	Factory setting:	According to CFG and tCC ⁽¹⁾
	DeviceNet path: 9C/01/91		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	See: "Assignment conditions for logic inputs and control bits", page 192 . • If the assigned input or bit is at 0, the function is inactive (the PID integral is enabled). • If the assigned input or bit is at 1, the function is active (the PID integral is disabled).	
FPI	Terminal display: [Speed ref. assign.]		
	Logic address: 11950 = 16#2EAE	Type:	WORD (listing)
	CANopen index: 2059/33	Read/write:	R/WS
	INTERBUS index: 5FBD/1F	Factory setting:	0
	DeviceNet path: 9C/01/97		
	0 = [No] (nO) : Not assigned (function inactive) 1 = [AI1] (AI1) : Analog input 2 = [AI2] (AI2) : Analog input 3 = [AI3] (AI3) : Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4) : Analog input, if VW3A3202 extension card has been inserted 163 = [HMI] (LCC) : Graphic display terminal 164 = [Modbus] (Mdb) : Integrated Modbus 167 = [CANopen] (CAn) : Integrated CANopen 169 = [Com. card] (nEt) : Communication card (if inserted) 170 = [Prog. card] (APP) : Controller Inside card (if inserted) 181 = [RP] (PI) : Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG) : Encoder input, if encoder card has been inserted		
PSr	Terminal display: [Speed input %]		
	Logic address: 11951 = 16#2EAF	Type:	UINT
	CANopen index: 2059/34	Read/write:	R/W
	INTERBUS index: 5FB3/FD	Unit:	1%
	DeviceNet path: 9C/01/98	Factory setting:	100
		Adjustment range:	1 to 100
	Multiplying coefficient for predictive speed input.		

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 4 and **tCC** = 0: Factory setting = **[LI3] (LI3)**

If **CFG** = 4 and **tCC** = 1: Factory setting = **[LI4] (LI4)**

Otherwise, factory setting = 0.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
PAU	Terminal display: [Auto/Manual assign.]		
	Logic address: 11970 = 16#2EC2	Type:	WORD (listing)
	CANopen index: 2059/47	Read/write:	R/WS
	INTERBUS index: 5FBD/22	Factory setting:	0
	DeviceNet path: 9C/01/AB		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	See: "Assignment conditions for logic inputs and control bits", page 192 . <ul style="list-style-type: none">• If the assigned input or bit is at 0, the PID is active.• If the assigned input or bit is at 1, manual operation is active.	
PIM	Terminal display: [Manual reference]		
	Logic address: 11954 = 16#2EB2	Type:	WORD (listing)
	CANopen index: 2059/37	Read/write:	R/WS
	INTERBUS index: 5FBD/20	Factory setting:	0
	DeviceNet path: 9C/01/9B		
	Manual speed input 0 = [No] (nO) : Not assigned (function inactive) 1 = [AI1] (AI1) : Analog input 2 = [AI2] (AI2) : Analog input 3 = [AI3] (AI3) : Analog input, if VW3A3202 extension card has been inserted 4 = [AI4] (AI4) : Analog input, if VW3A3202 extension card has been inserted 181 = [RP] (PI) : Frequency input, if VW3A3202 extension card has been inserted 182 = [Encoder] (PG) : Encoder input, if encoder card has been inserted		
tLS	Terminal display: [Low speed time out]		
	Logic address: 11701 = 16#2DB5	Type:	UINT
	CANopen index: 2057/2	Read/write:	R/W
	INTERBUS index: 5FB3/F8	Unit:	0.1 s
	DeviceNet path: 9B/01/66	Factory setting:	0
		Adjustment range:	0 to 9999
	Maximum operating time at [Low speed] (LSP) , see "LSP", page 145 .		
rSL	Terminal display: [PID wake up thresh.]		
	Logic address: 11960 = 16#2DB5	Type:	UINT
	CANopen index: 2059/3D	Read/write:	R/WS
	INTERBUS index: 5FBD/21	Unit:	0.1
	DeviceNet path: 9C/01/A1	Factory setting:	0
		Adjustment range:	0 to 1000
	Restart error threshold		

[1.7 APPLICATION FUNCT.] (FUn-)

[PID PRESET REFERENCES] (PRI-)

Code	Description		
Pr2	Terminal display: [2 preset PID ref.] Logic address: 11909 = 16#2E85 CANopen index: 2059/A INTERBUS index: 5FBD/1B DeviceNet path: 9C/01/6E		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type: Read/write: Factory setting:	WORD (listing) R/WS According to CFG and tCC ⁽¹⁾
	See: "Assignment conditions for logic inputs and control bits", page 192 . <ul style="list-style-type: none"> • If the assigned input or bit is at 0, the function is inactive. • If the assigned input or bit is at 1, the function is active. 		
Pr4	Terminal display: [4 preset PID ref.] Logic address: 11910 = 16#2E86 CANopen index: 2059/B INTERBUS index: 5FBD/1C DeviceNet path: 9C/01/6F		
	Make sure that [2 preset PID ref.] (Pr2) has been assigned before assigning this function. 0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type: Read/write: Factory setting:	WORD (listing) R/WS According to CFG and tCC ⁽²⁾
	See: "Assignment conditions for logic inputs and control bits", page 192 . <ul style="list-style-type: none"> • If the assigned input or bit is at 0, the function is inactive. • If the assigned input or bit is at 1, the function is active. 		

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 4 and **tCC** = 0: Factory setting = **[LI4]** (LI4)

If **CFG** = 4 and **tCC** = 1: Factory setting = **[LI5]** (LI5)

Otherwise, factory setting = 0.

⁽²⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 4 and **tCC** = 0: Factory setting = **[LI5]** (LI5)

If **CFG** = 4 and **tCC** = 1: Factory setting = **[LI6]** (LI6)

Otherwise, factory setting = 0.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
rP2	Terminal display: [Preset ref. PID 2]		
	Logic address: 11921 = 16#2E91	Type: uint	
	CANopen index: 2059/16	Read/write:	R/W
	INTERBUS index: 5FBF/6F	Unit:	1
	DeviceNet path: 9C/01/7A	Factory setting:	300
		Adjustment range:	PIP1 to PIP2
rP3	Terminal display: [Preset ref. PID 3]		
	Logic address: 11922 = 16#2E92	Type: uint	
	CANopen index: 2059/17	Read/write:	R/W
	INTERBUS index: 5FBF/70	Unit:	1
	DeviceNet path: 9C/01/7B	Factory setting:	600
		Adjustment range:	PIP1 to PIP2
rP4	Terminal display: [Preset ref. PID 4]		
	Logic address: 11923 = 16#2E93	Type: uint	
	CANopen index: 2059/18	Read/write:	R/W
	INTERBUS index: 9F/01/71	Unit:	1
	DeviceNet path: 9C/01/7C	Factory setting:	900
		Adjustment range:	PIP1 to PIP2

[1.7 APPLICATION FUNCT.] (FUn-)

[TORQUE CONTROL] (tOR-)

Code	Description		
tSS	Terminal display: [Trq/spd switching] Logic address: 9220 = 16#2404 CANopen index: 203E/15 INTERBUS index: 5FBC/B9 DeviceNet path: 8F/01/15		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type: Read/write: Factory setting:	WORD (listing) R/WS 0
	See: "Assignment conditions for logic inputs and control bits", page 192 . <ul style="list-style-type: none"> If the assigned input or bit is at 1: Torque control If the assigned input or bit is at 0: Speed regulation 		
tr1	Terminal display: [Torque ref. channel] Logic address: 9221 = 16#2405 CANopen index: 203E/16 INTERBUS index: 5FBC/BA DeviceNet path: 8F/01/16		
	0 = [No] (nO): Not assigned (zero torque reference) 1 = [AI1] (AI1): Analog input 2 = [AI2] (AI2): Analog input 3 = [AI3] (AI3): Analog input, if VW3A3202 I/O card has been inserted 4 = [AI4] (AI4): Analog input, if VW3A3202 I/O card has been inserted 163 = [HMI] (LCC): Graphic display terminal 164 = [Modbus] (Mdb): Integrated Modbus 167 = [CANopen] (CAN): Integrated CANopen 169 = [Com. card] (nEt): Communication card (if inserted) 170 = [Prog. card] (APP): Controller Inside card (if inserted) 181 = [RP] (PI): Frequency input, if VW3A3202 I/O card has been inserted 182 = [Encoder] (PG): Encoder input, if encoder card has been inserted		
Int	Parameter name: [Torque unit] Logic address: 9260 = 16#242C CANopen index: 203E/3D INTERBUS index: 5FBF/35 DeviceNet path: 8F/01/3D		
	0 = 0.01 Nm 1 = 0.1 Nm 2 = 1 Nm 3 = 10 Nm This parameter is only used and can only be configured with DeviceNet. It sets the unit for the LTCR and OTRN parameters.		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
tSd	Terminal display: [Torque ref. sign]		
	Logic address: 9222 = 16#2406	Type:	WORD (listing)
	CANopen index: 203E/17	Read/write:	R/WS
	INTERBUS index: 5FBC/BB	Factory setting:	0
	DeviceNet path: 8F/01/17		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
	• If the assigned input or bit is at 0, the torque sign is the same as the reference. • If the assigned input or bit is at 1, the torque sign is the opposite of the reference.		
trt	Terminal display: [Torque ratio]		
	Logic address: 9225 = 16#2409	Type:	UINT
	CANopen index: 203E/1A	Read/write:	R/W
	INTERBUS index: 5FBF/30	Unit:	1%
	DeviceNet path: 8F/01/1A	Factory setting:	100
		Adjustment range:	0 to 1000
	Coefficient applied to [Torque reference] (tr1)		
trP	Terminal display: [Torque ramp time]		
	Logic address: 9226 = 16#240A	Type:	UINT
	CANopen index: 203E/1B	Read/write:	R/W
	INTERBUS index: 5FBF/31	Unit:	0.01 s
	DeviceNet path: 8F/01/1B	Factory setting:	300
		Adjustment range:	0 to 9999
	Torque rise and fall time for a variation of 100% of the reference.		
tSt	Terminal display: [Torque control stop]		
	Logic address: 9227 = 16#240B	Type:	Word (listing)
	CANopen index: 203E/1C	Read/write:	R/WS
	INTERBUS index: 5FBC/BC	Factory setting:	0
	DeviceNet path: 8F/01/1C		
	0 = [Speed] (SPd) : Speed regulation stop 1 = [Freewheel] (nSt) : Freewheel stop 2 = [Spin] (SPn) : Zero torque stop, but maintaining the flux in the motor.		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
SPt	Terminal display: [Spin time]		
	Logic address: 9230 = 16#240E	Type: UINT	
	CANopen index: 203E/1F	Read/write: R/W	
	INTERBUS index: 5FBF/33	Unit: 0.1 s	
	DeviceNet path: 8F/01/1F	Factory setting: 10	Adjustment range: 0 to 36,000
Spin time following stop, in order to remain ready to restart quickly.			
dbP	Terminal display: [Positive deadband]		
	Logic address: 9224 = 16#2408	Type: UINT	
	CANopen index: 203E/19	Read/write: R/W	
	INTERBUS index: 5FBF/2F	Unit: 0.1 Hz	
	DeviceNet path: 8F/01/19	Factory setting: 100	Adjustment range: 0 to 2 tFr
Positive deadband. Value added algebraically to the speed reference.			
dbn	Terminal display: [Negative deadband]		
	Logic address: 9223 = 16#2407	Type: UINT	
	CANopen index: 203E/18	Read/write: R/W	
	INTERBUS index: 5FBF/2E	Unit: 0.1 Hz	
	DeviceNet path: 8F/01/18	Factory setting: 100	Adjustment range: 0 to 2 tFr
Negative deadband. Value subtracted algebraically from the speed reference.			
rtO	Terminal display: [Torque R. time out]		
	Logic address: 9229 = 16#240D	Type: UINT	
	CANopen index: 203E/1E	Read/write: R/W/S	
	INTERBUS index: 5FBF/32	Unit: 0.1 s	
	DeviceNet path: 8F/01/1E	Factory setting: 600	Adjustment range: 0 to 9999
Time following automatic exit of torque control mode in the event of a fault or alarm.			
tOb	Terminal display: [Torq. ctrl fault mgt]		
	Logic address: 9228 = 16#240C	Type: WORD (listing)	
	CANopen index: 203E/1D	Read/write: R/W/S	
	INTERBUS index: 5FBC/BD	Factory setting: 0	
	DeviceNet path: 8F/01/1D		
Response of drive once time [Torque ctrl time out] (rtO) has elapsed. 0 = [Alarm] (ALrM) 1 = [Fault] (FLt) : Fault with freewheel stop			

[1.7 APPLICATION FUNCT.] (FUn-)

[TORQUE LIMITATION] (tOL-)

Code	Description		
tLA	Terminal display: [AI torque limit. activ.] Logic address: 9210 = 16#23FA CANopen index: 203E/B INTERBUS index: 5FBC/B6 DeviceNet path: 8F/01/0B		
	0 = [No] (nO): Not assigned 128 = [Yes] (YES) 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type: Read/write: Factory setting:	WORD (listing) R/W/S According to CFG and tCC ⁽¹⁾
	See: "Assignment conditions for logic inputs and control bits", page 192 . <ul style="list-style-type: none"> • If the assigned input or bit is at 0, the function is inactive. • If the assigned input or bit is at 1, the function is active. 		
tLIM	Terminal display: [Motoring torque lim] Logic address: 9211 = 16#23FB CANopen index: 203E/C INTERBUS index: 5FBF/2C DeviceNet path: 8F/01/0C		
		Type: Read/write: Unit: Factory setting: Adjustment range:	UINT R/W 1% 100 0 to 300
	Torque limitation in motor mode, as a % of the rated torque.		
tLIG	Terminal display: [Gen. torque lim] Logic address: 9212 = 16#23FC CANopen index: 203E/D INTERBUS index: 5FBF/2D DeviceNet path: 8F/01/0D		
		Type: Read/write: Unit: Factory setting: Adjustment range:	UINT R/W 1% 100 0 to 300
	Torque limitation in generator mode, as a % of the rated torque.		

⁽¹⁾ According to CFG, see "CFG", page [144](#) and tCC, see "tCC", page [166](#).

If CFG = 3 and tCC = 0: Factory setting = **[LI5]** (LI5)

If CFG = 3 and tCC = 1: Factory setting = **[LI6]** (LI6)

Otherwise, factory setting = 0.

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
tAA	Terminal display: [Torque ref. assign.]		
	Logic address: 9214 = 16#23FE	Type:	WORD (listing)
	CANopen index: 203E/F	Read/write:	R/WS
	INTERBUS index: 5FBC/B8	Factory setting:	0
	DeviceNet path: 8F/01/0F		
0 = [No] (nO) : Not assigned (zero torque reference) 1 = [AI1] (AI1) : Analog input 2 = [AI2] (AI2) : Analog input 3 = [AI3] (AI3) : Analog input, if VW3A3202 I/O card has been inserted 4 = [AI4] (AI4) : Analog input, if VW3A3202 I/O card has been inserted 181 = [RP] (PI) : Frequency input, if VW3A3202 I/O card has been inserted 182 = [Encoder] (PG) : Encoder input, if encoder card has been inserted			
tLC	Terminal display: [Analog limit. act.]		
	Logic address: 9213 = 16#23FD	Type:	WORD (listing)
	CANopen index: 203E/E	Read/write:	R/WS
	INTERBUS index: 5FBC/B7	Factory setting:	128
	DeviceNet path: 8F/01/0E		
128 = [Yes] (YES) : The limitation depends on the input assigned by [Torque ref. assign.] (tAA) . 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)			
See: "Assignment conditions for logic inputs and control bits", page 192			

[1.7 APPLICATION FUNCT.] (FUn-)

[2nd CURRENT LIMIT.] (CLI-)

Code	Description				
LC2	Terminal display: [Current limit 2] Logic address: 9202 = 16#23F2 CANopen index: 203E/3 INTERBUS index: 5FBC/B5 DeviceNet path: 8F/01/03				
	0 = [No] (nO) : Not assigned	Type:	WORD (listing)		
	129 = [LI1] (LI1)	Read/write:	R/WS		
	:	Factory setting:	0		
	142 = [LI14] (LI14)				
	160 = [CD00] (Cd00)				
	:				
	175 = [CD15] (Cd15)				
	177 = [C101] (C101)				
	:				
	239 = [C415] (C415)				
	See: "Assignment conditions for logic inputs and control bits", page 192 .				
	<ul style="list-style-type: none"> If the assigned input or bit is at 0, the first current limitation is active. If the assigned input or bit is at 1, the second current limitation is active. 				
CL2	Terminal display: [I Limit. 2 value] Logic address: 9203 = 16#23F3 CANopen index: 203E/4 INTERBUS index: 5FBF/2B DeviceNet path: 8F/01/04				
	Type:	UINT			
	Read/write:	R/W			
	Unit:	0.1 A			
	Factory setting:	1.5 ln ⁽¹⁾			
	Adjustment range:	According to SFr ⁽²⁾			
	Second current limitation				
CLI	Terminal display: [Current Limitation] Logic address: 9201 = 16#23F1 CANopen index: 203E/2 INTERBUS index: 5FB3/D2 DeviceNet path: 93/01/D2				
	Type:	UINT			
	Read/write:	R/W			
	Unit:	0.1 A			
	Factory setting:	1.5 ln ⁽¹⁾			
	Adjustment range:	According to SFr ⁽²⁾			
	First current limitation				

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

⁽²⁾ According to **SFr**, see "SFr", page [162](#).

If **SFr** ≥ 20: Adjustment range = 0 to 1.65 ln⁽¹⁾

If **SFr** < 20: Adjustment range = 0 to 1.36 ln⁽¹⁾

[1.7 APPLICATION FUNCT.] (FUn-)

[LINE CONTACTOR COMMAND] (LLC-)

Code	Description		
LLC	Terminal display: [Line contactor ass.] Logic address: 13602 = 16#3522 CANopen index: 206A/3 INTERBUS index: 5FBD/6E DeviceNet path: A5/01/03		
	Logic output or control relay 0 = [No] (nO): Function not assigned (in this case, none of the function parameters can be accessed) 129 = [LO1] (LO1): Logic output (if one or two I/O cards have been inserted, LO1 to LO2 or LO4 can be selected) 130 = [LO2] (LO2) 131 = [LO3] (LO3) 132 = [LO4] (LO4) 146 = [R2] (r2): Relay (selection of R2 extended to R3 or R4 if one or two I/O cards have been inserted) 147 = [R3] (r3) 148 = [R4] (r4)		
LES	Terminal display: [Drive lock] Logic address: 13601 = 16#3521 CANopen index: 206A/2 INTERBUS index: 5FBD/6D DeviceNet path: A5/01/02		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192 . The drive locks when the assigned input or bit changes to 0.		
LCt	Terminal display: [Mains V. time out] Logic address: 13603 = 16#3523 CANopen index: 206A/4 INTERBUS index: 5FBD/6F DeviceNet path: A5/01/04		
	Type: UINT Read/write: R/WS Unit: 1 s Factory setting: 5 Adjustment range: 5 to 999 Monitoring time for closing of line contactor.		

[1.7 APPLICATION FUNCT.] (FUn-)

[OUTPUT CONTACTOR CMD] (OCC-)

Code	Description		
OCC	Terminal display: [Out. contactor ass.] Logic address: 13104 = 16#3330 CANopen index: 2065/5 INTERBUS index: 5FB3/57 DeviceNet path: A2/01/69		
	Logic output or control relay 0 = [No] (nO): Function not assigned (in this case, none of the function parameters can be accessed) 129 = [LO1] (LO1): Logic output (if one or two I/O cards have been inserted, LO1 to LO2 or LO4 can be selected) 130 = [LO2] (LO2) 131 = [LO3] (LO3) 132 = [LO4] (LO4) 146 = [R2] (r2): Relay (selection of R2 extended to R3 or R4 if one or two I/O cards have been inserted) 147 = [R3] (r3) 148 = [R4] (r4)		
rCA	Terminal display: [Output contact. fdbk] Logic address: 13103 = 16#332F CANopen index: 2065/4 INTERBUS index: 5FB3/56 DeviceNet path: A2/01/68		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415) See: "Assignment conditions for logic inputs and control bits", page 192 . The motor starts up when the assigned input or bit changes to 0.		
dbS	Terminal display: [Time to motor run] Logic address: 13101 = 16#332D CANopen index: 2065/2 INTERBUS index: 5FB4/02 DeviceNet path: A2/01/66		
	Time delay for: • Motor control following the sending of a run command • Output contactor fault monitoring, if the feedback is assigned		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description			
dAS	Terminal display: [Time to open cont.]			
Logic address:	13102 = 16#332E	Type:	UINT	
CANopen index:	2065/3	Read/write:	R/W	
INTERBUS index:	5FB4/03	Unit:	0.01 s	
DeviceNet path:	A2/01/67	Factory setting:	10	
		Adjustment range:	0 to 500	
Time delay for output contactor opening command following motor stop.				

[1.7 APPLICATION FUNCT.] (FUn-)

[POSITIONING BY SENSORS] (LPO-)

Code	Description		
SAF	Terminal display: [Stop FW limit sw.] Logic address: 12501 = 16#30D5 CANopen index: 205F/2 INTERBUS index: 5FBD/35 DeviceNet path: 9F/01/66		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
SAr	Terminal display: [Stop RV limit sw.] Logic address: 12502 = 16#30D6 CANopen index: 205F/3 INTERBUS index: 5FBD/36 DeviceNet path: 9F/01/67		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
	See: "Assignment conditions for logic inputs and control bits", page 192 . Forward stop, controlled on a rising edge (change from 0 to 1) of the assigned bit or input.		
	See: "Assignment conditions for logic inputs and control bits", page 192 . Reverse stop, controlled on a rising edge (change from 0 to 1) of the assigned bit or input.		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
dAF	<p>Terminal display: [Slowdown forward]</p> <p>Logic address: 12503 = 16#30D7</p> <p>CANopen index: 205F/4</p> <p>INTERBUS index: 5FBD/37</p> <p>DeviceNet path: 9F/01/68</p> <p>0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)</p> <p>See: "Assignment conditions for logic inputs and control bits", page 192. Forward slowdown, controlled on a rising edge (change from 0 to 1) of the assigned bit or input.</p>	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
dAr	<p>Terminal display: [Slowdown reverse]</p> <p>Logic address: 12504 = 16#30D8</p> <p>CANopen index: 205F/5</p> <p>INTERBUS index: 5FBD/38</p> <p>DeviceNet path: 9F/01/69</p> <p>0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) In I/O profile only: 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)</p> <p>See: "Assignment conditions for logic inputs and control bits", page 192. Reverse slowdown, controlled on a rising edge (change from 0 to 1) of the assigned bit or input.</p>	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0
CLS	<p>Terminal display: [Disable limit sw.]</p> <p>Logic address: 12507 = 16#30DB</p> <p>CANopen index: 205F/8</p> <p>INTERBUS index: 5FBD/3B</p> <p>DeviceNet path: 9F/01/6C</p> <p>0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)</p> <p>See: "Assignment conditions for logic inputs and control bits", page 192. The action of the limit switches is deactivated on a rising edge (change from 0 to 1) of the assigned bit or input.</p>	Type:	WORD (listing)
		Read/write:	R/WS
		Factory setting:	0

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
PAS	Terminal display: [Stop type]		
	Logic address: 12506 = 16#30DA	Type:	WORD (listing)
	CANopen index: 205F/7	Read/write:	R/WS
	INTERBUS index: 5FBD/3A	Factory setting:	0
	DeviceNet path: 9F/01/6B		
0 = [Ramp stop] (rMP): Follow ramp 1 = [Fast stop] (FSt): Fast stop (ramp reduced by [Ramp divider] (dCF), see "dCF", page 206) 2 = [Freewheel] (YES): Freewheel stop			
dSF	Terminal display: [Deceleration type]		
	Logic address: 12505 = 16#30D9	Type:	WORD (listing)
	CANopen index: 205F/6	Read/write:	R/WS
	INTERBUS index: 5FBD/39	Factory setting:	0
	DeviceNet path: 9F/01/6A		
0 = [standard] (Std): Uses the [Deceleration] (dEC) or [Deceleration 2] (dE2) ramp (depending on which has been enabled) 1 = [Optimized] (OPt): Ramp time			

[1.7 APPLICATION FUNCT.] (FUn-)

[PARAM. SET SWITCHING] (MLP-)

Code	Description		
CHA1	Terminal display: [2 Parameter sets] Logic address: 12902 = 16#3266 CANopen index: 2063/3 INTERBUS index: 5FBD/54 DeviceNet path: A1/01/67		
	0 = [No] (nO): Not assigned	Type:	WORD (listing)
	129 = [LI1] (LI1)	Read/write:	R/WS
	:	Factory setting:	0
	142 = [LI14] (LI14)		
	160 = [CD00] (Cd00)		
	:		
	175 = [CD15] (Cd15)		
	177 = [C101] (C101)		
	:		
	239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
	Switching 2 parameter sets		
CHA2	Terminal display: [3 Parameter sets] Logic address: 12903 = 16#3267 CANopen index: 2063/4 INTERBUS index: 5FBD/55 DeviceNet path: A1/01/68		
	0 = [No] (nO): Not assigned	Type:	WORD (listing)
	129 = [LI1] (LI1)	Read/write:	R/WS
	:	Factory setting:	0
	142 = [LI14] (LI14)		
	160 = [CD00] (Cd00)		
	:		
	175 = [CD15] (Cd15)		
	177 = [C101] (C101)		
	:		
	239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
	Switching 3 parameter sets		

[1.7 APPLICATION FUNCT.] (FUn-)

[MULTIMOTORS/CONFIG.] (MMC-)

Code	Description		
CHM	Terminal display: [Multimotors] Logic address: 8025 = 16#1F59 CANopen index: 2032/1A INTERBUS index: 5FBC/9E DeviceNet path: 89/01/1A		
	0 = [No] (nO) : Multiconfiguration possible 1 = [Yes] (YES) : Multimotor possible		
CnF1	Terminal display: [2 Configurations] Logic address: 8021 = 16#1F55 CANopen index: 2032/16 INTERBUS index: 5FBC/9C DeviceNet path: 89/01/16		
	0 = [No] (nO) : No switching 129 = [LI1] (LI1) : 134 = [LI6] (LI6) 135 = [LI7] (LI7) : If VW3A3201 logic I/O card has been inserted : 138 = [LI10] (LI10) : If VW3A3201 logic I/O card has been inserted 139 = [LI11] (LI11) : If VW3A3202 extended I/O card has been inserted : 142 = [LI14] (LI14) : If VW3A3202 extended I/O card has been inserted 187 = [C111] (C111) : With integrated Modbus : 191 = [C115] (C115) : With integrated Modbus 203 = [C211] (C211) : With integrated CANopen : 207 = [C215] (C215) : With integrated CANopen 219 = [C311] (C311) : With a communication card : 223 = [C315] (C315) : With a communication card 235 = [C411] (C411) : With a Controller Inside card : 239 = [C415] (C415) : With a Controller Inside card		
	See: "Assignment conditions for logic inputs and control bits", page 192 . Switching of 2 motors or 2 configurations		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
CnF2	Terminal display: [3 Configurations]		
	Logic address: 8022 = 16#1F56	Type:	WORD (listing)
	CANopen index: 2032/17	Read/write:	R/WS
	INTERBUS index: 5FBC/9D	Factory setting:	0
	DeviceNet path: 89/01/17		
	0 = [No] (nO) : No switching 129 = [LI1] (LI1) : 134 = [LI6] (LI6) 135 = [LI7] (LI7) : If VW3A3201 logic I/O card has been inserted : 138 = [LI10] (LI10) : If VW3A3201 logic I/O card has been inserted 139 = [LI11] (LI11) : If VW3A3202 extended I/O card has been inserted : 142 = [LI14] (LI14) : If VW3A3202 extended I/O card has been inserted 187 = [C111] (C111) : With integrated Modbus : 191 = [C115] (C115) : With integrated Modbus 203 = [C211] (C211) : With integrated CANopen : 207 = [C215] (C215) : With integrated CANopen 219 = [C311] (C311) : With a communication card : 223 = [C315] (C315) : With a communication card 235 = [C411] (C411) : With a Controller Inside card : 239 = [C415] (C415) : With a Controller Inside card		
	See: "Assignment conditions for logic inputs and control bits", page 192 . Switching of 3 motors or 3 configurations		

[1.7 APPLICATION FUNCT.] (FUn-)

[AUTO TUNING BY LI] (tnL-)

Code	Description		
tUL	Terminal display: [Auto-tune assign.]		
	Logic address: 9610 = 16#258A	Type:	WORD (listing)
	CANopen index: 2042/B	Read/write:	R/WS
	INTERBUS index: 5FBC/EF	Factory setting:	0
	DeviceNet path: 91/01/0B		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 . Auto-tuning is performed when the assigned input or bit changes to 1.		

[1.7 APPLICATION FUNCT.] (FUn-)

[TRAVERSE CONTROL] (tr0-)

Code	Description		
trC	Terminal display: [Yarn control] Logic address: 12201 = 16#2FA9 CANopen index: 205C/2 INTERBUS index: 5FBD/24 DeviceNet path: 9E/01/02		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 . The "traverse control" cycle starts when the assigned input or bit changes to 1 and stops when it changes to 0.		
trH	Terminal display: [Traverse high] Logic address: 12202 = 16#2FAA CANopen index: 205C/3 INTERBUS index: 5FBF/7F DeviceNet path: 9E/01/03		
trL	Terminal display: [Traverse Low] Logic address: 12203 = 16#2FAB CANopen index: 205C/4 INTERBUS index: 5FBF/80 DeviceNet path: 9E/01/04		
qSH	Terminal display: [Quick step High] Logic address: 12204 = 16#2FAC CANopen index: 205C/5 INTERBUS index: 5FBF/81 DeviceNet path: 9E/01/05		
qSL	Terminal display: [Quick step Low] Logic address: 12205 = 16#2FAD CANopen index: 205C/6 INTERBUS index: 5FBF/82 DeviceNet path: 9E/01/06		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
tUP	Terminal display: [Traverse ctrl. accel.]		
Logic address:	12206 = 16#2FAE	Type:	UINT
CANopen index:	205C/7	Read/write:	R/W
INTERBUS index:	5FBF/83	Unit:	0.1 s
DeviceNet path:	9E/01/07	Factory setting:	40
		Adjustment range:	1 to 9999
tdn	Terminal display: [Traverse ctrl. decel]		
Logic address:	12207 = 16#2FAF	Type:	UINT
CANopen index:	205C/8	Read/write:	R/W
INTERBUS index:	5FBF/84	Unit:	0.1 s
DeviceNet path:	9E/01/08	Factory setting:	40
		Adjustment range:	1 to 9999
tbO	Terminal display: [Reel time]		
Logic address:	12208 = 16#2FAB	Type:	UINT
CANopen index:	205C/9	Read/write:	R/W
INTERBUS index:	5FBF/85	Unit:	1 min
DeviceNet path:	9E/01/09	Factory setting:	0
		Adjustment range:	0 to 9999
Reel execution time			
EbO	Terminal display: [End reel]		
Logic address:	12213 = 16#2FB5	Type:	WORD (listing)
CANopen index:	205C/E	Read/write:	R/WS
INTERBUS index:	5FBD/27	Factory setting:	0
DeviceNet path:	9E/01/0E		
0 = [No] (nO) : Function not assigned (in this case, none of the function parameters can be accessed) 129 = [LO1] (LO1) : Logic output (if one or two I/O cards have been inserted, LO1 to LO2 or LO4 can be selected) 130 = [LO2] (LO2) 131 = [LO3] (LO3) 132 = [LO4] (LO4) 146 = [R2] (r2) : Relay (selection of R2 extended to R3 or R4 if one or two I/O cards have been inserted) 147 = [R3] (r3) 148 = [R4] (r4)			
The assigned output or relay changes to state 1 when the traverse control operating time reaches the [Reel time] (tbO).			

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
SnC	Terminal display: [Counter wobble]		
	Logic address: 12212 = 16#2FB4	Type:	WORD (listing)
	CANopen index: 205C/D	Read/write:	R/WS
	INTERBUS index: 5FBD/26	Factory setting:	0
	DeviceNet path: 9E/01/0D		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 .		
tSY	Terminal display: [Sync. wobble]		
	Logic address: 12214 = 16#2FB6	Type:	WORD (listing)
	CANopen index: 205C/F	Read/write:	R/WS
	INTERBUS index: 5FBD/28	Factory setting:	0
	DeviceNet path: 9E/01/0F		
	0 = [No] (nO) : Function not assigned (in this case, none of the function parameters can be accessed) 129 = [LO1] (LO1) : Logic output (if one or two I/O cards have been inserted, LO1 to LO2 or LO4 can be selected) 130 = [LO2] (LO2) 131 = [LO3] (LO3) 132 = [LO4] (LO4) 146 = [R2] (r2) : Relay (selection of R2 extended to R3 or R4 if one or two I/O cards have been inserted) 147 = [R3] (r3) 148 = [R4] (r4)		
	The assigned output or relay changes to state 1 when the traverse control operating time reaches the [Reel time] (tbO) .		
dtF	Terminal display: [Decrease ref. speed]		
	Logic address: 12211 = 16#2FB3	Type:	UINT
	CANopen index: 205C/C	Read/write:	R/W
	INTERBUS index: 5FBF/86	Unit:	0.1 Hz
	DeviceNet path: 9E/01/0C	Factory setting:	0
		Adjustment range:	0 to 10,000
	Decrease in the base reference during the traverse control cycle.		

[1.7 APPLICATION FUNCT.] (FUn-)

Code	Description		
rtr	Terminal display: [Init. traverse ctrl]		
	Logic address: 12210 = 16#2FB2	Type:	WORD (listing)
	CANopen index: 205C/B	Read/write:	R/WS
	INTERBUS index: 5FBD/25	Factory setting:	0
	DeviceNet path: 9E/01/0B		
	0 = [No] (nO) : Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 177 = [C101] (C101) : 239 = [C415] (C415)		
	See: "Assignment conditions for logic inputs and control bits", page 192 . When the state of the assigned input or bit changes to 1, the traverse control operating time is reset to zero, along with [Decrease ref. speed] (dtF) .		

[1.7 APPLICATION FUNCT.] (FUn-)

[EVACUATION] (rFt-)

This function is only accessible for ATV71●●●N4 drives (380/480 V).

Code	Description				
rFt	Terminal display: [Evacuation assign.] Logic address: 13831 = 16#3607 CANopen index: 206C/20 INTERBUS index: 5FBD/76 DeviceNet path: A6/01/20				
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) : Assignment to a logic input	Type:	WORD (listing)		
		Read/write:	R/WS		
		Factory setting:	0		
	Evacuation is activated when the assigned input is at 1, if the drive is stationary. Evacuation is activated when the assigned input is at 0, as soon as the drive stops.				
rSU	Terminal display: [Evacuation Input V.] Logic address: 13832 = 16#3608 CANopen index: 206C/21 INTERBUS index: 5FBD/77 DeviceNet path: A6/01/21				
	Type:	UINT			
	Read/write:	R/WS			
	Unit:	1 V			
	Factory setting:	220			
	Adjustment range:	220 to 320			
	Minimum permissible AC voltage value of the emergency power supply				
rSP	Terminal display: [Evacuation freq.] Logic address: 13833 = 16#3609 CANopen index: 206C/22 INTERBUS index: 5FB4/04 DeviceNet path: A6/01/22				
	Type:	UINT			
	Read/write:	R/W			
	Unit:	0.1 Hz			
	Factory setting:	50			
	Adjustment range:	Depends on the LsP, FrS, rSU and UnS parameters			
	Value of the "evacuation" mode frequency reference				

[1.7 APPLICATION FUNCT.] (FUn-)

[DC BUS SUPPLY] (dCO-)

This function is only accessible for ATV71●●●M3 drives ≥ 18.5 kW and ATV71●●●N4 drives > 18.5 kW.

Code	Description		
dCO	Terminal display: [Precharge cont. ass.]		
	Logic address: 13841 = 16#3611	Type:	WORD (listing)
	CANopen index: 206C/2A	Read/write:	R/W/S
	INTERBUS index: 5FBD/78	Factory setting:	0
	DeviceNet path: A6/01/2A		
	<p>Logic output or control relay 0 = [No] (nO): Function not assigned 129 = [LO1] (LO1): Logic output (if one or two I/O cards have been inserted, LO1 to LO2 or LO4 can be selected) 130 = [LO2] (LO2) 131 = [LO3] (LO3) 132 = [LO4] (LO4) 146 = [R2] (r2): Relay (selection of R2 extended to R3 or R4 if one or two I/O cards have been inserted) 147 = [R3] (r3) 148 = [R4] (r4)</p>		

[1.8 FAULT MANAGEMENT] (FLt-)

[PTC MANAGEMENT] (PtC-)

Code	Description		
PtCL	Terminal display: [L16 = PTC probe] Logic address: 13203 = 16#3396 Type: WORD (listing) CANopen index: 2066/4 Read/write: R/WS INTERBUS index: 5FBF/CD Factory setting: 0 DeviceNet path: A3/01/04		
	0 = [No] (nO): Not used 1 = [Always] (AS): "PTC probe" faults are monitored continuously, even if the power supply is not connected (as long as the control remains connected to the power supply). 2 = [Power ON] (rdS): "PTC probe" faults are monitored while the drive power supply is connected. 3 = [Motor ON] (rS): "PTC probe" faults are monitored while the motor power supply is connected.		
PtC1	Terminal display: [PTC1 probe] Logic address: 13201 = 16#3391 Type: WORD (listing) CANopen index: 2066/2 Read/write: R/WS INTERBUS index: 5FBF/CB Factory setting: 0 DeviceNet path: A3/01/02		
	0 = [No] (nO): Not used 1 = [Always] (AS): "PTC probe" faults are monitored continuously, even if the power supply is not connected (as long as the control remains connected to the power supply). 2 = [Power ON] (rdS): "PTC probe" faults are monitored while the drive power supply is connected. 3 = [Motor ON] (rS): "PTC probe" faults are monitored while the motor power supply is connected.		
PtC2	Terminal display: [PTC2 probe] Logic address: 13202 = 16#3392 Type: WORD (listing) CANopen index: 2066/3 Read/write: R/WS INTERBUS index: 5FBF/CC Factory setting: 0 DeviceNet path: A3/01/03		
	0 = [No] (nO): Not used 1 = [Always] (AS): "PTC probe" faults are monitored continuously, even if the power supply is not connected (as long as the control remains connected to the power supply). 2 = [Power ON] (rdS): "PTC probe" faults are monitored while the drive power supply is connected. 3 = [Motor ON] (rS): "PTC probe" faults are monitored while the motor power supply is connected.		

[1.8 FAULT MANAGEMENT] (FLt-)

[FAULT RESET] (rSt-)

Code	Description		
rSF	Terminal display: [Fault reset]		
	Logic address: 7124 = 16#1BD4	Type:	WORD (listing)
	CANopen index: 2029/19	Read/write:	R/W/S
	INTERBUS index: 5FBC/97	Factory setting:	According to CFG and tCC (1)
	DeviceNet path: 84/01/7D		
	0 = [No] (nO): Function inactive 129 = [LI1] (LI1) to [LI6] (LI6) 135 = [LI7] (LI7) to [LI10] (LI10) if VW3A3201 I/O card has been inserted 139 = [LI11] (LI11) to [LI14] (LI14): If VW3A3202 extended I/O card has been inserted 160 = [CD00] (Cd00) to [CD10] (Cd10): In [I/O profile] (IO) 171 = [CD11] (Cd11) to [CD15] (Cd15): Regardless of configuration 177 = [C101] (C101) to [C115] (C115): With integrated Modbus in [I/O profile] (IO) 193 = [C201] (C201) to [C215] (C215): With integrated CANopen in [I/O profile] (IO) 209 = [C301] (C301) to [C315] (C315): With a communication card in [I/O profile] (IO) 225 = [C401] (C401) to [C415] (C415): With a Controller Inside card in [I/O profile] (IO)		

(1) According to **CFG**, see “CFG”, page [144](#) and **tCC**, see “tCC”, page [166](#)

If **CFG** = 1:

If **tCC** = 0: Factory setting = **[LI6]** (LI6)

If **tCC** = 1: Factory setting = 0

If **CFG** = 2:

If **tCC** = 0: Factory setting = **[LI3]** (LI3)

If **tCC** = 1: Factory setting = **[LI4]** (LI4)

If **CFG** = 3, 5 or 6:

If **tCC** = 0: Factory setting = **[LI4]** (LI4)

If **tCC** = 1: Factory setting = **[LI5]** (LI5)

Otherwise, factory setting = 0

[1.8 FAULT MANAGEMENT] (FLt-)

[AUTOMATIC RESTART] (Atr-)

Code	Description		
Atr	Terminal display: [Automatic restart]		
	Logic address:	7122 = 16#1BD2	Type: WORD (listing)
	CANopen index:	2029/17	Read/write: R/WS
	INTERBUS index:	5FB3/C9	Factory setting: 0
	DeviceNet path:	84/01/7B	
	0 = [No] (nO) Function inactive 1 = [YES] (YES) : Automatic restart		
tAr	Terminal display: [Max. restart time]		
	Logic address:	7123 = 16#1BD3	Type: WORD (listing)
	CANopen index:	2029/18	Read/write: R/WS
	INTERBUS index:	5FBC/96	Factory setting: 0
	DeviceNet path:	84/01/7C	
	0 = [5 minutes] (5) : 5 minutes 1 = [10 minutes] (10) : 10 minutes 2 = [30 minutes] (30) : 30 minutes 3 = [1 h] (1 hour) : 1 hour 4 = [2 hours] (2h) : 2 hours 5 = [3 hours] (3h) : 3 hours 6 = [Unlimited] : Unlimited		

[1.8 FAULT MANAGEMENT] (FLt-)

[CATCH ON THE FLY] (FLr-)

Code	Description		
FLr	Terminal display: [Catch on the fly]		
	Logic address:	3110 = 16#C26	Type: WORD (listing)
	CANopen index:	2001/B	Read/write: R/W
	INTERBUS index:	5FB3/7A	Factory setting: 0
	DeviceNet path:	70/01/6F	
	0 = [No] (nO) Function inactive 1 = [YES] (YES) : Function active		
UCb	Terminal display: [Sensitivity]		
	Logic address:	3111 = 16#C27	Type: UINT
	CANopen index:	2001/C	Read/write: R/W
	INTERBUS index:	5FBF/06	Unit: 0.11%
	DeviceNet path:	70/01/70	Factory setting: 120
	Adjusts the catch-on-the-fly sensitivity around the zero speed. The parameter can be accessed from 55 kW (75 HP) upwards for the ATV71●●●M3X and from 90 kW (120 HP) upwards for the ATV71●●●N4.		

[1.8 FAULT MANAGEMENT] (FLt-)

[MOTOR THERMAL PROT.] (tHt-)

Code	Description			
tHt	Terminal display: [Motor protect. type]			
	Logic address: 9612 = 16#258C	Type:	WORD (listing)	
	CANopen index: 2042/D	Read/write:	R/W/S	
	INTERBUS index: 5FB3/D9	Factory setting:	1	
DeviceNet path: 91/01/0D				
0 = [No active] (nO): No protection 1 = [Self cooled] (ACL): For self-cooled motors 2 = [Force-cool] (FCL): For force-cooled motors				
ttd	Terminal display: [Motor therm. level]			
	Logic address: 11002 = 16#2AFA	Type:	UINT	
	CANopen index: 2050/3	Read/write:	R/W	
	INTERBUS index: 5FB3/E4	Unit	1%	
	DeviceNet path: 98/01/03	Factory setting:	100	
	Adjustment range 0 to 118			
Trip threshold for motor thermal alarm (logic output or relay)				
ttd2	Terminal display: [Motor2 therm. level]			
	Logic address: 11006 = 16#2AFE	Type:	UINT	
	CANopen index: 2050/7	Read/write:	R/W	
	INTERBUS index: 5FBF/4C	Unit	1%	
	DeviceNet path: 98/01/07	Factory setting:	100	
	Adjustment range 0 to 118			
Trip threshold for motor thermal alarm (logic output or relay)				
ttd3	Terminal display: [Motor3 therm. level]			
	Logic address: 11007 = 16#2AFF	Type:	UINT	
	CANopen index: 2050/8	Read/write:	R/W	
	INTERBUS index: 5FBF/4D	Unit	1%	
	DeviceNet path: 98/01/08	Factory setting:	100	
	Adjustment range 0 to 118			
Trip threshold for motor thermal alarm (logic output or relay)				

[1.8 FAULT MANAGEMENT] (FLt-)

Code	Description		
OLL	Terminal display: [Overload fault mgt]		
	Logic address: 7009 = 16#1B61	Type:	WORD (listing)
	CANopen index: 2028/A	Read/write:	R/WS
	INTERBUS index: 5FBC/8D	Factory setting:	1
	DeviceNet path: 84/01/0A		
	0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop 2 = [Per STT] (Stt) : Stop according to configuration of [Type of stop] (Stt) , see "Stt", page 205 . 4 = [fallback spd] (LFF) : Change to fallback speed, maintained as long as the fault persists and the run command has not been removed. 5 = [Spd maint.] (rLS) : The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 6 = [Ramp stop] (rMP) : Stop on ramp 7 = [Fast stop] (FSt) : Fast stop 8 = [DC injection] (dCI) : DC injection stop		

[1.8 FAULT MANAGEMENT] (FLt-)

[OUTPUT PHASE LOSS] (OPL-)

Code	Description		
OPL	Terminal display: [Output Phase Loss]		
	Logic address: 9611 = 16#258B	Type:	WORD (listing)
	CANopen index: 2042/C	Read/write:	R/W/S
	INTERBUS index: 5FB3/D8	Factory setting:	1
	DeviceNet path: 91/01/0C		
	0 = [No] (nO) : Function inactive 1 = [Yes] (YES) : Tripping on OPF fault with freewheel stop 2 = [Output cut] (OAC) : No fault triggered, but management of the output voltage in order to avoid an overcurrent when the link with the motor is re-established and catch on the fly performed (even if this function has not been configured).		
Odt	Terminal display: [OutPh time detect]		
	Logic address: 7081 = 16#1BA9	Type:	UINT
	CANopen index: 2028/52	Read/write:	R/W
	INTERBUS index: 5FBF/08	Unit:	0.11 s
	DeviceNet path: 84/01/52	Factory setting:	5
	Adjustment range: 5 to 100		
	Time delay for taking [Output Phase Loss] (OPL) fault into account.		

[INPUT PHASE LOSS] (IPL-)

Code	Description		
IPL	Terminal display: [Input phase loss]		
	Logic address: 7002 = 16#1B5A	Type:	WORD (listing)
	CANopen index: 2028/3	Read/write:	R/W/S
	INTERBUS index: 5FB3/BE	Factory setting:	According to drive rating ⁽¹⁾
	DeviceNet path: 84/01/03		
	0 = [Ignore] (nO) : Fault ignored, to be used when the drive is supplied via a single phase supply or by the DC bus. 1 = [Freewheel] (YES) : Fault, with freewheel stop		

⁽¹⁾ 0 = **[Ignore] (nO)** for ATV71H037M3 to HU75M3, 1 = **[Freewheel] (YES)** for others.

[1.8 FAULT MANAGEMENT] (FLt-)

[DRIVE OVERHEAT] (OHL-)

Code	Description		
OHL	Terminal display: [Overtemp fault mgt]		
	Logic address: 7008 = 16#1B60	Type:	WORD (listing)
	CANopen index: 2028/9	Read/write:	R/W/S
	INTERBUS index: 5FBC/8C	Factory setting:	1
	DeviceNet path: 84/01/09		
	0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop 2 = [Per STT] (Stt) : Stop according to configuration of [Type of stop] (Stt) , see "Stt", page 205 . 4 = [fallback spd] (LFF) : Change to fallback speed, maintained as long as the fault persists and the run command has not been removed. 5 = [Spd maint.] (rLS) : The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 6 = [Ramp stop] (rMP) : Stop on ramp 7 = [Fast stop] (FSt) : Fast stop 8 = [DC injection] (dCI) : DC injection stop		
tHA	Terminal display: [Drv therm. state al]		
	Logic address: 11009 = 16#2B01	Type:	UINT
	CANopen index: 2050/A	Read/write:	R/W
	INTERBUS index: 5FBF/4E	Unit	1%
	DeviceNet path: 98/01/0A	Factory setting:	100
		Adjustment range	0 to 118
	Trip threshold for drive thermal alarm (logic output or relay)		

[1.8 FAULT MANAGEMENT] (FLt-)

[THERMAL ALARM STOP] (SAT-)

Code	Description			
SAt	Terminal display: [Thermal alarm stop]			
	Logic address: 11021 = 16#2B0D	Type:	WORD (listing)	
	CANopen index: 2050/16	Read/write:	R/W/S	
	INTERBUS index: 5FB3/E7	Factory setting:	0	
DeviceNet path: 98/01/16				
0 = [No] (nO): Function inactive (in this case, the following parameters cannot be accessed) 1 = [Yes] (YES): Freewheel stop on drive or motor thermal alarm				
tHA	Terminal display: [Drv therm. state al]			
	Logic address: 11009 = 16#2B01	Type:	UINT	
	CANopen index: 2050/A	Read/write:	R/W	
	INTERBUS index: 5FBF/4E	Unit	1%	
DeviceNet path: 98/01/0A				
Factory setting: 100 Adjustment range 0 to 118				
Thermal state threshold of the drive tripping the deferred stop				
ttd	Terminal display: [Motor therm. level]			
	Logic address: 11002 = 16#2AFA	Type:	UINT	
	CANopen index: 2050 / 3	Read/write:	R/W	
	INTERBUS index: 5FB3/E4	Unit	1%	
DeviceNet path: 98/01/03				
Factory setting: 100 Adjustment range 0 to 118				
Thermal state threshold of the motor tripping the deferred stop				
ttd2	Terminal display: [Motor2 therm. level]			
	Logic address: 11006 = 16#2AFE	Type:	UINT	
	CANopen index: 2050/7	Read/write:	R/W	
	INTERBUS index: 5FBF/4C	Unit	1%	
DeviceNet path: 98/01/07				
Factory setting: 100 Adjustment range 0 to 118				
Thermal state threshold of the motor 2 tripping the deferred stop				
ttd3	Terminal display: [Motor3 therm. level]			
	Logic address: 11007 = 16#2AFF	Type:	UINT	
	CANopen index: 2050/8	Read/write:	R/W	
	INTERBUS index: 5FBF/4D	Unit	1%	
DeviceNet path: 98/01/08				
Factory setting: 100 Adjustment range 0 to 118				
Thermal state threshold of the motor 3 tripping the deferred stop				

[1.8 FAULT MANAGEMENT] (FLt-)

[EXTERNAL FAULT] (EtF-)

Code	Description		
EtF	Terminal display: [External fault ass.] Logic address: 7131 = 16#1BDB CANopen index: 2029/20 INTERBUS index: 5FBC/99 DeviceNet path: 84/01/84		
	0 = [No] (nO): Not assigned 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 170 = [C101] (C101) : 239 = [C415] (C415)	Type: Read/write: Factory setting:	WORD (listing) R/WS According to CFG and tCC ⁽¹⁾
	See "Assignment conditions for logic inputs and control bits", page 192 . No external fault if the assigned input or bit is at 0. External fault if the assigned input or bit is at 1.		
EPL	Terminal display: [External fault mgt] Logic address: 7006 = 16#1B5E CANopen index: 2028/7 INTERBUS index: 5FB3/C2 DeviceNet path: 84/01/07		
	0 = [Ignore] (nO): Fault ignored 1 = [Freewheel] (YES): Freewheel stop 2 = [Per STT] (Stt): Stop according to configuration of [Type of stop] (Stt), see "Stt", page 205 . 4 = [fallback spd] (LFF): Change to fallback speed, maintained as long as the fault persists and the run command has not been removed. 5 = [Spd maint.] (rLS): The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 6 = [Ramp stop] (rMP): Stop on ramp 7 = [Fast stop] (FSt): Fast stop 8 = [DC injection] (dCI): DC injection stop	Type: Read/write: Factory setting:	WORD (listing) R/WS 1

⁽¹⁾ According to **CFG**, see "CFG", page [144](#) and **tCC**, see "tCC", page [166](#).

If **CFG** = 2 and **tCC** = 0: Factory setting = **[LI4]** (LI4)

If **CFG** = 2 and **tCC** = 1: Factory setting = **[LI5]** (LI5)

Otherwise, factory setting = 0

[1.8 FAULT MANAGEMENT] (FLt-)

[UNDERVOLTAGE MGT] (USb-)

Code	Description		
USb	Terminal display: [UnderV. fault mgt] Logic address: 13803 =16#35EB CANopen index: 206C/4 INTERBUS index: 5FBD/72 DeviceNet path: A6/01/04		
	0 = [Flt&R1open] (0): Fault and fault relay open. 1 = [Flt&R1close] (1): Fault and fault relay closed. 2 = [Alarm] (2): Alarm and fault relay remains closed. The alarm may be assigned to a logic output or a relay.		
UrES	Terminal display: [Evacuation Input V.] Logic address: 13801 =16#35E9 CANopen index: 206C/2 INTERBUS index: 5FBD/70 DeviceNet path: A6/01/02		
	Rated voltage of the line supply in V. 20 = [200] (200): 200 V 22 = [220] (220): 220 V 24 = [240] (240): 240 V 26 = [260] (260): 260 V 38 = [380] (380): 380 V 40 = [400] (400): 400 V 44 = [440] (440): 440 V 46 = [460] (460): 460 V 48 = [480] (480): 480 V		
USL	Terminal display: [Undervoltage level] Logic address: 13802 =16#35EA CANopen index: 206C/3 INTERBUS index: 5FBD/71 DeviceNet path: A6/01/03		
	Undervoltage fault trip level setting in V. The adjustment range and factory setting are determined by the drive rating and the value of [Evacuation Input V.] (UrES).		
USt	Terminal display: [Undervolt. time out] Logic address: 13804 =16#35EC CANopen index: 206C/5 INTERBUS index: 5FBD/73 DeviceNet path: A6/01/05		
	Time delay for taking undervoltage fault into account		

[1.8 FAULT MANAGEMENT] (FLt-)

Code	Description		
StP	<p>Terminal display: [UnderV. prevention]</p> <p>Logic address: 7004 =16#1B5C CANopen index: 2028/5 INTERBUS index: 5FB3/C0 DeviceNet path: 84/01/05</p> <p>0 = [No] (nO): No action 1 = [DC Maintain] (MMS): This stop mode uses the inertia to maintain the DC bus voltage as long as possible. 2 = [Ramp stop] (rMP): Stop following an adjustable ramp [Max stop time] (StM) 4 = [Lock-out] (LnF): Lock-out (freewheel stop) without fault</p>		
tSM	<p>Terminal display: [UnderV. restart tm]</p> <p>Logic address: 13813 =16#35F5 CANopen index: 206C/E INTERBUS index: 5FBF/CF DeviceNet path: A6/01/0E</p> <p>Type: UINT Read/write: R/W Unit: 0.11 s Factory setting: 10 Adjustment range: 10 to 9999</p> <p>Time delay before authorizing the restart after a complete stop for [UnderV. prevention] (StP) = [Ramp stop] (rMP), if the voltage has returned to normal.</p>		
UPL	<p>Terminal display: [Prevention level]</p> <p>Logic address: 13811 =16#35F3 CANopen index: 206C/C INTERBUS index: 5FBD/74 DeviceNet path: A6/01/0C</p> <p>Type: UINT Read/write: R/WS Unit: 1 V</p> <p>Undervoltage fault prevention level setting in V. The adjustment range and factory setting are determined by the drive rating and the value of [Evacuation Input V.] (UrES).</p>		
StM	<p>Terminal display: [Max stop time]</p> <p>Logic address: 13814 =16#35EC CANopen index: 206C/F INTERBUS index: 5FBF/D0 DeviceNet path: A6/01/0F</p> <p>Type: UINT Read/write: R/W Unit: 0.101 s Factory setting: 100 Adjustment range: 1 to 6000</p> <p>Ramp time if [UnderV. prevention] (StP) = [Ramp stop] (rMP).</p>		
tbS	<p>Terminal display: [DC bus maintain tm]</p> <p>Logic address: 13812 =16#35F4 CANopen index: 206C/D INTERBUS index: 5FBF/CE DeviceNet path: A6/01/0D</p> <p>Type: UINT Read/write: R/W Unit: 1 s Factory setting: 9999 Adjustment range: 1 to 9999</p> <p>DC bus maintain time if [UnderV. prevention] (StP) = [DC Maintain] (MMS).</p>		

[1.8 FAULT MANAGEMENT] (FLt-)

[IGBT TESTS] (tlt-)

Code	Description		
Strt	Terminal display:	[IGBT test]	
	Logic address:	3112 =16#C28	Type: WORD (listing)
	CANopen index:	2001/D	Read/write: R/WS
	INTERBUS index:	5FBC/0D	Factory setting: 0
	DeviceNet path:	70/01/71	
0 = [No] (nO): No test 1 = [YES] (YES): The IGBTs are tested on power up and every time a run command is sent.			

[1.8 FAULT MANAGEMENT] (FLt-)

[4-20mA LOSS] (LFL-)

Code	Description		
LFL2	Terminal display: [AI2 4-20mA loss]		
	Logic address:	7003 =16#1B5B	Type: WORD (listing)
	CANopen index:	2028/4	Read/write: R/W/S
	INTERBUS index:	5FB3/BF	Factory setting: 0
	DeviceNet path:	84/01/04	
	<p>0 = [Ignore] (nO): Fault ignored. This configuration is the only one possible if [AI2 min. value] (CrL2), see “CrL2”, page 170, is not greater than 3 mA.</p> <p>1 = [Freewheel] (YES): Freewheel stop</p> <p>2 = [Per STT] (Stt): Stop according to configuration of [Type of stop] (Stt), see “Stt”, page 205.</p> <p>4 = [fallback spd] (LFF): Change to fallback speed, maintained as long as the fault persists and the run command has not been removed.</p> <p>5 = [Spd maint.] (rLS): The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed.</p> <p>6 = [Ramp stop] (rMP): Stop on ramp</p> <p>7 = [Fast stop] (FSt): Fast stop</p> <p>8 = [DC injection] (dCI): DC injection stop</p>		
LFL3	Terminal display: [AI3 4-20mA loss]		
	Logic address:	7013 =16#1B65	Type: WORD (listing)
	CANopen index:	2028/E	Read/write: R/W/S
	INTERBUS index:	5FB3/C3	Factory setting: 0
	DeviceNet path:	84/01/0E	
	<p>0 = [Ignore] (nO): Fault ignored. This configuration is the only one possible if [AI3 min. value] (CrL3), see “CrL3”, page 172, is not greater than 3 mA.</p> <p>1 = [Freewheel] (YES): Freewheel stop</p> <p>2 = [Per STT] (Stt): Stop according to configuration of [Type of stop] (Stt), see “Stt”, page 205.</p> <p>4 = [fallback spd] (LFF): Change to fallback speed, maintained as long as the fault persists and the run command has not been removed.</p> <p>5 = [Spd maint.] (rLS): The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed.</p> <p>6 = [Ramp stop] (rMP): Stop on ramp</p> <p>7 = [Fast stop] (FSt): Fast stop</p> <p>8 = [DC injection] (dCI): DC injection stop</p>		
LFL4	Terminal display: [AI4 4-20mA loss]		
	Logic address:	7014 =16#1B66	Type: WORD (listing)
	CANopen index:	2028/F	Read/write: R/W/S
	INTERBUS index:	5FB3/C4	Factory setting: 0
	DeviceNet path:	84/01/0F	
	<p>0 = [Ignore] (nO): Fault ignored. This configuration is the only one possible if [AI4 min. value] (CrL4), see “CrL4”, page 174, is not greater than 3 mA.</p> <p>1 = [Freewheel] (YES): Freewheel stop</p> <p>2 = [Per STT] (Stt): Stop according to configuration of [Type of stop] (Stt), see “Stt”, page 205.</p> <p>4 = [fallback spd] (LFF): Change to fallback speed, maintained as long as the fault persists and the run command has not been removed.</p> <p>5 = [Spd maint.] (rLS): The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed.</p> <p>6 = [Ramp stop] (rMP): Stop on ramp</p> <p>7 = [Fast stop] (FSt): Fast stop</p> <p>8 = [DC injection] (dCI): DC injection stop</p>		

[1.8 FAULT MANAGEMENT] (FLt-)

[FAULT INHIBITION] (InH-)

Code	Description		
Inh	Terminal display: [Fault inhibit assign.]		
	Logic address: 7125 =16#1BD5	Type:	WORD (listing)
	CANopen index: 2029/1A	Read/write:	R/W/S
	INTERBUS index: 5FBC/98	Factory setting:	0
	DeviceNet path: 84/01/7E		
	<p>0 = [No] (nO): Function inactive 129 = [LI1] (LI1) : 142 = [LI14] (LI14) 160 = [CD00] (Cd00) : 175 = [CD15] (Cd15) 170 = [C101] (C101) : 239 = [C415] (C415)</p>		
	<p>See "Assignment conditions for logic inputs and control bits", page 192. If the assigned input or bit is at 0, fault monitoring is active. If the assigned input or bit is at 1, fault monitoring is inactive. Active faults are reset on a rising edge (change from 0 to 1) of the assigned input or bit.</p>		

[1.8 FAULT MANAGEMENT] (FLt-)

[COM. FAULT MANAGEMENT] (CLL-)

Code	Description		
CLL	Terminal display: [Network fault mgt] Logic address: 7015 =16#1B67 CANopen index: 2028/10 INTERBUS index: 5FBC/93 DeviceNet path: 84/01/10		
	0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop 2 = [Per STT] (Stt) : Stop according to configuration of [Type of stop] (Stt) , see "Stt", page 205. 4 = [fallback spd] (LFF) : Change to fallback speed, maintained as long as the fault persists and the run command has not been removed. 5 = [Spd maint.] (rLS) : The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 6 = [Ramp stop] (rMP) : Stop on ramp 7 = [Fast stop] (FSt) : Fast stop 8 = [DC injection] (dCI) : DC injection stop		
COL	Terminal display: [CANopen fault mgt] Logic address: 7011 =16#1B63 CANopen index: 2028/C INTERBUS index: 5FBC/8F DeviceNet path: 84/01/0C		
	0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop 2 = [Per STT] (Stt) : Stop according to configuration of [Type of stop] (Stt) , see "Stt", page 205. 4 = [fallback spd] (LFF) : Switch to fallback speed, maintained as long as the fault is present and the run command is not disabled. 5 = [Spd maint.] (rLS) : The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 6 = [Ramp stop] (rMP) : Stop on ramp 7 = [Fast stop] (FSt) : Fast stop 8 = [DC injection] (dCI) : DC injection stop		
SLL	Terminal display: [Modbus fault mgt] Logic address: 7010 =16#1B62 CANopen index: 2028/B INTERBUS index: 5FBC/8E DeviceNet path: 84/01/0B		
	0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop 2 = [Per STT] (Stt) : Stop according to configuration of [Type of stop] (Stt) , see "Stt", page 205. 3 = [fallback spd] (LFF) : Switch to fallback speed, maintained as long as the fault is present and the run command is not disabled. 4 = [Spd maint.] (rLS) : The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 5 = [Ramp stop] (rMP) : Stop on ramp 6 = [Fast stop] (FSt) : Fast stop 8 = [DC injection] (dCI) : DC injection stop		

[1.8 FAULT MANAGEMENT] (FLt-)

[ENCODER FAULT] (Sdd-)

Code	Description		
Sdd	Terminal display: [Load slip detection]		
	Logic address:	7005 =16#1B5D	Type: WORD (listing)
	CANopen index:	2028/6	Read/write: R/W/S
	INTERBUS index:	5FB3/C1	Factory setting: 0
	DeviceNet path:	84/01/06	
0 = [No] (nO) : Fault not monitored. Only the alarm may be assigned to a logic output or a relay. 1 = [Yes] (YES) : Fault monitored			
ECC	Terminal display: [Encoder coupling]		
	Logic address:	5607 =16#15E7	Type: WORD (listing)
	CANopen index:	201A/8	Read/write: R/W/S
	INTERBUS index:	5FBC/74	Factory setting: 0 ⁽¹⁾
	DeviceNet path:	7D/01/08	
0 = [No] (nO) : Fault not monitored. Only the alarm may be assigned to a logic output or a relay. 1 = [Yes] (YES) : Fault monitored			
ECt	Terminal display: [Encoder check time]		
	Logic address:	5609 =16#15E9	Type: UINT
	CANopen index:	201A/A	Read/write: R/W/S
	INTERBUS index:	5FBF/07	Unit: 0.11 s
	DeviceNet path:	7D/01/0A	Factory setting: 20 Adjustment range: 20 to 100
Encoder faults filtering time			

⁽¹⁾ If the brake logic control function has been configured, the factory setting changes to **[Yes] (YES)**. **[Encoder coupling] (ECC) = [Yes] (YES)** is only possible if **[Load slip detection] (Sdd) = [Yes] (YES)**, see below, and **[Motor control type] (Ctt) = [FVC] (FUC)**, see "Ctt", page [150](#) and **[Brake assignment] (bLC)** is not **[No] (nO)**, see "bLC", page [221](#).

[1.8 FAULT MANAGEMENT] (FLt-)

[TORQUE OR I LIM. DETECT.] (tId-)

Code	Description		
SSb	Terminal display: [Trq/I limit. stop] Logic address: 9240 =16#2418 CANopen index: 203E/29 INTERBUS index: 5FBC/BE DeviceNet path: 8F/01/29		
	0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop 2 = [Per STT] (Stt) : Stop according to configuration of [Type of stop] (Stt) , see "Stt", page 205 . 4 = [fallback spd] (LFF) : Change to fallback speed, maintained as long as the fault persists and the run command has not been removed. 5 = [Spd maint.] (rLS) : The drive maintains the speed at the time the fault occurred, as long as the fault persists and the run command has not been removed. 6 = [Ramp stop] (rMP) : Stop on ramp 7 = [Fast stop] (FSt) : Fast stop 8 = [DC injection] (dCI) : DC injection stop		
StO	Terminal display: [Trq/I limit. time out] Logic address: 9241 =16#2419 CANopen index: 203E/2A INTERBUS index: 5FBF/34 DeviceNet path: 8F/01/2A		
	Type: UINT Read/write: R/W Unit: 1 ms Factory setting: 1000 Adjustment range: 0 to 9999 Time delay for taking SSF "Limitation" fault into account		

[1.8 FAULT MANAGEMENT] (FLt-)

[DB RES. PROTECTION] (brP-)

Code	Description			
brO	Terminal display: [DB res. protection]			
	Logic address: 14111 =16#371F	Type:	WORD (listing)	
	CANopen index: 206F/C	Read/write:	R/W	
	INTERBUS index: 5FBD/7C	Factory setting:	0	
	DeviceNet path: A7/01/70			
0 = [No] (nO): No braking resistor protection (thereby preventing access to the other function parameters) 1 = [Alarm] (ALrM): The alarm may be assigned to a logic output or a relay, see "r1", page 179 . 2 = [Fault] (FLt): Switch to fault (bOF) with locking of drive (freewheel stop).				
brP	Terminal display: [DB Resistor Power]			
	Logic address: 14112 =16#3720	Type:	UINT	
	CANopen index: 206F/D	Read/write:	R/W	
	INTERBUS index: 5FBD/7D	Unit:	0.11 kW	
	DeviceNet path: A7/01/71	Factory setting:	1	
Rated power of the resistor used				
brU	Terminal display: [DB Resistor value]			
	Logic address: 14113 =16#3721	Type:	UINT	
	CANopen index: 206F/E	Read/write:	R/W	
	INTERBUS index: 5FBD/7E	Unit:	0.11 Ω	
	DeviceNet path: A7/01/72	Factory setting:	1	
Rated value of the braking resistor in Ohms				

[1.8 FAULT MANAGEMENT] (FLt-)

[AUTO-TUNING FAULT] (tnF-)

Code	Description		
tnL	Terminal display: [Autotune fault mgt]		
	Logic address:	7012 =16#1B64	Type: WORD (listing)
	CANopen index:	2028/D	Read/write: R/WS
	INTERBUS index:	5FBC/90	Factory setting: 1
	DeviceNet path:	84/01/0D	
0 = [Ignore] (nO) : Fault ignored 1 = [Freewheel] (YES) : Freewheel stop			

[CARDS PAIRING] (PPI-)

Code	Description		
PPI	Terminal display: [Pairing password]		
	Logic address:	14001 = 16#36B1	Type: UINT
	CANopen index:	206E/2	Read/write: R/WS
	INTERBUS index:	5FBD/7A	Unit: 1
	DeviceNet path:	A7/01/02	Factory setting: 0 Adjustment range: 0 to 9999

[FALLBACK SPEED] (LFF-)

Code	Description		
LFF	Terminal display: [Fallback speed]		
	Logic address:	7080 =16#1BA8	Type: UINT
	CANopen index:	2028/51	Read/write: R/WS
	INTERBUS index:	5FB3/C5	Unit: 0.11 Hz
	DeviceNet path:	84/01/51	Factory setting: 0 Adjustment range: 0 to 10,000
Selection of the fallback speed			

[1.8 FAULT MANAGEMENT] (FLt-)

[RAMP DIVIDER] (FSt-)

Code	Description		
dCF	Terminal display:	[Ramp divider]	
	Logic address:	11230 = 16#2BDE	Type: UINT
	CANopen index:	2052/1F	Read/write: R/W
	INTERBUS index:	5FB3/ED	Unit: 1
	DeviceNet path:	99/01/1F	Factory setting: 4 Adjustment range: 0 to 10
The ramp that is enabled (dEC or dE2) is then divided by this coefficient when stop requests are sent.			

[1.8 FAULT MANAGEMENT] (FLt-)

[DC INJECTION] (dCI-))

Code	Description		
IdC	Terminal display:	[DC inject. level 1]	
	Logic address:	11210 = 16#2BCA	Type: UINT
	CANopen index:	2052/B	Read/write: R/W
	INTERBUS index:	5FB3/EC	Unit: 0.11 A
	DeviceNet path:	99/01/0B	Factory setting: 0.164 In ⁽¹⁾ Adjustment range: 0.11 In ⁽¹⁾ to 1.41 In ⁽¹⁾
Level of DC injection braking current activated via logic input or selected as stop mode.			
tdl	Terminal display:	[DC injection time 1]	
	Logic address:	11213 = 16#2BCD	Type: UINT
	CANopen index:	2052/E	Read/write: R/W
	INTERBUS index:	5FBF/55	Unit: 0.11 s
	DeviceNet path:	99/01/0E	Factory setting: 5 Adjustment range: 1 to 300
Maximum current injection time [DC inject. level 1] (IdC)			
IdC2	Terminal display:	[DC inject. level 2]	
	Logic address:	11212 = 16#2BCC	Type: UINT
	CANopen index:	2052/D	Read/write: R/W
	INTERBUS index:	5FBF/54	Unit: 0.11 A
	DeviceNet path:	99/01/0D	Factory setting: 0.5 In ⁽¹⁾ Adjustment range: 0.1 In ⁽¹⁾ to 1.41 In ⁽¹⁾
Injection current activated by logic input or selected as stop mode, once period of time [DC injection time 1] (tdl) has elapsed.			
tdC	Terminal display:	[DC injection time 2]	
	Logic address:	11211 = 16#2BCB	Type: UINT
	CANopen index:	2052/C	Read/write: R/W
	INTERBUS index:	5FBF/53	Unit: 0.11 s
	DeviceNet path:	99/01/0C	Factory setting: 5 Adjustment range: 1 to 300
Maximum current injection time [DC inject. level 2] (IdC2)			

⁽¹⁾ In corresponds to the rated drive current indicated in the installation manual and on the drive nameplate.

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
AC2	[Acceleration 2]	9012 = 16#2334	203C/D	5FBF/24	8E/01/0D	203
AC2	[Acceleration 2]	9012 = 16#2334	203C/D	5FBF/24	8E/01/0D	217
ACC	[Acceleration]	9001 = 16#2329	203C/2	5FBF/1E	8E/01/02	201
ACCd	ODVA acceleration time	-	-	-	2A/01/12	143
AdC	[Auto DC injection]	10401 = 16#28A1	204A/2	5FB3/E2	95/01/02	208
AI1C	Physical image of analog input 1	5242 = 16#147A	2016/2B	5FB9/33	7B/01/2B	96
AI1E	[AI1 Interm. point X]	4462 = 16#116E	200E/3F	5FBC/48	77/01/3F	169
AI1F	[AI1 filter]	4452 = 16#1164	200E/35	5FBC/44	77/01/35	169
AI1r	Standardized image of analog input 1	5232 = 16#1470	2016/21	5FB9/2F	7B/01/21	96
AI1S	[AI1 Interm. point Y]	4472 = 16#1178	200E/49	5FBC/4C	77/01/49	169
AI1t	[AI1 Type]	4402 = 16#1132	200E/3	5FBC/34	77/01/03	169
AI2C	Physical image of analog input 2	5243 = 16#147B	2016/2C	5FB9/34	7B/01/2C	96
AI2E	[AI2 Interm. point X]	4463 = 16#116F	200E/40	5FBC/49	77/01/40	171
AI2F	[AI2 filter]	4453 = 16#1165	200E/36	5FBC/45	77/01/36	170
AI2L	[AI2 range]	4483 = 16#1183	200E/54	5FBC/50	77/01/54	171
AI2r	Standardized image of analog input 2	5233 = 16#1471	2016/22	5FB9/30	7B/01/22	96
AI2S	[AI2 Interm. point Y]	4473 = 16#1179	200E/4A	5FBC/4D	77/01/4A	171
AI2t	[AI2 Type]	4403 = 16#1133	200E/4	5FBC/35	77/01/04	170
AI3C	Physical image of analog input 3	5244 = 16#147C	2016/2D	5FB9/35	7B/01/2D	96
AI3E	[AI3 Interm. point X]	4464 = 16#1170	200E/41	5FBC/4A	77/01/4A	173
AI3F	[AI3 filter]	4454 = 16#1166	200E/37	5FBC/46	77/01/37	172
AI3L	[AI3 range]	4484 = 16#1184	200E/55	5FBC/51	77/01/55	172
AI3r	Standardized image of analog input 3	5234 = 16#1472	2016/23	5FB9/31	7B/01/23	96
AI3S	[AI3 Interm. point Y]	4474 = 16#117A	200E/4B	5FBC/4E	77/01/4A	173
AI3t	[AI3 Type]	4404 = 16#1134	200E/5	5FBC/36	77/01/05	172
AI4C	Physical image of analog input 4	5245 = 16#147D	2016/2E	5FB9/36	7B/01/2E	96
AI4E	[AI4 Interm. point X]	4465 = 16#1171	200E/42	5FBC/4B	77/01/42	175
AI4F	[AI4 filter]	4455 = 16#1167	200E/38	5FBC/47	77/01/38	174
AI4L	[AI4 range]	4485 = 16#1185	200E/56	5FBC/52	77/01/56	175

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
AI4r	Standardized image of analog input 4	5235 = 16#1473	2016/24	5FB9/32	7B/01/24	97
AI4S	[AI4 Interm. point Y]	4475 = 16#117B	200E/4C	5FBC/4F	77/01/4C	175
AI4t	[AI4 Type]	4405 = 16#1135	200E/6	5FBC/37	77/01/06	174
AIC1	[AI net. channel]	5282 = 16#14A2	2016/53	5FBC/70	7B/01/53	230
AIU1	PID regulator feedback	5281 = 16 #14A1	2016/52	5FB9/40	7B/01/52	78
AO1	[AO1 assignment]	5021 = 16#139D	2014/16	5FBC/6D	7A/01/16	188
AO1C	Physical image of analog output 1	5271 = 16#1497	2016/48	5FB9/3D	7B/01/48	97
AO1F	[AO1 Filter]	4611 = 16#1203	2010/C	5FBC/56	78/01/0C	189
AO1r	Standardized image of analog output 1	5261 = 16#148D	2016/3E	5FB9/3A	7B/01/3E	97
AO1t	[AO1 Type]	4601 = 16#11F9	2010/2	5FBC/53	78/01/02	188
AO2	[AO2 assignment]	5022 = 16#139E	2014/17	5FBC/6E	7A/01/17	190
AO2C	Physical image of analog output 2	5272 = 16#1498	2016/49	5FB9/3E	7B/01/49	97
AO2F	[AO2 Filter]	4612 = 16#1204	2010/D	5FBC/57	78/01/0D	190
AO2r	Standardized image of analog output 2	5262 = 16#148E	2016/3F	5FB9/3B	7B/01/3F	97
AO2t	[AO2 Type]	4602 = 16#11FA	2010/3	5FBC/54	78/01/03	190
AO3	[AO3 assignment]	5023 = 16#139F	2014/18	5FBC/6F	7A/01/18	191
AO3C	Physical image of analog output 3	5273 = 16#1499	2016/4A	5FB9/3F	7B/01/4A	97
AO3F	[AO3 Filter]	4613 = 16#1205	2010/E	5FBC/58	78/01/0E	191
AO3r	Standardized image of analog output 3	5263 = 16#148F	2016/40	5FB9/3C	7B/01/40	98
AO3t	[AO3 Type]	4603 = 16#11FB	2010/4	5FBC/55	78/01/04	191
AOH1	[AO1 max Output]	4651 = 16#122B	2010/34	5FBC/62	78/01/34	188
AOH2	[AO2 max Output]	4652 = 16#122C	2010/35	5FBC/63	78/01/35	190
AOH3	[AO3 max Output]	4653 = 16#122D	2010/36	5FBC/64	78/01/36	191
AOL1	[AO1 min Output]	4641 = 16#1221	2010/2A	5FBC/5F	78/01/2A	188
AOL2	[AO2 min Output]	4642 = 16#1222	2010/2B	5FBC/60	78/01/2B	190
AOL3	[AO3 min Output]	4643 = 16#1223	2010/2C	5FBC/61	78/01/2C	191
APF	"Controller Inside" card fault code	7133 = 16#1BDD	2029/22	5FB0/97	84/01/86	102
APH	Energy consumption	3230 = 16#0C9E	2002/1F	5FB9/15	71/01/1F	92
Atr	[Automatic restart]	7122 = 16#1BD2	2029/17	5FB3/C9	84/01/7B	262
AUS	ENA average speed	12102 = 16#2F46	205B/3	5FB9/EA	9D/01/67	88
AUt	[Automatic autotune]	9615 = 16#258F	2042/10	5FC2/0B	91/01/10	149

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
bbA	[Braking balance]	14102 = 16#3716	206F/3	5FBD/7B	A7/01/67	164
bCl	[Brake contact]	10009 = 16#2719	2046/A	5FBC/F2	93/01/0A	221
bEd	[Engage at reversal]	10020 = 16#2724	2046/15	5FBF/41	93/01/15	223
bEn	[Brake engage freq]	10003 = 16#2713	2046/4	5FBF/36	93/01/04	222
bEt	[Brake engage time]	10005 = 16#2715	2046/6	5FB3/DE	93/01/06	223
bFr	[Standard mot. freq]	3015 = 16#BC7	2000/10	5FBC/05	70/01/10	148
bIP	[Brake impulse]	10007 = 16#2717	2046/8	5FB3/E0	93/01/08	221
bIr	[Brake release freq]	10012 = 16#271C	2046/D	5FB3/E1	93/01/0D	222
bLC	[Brake assignment]	10001 = 16#2711	2046/2	5FBC/F1	93/01/02	221
bMP	[HMI cmd.]	13529 = 16#34D9	2069/1E	5FBD/6C	A4/01/82	197
brA	[Dec ramp adapt.]	9003 = 16#232B	203C/4	5FB3/CA	8E/01/04	204
brH0	[BRH b0]	10050 = 16# 2742 (bit 0)	2046/33 (bit 0)	5FBC/F3 (bit 0)	93/01/33 (bit 0)	224
brH1	[BRH b1]	10050 = 16# 2742 (bit 1)	2046/33 (bit 1)	5FBC/F3 (bit 1)	93/01/33 (bit 1)	224
brH2	[BRH b2]	10050 = 16# 2742 (bit 2)	2046/33 (bit 2)	5FBC/F3 (bit 2)	93/01/33 (bit 2)	224
brH3	[BRH b3]	10050 = 16# 2742 (bit 3)	2046/33 (bit 3)	5FBC/F3 (bit 3)	93/01/33 (bit 3)	224
brH4	[BRH b4]	10050 = 16# 2742 (bit 4)	2046/33 (bit 4)	5FBC/F3 (bit 4)	93/01/33 (bit 4)	225
brO	[DB res. protection]	14111 =16#371F	206F/C	5FBD/7C	A7/01/70	278
brP	[DB Resistor Power]	14112 =16#3720	206F/D	5FBD/7D	A7/01/71	278
brr	[Current ramp time]	10015 = 16#271F	2046/10	5FBF/40	93/01/10	225
brt	[Brake Release time]	10004 = 16#2714	2046/5	5FB3/DD	93/01/05	222
brU	[DB Resistor value]	14113 =16#3721	206F/E	5FBD/7E	A7/01/72	278
bSP	[Reference template]	3106 = 16#C22	2001/7	5FB3/77	70/01/6B	168
bSt	[Movement type]	10008 = 16#2718	2046/9	5FBF/3B	93/01/09	221
CCC	Active command channel	8442 = 16#20FA	2036/2B	5FB9/CF	8B/01/2B	86
CCFG	[Customized macro]	3053 = 16#BED	2000/36	5FB9/02	70/01/36	144
CCS	[Cmd switching]	8421 = 16#20D1	2036/16	5FBC/A7	8B/01/16	194
Cd1	[Cmd channel 1]	8423 = 16#20E7	2036/18	5FBC/A8	8B/01/18	194
Cd2	[Cmd channel 2]	8424 = 16#20E8	2036/19	5FBC/A9	8B/01/19	194
CFG	[Macro configuration]	3052 = 16#BEC	2000/35	5FBC/07	70/01/35	144

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
CFPS	Active parameter set	12900 = 16#3264	2063/01	5FB9/EC	A1/01/65	70
CFPS	Active parameter set	12900 = 16#3264	2063/01	5FB9/EC	A1/01/65	86
CHA1	[2 Parameter sets]	12902 = 16#3266	2063/3	5FBD/54	A1/01/67	250
CHA1	Assignment for 2 sets	8021 = 16#1F55	2032/16	5FBC/9C	9C/01/9C	70
CHA2	[3 Parameter sets]	12903 = 16#3267	2063/4	5FBD/55	A1/01/68	250
CHA2	Assignment for 3 sets	8022 = 16#1F56	2032/17	5FBC/9D	9C/01/9D	70
CHCF	[Profile]	8401 = 16#20D1	2036/2	5FBC/9F	8B/01/02	193
CHM	[Multimotors]	8025 = 16#1F59	2032/1A	5FBC/9E	89/01/1A	251
CIC	Incorrect configuration	7130 = 16#1BDA	2029/1F	5FB6/1A	84/01/83	102
CL2	[I Limit. 2 value]	9203 = 16#23F3	203E/4	5FBF/2B	8F/01/04	243
CLI	[Current Limitation]	9201 = 16#23F1	203E/2	5FB3/D2	8F/01/02	162
CLI	[Current Limitation]	9201 = 16#23F1	203E/2	5FB3/D2	93/01/D2	243
CLI	[Current Limitation]	9201 = 16#23F1	203E/2	5FB3/D2	8F/01/02	146
CLL	[Network fault mgt]	7015 = 16#1B67	2028/10	5FBC/93	84/01/10	275
CLO	[High speed I Limit]	12302 = 16#300E	205D/3	5FBF/87	9E/01/67	229
CLS	[Disable limit sw.]	12507 = 16#30DB	205F/8	5FBD/3B	9F/01/6C	248
CMd	Control word	8601 = 16#2199	6040	6040	B7/01/01	74
CMI	Extended control word	8504 = 16#2138	2037/5	5FB6/1E	8B/01/69	76
CMP0	Command word on last fault	7230 = 16#1C3E	202A/1F	5FB9/70	85/01/1F	106
CMP1	Command word on fault n-1	7231 = 16#1C3F	202A/20	5FB9/71	85/01/20	108
CMP2	Command word on fault n-2	7232 = 16#1C40	202A/21	5FB9/72	85/01/21	110
CMP3	Command word on fault n-3	7233 = 16#1C41	202A/22	5FB9/73	85/01/22	112
CMP4	Command word on fault n-4	7234 = 16#1C42	202A/23	5FB9/74	85/01/23	114
CMP5	Command word on fault n-5	7235 = 16#1C43	202A/24	5FB9/75	85/01/24	116
CMP6	Command word on fault n-6	7236 = 16#1C44	202A/25	5FB9/76	85/01/25	118
CMP7	Command word on fault n-7	7237 = 16#1C45	202A/26	5FB9/77	85/01/26	120
CMP8	Command word on fault n-8	7238 = 16#1C46	202A/27	5FB9/78	85/01/27	122
CnF	Network card fault code	7132 = 16#1BDC	2029/21	5FB0/96	84/01/85	102
CnF1	[2 Configurations]	8021 = 16#1F55	2032/16	5FBC/9C	89/01/16	251
CNF1	Assignment for 2 configurations	8021 = 16#1F55	2032/16	5FBC/9C	9C/01/9C	67
CnF2	[3 Configurations]	8022 = 16#1F56	2032/17	5FBC/9D	89/01/17	252

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
CNF2	Assignment for 3 configurations	8022 = 16#1F56	2032/17	5FBC/9D	9C/01/9D	67
CNFS	[Config. Active]	8020 = 16#1F54	2032/15	5FB9/CD	89/01/15	86
CNFS	Active configuration	8020 = 16#1F54	2032/15	5FB9/CD	89/01/15	67
COF	[Motor speed coeff.]	12303 = 16#300F	205D/4	5FBF/88	9E/01/68	228
COL	[CANopen fault mgt]	7011 = 16#1B63	2028/C	5FBC/8F	84/01/0C	275
COP	[Copy channel 1 <-> 2]	8402 = 16#20D2	2036/3	5FBC/A0	8B/01/03	195
COOr	[Gen. speed coeff]	12304 = 16#3010	205D/5	5FBF/89	9E/01/69	228
CP1	[Point 1Y]	10072 = 16#2758	2046/49	5FBC/F6	93/01/49	226
CP2	[Point 2Y]	10074 = 16#275A	2046/4B	5FBC/F8	93/01/4B	227
CRC	Active reference channel	8441 = 16#20F9	2036/2A	5FB9/CE	8B/01/2A	85
CrH2	[AI2 max value]	4443 = 16#115B	200E/2C	5FBC/41	77/01/2C	170
CrH3	[AI3 max value]	4444 = 16#115C	200E/2D	5FBC/42	77/01/2D	172
CrH4	[AI4 max value]	4445 = 16#115D	200E/2E	5FBC/43	77/01/2E	174
CrL2	[AI2 min value]	4433 = 16#1151	200E/22	5FBC/3E	77/01/22	170
CrL3	[AI3 min value]	4434 = 16#1152	200E/23	5FBC/3F	77/01/23	172
CrL4	[AI4 min value]	4435 = 16#1153	200E/24	5FBC/40	77/01/24	174
CrP0	Channels active on last fault	7290 = 16#1C7A	202A/5B	5FB9/A6	85/01/5B	107
CrP1	Active channels on fault n-1	7291 = 16#1C7B	202A/5C	5FB9/A7	85/01/5C	109
CrP2	Active channels on fault n-2	7292 = 16#1C7C	202A/5D	5FB9/A8	85/01/5D	111
CrP3	Active channels on fault n-3	7293 = 16#1C7D	202A/5E	5FB9/A9	85/01/5E	113
CrP4	Active channels on fault n-4	7294 = 16#1C7E	202A/5F	5FB9/AA	85/01/5F	115
CrP5	Active channels on fault n-5	7295 = 16#1C7F	202A/60	5FB9/AB	85/01/60	117
CrP6	Active channels on fault n-6	7296 = 16#1C80	202A/61	5FB9/AC	85/01/61	119
CrP7	Active channels on fault n-7	7297 = 16#1C81	202A/62	5FB9/AD	85/01/62	121
CrP8	Active channels on fault n-8	7298 = 16#1C82	202A/63	5FB9/AE	85/01/63	123
Ctd	[Current threshold]	11001 = 16#2AF9	2050/2	5FB3/E3	98/01/02	146
Ctt	[Motor control type]	9607 = 16#2587	2042/8	5FC2/06	91/01/08	150
dA2	[Subtract ref. 2]	11811 = 16#2E23	2058/C	5FBD/15	9C/01/0C	199
dA3	[Subtract ref. 3]	11812 = 16#2E24	2058/D	5FBD/16	9C/01/0D	199
dAF	[Slowdown forward]	12503 = 16#30D7	205F/4	5FBD/37	9F/01/68	248
dAr	[Slowdown reverse]	12504 = 16#30D8	205F/5	5FBD/38	9F/01/69	248

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
dAS	[Time to open cont.]	13102 = 16#332E	2065/3	5FB4/03	A2/01/67	246
dAY	Date	7391 = 16#1CDF	202B/5C	5FB9/CA	85/01/C0	132
dbn	[Negative deadband]	9223 = 16#2407	203E/18	5FBF/2E	8F/01/18	240
dbP	[Positive deadband]	9224 = 16#2408	203E/19	5FBF/2F	8F/01/19	240
dBs	[Time to motor run]	13101 = 16#332D	2065/2	5FB4/02	A2/01/66	245
dCC0	Command channel active on last fault	64300 = 16#FB2C	2265/1	5FBA/08	9A/01/08	106
dCC1	Active command channel on fault n-1	64301 = 16#FB2D	2265/2	5FBA/09	9A/01/09	109
dCC2	Active command channel on fault n-2	64302 = 16#FB2E	2265/3	5FBA/0A	9A/01/0A	111
dCC3	Active command channel on fault n-3	64303 = 16#FB2F	2265/4	5FBA/0B	9A/01/0B	113
dCC4	Active command channel on fault n-4	64304 = 16#FB30	2265/5	5FBA/0C	9A/01/0C	115
dCC5	Active command channel on fault n-5	64305 = 16#FB31	2265/6	5FBA/0D	9A/01/0D	117
dCC6	Active command channel on fault n-6	64306 = 16#FB32	2265/7	5FBA/0E	9A/01/0E	119
dCC7	Active command channel on fault n-7	64307 = 16#FB33	2265/8	5FBA/0F	9A/01/0F	121
dCC8	Active command channel on fault n-8	64308 = 16#FB34	2265/9	5FBA/10	9A/01/10	123
dCF	[Ramp divider]	11230 = 16#2BDE	2052/1F	5FB3/ED	99/01/1F	206
dCF	[Ramp divider]	11230 = 16#2BDE	2052/1F	5FB3/ED	99/01/1F	280
dCI	[DC injection assign.]	11203 = 16#2BC3	2052/4	5FBD/03	99/01/04	206
dCO	[Precharge cont. ass.]	13841 = 16#3611	206C/2A	5FBD/78	A6/01/2A	259
dE2	[Deceleration 2]	9013 = 16#2335	203C/E	5FBF/25	8E/01/0E	203
dE2	[Deceleration 2]	9013 = 16#2335	203C/E	5FBF/25	8E/01/0E	217
dEC	[Deceleration]	9002 = 16#232A	203C/3	5FBF/1F	8E/01/03	201
dEcD	ODVA deceleration time	-	-	-	2A/01/13	143
dM0	Time on last fault	7310 = 16#1C8E	202B/B	5FB9/B8	85/01/6F	107
dM1	Time on fault n-1	7311 = 16#1C8F	202B/C	5FB9/B9	85/01/70	109
dM2	Time on fault n-2	7312 = 16#1C90	202B/D	5FB9/BA	85/01/71	111
dM3	Time on fault n-3	7313 = 16#1C91	202B/E	5FB9/BB	85/01/72	113
dM4	Time on fault n-4	7314 = 16#1C92	202B/F	5FB9/BC	85/01/73	115

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
dM5	Time on fault n-5	7315 = 16#1C93	202B/10	5FB9/BD	85/01/74	117
dM6	Time on fault n-6	7316 = 16#1C94	202B/11	5FB9/B5	85/01/75	119
dM7	Time on fault n-7	7317 = 16#1C95	202B/12	5FB9/BF	85/01/76	121
dM8	Time on fault n-8	7318 = 16#1C96	202B/13	5FB9/C0	85/01/77	123
dP0	Fault code on last fault	7200 = 16#1C20	202A/1	5FB9/55	85/01/01	105
dP1	Fault code on fault n-1	7201 = 16#1C21	202A/2	5FB9/56	85/01/02	107
dP2	Fault code on fault n-2	7202 = 16#1C22	202A/3	5FB9/57	85/01/03	109
dP3	Fault code on fault n-3	7203 = 16#1C23	202A/4	5FB9/58	85/01/04	111
dP4	Fault code on fault n-4	7204 = 16#1C24	202A/5	5FB9/59	85/01/05	113
dP5	Fault code on fault n-5	7205 = 16#1C25	202A/6	5FB9/5A	85/01/06	115
dP6	Fault code on fault n-6	7206 = 16#1C26	202A/7	5FB9/5B	85/01/07	117
dP7	Fault code on fault n-7	7207 = 16#1C27	202A/8	5FB9/5C	85/01/08	119
dP8	Fault code on fault n-8	7208 = 16#1C28	202A/9	5FB9/5D	85/01/09	121
drC0	Reference channel active on last fault	64310 = 16#FB36	2265/B	5FBA/11	9A/01/11	106
drC1	Active reference channel on fault n-1	64311 = 16#FB37	2265/C	5FBA/12	9A/01/12	109
drC2	Active reference channel on fault n-2	64312 = 16#FB38	2265/D	5FBA/13	9A/01/13	111
drC3	Active reference channel on fault n-3	64313 = 16#FB39	2265/E	5FBA/14	9A/01/14	113
drC4	Active reference channel on fault n-4	64314 = 16#FB3A	2265/F	5FBA/15	9A/01/15	115
drC5	Active reference channel on fault n-5	64315 = 16#FB3B	2265/10	5FBA/16	9A/01/16	117
drC6	Active reference channel on fault n-6	64316 = 16#FB3C	2265/11	5FBA/17	9A/01/17	119
drC7	Active reference channel on fault n-7	64317 = 16#FB3D	2265/12	5FBA/18	9A/01/18	121
drC8	Active reference channel on fault n-8	64318 = 16#FB3E	2265/13	5FBA/19	9A/01/19	123
dSF	[Deceleration type]	12505 = 16#30D9	205F/6	5FBD/39	9F/01/6A	249
dSI	[Speed assignment]	11521 = 16#2D01	2055/16	5FBD/0F	9A/01/7A	216
dSP	[Speed assignment]	11502 = 16#2CEE	2055/3	5FBD/0B	9A/01/67	215
dtF	[Decrease ref. speed]	12211 = 16#2FB3	205C/C	5FBF/86	9E/01/0C	256
EbO	[End reel]	12213 = 16#2FB5	205C/E	5FBD/27	9E/01/0E	255
EbOt	Current bobbin time	12209 = 16#2FB1	205C/A	5FB9/EB	9E/01/0A	93
ECC	[Encoder coupling]	5607 = 16#15E7	201A/8	5FBC/74	7D/01/08	276
ECt	[Encoder check time]	5609 = 16#15E9	201A/A	5FBF/07	7D/01/0A	276

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
EFI	[Freq. signal filter]	13312 = 16#3400	2067/D	5FBD/5E	A3/01/71	178
EFr	[Freq. max value]	13311 = 16#33FF	2067/C	5FBD/5D	A3/01/70	178
EIL	[Freq. min value]	13310 = 16#33FE	2067/B	5FBD/5C	A3/01/6F	178
EnA	[ENA system]	12101 = 16#2F45	205B/2	5FBD/23	9D/01/66	161
EnC	[Encoder check]	5605 = 16#15E5	201A/6	5FB3/7F	7D/01/06	160
EnC	[Encoder check]	5605 = 16#15E5	201A/6	5FB3/7F	7D/01/06	177
EnS	[Encoder type]	5608 = 16#15E8	201A/A	5FBC/75	7D/01/09	159
EnS	[Encoder type]	5608 = 16#15E8	201A/9	5FBC/75	7D/01/09	177
EnU	[Encoder usage]	5606 = 16#15E6	201A/7	5FBC/73	7D/01/07	160
EnU	[Encoder usage]	5606 = 16#15E6	201A/7	5FBC/73	7D/01/07	177
EP0	Status word on last fault	7210 = 16#1C2A	202A/B	5FB9/5E	85/01/0B	105
EP1	Status word on fault n-1	7211 = 16#1C2B	202A/C	5FB9/5F	85/01/0C	108
EP2	Status word on fault n-2	7212 = 16#1C2C	202A/D	5FB9/60	85/01/0D	110
EP3	Status word on fault n-3	7213 = 16#1C2D	202A/E	5FB9/61	85/01/0E	112
EP4	Status word on fault n-4	7214 = 16#1C2E	202A/F	5FB9/62	85/01/0F	114
EP5	Status word on fault n-5	7215 = 16#1C2F	202A/10	5FB9/63	85/01/10	116
EP6	Status word on fault n-6	7216 = 16#1C30	202A/11	5FB9/64	85/01/11	118
EP7	Status word on fault n-7	7217 = 16#1C31	202A/12	5FB9/65	85/01/12	120
EP8	Status word on fault n-8	7218 = 16#1C32	202A/13	5FB9/66	85/01/13	122
EPL	[External fault mgt]	7006 = 16#1B5E	2028/7	5FB3/C2	84/01/07	269
Errd	DSP402 fault code	8606 = 16#219E	603F	5FB9/D7	8C/01/07	99
ETA	Status word	8603 = 16#219B	6041	6041	71/01/02	79
EtF	[External fault ass.]	7131 = 16#1BDB	2029/20	5FBC/99	84/01/84	269
ETI	Extended status word 0	3206 = 16#0C86	2002/7	5FB9/08	71/01/07	81
F1	[F1]	12404 = 16#3074	205E/5	5FBD/2C	9F/01/05	151
F2	[F2]	12406 = 16#3076	205E/7	5FBD/2E	9F/01/07	151
F2d	[Freq. threshold 2]	11004 = 16#2AFC	2050/5	5FB3/E6	98/01/05	147
F3	[F3]	12408 = 16#3078	205E/9	5FBD/30	9F/01/09	152
F4	[F4]	12410 = 16#307A	205E/B	5FBD/32	9F/01/0B	152
F5	[F5]	12412 = 16#307C	205E/D	5FBD/34	9F/01/0D	152
FCP	[Freq. Const Power]	14203 = 16#377B	2070/4	5FBD/82	A8/01/04	153

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
FCS	Restore configuration	8002 = 16#1F42	2032/3	5FBC/9B	9C/01/9B	69
Fdrv	Ethernet fault code	64233 = 16#FAE9	2264/22	-	-	103
FLI	[Fluxing assignment]	13901 = 16#364D	206D/2	5FBD/79	A6/01/66	219
FLr	[Catch on the fly]	3110 = 16#C26	2001/B	5FB3/7A	70/01/6F	263
FLU	[Motor fluxing]	13902 = 16#364E	206D/3	5FB4/05	A6/01/67	146
FLU	[Motor fluxing]	13902 = 16#364E	206D/3	5FB4/05	A6/01/67	219
FN1	[F1 key assignment]	13501 = 16#34BD	2069/2	5FBD/60	A4/01/66	196
FN2	[F2 key assignment]	13502 = 16#34BE	2069/3	5FBD/61	A4/01/67	196
FN3	[F3 key assignment]	13503 = 16#34BF	2069/4	5FBD/62	A4/01/68	196
FN4	[F4 key assignment]	13504 = 16#34C0	2069/5	5FBD/63	A4/01/69	197
Fnb	Fault counter	7393 = 16#1CE1	202B/5E	5FB9/CC	99/01/CC	104
FPI	[Speed ref. assign.]	11950 = 16#2EAE	2059/33	5FBD/1F	9C/01/97	234
Fr1	[Ref.1 channel]	8413 = 16#20DD	2036/E	5FBC/A4	8B/01/0E	193
Fr1b	[Ref.1B channel]	8415 = 16#20DF	2036/10	5FBC/A6	8B/01/10	198
Fr2	[Ref.2 channel]	8414 = 16#20DE	2036/F	5FBC/A5	8B/01/0F	195
FrH	Frequency reference before ramp	3203 = 16#0C83	2002/4	5FB9/05	71/01/04	89
FrHd	Speed reference before ramp	8605 = 16#219D	2038/6	5FB9/D6	8C/01/06	89
FrO	Frequency reference after ramp	9021 = 16#233D	203C/16	5FB9/D9	8E/01/16	89
FrOd	Speed reference after ramp	8641 = 16#21C1	6043	5FB9/D8	8C/01/2A	89
FrS	[Rated motor freq.]	9602 = 16#2582	2042/3	5FB3/D4	91/01/03	149
FrSS	[Nominal freq sync.]	9679 = 16#25CF	2042/50	5FC2/24	91/01/50	159
FrT	[Ramp 2 threshold]	9011 = 16#2333	203C/C	5FB3/D0	8E/01/0C	202
FSt	[Fast stop assign.]	11204 = 16#2BC4	2052/5	5FBD/04	99/01/05	205
Ftd	[Freq. threshold]	11003 = 16#2AFB	2050/4	5FB3/E5	98/01/04	147
GIE	[ENA integral gain]	12104 = 16#2F48	205B/5	5FBF/7D	9D/01/69	161
GPE	[ENA prop.gain]	12103 = 16#2F47	205B/4	5FBF/7C	9D/01/68	161
HSO	[High speed hoisting]	12301 = 16#300D	205D/2	5FBD/29	9E/01/66	228
HSP	[High speed]	3104 = 16#C20	2001/5	5FB3/75	70/01/69	145
Ibr	[Brake release I FW]	10006 = 16#2716	2046/7	5FB3/DF	93/01/07	222
IbrA	[Ibr 4-20 mA loss]	10075 = 16#275B	2046/4C	5FBC/F9	93/01/4C	227
IdA	[Idw]	9652 = 16#25B4	2042/35	5FC2/15	91/01/35	158

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
IdC	[DC inject. level 1]	11210 = 16#2BCA	2052/B	5FB3/EC	99/01/0B	206
IdC	[DC inject. level 1]	11210 = 16#2BCA	2052/B	5FB3/EC	99/01/0B	281
IdC2	[DC inject. level 2]	11212 = 16#2BCC	2052/D	5FBF/54	99/01/0D	207
IdC2	[DC inject. level 2]	11212 = 16#2BCC	2052/D	5FBF/54	99/01/0D	281
IdM	[ldr]	9650 = 16#25B2	2042/33	5FB9/E0	91/01/33	157
IdM	Magnetizing current	9650 = 16#25B2	2042/33	5FB9/E0	91/01/33	93
IL1r	Logic input map	5202 = 16#1452	2016/3	5FB9/28	7B/01/03	95
ILF1	Option card 1 fault code	7134 = 16#1BDE	2029/23	5FB0/98	84/01/87	103
ILF2	Option card 2 fault code	7135 = 16#1BDF	2029/24	5FB0/99	84/01/88	103
Inh	[Fault inhibit assign.]	7125 = 16#1BD5	2029/1A	5FBC/98	84/01/7E	274
Inr	[Ramp increment]	9020 = 16#233C	203C/15	5FBF/26	8E/01/15	201
Int	[Torque unit]	9260 = 16#242C	203E/3D	5FBF/35	8F/01/3D	238
InV	Rated drive current	3017 = 16#0BC9	2000/12	5FB0/07	70/01/12	124
IP0	Extended status word 0 on last fault	7220 = 16#1C34	202A/15	5FB9/67	85/01/15	106
IP1	Extended status word on fault n-1	7221 = 16#1C35	202A/16	5FB9/68	85/01/16	108
IP2	Extended status word on fault n-2	7222 = 16#1C36	202A/17	5FB9/69	85/01/17	110
IP3	Extended status word on fault n-3	7223 = 16#1C37	202A/18	5FB9/6A	85/01/18	112
IP4	Extended status word on fault n-4	7224 = 16#1C38	202A/19	5FB9/6B	85/01/19	114
IP5	Extended status word on fault n-5	7225 = 16#1C39	202A/1A	5FB9/6C	85/01/1A	116
IP6	Extended status word on fault n-6	7226 = 16#1C3A	202A/1B	5FB9/6D	85/01/1B	118
IP7	Extended status word on fault n-7	7227 = 16#1C3B	202A/1C	5FB9/6E	85/01/1C	120
IP8	Extended status word on fault n-8	7228 = 16#1C3C	202A/1D	5FB9/6F	85/01/1D	122
IPL	[Input phase loss]	7002 = 16#1B5A	2028/3	5FB3/BE	84/01/03	266
Ird	[Brake release l Rev]	10011 = 16#271B	2046/C	5FBF/3D	93/01/0C	222
ItH	[Mot. therm. current]	9622 = 16#2596	2042/17	5FB3/DB	2A/01/0A	145
JdC	[Jump at reversal]	10013 = 16#271D	2046/E	5FBF/3F	93/01/0E	223
JGF	[Jog frequency]	11111 = 16#2B67	2051/C	5FBF/50	98/01/70	209
JGT	[Jog delay]	11112 = 16#2B68	2051/D	5FB3/EA	98/01/71	209
JOG	[JOG]	11110 = 16#2B66	2051/B	5FBC/FF	98/01/6F	209
L10d	[LI10 On Delay]	4010 = 16#FAA	200A/B	5FBC/17	75/01/0B	167
L11d	[LI11 On Delay]	4011 = 16#FAB	200A/C	5FBC/18	75/01/0C	167

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
L12d	[LI12 On Delay]	4012 = 16#FAC	200A/D	5FBC/19	75/01/0D	167
L13d	[LI13 On Delay]	4013 = 16#FAD	200A/E	5FBC/1A	75/01/0E	167
L14d	[LI14 On Delay]	4014 = 16#FAE	200A/F	5FBC/1B	75/01/0F	167
L1d	[LI1 On Delay]	4001 = 16#FA1	200A/2	5FBC/0E	75/01/02	167
L2d	[LI2 On Delay]	4002 = 16#FA2	200A/3	5FBC/0F	75/01/03	167
L3d	[LI3 On Delay]	4003 = 16#FA3	200A/4	5FBC/10	75/01/04	167
L4d	[LI4 On Delay]	4004 = 16#FA4	200A/5	5FBC/11	75/01/05	167
L5d	[LI5 On Delay]	4005 = 16#FA5	200A/6	5FBC/12	75/01/06	167
L6d	[LI6 On Delay]	4006 = 16#FA6	200A/7	5FBC/13	75/01/07	167
L7d	[LI7 On Delay]	4007 = 16#FA7	200A/8	5FBC/14	75/01/08	167
L8d	[LI8 On Delay]	4008 = 16#FA8	200A/9	5FBC/15	75/01/09	167
L9d	[LI9 On Delay]	4009 = 16#FA9	200A/A	5FBC/16	75/01/0A	167
LAF	[Stop FW limit sw.]	11601 = 16#2D51	2056/2	5FBD/10	9B/01/02	220
LAr	[Stop RV limit sw.]	11602 = 16#2D52	2056/3	5FBD/11	9B/01/03	220
LAS	[Stop type]	11603 = 16#2D53	2056/4	5FBD/12	9B/01/04	220
LbA	[Load sharing]	14301 = 16#37DD	2071/2	5FBD/83	A8/01/66	164
LbC	[Load correction]	14302 = 16#37DE	2071/3	5FBF/D5	A8/01/67	164
LbC1	[Correction min spd]	14303 = 16#37DF	2071/4	5FBF/D6	A8/01/68	165
LbC2	[Correction max spd]	14304 = 16#37E0	2071/5	5FBF/D7	A8/01/69	165
LbC3	[Torque offset]	14305 = 16#37E1	2071/6	5FBF/D8	A8/01/6A	165
LbF	[Sharing filter]	14306 = 16#37E2	2071/7	5FBF/D9	A8/01/6B	165
LC2	[Current limit 2]	9202 = 16#23F2	203E/3	5FBC/B5	8F/01/03	243
LCP0	Motor current on last fault	7240 = 16#1C48	202A/29	5FB9/79	85/01/29	105
LCP1	Motor current on fault n-1	7241 = 16#1C49	202A/2A	5FB9/7A	85/01/2A	108
LCP2	Motor current on fault n-2	7242 = 16#1C4A	202A/2B	5FB9/7B	85/01/2B	110
LCP3	Motor current on fault n-3	7243 = 16#1C4B	202A/2C	5FB9/7C	85/01/2C	112
LCP4	Motor current on fault n-4	7244 = 16#1C4C	202A/2D	5FB9/7D	85/01/2D	114
LCP5	Motor current on fault n-5	7245 = 16#1C4D	202A/2E	5FB9/7E	85/01/2E	116
LCP6	Motor current on fault n-6	7246 = 16#1C4E	202A/2F	5FB9/7F	85/01/2F	118
LCP7	Motor current on fault n-7	7247 = 16#1C4F	202A/30	5FB9/80	85/01/30	120
LCP8	Motor current on fault n-8	7248 = 16#1C50	202A/31	5FB9/81	85/01/31	122

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
LCr	Motor current	3204 = 16#0C84	2002/5	5FB9/06	2A/01/09	88
LCt	[Mains V. time out]	13603 = 16#3523	206A/4	5FBD/6F	A5/01/04	244
LdS	[Autotune L d-axis]	9674 = 16#25CA	2042/4B	5FC2/22	91/01/4B	154
LES	[Drive lock]	13601 = 16#3521	206A/2	5FBD/6D	A5/01/02	244
LFA	[Lfw]	9662 = 16#25BE	2042/3F	5FC2/19	91/01/3F	158
LFF	[Fallback speed]	7080 = 16#1BA8	2028/51	5FB3/C5	84/01/51	279
LFL2	[AI2 4-20mA loss]	7003 = 16#1B5B	2028/4	5FB3/BF	84/01/04	273
LFL3	[AI3 4-20mA loss]	7013 = 16#1B65	2028/E	5FB3/C3	84/01/0E	273
LFL4	[AI4 4-20mA loss]	7014 = 16#1B66	2028/F	5FB3/C4	84/01/0F	273
LFM	[Lfr]	9660 = 16#25BC	2042/3D	5FB9/E1	91/01/3D	157
LFM	Leakage inductance	9660 = 16#25BC	2042/3D	5FB9/E1	91/01/3D	94
LFR	Frequency reference	8502 = 16#2136	2037/3	5FB6/1C	8B/01/67	77
LFRD	Speed reference	8602 = 16#219A	6042	6042	2A/01/08	77
LFt	Altivar fault code	7121 = 16#1BD1	2029/16	5FB3/C8	84/01/7A	100
LLC	[Line contactor ass.]	13602 = 16#3522	206A/3	5FBD/6E	A5/01/03	244
LO1	[LO1 assignment]	5009 = 16#1391	2014/A	5FB3/7D	7A/01/0A	184
LO1d	[LO1 delay time]	4249 = 16#1099	200C/32	5FBC/30	76/01/32	184
LO1H	[LO1 holding time]	4229 = 16#1085	200C/1E	5FBC/28	76/01/1E	184
LO1S	[LO1 active at]	4209 = 16#1071	200C/2D	5FBC/20	76/01/0A	184
LO2	[LO2 assignment]	5010 = 16#1392	2014/B	5FBC/6A	7A/01/0B	185
LO2d	[LO2 delay time]	4250 = 16#109A	200C/33	5FBC/31	76/01/33	185
LO2H	[LO2 holding time]	4230 = 16#1086	200C/1F	5FBC/29	76/01/1F	185
LO2S	[LO2 active at]	4210 = 16#1072	200C/B	5FBC/21	76/01/0B	185
LO3	[LO3 assignment]	5011 = 16#1393	2014/C	5FBC/6B	7A/01/0C	186
LO3d	[LO3 delay time]	4251 = 16#109B	200C/34	5FBC/32	76/01/34	186
LO3H	[LO3 holding time]	4231 = 16#1087	200C/20	5FBC/2A	76/01/20	186
LO3S	[LO3 active at]	4211 = 16#1073	200C/C	5FBC/22	76/01/0C	186
LO4	[LO4 assignment]	5012 = 16#1394	2014/D	5FBC/6C	7A/01/0D	187
LO4d	[LO4 delay time]	4252 = 16#109C	200C/35	5FBC/33	76/01/35	187
LO4H	[LO4 holding time]	4232 = 16#1088	200C/21	5FBC/2B	76/01/21	187
LO4S	[LO4 active at]	4212 = 16#1074	200C/D	5FBC/23	76/01/0D	187

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
LP1	[Point 1 X]	10071 = 16#2757	2046/48	5FBC/F5	93/01/48	226
LP2	[Point 2 X]	10073 = 16#2759	2046/4A	5FBC/F7	93/01/4A	226
LqS	[Autotune L q-axis]	9675 = 16#25CB	2042/4C	5FC2/23	91/01/4C	155
LRS1	Extended status word 1	3250 = 16#0CB2	2002/33	5FB9/1C	71/01/33	81
LRS2	Extended status word 2	3251 = 16#0CB3	2002/34	5FB9/1D	71/01/34	82
LRS3	Extended status word 3	3252 = 16#0CB4	2002/35	5FB9/1E	71/01/35	82
LRS4	Extended status word 4	3253 = 16#0CB5	2002/36	5FB9/1F	71/01/36	83
LRS5	Extended status word 5	3254 = 16#0CB6	2002/37	5FB9/20	71/01/37	83
LRS6	Extended status word 6	3255 = 16#0CB7	2002/38	5FB9/21	71/01/38	84
LRS7	Extended status word 7	3256 = 16#0CB8	2002/39	5FB9/22	71/01/39	84
LRS8	Extended status word 8	3257 = 16#0C89	2002/3A	5FB9/23	71/01/3A	85
LSP	[Low speed]	3105 = 16#C21	2001/6	5FB3/76	70/01/6A	145
LtCr	Torque reference (Nm)	9261 = 16#242D	203E/3E	5FB6/3D	2A/01/0C	77
LTR	Torque reference	8505 = 16#2139	6071	6071	8B/01/6A	77
MA2	[Multiplier ref. 2]	11821 = 16#2E2D	2058/16	5FBD/17	9C/01/16	200
MA3	[Multiplier ref. 3]	11822 = 16#2E2E	2058/17	5FBD/18	9C/01/17	200
Md0	Date on last fault	7300 = 16#1C84	202B/1	5FB9/AF	85/01/65	107
Md1	Date on fault n-1	7301 = 16#1C85	202B/2	5FB9/B0	85/01/66	109
Md2	Date on fault n-2	7302 = 16#1C86	202B/3	5FB9/B1	85/01/67	111
Md3	Date on fault n-3	7303 = 16#1C87	202B/4	5FB9/B2	85/01/68	113
Md4	Date on fault n-4	7304 = 16#1C88	202B/5	5FB9/B3	85/01/69	115
Md5	Date on fault n-5	7305 = 16#1C89	202B/6	5FB9/B4	85/01/6A	117
Md6	Date on fault n-6	7306 = 16#1C8A	202B/7	5FB9/BE	85/01/6B	119
Md7	Date on fault n-7	7307 = 16#1C8B	202B/8	5FB9/B6	85/01/6C	121
Md8	Date on fault n-8	7308 = 16#1C8C	202B/9	5FB9/B7	85/01/6D	123
MFr	Multiplying coefficient	11831 = 16#2E37	2058/20	5FB6/3E	9C/01/20	78
nC1	Communication scanner, value of write word 1	12761 = 16#31D9	2061/3E	5FB6/4B	A0/01/A2	126
nC2	Communication scanner, value of write word 2	12762 = 16#31DA	2061/3F	5FB6/4C	A0/01/A3	126
nC3	Communication scanner, value of write word 3	12763 = 16#31DB	2061/40	5FB6/4D	A0/01/A4	126

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
nC4	Communication scanner, value of write word 4	12764 = 16#31DC	2061/41	5FB6/4E	A0/01/A5	126
nC5	Communication scanner, value of write word 5	12765 = 16#31DD	2061/42	5FB6/4F	A0/01/A6	126
nC6	Communication scanner, value of write word 6	12766 = 16#31DE	2061/43	5FB6/50	A0/01/A7	126
nC7	Communication scanner, value of write word 7	12767 = 16#31DF	2061/44	5FB6/51	A0/01/A8	126
nC8	Communication scanner, value of write word 8	12768 = 16#31E0	2061/45	5FB6/52	A0/01/A9	127
nCA1	Communication scanner, address of write word 1	12721 = 16#31B1	2061/16	5FBD/46	A0/01/7A	128
nCA2	Communication scanner, address of write word 2	12722 = 16#31B2	2061/17	5FBD/47	A0/01/7B	128
nCA3	Communication scanner, address of write word 3	12723 = 16#31B3	2061/18	5FBD/48	A0/01/7C	128
nCA4	Communication scanner, address of write word 4	12724 = 16#31B4	2061/19	5FBD/49	A0/01/7D	128
nCA5	Communication scanner, address of write word 5	12725 = 16#31B5	2061/1A	5FBD/4A	A0/01/7E	128
nCA6	Communication scanner, address of write word 6	12726 = 16#31B6	2061/1B	5FBD/4B	A0/01/7F	129
nCA7	Communication scanner, address of write word 7	12727 = 16#31B7	2061/1C	5FBD/4C	A0/01/80	129
nCA8	Communication scanner, address of write word 8	12728 = 16#31B8	2061/1D	5FBD/4D	A0/01/81	129
nCr	[Rated mot. current]	9603 = 16#2583	2042/4	5FB3/D5	91/01/04	148
nCrS	[Nominal I sync.]	9670 = 16#25C6	2042/47	5FC2/1E	91/01/47	154
nCV	Drive nominal rating	3011 = 16#0BC3	2000/C	5FB0/02	70/01/0C	124
nM1	Communication scanner, value of read word 1	12741 = 16#31C5	2061/2A	5FB6/43	A0/01/8E	127
nM2	Communication scanner, value of read word 2	12742 = 16#31C6	2061/2B	5FB6/44	A0/01/8F	127
nM3	Communication scanner, value of read word 3	12743 = 16#31C7	2061/2C	5FB6/45	A0/01/90	127
nM4	Communication scanner, value of read word 4	12744 = 16#31C8	2061/2D	5FB6/46	A0/01/91	127
nM5	Communication scanner, value of read word 5	12745 = 16#31C9	2061/2E	5FB6/47	A0/01/92	127
nM6	Communication scanner, value of read word 6	12746 = 16#31CA	2061/2F	5FB6/48	A0/01/93	127

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
nM7	Communication scanner, value of read word 7	12747 = 16#31CB	2061/30	5FB6/49	A0/01/94	128
nM8	Communication scanner, value of read word 8	12748 = 16#31CC	2061/31	5FB6/4A	A0/01/95	128
nMA1	Communication scanner, address of read word 1	12701 = 16#319D	2061/2	5FBD/3E	A0/01/66	129
nMA2	Communication scanner, address of read word 2	12702 = 16#319E	2061/3	5FBD/3F	A0/01/67	129
nMA3	Communication scanner, address of read word 3	12703 = 16#319F	2061/4	5FBD/40	A0/01/68	129
nMA4	Communication scanner, address of read word 4	12704 = 16#31A0	2061/5	5FBD/41	A0/01/69	129
nMA5	Communication scanner, address of read word 5	12705 = 16#31A1	2061/6	5FBD/42	A0/01/6A	130
nMA6	Communication scanner, address of read word 6	12706 = 16#31A2	2061/7	5FBD/43	A0/01/6B	130
nMA7	Communication scanner, address of read word 7	12707 = 16#31A3	2061/8	5FBD/44	A0/01/6C	130
nMA8	Communication scanner, address of read word 8	12708 = 16#31A4	2061/9	5FBD/45	A0/01/6D	130
nPr	[Rated motor power]	9613 = 16#258D	2042/E	5FC2/0A	91/01/0E	148
nd	[Noise reduction]	3107 = 16#C23	2001/8	5FB3/78	70/01/6C	162
nSL	[Nominal motor slip]	9605 = 16#2585	2042/6	5FB9/DD	91/01/06	157
nSP	[Nom motor speed]	9604 = 16#2584	2042/5	5FB3/D6	91/01/05	149
nSPS	[Nom motor spdsync]	9671 = 16#25C7	2042/48	5FC2/1F	91/01/48	154
nSt	[Freewheel stop ass.]	11202 = 16#2BC2	2052/3	5FBD/02	99/01/03	205
O01	"Controller Inside" HMI exchange word 1	6401 = 16#1901	2022/2	5FB3/81	81/01/02	132
O02	"Controller Inside" HMI exchange word 2	6402 = 16#1902	2022/3	5FB3/82	81/01/03	132
O03	"Controller Inside" HMI exchange word 3	6403 = 16#1903	2022/4	5FB3/83	81/01/04	132
O04	"Controller Inside" HMI exchange word 4	6404 = 16#1904	2022/5	5FB3/84	81/01/05	132
O05	"Controller Inside" HMI exchange word 5	6405 = 16#1905	2022/6	5FB3/85	81/01/06	133
O06	"Controller Inside" HMI exchange word 6	6406 = 16#1906	2022/7	5FB3/86	81/01/07	133
O07	"Controller Inside" HMI exchange word 7	6407 = 16#1907	2022/8	5FB3/87	81/01/08	133

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
O08	"Controller Inside" HMI exchange word 8	6408 = 16#1908	2022/9	5FB3/88	81/01/09	133
O09	"Controller Inside" HMI exchange word 9	6409 = 16#1909	2022/A	5FB3/89	81/01/0A	133
O10	"Controller Inside" HMI exchange word 10	6410 = 16#190A	2022/B	5FB3/8A	81/01/0B	133
O11	"Controller Inside" HMI exchange word 11	6411 = 16#190B	2022/C	5FB3/8B	81/01/0C	133
O12	"Controller Inside" HMI exchange word 12	6412 = 16#190C	2022/D	5FB3/8C	81/01/0D	134
O13	"Controller Inside" HMI exchange word 13	6413 = 16#190D	2022/E	5FB3/8D	81/01/0E	134
O14	"Controller Inside" HMI exchange word 14	6414 = 16#190E	2022/F	5FB3/8E	81/01/0F	134
O15	"Controller Inside" HMI exchange word 15	6415 = 16#190F	2022/10	5FB3/8F	81/01/10	134
O16	"Controller Inside" HMI exchange word 16	6416 = 16#1910	2022/11	5FB3/90	81/01/11	134
O17	"Controller Inside" HMI exchange word 17	6417 = 16#1911	2022/12	5FB3/91	81/01/12	134
O18	"Controller Inside" HMI exchange word 18	6418 = 16#1912	2022/13	5FB3/92	81/01/13	134
O19	"Controller Inside" HMI exchange word 19	6419 = 16#1913	2022/14	5FB3/93	81/01/14	135
O20	"Controller Inside" HMI exchange word 20	6420 = 16#1914	2022/15	5FB3/94	81/01/15	135
O21	"Controller Inside" HMI exchange word 21	6421 = 16#1915	2022/16	5FB3/95	81/01/16	135
O22	"Controller Inside" HMI exchange word 22	6422 = 16#1916	2022/17	5FB3/96	81/01/17	135
O23	"Controller Inside" HMI exchange word 23	6423 = 16#1917	2022/18	5FB3/97	81/01/18	135
O24	"Controller Inside" HMI exchange word 24	6424 = 16#1918	2022/19	5FB3/98	81/01/19	135
O25	"Controller Inside" HMI exchange word 25	6425 = 16#1919	2022/1A	5FB3/99	81/01/1A	135
O26	"Controller Inside" HMI exchange word 26	6426 = 16#191A	2022/1B	5FB3/9A	81/01/1B	136
O27	"Controller Inside" HMI exchange word 27	6427 = 16#191B	2022/1C	5FB3/9B	81/01/1C	136
O28	"Controller Inside" HMI exchange word 28	6428 = 16#191C	2022/1D	5FB3/9C	81/01/1D	136

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
O29	"Controller Inside" HMI exchange word 29	6429 = 16#191D	2022/1E	5FB3/9D	81/01/1E	136
O30	"Controller Inside" HMI exchange word 30	6430 = 16#191E	2022/1F	5FB3/9E	81/01/1F	136
O31	"Controller Inside" HMI exchange word 31	6431 = 16#191F	2022/20	5FB3/9F	81/01/20	136
O32	"Controller Inside" HMI exchange word 32	6432 = 16#1920	2022/21	5FB3/A0	81/01/21	136
O33	"Controller Inside" HMI exchange word 33	6433 = 16#1921	2022/22	5FB3/A1	81/01/22	137
O34	"Controller Inside" HMI exchange word 34	6434 = 16#1922	2022/23	5FB3/A2	81/01/23	137
O35	"Controller Inside" HMI exchange word 35	6435 = 16#1923	2022/24	5FB3/A3	81/01/24	137
O36	"Controller Inside" HMI exchange word 36	6436 = 16#1924	2022/25	5FB3/A4	81/01/25	137
O37	"Controller Inside" HMI exchange word 37	6437 = 16#1925	2022/26	5FB3/A5	81/01/26	137
O38	"Controller Inside" HMI exchange word 38	6438 = 16#1926	2022/27	5FB3/A6	81/01/27	137
O39	"Controller Inside" HMI exchange word 39	6439 = 16#1927	2022/28	5FB3/A7	81/01/28	137
O40	"Controller Inside" HMI exchange word 40	6440 = 16#1928	2022/29	5FB3/A8	81/01/29	138
O41	"Controller Inside" HMI exchange word 41	6441 = 16#1929	2022/2A	5FB3/A9	81/01/2A	138
O42	"Controller Inside" HMI exchange word 42	6442 = 16#192A	2022/2B	5FB3/AA	81/01/2B	138
O43	"Controller Inside" HMI exchange word 43	6443 = 16#192B	2022/2C	5FB3/AB	81/01/2C	138
O44	"Controller Inside" HMI exchange word 44	6444 = 16#192C	2022/2D	5FB3/AC	81/01/2D	138
O45	"Controller Inside" HMI exchange word 45	6445 = 16#192D	2022/2E	5FB3/AD	81/01/2E	138
O46	"Controller Inside" HMI exchange word 46	6446 = 16#192E	2022/2F	5FB3/AE	81/01/2F	138
O47	"Controller Inside" HMI exchange word 47	6447 = 16#192F	2022/30	5FB3/AF	81/01/30	139
O48	"Controller Inside" HMI exchange word 48	6448 = 16#1930	2022/31	5FB3/B0	81/01/31	139
O49	"Controller Inside" HMI exchange word 49	6449 = 16#1931	2022/32	5FB3/B1	81/01/32	139

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
O50	"Controller Inside" HMI exchange word 50	6450 = 16#1932	2022/33	5FC8/32	81/01/33	139
OCC	[Out. contactor ass.]	13104 = 16#3330	2065/5	5FBD/57	A2/01/69	245
Odt	[OutPh time detect]	7081 = 16#1BA9	2028/52	5FBF/08	84/01/52	266
OFI	[Sinus filter]	3109 = 16#C25	2001/A	5FBC/0B	70/01/6E	162
OHL	[Overtemp fault mgt]	7008 = 16#1B60	2028/9	5FBC/8C	84/01/09	267
OL1r	Logic output map	5212 = 16#145C	2016/D	5FB9/2A	7B/01/0D	95
OLL	[Overload fault mgt]	7009 = 16#1B61	2028/A	5FBC/8D	84/01/0A	265
OPL	[Output Phase Loss]	9611 = 16#258B	2042/C	5FB3/D8	91/01/0C	266
OPr	Motor power	3211 = 16#0C8B	2002/C	5FB9/0C	71/01/0C	88
OSP	[Measurement spd]	12305 = 16#3011	205D/6	5FBF/8A	9E/01/6A	228
Otr	Output torque	3205 = 16#0C85	6077	6077	71/01/06	87
Otrn	Output torque (Nm)	3216 = 16#0C90	2002/11	5FB9/10	2A/01/0B	88
PAH	[Max fbk alarm]	11962 = 16#2EBA	2059/3F	5FB3/FF	9C/01/A3	233
PAI1	Physical image of analog input 1	6942 = 16#1B1E	2027/2B	5FB9/4B	83/01/8F	131
PAI2	Physical image of analog input 2	6943 = 16#1B1F	2027/2C	5FB9/4C	83/01/90	131
PAL	[Min fbk alarm]	11961 = 16#2EB9	2059/3E	5FB3/FE	9C/01/A2	233
PAn0	Device name: characters 1 and 2	3340 = 16#0D0C	2003/29	5FB0/25	71/01/8D	124
PAn1	Device name: characters 3 and 4	3341 = 16#0D0D	2003/2A	5FB0/26	71/01/8E	124
PAn2	Device name: characters 5 and 6	3342 = 16#0D0E	2003/2B	5FB0/27	71/01/8F	125
PAn3	Device name: characters 7 and 8	3343 = 16#0D0F	2003/2C	5FB0/28	71/01/90	125
PAn4	Device name: characters 9 and 10	3344 = 16#0D10	2003/2D	5FB0/29	71/01/91	125
PAn5	Device name: characters 11 and 12	3345 = 16#0D11	2003/2E	5FB0/2A	71/01/92	125
PAn6	Device name: characters 13 and 14	3346 = 16#0D12	2003/2F	5FB0/2B	71/01/93	125
PAn7	Device name: characters 15 and 16	3346 = 16#0D12	2003/30	5FB0/2C	71/01/94	125
PAO1	Physical image of analog output 1	6971 = 16#1B3B	2027/48	5FB9/4D	83/01/AC	131
PAO2	Physical image of analog output 2	6972 = 16#1B3C	2027/49	5FB9/4E	83/01/AD	131
PAS	[Stop type]	12506 = 16#30DA	205F/7	5FBD/3A	9F/01/6B	249
PAU	[Auto/Manual assign.]	11970 = 16#2EC2	2059/47	5FBD/22	9C/01/AB	235
Pdl	Drive encoder counter divisor	5610 = 16#15EA	201A/B	5FBC/76	7D/01/0B	131
PER	[PID error Alarm]	11963 = 16#2EBB	2059/40	5FB4/01	9C/01/A4	233
PES	[Weight sensor ass.]	10070 = 16#2756	2046/47	5FBC/F4	93/01/47	226

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
PFI	[RP filter]	13304 = 16#33F8	2067/5	5FBD/5B	A3/01/69	176
PFr	[RP max value]	13303 = 16#33F7	2067/4	5FBD/5A	A3/01/68	176
PGA	[Reference type]	13301 = 16#33F5	2067/2	5FBD/58	A3/01/66	177
PGI	[Number of pulses]	5604 = 16#15E4	201A/5	5FB3/7E	7D/01/05	159
PGI	[Number of pulses]	5604 = 16#15E4	201A/5	5FB3/7E	7D/01/05	177
PGI	Encoder pulse counter	5604 = 16#15E4	201A/5	5FB3/7E	7D/01/05	98
PHr	[Output Ph rotation]	13401 = 16#3459	2068/2	5FBD/5F	A4/01/02	150
PHS	[Syn. EMF constant]	9673 = 16#25C9	2042/4A	5FC2/21	91/01/4A	154
PIC	[PID correct. reverse]	11940 = 16#2EA4	2059/29	5FB3/F9	9C/01/8D	232
PIF	[PID feedback ass.]	11901 = 16#2E7D	2059/2	5FBD/19	9C/01/66	230
PIF1	[Min PID feedback]	11904 = 16#2E80	2059/5	5FBF/6A	9C/01/69	230
PIF2	[Max PID feedback]	11905 = 16#2E81	2059/6	5FBF/6B	9C/01/6A	230
PII	[Act. internal PID ref.]	11908 = 16#2E84	2059/9	5FBD/1A	9C/01/6D	231
PIL	[RP min value]	13302 = 16#33F6	2067/3	5FBD/59	A3/01/67	176
PIL1	"Controller Inside" logic input map	6901 = 16#1AF5	2027/2	5FB9/49	83/01/66	131
PIM	[Manual reference]	11954 = 16#2EB2	2059/37	5FBD/20	9C/01/9B	235
PIP1	[Min PID reference]	11906 = 16#2E82	2059/7	5FBF/6C	9C/01/6B	231
PIP2	[Max PID reference]	11907 = 16#2E83	2059/8	5FBF/6D	9C/01/6C	231
PIS	[PID integral reset]	11944 = 16#2EA8	2059/2D	5FBD/1E	9C/01/91	234
PISP	PID regulator reference	8503 = 16#2137	2037/4	5FB6/1D	8B/01/68	78
POH	[Max PID output]	11953 = 16#2EB1	2059/36	5FBF/77	9C/01/9A	233
POL	[Min PID output]	11952 = 16#2EB0	2059/35	5FBF/76	9C/01/99	232
POL1	"Controller Inside" logic output map	6911 = 16#1AFF	2027/C	5FB9/4A	83/01/70	131
PPI	[Pairing password]	14001 = 16#36B1	206E/2	5FBD/7A	A7/01/02	279
PPn	[Pr]	9618 = 16#2592	2042/13	5FB3/DA	91/01/13	158
PPnS	[Pole pairs]	9672 = 16#25C8	2042/49	5FC2/20	91/01/49	154
Pr2	[2 preset PID ref.]	11909 = 16#2E85	2059/A	5FBD/1B	9C/01/6E	236
Pr4	[4 preset PID ref.]	11910 = 16#2E86	2059/B	5FBD/1C	9C/01/6F	236
PrP	[PID ramp]	11984 = 16#2ED0	2059/55	5FBF/7B	9C/01/B9	232
PS16	[16 preset speeds]	11404 = 16#2C8C	2054/5	5FBD/09	9A/01/05	211
PS2	[2 preset speeds]	11401 = 16#2C89	2054/2	5FBD/06	9A/01/02	210

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
PS4	[4 preset speeds]	11402 = 16#2C8A	2054/3	5FBD/07	9D/01/07	210
PS8	[8 preset speeds]	11403 = 16#2C8B	2054/4	5FBD/08	9A/01/04	211
PSr	[Speed input %]	11951 = 16#2EAF	2059/34	5FB3/FD	9C/01/98	234
PSt	[Stop Key priority]	64002 = 16#FA02	2262/3	5FBD/88	94/01/06	193
PtC1	[PTC1 probe]	13201 = 16#3391	2066/2	5FBF/CB	A3/01/02	260
PtC2	[PTC2 probe]	13202 = 16#3392	2066/3	5FBF/CC	A3/01/03	260
PtCL	[LI6 = PTC probe]	13203 = 16#3396	2066/4	5FBF/CD	A3/01/04	260
PtH	Total drive operating time	3233 = 16#0CA1	2002/22	5FB9/18	71/01/22	93
PUC	Drive encoder divided counter	5611 = 16#15EB	201A/C	5FB9/41	7D/01/0C	132
qSH	[Quick step High]	12204 = 16#2FAC	205C/5	5FBF/81	9E/01/05	254
qSL	[Quick step Low]	12205 = 16#2FAD	205C/6	5FBF/82	9E/01/06	254
r1	[R1 Assignment]	5001 = 16#1389	2014/2	5FB3/7B	7A/01/02	179
r1d	[R1 Delay time]	4241 = 16#1091	200C/2A	5FBC/2C	76/01/2A	180
r1H	[R1 Holding time]	4221 = 16#107D	200C/16	5FBC/24	76/01/16	180
r1S	[R1 Active at]	4201 = 16#1069	200C/2	5FBC/1C	76/01/02	180
r2	[R2 Assignment]	5002 = 16#138A	2014/3	5FB3/7C	7A/01/03	181
r2d	[R2 Delay time]	4242 = 16#1092	200C/2B	5FBC/2D	76/01/2B	181
r2H	[R2 Holding time]	4222 = 16#107E	200C/17	5FBC/25	76/01/17	181
r2S	[R2 Active at]	4202 = 16#106A	200C/3	5FBC/1D	76/01/03	181
r3	[R3 Assignment]	5003 = 16#138B	2014/4	5FBC/67	7A/01/04	182
r3d	[R3 Delay time]	4243 = 16#1093	200C/2C	5FBC/2E	76/01/2C	182
r3H	[R3 Holding time]	4223 = 16#107F	200C/18	5FBC/26	76/01/18	182
r3S	[R3 Active at]	4203 = 16#106B	200C/4	5FBC/1E	76/01/04	182
r4	[R4 Assignment]	5004 = 16#138C	2014/5	5FBC/68	7A/01/05	183
r4d	[R4 Delay time]	4244 = 16#1094	200C/2D	5FBC/2F	76/01/2D	183
r4H	[R4 Holding time]	4224 = 16#1080	200C/19	5FBC/27	76/01/19	183
r4S	[R4 Active at]	4204 = 16#106C	200C/5	5FBC/1F	76/01/05	183
rAP	[Reduction ratio]	12105 = 16#2F49	205B/6	5FBF/7E	9D/01/6A	161
rCA	[Output contact. fdbk]	13103 = 16#332F	2065/4	5FBD/56	A2/01/68	245
rCb	[Ref 1B switching]	8412 = 16#20DC	2036/D	5FBC/A3	8B/01/0D	198
rdG	[PID derivative gain]	11943 = 16#2EA7	2059/2C	5FB3/FC	9C/01/90	232

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
rFC	[Ref. 2 switching]	8411 = 16#20DB	2036/C	5FBC/A2	8B/01/0C	195
rFP0	Output frequency on last fault	7250 = 16#1C52	202A/33	5FB9/82	85/01/33	105
rFP1	Output frequency on fault n-1	7251 = 16#1C53	202A/34	5FB9/83	85/01/34	108
rFP2	Output frequency on fault n-2	7252 = 16#1C54	202A/35	5FB9/84	85/01/35	110
rFP3	Output frequency on fault n-3	7253 = 16#1C55	202A/36	5FB9/85	85/01/36	112
rFP4	Output frequency on fault n-4	7254 = 16#1C56	202A/37	5FB9/86	85/01/37	114
rFP5	Output frequency on fault n-5	7255 = 16#1C57	202A/38	5FB9/87	85/01/38	116
rFP6	Output frequency on fault n-6	7256 = 16#1C58	202A/39	5FB9/88	85/01/39	118
rFP7	Output frequency on fault n-7	7257 = 16#1C59	202A/3A	5FB9/89	85/01/3A	120
rFP8	Output frequency on fault n-8	7258 = 16#1C5A	202A/3B	5FB9/8A	85/01/3B	122
rFr	Output frequency	3202 = 16#C82	2002/3	5FB9/04	71/01/03	87
rFrd	Output speed	8604 = 16#219C	6044	6044	2A/01/07	87
rFt	[Evacuation assign.]	13831 = 16#3607	206C/20	5FBD/76	A6/01/20	258
rIG	[PID integral gain]	11942 = 16#2EA6	2059/2B	5FB3/FB	9C/01/8F	232
rIn	[RV Inhibition]	3108 = 16#C24	2001/9	5FB3/79	70/01/6D	193
rP2	[Preset ref. PID 2]	11921 = 16#2E91	2059/16	5FBF/6F	9C/01/7A	237
rP3	[Preset ref. PID 3]	11922 = 16#2E92	2059/17	5FBF/70	9C/01/7B	237
rP4	[Preset ref. PID 4]	11923 = 16#2E93	2059/18	9F/01/71	9C/01/7C	237
rPC	PID reference after ramp	11982 = 16#2ECE	2059/53	5FB9/E7	9C/01/B7	91
rPE	PID regulator discrepancy	11980 = 16#2ECC	2059/51	5FB9/E5	9C/01/B5	91
rPF	PID regulator feedback reference	11981 = 16#2ECD	2059/52	5FB9/E6	9C/01/B6	91
RPg	[PID prop. gain]	11941 = 16#2EA5	2059/2A	5FB3/FA	9C/01/8E	231
RPI	[Internal PID ref.]	11920 = 16#2E90	2059/15	5FBF/6E	9C/01/79	231
rPO	PID regulator limit output reference	11983 = 16#2ECF	2059/54	5FB9/E8	9C/01/B8	91
rPS	[Ramp switching]	9010 = 16#2332	203C/B	5FBC/B3	8E/01/0B	203
rPt	[Ramp type]	9004 = 16#232C	203C/5	5FB3/CB	8E/01/05	201
rrS	[Reverse assign.]	11105 = 16#2B61	2051/6	5FBC/FE	98/01/6A	166
rSA	[R1w]	9642 = 16#25AA	2042/2B	5FC2/11	91/01/2B	158
rSAS	[Cust. stator R syn]	9682 = 16#25D2	2042/53	5FC2/26	91/01/53	155
rSF	[Fault reset]	7124 = 16#1BD4	2029/19	5FBC/97	84/01/7D	261
rSL	[PID wake up thresh.]	11960 = 16#2DB5	2059/3D	5FBD/21	9C/01/A1	235

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
rSM	[Stator R measured]	9640 = 16#25A8	2042/29	5FB9/DF	91/01/29	157
rSM	Asynchronous motor cold state stator resistance	9640 = 16#25A8	2042/29	5FB9/DF	91/01/29	94
rSMS	[R1rS]	9680 = 16#25D0	2042/51	5FB9/E3	91/01/51	159
rSMS	Synchronous motor cold state stator resistance	9680 = 16#25D0	2042/51	5FB9/E3	91/01/51	94
rSP	[Evacuation freq.]	13833 = 16#3609	206C/22	5FB4/04	A6/01/22	258
rSU	[Evacuation Input V.]	13832 = 16#3608	206C/21	5FBD/77	A6/01/21	258
rtH	Total motor operating time	3231 = 16#0C9F	2002/20	5FB9/16	71/01/20	93
rtHI	Internal motor operating time	3232 = 16#0CA0	2002/21	5FB9/17	71/01/21	93
rtO	[Torque R. time out]	9229 = 16#240D	203E/1E	5FBF/32	8F/01/1E	240
rtP0	Motor operating time on last fault	7260 = 16#1C5C	202A/3D	5FB9/8B	85/01/3D	107
rtP1	Motor operating time on fault n-1	7261 = 16#1C5D	202A/3E	5FB9/8C	85/01/3E	109
rtP2	Motor operating time on fault n-2	7262 = 16#1C5E	202A/3F	5FB9/8D	85/01/3F	111
rtP3	Motor operating time on fault n-3	7263 = 16#1C5F	202A/40	5FB9/8E	85/01/40	113
rtP4	Motor operating time on fault n-4	7264 = 16#1C60	202A/41	5FB9/8F	85/01/41	115
rtP5	Motor operating time on fault n-5	7265 = 16#1C61	202A/42	5FB9/90	85/01/42	117
rtP6	Motor operating time on fault n-6	7266 = 16#1C62	202A/43	5FB9/91	85/01/43	119
rtP7	Motor operating time on fault n-7	7267 = 16#1C63	202A/44	5FB9/92	85/01/44	121
rtP8	Motor operating time on fault n-8	7268 = 16#1C64	202A/45	5FB9/93	85/01/45	123
rtr	[Init. traverse ctrl]	12210 = 16#2FB2	205C/B	5FBD/25	9E/01/0B	257
SA2	[Summing ref. 2]	11801 = 16#2E19	2058/2	5FBD/13	9C/01/02	199
SA3	[Summing ref. 3]	11802 = 16#2E1A	2058/3	5FBD/14	9C/01/03	199
SAF	[Stop FW limit sw.]	12501 = 16#30D5	205F/2	5FBD/35	9F/01/66	247
SAr	[Stop RV limit sw.]	12502 = 16#30D6	205F/3	5FBD/36	9F/01/67	247
SAt	[Thermal alarm stop]	11021 = 16#2B0D	2050/16	5FB3/E7	98/01/16	268
SCL	[I Limit. frequency]	12306 = 16#3012	205D/7	5FBF/8B	9E/01/6B	229
SCS	Save configuration	8001 = 16#1F41	2032/2	5FBC/9A	9C/01/9A	69
SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	204A/4	5FBF/45	95/01/04	208
SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	204A/4	5FBF/45	95/01/04	223
SdC2	[Auto DC inj. level 2]	10405 = 16#28A5	204A/6	5FBF/47	95/01/06	208
Sdd	[Load slip detection]	7005 = 16#1B5D	2028/6	5FB3/C1	84/01/06	276

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
SFC	[K speed loop filter]	9105 = 16#2391	203D/6	5FBF/29	8E/01/6A	145
SFr	[Switching freq.]	3102 = 16#C1E	2001/3	5FBF/02	70/01/67	162
Slt	[Speed time integral]	9104 = 16#2390	203D/5	5FBF/28	8E/01/69	146
SLL	[Modbus fault mgt]	7010 = 16#1B62	2028/B	5FBC/8E	84/01/0B	275
SLP	[Slip compensation]	9625 = 16#2599	2042/1A	5FB3/DC	91/01/1A	156
SnC	[Counter wobble]	12212 = 16#2FB4	205C/D	5FBD/26	9E/01/0D	256
SOP	[Volt surge limit. opt]	12602 = 16#313A	2060/3	5FBD/3D	A0/01/03	163
SP10	[Preset speed 10]	11418 = 16#2C9A	2054/13	5FBF/60	9A/01/13	213
SP11	[Preset speed 11]	11419 = 16#2C9B	2054/14	5FBF/61	9A/01/14	213
SP12	[Preset speed 12]	11420 = 16#2C9C	2054/15	5FBF/62	9A/01/15	213
SP13	[Preset speed 13]	11421 = 16#2C9D	2054/16	9F/01/63	9A/01/16	214
SP14	[Preset speed 14]	11422 = 16#2C9E	2054/17	5FBF/64	9A/01/17	214
SP15	[Preset speed 15]	11423 = 16#2C9F	2054/18	5FBF/65	9A/01/18	214
SP16	[Preset speed 16]	11424 = 16#2CA0	2054/19	5FBF/66	9A/01/19	214
SP2	[Preset speed 2]	11410 = 16#2C92	2054/B	5FB3/F0	9A/01/0B	211
SP3	[Preset speed 3]	11411 = 16#2C93	2054/C	5FB3/F1	9A/01/0C	212
SP4	[Preset speed 4]	11412 = 16#2C94	2054/D	5FB3/F2	9A/01/0D	212
SP5	[Preset speed 5]	11413 = 16#2C95	2054/E	5FB3/F3	9A/01/0E	212
SP6	[Preset speed 6]	11414 = 16#2C96	2054/F	5FB3/F4	9A/01/0F	212
SP7	[Preset speed 7]	11415 = 16#2C97	2054/10	5FB3/F5	9A/01/10	212
SP8	[Preset speed 8]	11416 = 16#2C98	2054/11	5FB3/F6	9A/01/11	213
SP9	[Preset speed 9]	11417 = 16#2C99	2054/12	5FBF/5F	9A/01/12	213
SPAL	Acceleration speed delta	8611 = 16#21A3	6048/1	6048/1	8C/01/0C	140
SPAt	Acceleration time delta	8613 = 16#21A5	6048/2	6048/2	8C/01/0E	140
SPdL	Deceleration speed delta	8614 = 16#21A6	6049/1	6049/1	8C/01/0F	140
SPdt	Deceleration time delta	8616 = 16#21A8	6049/2	6049/2	8C/01/11	141
SPFd	Speed setpoint factor denominator	8643 = 16#21C3	604B/2	604B/2	8C/01/2C	142
SPFn	Speed setpoint factor numerator	8642 = 16#21C2	604B/1	604B/1	8C/01/2B	141
SPG	[Speed prop. gain]	9103 = 16#238F	203D/4	5FB3/D1	8E/01/68	145
SPM	[Ref. memo ass.]	8491 = 16#212B	2036/5C	5FBC/AD	8B/01/5C	218
SPt	[Spin time]	9230 = 16#240E	203E/1F	5FBF/33	8F/01/1F	240

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
SrP	[+/-Speed limitation]	11505 = 16#2CF1	2055/6	5FB3/F7	9A/01/6A	216
SSb	[Trq/I limit. stop]	9240 =16#2418	203E/29	5FBC/BE	8F/01/29	277
StM	[Max stop time]	13814 =16#35EC	206C/F	5FBF/D0	A6/01/0F	271
StO	[Trq/I limit. time out]	9241 =16#2419	203E/2A	5FBF/34	8F/01/2A	277
StP	[UnderV. prevention]	7004 =16#1B5C	2028/5	5FB3/C0	84/01/05	271
Str	[Reference saved]	11503 =16#2CEF	2055/4	5FBD/0C	9A/01/68	215
Strt	[IGBT test]	3112 =16#C28	2001/D	5FBC/0D	70/01/71	272
Stt	[Stop type]	11201 = 16#2BC1	2052/2	5FB3/EB	99/01/02	205
SUL	[Motor surge limit.]	12601 = 16#3139	2060/2	5FBD/3C	A0/01/02	162
tA1	[Begin Acc round]	9005 = 16#232D	203C/6	5FB3/CC	8E/01/06	202
tA2	[End Acc round]	9006 = 16#232E	203C/7	5FB3/CD	8E/01/07	202
tA3	[Begin Dec round]	9007 = 16#232F	203C/8	5FB3/CE	8E/01/08	202
tA4	[End Dec round]	9008 = 16#2330	203C/9	5FB3/CF	8E/01/09	202
tAA	[Torque ref. assign.]	9214 = 16#23FE	203E/F	5FBC/B8	8F/01/0F	242
tAC	IGBT alarm time	3235 = 16#0CA3	2002/24	5FB9/1A	71/01/24	93
tAr	[Max. restart time]	7123 = 16#1BD3	2029/18	5FBC/96	84/01/7C	262
tbE	[Brake engage delay]	10010 = 16#271A	2046/B	5FBF/3C	93/01/0B	223
tbO	[Reel time]	12208 = 16#2FAB	205C/9	5FBF/85	9E/01/09	255
tbS	[DC bus maintain tm]	13812 =16#35F4	206C/D	5FBF/CE	A6/01/0D	271
tCC	[2/3 wire control]	11101 = 16#2B5D	2051/2	5FB3/E8	98/01/66	166
tCt	[2 wire type]	11102 = 16#2B5E	2051/3	5FB3/E9	98/01/67	166
tdC	[DC injection time 2]	11211 = 16#2BCB	2052/C	5FBF/53	99/01/0C	207
tdC	[DC injection time 2]	11211 = 16#2BCB	2052/C	5FBF/53	99/01/0C	281
tdC1	[Auto DC inj. time 1]	10402 = 16#28A2	204A/3	5FBF/44	95/01/03	208
tdC2	[Auto DC inj. time 2]	10404 = 16#28A4	204A/5	5FBF/46	95/01/05	208
tdl	[DC injection time 1]	11213 = 16#2BCD	2052/E	5FBF/55	99/01/0E	206
tdl	[DC injection time 1]	11213 = 16#2BCD	2052/E	5FBF/55	99/01/0E	281
tdn	[Traverse ctrl. decel]	12207 = 16#2FAF	205C/8	5FBF/84	9E/01/08	255
tFr	[Max frequency]	3103 = 16#C1F	2001/4	5FB3/74	70/01/68	149
tHA	[Drv therm. state al]	11009 = 16#2B01	2050/A	5FBF/4E	98/01/0A	267
tHA	[Drv therm. state al]	11009 = 16#2B01	2050/A	5FBF/4E	98/01/0A	268

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
tHb	DBR thermal state	14114 = 16#3722	206F/F	5FBD/7F	A7/01/73	92
tHd	Drive thermal state	3209 = 16#0C89	2002/A	5FB9/0B	71/01/0A	92
tHP0	Motor thermal state on last fault	7280 = 16#1C70	202A/51	5FB9/9D	85/01/51	105
tHP1	Motor thermal state on fault n-1	7281 = 16#1C71	202A/52	5FB9/9E	85/01/52	108
tHP2	Motor thermal state on fault n-2	7282 = 16#1C72	202A/53	5FB9/9F	85/01/53	110
tHP3	Motor thermal state on fault n-3	7283 = 16#1C73	202A/54	5FB9/A0	85/01/54	112
tHP4	Motor thermal state on fault n-4	7284 = 16#1C74	202A/55	5FB9/A1	85/01/55	114
tHP5	Motor thermal state on fault n-5	7285 = 16#1C75	202A/56	5FB9/A2	85/01/56	116
tHP6	Motor thermal state on fault n-6	7286 = 16#1C76	202A/57	5FB9/A3	85/01/57	118
tHP7	Motor thermal state on fault n-7	7287 = 16#1C77	202A/58	5FB9/A4	85/01/58	120
tHP8	Motor thermal state on fault n-8	7288 = 16#1C78	202A/59	5FB9/A5	85/01/59	122
tHr	Motor thermal state	9630 = 16#259E	2042/1F	5FB9/DE	91/01/1F	92
tHt	[Motor protect. type]	9612 = 16#258C	2042/D	5FB3/D9	91/01/0D	264
tIME	Time	7392 = 16#1CE0	202B/5D	5FB9/CB	85/01/C1	132
tLA	[AI torque limit. activ.]	9210 = 16#23FA	203E/B	5FBC/B6	8F/01/0B	241
tLC	[Analog limit. act.]	9213 = 16#23FD	203E/E	5FBC/B7	8F/01/0E	242
tLIG	[Gen. torque lim]	9212 = 16#23FC	203E/D	5FBF/2D	8F/01/0D	241
tLIM	[Motoring torque lim]	9211 = 16#23FB	203E/C	5FBF/2C	8F/01/0C	241
tLS	[Low speed time out]	11701 = 16#2DB5	2057/2	5FB3/F8	9B/01/66	146
tLS	[Low speed time out]	11701 = 16#2DB5	2057/2	5FB3/F8	9B/01/66	235
tnL	[Autotune fault mgt]	7012 = 16#1B64	2028/D	5FBC/90	84/01/0D	279
tOb	[Torq. ctrl fault mgt]	9228 = 16#240C	203E/1D	5FBC/BD	8F/01/1D	240
tOS	[Load measuring tm.]	12307 = 16#3013	205D/8	5FBF/8C	9E/01/6C	228
tr1	[Torque ref. channel]	9221 = 16#2405	203E/16	5FBC/BA	8F/01/16	238
trA	[T2w]	9667 = 16#25C3	2042/44	5FC2/1C	91/01/44	158
trC	[Yarn control]	12201 = 16#2FA9	205C/2	5FBD/24	9E/01/02	254
trH	[Traverse high]	12202 = 16#2FAA	205C/3	5FBF/7F	9E/01/03	254
trL	[Traverse Low]	12203 = 16#2FAB	205C/4	5FBF/80	9E/01/04	254
trM	[T2r]	9665 = 16#25C1	2042/42	5FB9/E2	91/01/42	157
trM	Rotor time constant	9665 = 16#25C1	2042/42	5FB9/E2	91/01/42	94
trO	Torque reference after ramp	9232 = 16#2410	203E/21	5FB9/DC	8F/01/21	90

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
trP	[Torque ramp time]	9226 = 16#240A	203E/1B	5FBF/31	8F/01/1B	239
trr	Torque reference before ramp	9231 = 16#240F	203E/20	5FB9/DB	8F/01/20	90
trt	[Torque ratio]	9225 = 16#2409	203E/1A	5FBF/30	8F/01/1A	239
tSd	[Torque ref. sign]	9222 = 16#2406	203E/17	5FBC/BB	8F/01/17	239
tSM	[UnderV. restart tm]	13813 = 16#35F5	206C/E	5FBF/CF	A6/01/0E	271
tSS	[Trq/spd switching]	9220 = 16#2404	203E/15	5FBC/B9	8F/01/15	238
tSt	[Torque control stop]	9227 = 16#240B	203E/1C	5FBC/BC	8F/01/1C	239
tSY	[Sync. wobble]	12214 = 16#2FB6	205C/F	5FBD/28	9E/01/0F	256
ttd	[Motor therm. level]	11002 = 16#2AFA	2050/3	5FB3/E4	98/01/03	264
ttd	[Motor therm. level]	11002 = 16#2AFA	2050 / 3	5FB3/E4	98/01/03	268
ttd2	[Motor2 therm. level]	11006 = 16#2AFE	2050/7	5FBF/4C	98/01/07	264
ttd2	[Motor2 therm. level]	11006 = 16#2AFE	2050/7	5FBF/4C	98/01/07	268
ttd3	[Motor3 therm. level]	11007 = 16#2AFF	2050/8	5FBF/4D	98/01/08	264
ttd3	[Motor3 therm. level]	11007 = 16#2AFF	2050/8	5FBF/4D	98/01/08	268
ttr	[Time to restart]	10022 = 16#2726	2046/17	5FBF/42	93/01/17	224
tUL	[Auto-tune assign.]	9610 = 16#258A	2042/B	5FBC/EF	91/01/0B	253
tUn	[Auto-tuning]	9608 = 16#2588	2042/9	5FB3/D7	91/01/09	149
tUP	[Traverse ctrl. accel.]	12206 = 16#2FAE	205C/7	5FBF/83	9E/01/07	255
tUS	[Auto tuning status]	9609 = 16#2589	2042/A	5FC2/08	91/01/0A	150
U0	[U0]	12401 = 16#3071	205E/2	5FBD/2A	9F/01/02	150
U1	[U1]	12403 = 16#3073	205E/4	5FBD/2B	9F/01/04	151
U2	[U2]	12405 = 16#3075	205E/6	5FBD/2D	9F/01/06	151
U3	[U3]	12407 = 16#3077	205E/8	5FBD/2F	9F/01/08	151
U4	[U4]	12409 = 16#3079	205E/9	5FBD/31	9F/01/0A	152
U5	[U5]	12411 = 16#307B	205E/C	5FBD/33	9F/01/0C	152
Ubr	[Braking level]	14101 = 16#3715	206F/2	5FBF/D4	A7/01/66	164
UC2	[Vector Control 2pt]	14201 = 16#3779	2070/2	5FBD/80	A8/01/02	153
UCAL	Drive line voltage	3012 = 16#0BC4	2000/D	5FB0/03	70/01/0D	124
UCb	[Sensitivity]	3111 = 16#C27	2001/C	5FBF/06	70/01/70	263
UCP	[V. constant power]	14202 = 16#377A	2070/3	5FBD/81	A8/01/03	153
UdP	Drive software version	3302 = 16#0CE6	2003/3	5FB0/0D	71/01/67	124

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
UFr	[IR compensation]	9623 = 16#2597	2042/18	5FC2/0E	91/01/18	156
UIH1	[AI1 max value]	4422 = 16#1146	200E/17	5FBC/3B	77/01/17	169
UIH2	[AI2 max value]	4423 = 16#1147	200E/18	5FBC/3C	77/01/18	170
UIH4	[AI4 max value]	4425 = 16#1149	200E/1A	5FBC/3D	77/01/1A	174
UIL1	[AI1 min value]	4412 = 16#113C	200E/D	5FBC/38	77/01/0D	169
UIL2	[AI2 min value]	4413 = 16#113D	200E/E	5FBC/39	77/01/0E	170
UIL4	[AI4 min value]	4415 = 16#113D	200E/10	5FBC/3A	77/01/10	174
ULn	Power supply voltage	3207 = 16#0C87	2002/8	5FB9/09	71/01/08	92
ULP0	Power supply voltage on last fault	7270 = 16#1C66	202A/47	5FB9/94	85/01/47	105
ULP1	Supply voltage on fault n-1	7271 = 16#1C67	202A/48	5FB9/95	85/01/48	108
ULP2	Supply voltage on fault n-2	7272 = 16#1C68	202A/49	5FB9/96	85/01/49	110
ULP3	Supply voltage on fault n-3	7273 = 16#1C69	202A/4A	5FB9/97	85/01/4A	112
ULP4	Supply voltage on fault n-4	7274 = 16#1C6A	202A/4B	5FB9/98	85/01/4B	114
ULP5	Supply voltage on fault n-5	7275 = 16#1C6B	202A/4C	5FB9/99	85/01/4C	116
ULP6	Supply voltage on fault n-6	7276 = 16#1C6C	202A/4D	5FB9/9A	85/01/4D	118
ULP7	Supply voltage on fault n-7	7277 = 16#1C6D	202A/4E	5FB9/9B	85/01/4E	120
ULP8	Supply voltage on fault n-8	7278 = 16#1C6E	202A/4F	5FB9/9C	85/01/4F	122
UnS	[Rated motor volt.]	9601 = 16#2581	2042/2	5FB3/D3	77/01/10	148
UOH1	[AO1 max Output]	4631 = 16#1217	2010/20	5FBC/5C	78/01/20	189
UOH2	[AO2 max Output]	4632 = 16#1218	2010/21	5FBC/5D	78/01/21	190
UOH3	[AO3 max Output]	4633 = 16#1219	2010/22	5FBC/5E	78/01/22	191
UOL1	[AO1 min Output]	4621 = 16#120D	2010/16	5FBC/59	78/01/16	189
UOL2	[AO2 min Output]	4622 = 16#120E	2010/17	5FBC/5A	78/01/17	190
UOL3	[AO3 min Output]	4623 = 16#120F	2010/18	5FBC/5B	78/01/18	191
UOP	Motor voltage	3208 = 16#0C88	2002/9	5FB9/0A	71/01/09	88
UPL	[Prevention level]	13811 = 16#35F3	206C/C	5FBD/74	A6/01/0C	271
UrES	[Evacuation Input V.]	13801 = 16#35E9	206C/2	5FBD/70	A6/01/02	270
USb	[UnderV. fault mgt]	13803 = 16#35EB	206C/4	5FBD/72	A6/01/04	270
USI	[+ speed assignment]	11520 = 16#2D00	2055/15	5FBD/0E	9D/01/0E	216
USL	[Undervoltage level]	13802 = 16#35EA	206C/3	5FBD/71	A6/01/03	270
USP	[+ speed assignment]	11501 = 16#2CED	2055/2	5FBD/0A	9A/01/66	215

Index of parameter codes

Code	Name	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
USt	[Undervolt. time out]	13804 =16#35EC	206C/5	5FBD/73	A6/01/05	270
VAL	Load parameter set command	12901 = 16#3265	2063/02	5FB9/ED	A1/01/66	73

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[Speed input %]	PSr	11951 = 16#2EAF	2059/34	5FB3/FD	9C/01/98	234
[16 preset speeds]	PS16	11404 = 16#2C8C	2054/5	5FBD/09	9A/01/05	211
[2 Configurations]	CnF1	8021 = 16#1F55	2032/16	5FBC/9C	89/01/16	251
[2 Parameter sets]	CHA1	12902 = 16#3266	2063/3	5FBD/54	A1/01/67	250
[2 preset PID ref.]	Pr2	11909 = 16#2E85	2059/A	5FBD/1B	9C/01/6E	236
[2 preset speeds]	PS2	11401 = 16#2C89	2054/2	5FBD/06	9A/01/02	210
[3 Configurations]	CnF2	8022 = 16#1F56	2032/17	5FBC/9D	89/01/17	252
[3 Parameter sets]	CHA2	12903 = 16#3267	2063/4	5FBD/55	A1/01/68	250
[4 preset PID ref.]	Pr4	11910 = 16#2E86	2059/B	5FBD/1C	9C/01/6F	236
[4 preset speeds]	PS4	11402 = 16#2C8A	2054/3	5FBD/07	9D/01/07	210
[8 preset speeds]	PS8	11403 = 16#2C8B	2054/4	5FBD/08	9A/01/04	211
[Traverse ctrl. accel.]	tUP	12206 = 16#2FAE	205C/7	5FBF/83	9E/01/07	255
[Acceleration 2]	AC2	9012 = 16#2334	203C/D	5FBF/24	8E/01/0D	203
[Acceleration 2]	AC2	9012 = 16#2334	203C/D	5FBF/24	8E/01/0D	217
[Acceleration]	ACC	9001 = 16#2329	203C/2	5FBF/1E	8E/01/02	201
[Encoder coupling]	ECC	5607 = 16#15E7	201A/8	5FBC/74	7D/01/08	276
[Analog limit. act.]	tLC	9213 = 16#23FD	203E/E	5FBC/B7	8F/01/0E	242
[AI torque limit. activ.]	tLA	9210 = 16#23FA	203E/B	5FBC/B6	8F/01/0B	241
[Act. internal PID ref.]	PII	11908 = 16#2E84	2059/9	5FBD/1A	9C/01/6D	231
[Current limit 2]	LC2	9202 = 16#23F2	203E/3	5FBC/B5	8F/01/03	243
[Dec ramp adapt.]	brA	9003 = 16#232B	203C/4	5FB3/CA	8E/01/04	204
[Precharge cont. ass.]	dCO	13841 = 16#3611	206C/2A	5FBD/78	A6/01/2A	259
[Out. contactor ass.]	OCC	13104 = 16#3330	2065/5	5FBD/57	A2/01/69	245
[Line contactor ass.]	LLC	13602 = 16#3522	206A/3	5FBD/6E	A5/01/03	244
[Evacuation assign.]	rFt	13831 = 16#3607	206C/20	5FBD/76	A6/01/20	258
[Ref. memo ass.]	SPM	8491 = 16#212B	2036/5C	5FBC/AD	8B/01/5C	218
[Reverse assign.]	rrS	11105 = 16#2B61	2051/6	5FBC/FE	98/01/6A	166
[Fast stop assign.]	FSt	11204 = 16#2BC4	2052/5	5FBD/04	99/01/05	205
[Auto/Manual assign.]	PAU	11970 = 16#2EC2	2059/47	5FBD/22	9C/01/AB	235
[Auto-tune assign.]	tUL	9610 = 16#258A	2042/B	5FBC/EF	91/01/0B	253

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[External fault ass.]	EtF	7131 = 16#1BDB	2029/20	5FBC/99	84/01/84	269
[Fault inhibit assign.]	Inh	7125 = 16#1BD5	2029/1A	5FBC/98	84/01/7E	274
[DC injection assign.]	dCI	11203 = 16#2BC3	2052/4	5FBD/03	99/01/04	206
[Torque ref. assign.]	tAA	9214 = 16#23FE	203E/F	5FBC/B8	8F/01/0F	242
[Speed ref. assign.]	FPI	11950 = 16#2EAE	2059/33	5FBD/1F	9C/01/97	234
[PID feedback ass.]	PIF	11901 = 16#2E7D	2059/2	5FBD/19	9C/01/66	230
[Freewheel stop ass.]	nSt	11202 = 16#2BC2	2052/3	5FBD/02	99/01/03	205
[F1 key assignment]	FN1	13501 = 16#34BD	2069/2	5FBD/60	A4/01/66	196
[F2 key assignment]	FN2	13502 = 16#34BE	2069/3	5FBD/61	A4/01/67	196
[F3 key assignment]	FN3	13503 = 16#34BF	2069/4	5FBD/62	A4/01/68	196
[F4 key assignment]	FN4	13504 = 16#34C0	2069/5	5FBD/63	A4/01/69	197
[Drive lock]	LES	13601 = 16#3521	206A/2	5FBD/6D	A5/01/02	244
[+ speed assignment]	USI	11520 = 16#2D00	2055/15	5FBD/0E	9D/01/0E	216
[+ speed assignment]	USP	11501 = 16#2CED	2055/2	5FBD/0A	9A/01/66	215
[AO1 assignment]	AO1	5021 = 16#139D	2014/16	5FBC/6D	7A/01/16	188
[AO2 assignment]	AO2	5022 = 16#139E	2014/17	5FBC/6E	7A/01/17	190
[AO3 assignment]	AO3	5023 = 16#139F	2014/18	5FBC/6F	7A/01/18	191
[Fluxing assignment]	FLI	13901 = 16#364D	206D/2	5FBD/79	A6/01/66	219
[Brake assignment]	bLC	10001 = 16#2711	2046/2	5FBC/F1	93/01/02	221
[JOG]	JOG	11110 = 16#2B66	2051/B	5FBC/FF	98/01/6F	209
[LO1 assignment]	LO1	5009 = 16#1391	2014/A	5FB3/7D	7A/01/0A	184
[LO2 assignment]	LO2	5010 = 16#1392	2014/B	5FBC/6A	7A/01/0B	185
[LO3 assignment]	LO3	5011 = 16#1393	2014/C	5FBC/6B	7A/01/0C	186
[LO4 assignment]	LO4	5012 = 16#1394	2014/D	5FBC/6C	7A/01/0D	187
[Weight sensor ass.]	PES	10070 = 16#2756	2046/47	5FBC/F4	93/01/47	226
[R2 Assignment]	r2	5002 = 16#138A	2014/3	5FB3/7C	7A/01/03	181
[R3 Assignment]	r3	5003 = 16#138B	2014/4	5FBC/67	7A/01/04	182
[R4 Assignment]	r4	5004 = 16#138C	2014/5	5FBC/68	7A/01/05	183
[-Speed assignment]	dSI	11521 = 16#2D01	2055/16	5FBD/0F	9A/01/7A	216
[-Speed assignment]	dSP	11502 = 16#2CEE	2055/3	5FBD/0B	9A/01/67	215
[R1 Assignment]	r1	5001 = 16#1389	2014/2	5FB3/7B	7A/01/02	179

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[AI2 4-20mA loss]	LFL2	7003 =16#1B5B	2028/4	5FB3/BF	84/01/04	273
[AI3 4-20mA loss]	LFL3	7013 =16#1B65	2028/E	5FB3/C3	84/01/0E	273
[AI4 4-20mA loss]	LFL4	7014 =16#1B66	2028/F	5FB3/C4	84/01/0F	273
[PID error Alarm]	PEr	11963 = 16#2EBB	2059/40	5FB4/01	9C/01/A4	233
[Max fbk alarm]	PAH	11962 = 16#2EBA	2059/3F	5FB3/FF	9C/01/A3	233
[Min fbk alarm]	PAL	11961 = 16#2EB9	2059/3E	5FB3/FE	9C/01/A2	233
[Torque control stop]	tSt	9227 = 16#240B	203E/1C	5FBC/BC	8F/01/1C	239
[Thermal alarm stop]	SAt	11021 = 16#2B0D	2050/16	5FB3/E7	98/01/16	268
[Trq/l limit. stop]	SSb	9240 =16#2418	203E/29	5FBC/BE	8F/01/29	277
[Begin Acc round]	tA1	9005 = 16#232D	203C/6	5FB3/CC	8E/01/06	202
[Begin Dec round]	tA3	9007 = 16#232F	203C/8	5FB3/CE	8E/01/08	202
[End Acc round]	tA2	9006 = 16#232E	203C/7	5FB3/CD	8E/01/07	202
[End Dec round]	tA4	9008 = 16#2330	203C/9	5FB3/CF	8E/01/09	202
[Automatic autotune]	AUt	9615 = 16#258F	2042/10	5FC2/0B	91/01/10	149
[Auto-tuning]	tUn	9608 = 16#2588	2042/9	5FB3/D7	91/01/09	149
[Negative deadband]	dbn	9223 = 16#2407	203E/18	5FBF/2E	8F/01/18	240
[Positive deadband]	dbP	9224 = 16#2408	203E/19	5FBF/2F	8F/01/19	240
[BRH b0]	brH0	10050 = 16# 2742 (bit 0)	2046/33 (bit 0)	5FBC/F3 (bit 0)	93/01/33 (bit 0)	224
[BRH b1]	brH1	10050 = 16# 2742 (bit 1)	2046/33 (bit 1)	5FBC/F3 (bit 1)	93/01/33 (bit 1)	224
[BRH b2]	brH2	10050 = 16# 2742 (bit 2)	2046/33 (bit 2)	5FBC/F3 (bit 2)	93/01/33 (bit 2)	224
[BRH b3]	brH3	10050 = 16# 2742 (bit 3)	2046/33 (bit 3)	5FBC/F3 (bit 3)	93/01/33 (bit 3)	224
[BRH b4]	brH4	10050 = 16# 2742 (bit 4)	2046/33 (bit 4)	5FBC/F3 (bit 4)	93/01/33 (bit 4)	225
[AI net. channel]	AIC1	5282 = 16#14A2	2016/53	5FBC/70	7B/01/53	230
[Cmd channel 1]	Cd1	8423 = 16#20E7	2036/18	5FBC/A8	8B/01/18	194
[Cmd channel 2]	Cd2	8424 = 16#20E8	2036/19	5FBC/A9	8B/01/19	194
[Ref.1 channel]	Fr1	8413 = 16#20DD	2036/E	5FBC/A4	8B/01/0E	193
[Ref.1B channel]	Fr1b	8415 = 16#20DF	2036/10	5FBC/A6	8B/01/10	198
[Ref.2 channel]	Fr2	8414 = 16#20DE	2036/F	5FBC/A5	8B/01/0F	195
[Torque ref. channel]	tr1	9221 = 16#2405	203E/16	5FBC/BA	8F/01/16	238

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[2/3 wire control]	tCC	11101 = 16#2B5D	2051/2	5FB3/E8	98/01/66	166
[HMI cmd.]	bMP	13529 = 16#34D9	2069/1E	5FBD/6C	A4/01/82	197
[Pairing password]	PPI	14001 = 16#36B1	206E/2	5FBD/7A	A7/01/02	279
[Gen. speed coeff]	CO _r	12304 = 16#3010	205D/5	5FBF/89	9E/01/69	228
[Motor speed coeff.]	CO _F	12303 = 16#300F	205D/4	5FBF/88	9E/01/68	228
[Trq/spd switching]	tSS	9220 = 16#2404	203E/15	5FBC/B9	8F/01/15	238
[Ramp switching]	rPS	9010 = 16#2332	203C/B	5FBC/B3	8E/01/0B	203
[Ref 1B switching]	rCb	8412 = 16#20DC	2036/D	5FBC/A3	8B/01/0D	198
[Cmd switching]	CCS	8421 = 16#20D1	2036/16	5FBC/A7	8B/01/16	194
[Ref. 2 switching]	rFC	8411 = 16#20DB	2036/C	5FBC/A2	8B/01/0C	195
[Slip compensation]	SLP	9625 = 16#2599	2042/1A	5FB3/DC	91/01/1A	156
[IR compensation]	UFr	9623 = 16#2597	2042/18	5FC2/0E	91/01/18	156
[Config. Active]	CNFS	8020 = 16#1F54	2032/15	5FB9/CD	89/01/15	86
[Syn. EMF constant]	PHS	9673 = 16#25C9	2042/4A	5FC2/21	91/01/4A	154
[Brake contact]	bCl	10009 = 16#2719	2046/A	5FBC/F2	93/01/0A	221
[Yarn control]	trC	12201 = 16#2FA9	205C/2	5FBD/24	9E/01/02	254
[Vector Control 2pt]	UC2	14201 = 16#3779	2070/2	5FBD/80	A8/01/02	153
[Copy channel 1 <--> 2]	COP	8402 = 16#20D2	2036/3	5FBC/A0	8B/01/03	195
[Correction min spd]	LbC1	14303 = 16#37DF	2071/4	5FBF/D6	A8/01/68	165
[Load correction]	LbC	14302 = 16#37DE	2071/3	5FBF/D5	A8/01/67	164
[Correction max spd]	LbC2	14304 = 16#37E0	2071/5	5FBF/D7	A8/01/69	165
[Counter wobble]	SnC	12212 = 16#2FB4	205C/D	5FBD/26	9E/01/0D	256
[Rated mot. current]	nCr	9603 = 16#2583	2042/4	5FB3/D5	91/01/04	148
[Nominal l sync.]	nCrS	9670 = 16#25C6	2042/47	5FC2/1E	91/01/47	154
[Mot. therm. current]	ItH	9622 = 16#2596	2042/17	5FB3/DB	2A/01/0A	145
[Traverse ctrl. decel]	tdn	12207 = 16#2FAF	205C/8	5FBF/84	9E/01/08	255
[Deceleration 2]	dE2	9013 = 16#2335	203C/E	5FBF/25	8E/01/0E	203
[Deceleration 2]	dE2	9013 = 16#2335	203C/E	5FBF/25	8E/01/0E	217
[Deceleration]	dEC	9002 = 16#232A	203C/3	5FBF/1F	8E/01/03	201
[Brake engage delay]	tbE	10010 = 16#271A	2046/B	5FBF/3C	93/01/0B	223
[Decrease ref. speed]	dtF	12211 = 16#2FB3	205C/C	5FBF/86	9E/01/0C	256

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[Disable limit sw.]	CLS	12507 = 16#30DB	205F/8	5FBD/3B	9F/01/6C	248
[Motor2 therm. level]	ttd2	11006 = 16#2AFE	2050/7	5FBF/4C	98/01/07	264
[Motor2 therm. level]	ttd2	11006 = 16#2AFE	2050/7	5FBF/4C	98/01/07	268
[Motor3 therm. level]	ttd3	11007 = 16#2AFF	2050/8	5FBF/4D	98/01/08	264
[Motor3 therm. level]	ttd3	11007 = 16#2AFF	2050/8	5FBF/4D	98/01/08	268
[Motor therm. level]	ttd	11002 = 16#2AFA	2050/3	5FB3/E4	98/01/03	264
[Motor therm. level]	ttd	11002 = 16#2AFA	2050 / 3	5FB3/E4	98/01/03	268
[Load slip detection]	Sdd	7005 =16#1B5D	2028/6	5FB3/C1	84/01/06	276
[Ramp divider]	dCF	11230 = 16#2BDE	2052/1F	5FB3/ED	99/01/1F	206
[Ramp divider]	dCF	11230 = 16#2BDE	2052/1F	5FB3/ED	99/01/1F	280
[ENA system]	EnA	12101 = 16#2F45	205B/2	5FBD/23	9D/01/66	161
[Load sharing]	LbA	14301 = 16#37DD	2071/2	5FBD/83	A8/01/66	164
[Braking balance]	bbA	14102 = 16#3716	206F/3	5FBD/7B	A7/01/67	164
[Auto tuning status]	tUS	9609 = 16#2589	2042/A	5FC2/08	91/01/0A	150
[F1]	F1	12404 = 16#3074	205E/5	5FBD/2C	9F/01/05	151
[F2]	F2	12406 = 16#3076	205E/7	5FBD/2E	9F/01/07	151
[F3]	F3	12408 = 16#3078	205E/9	5FBD/30	9F/01/09	152
[F4]	F4	12410 = 16#307A	205E/B	5FBD/32	9F/01/0B	152
[F5]	F5	12412 = 16#307C	205E/D	5FBD/34	9F/01/0D	152
[Stop RV limit sw.]	LA	11602 = 16#2D52	2056/3	5FBD/11	9B/01/03	220
[Stop RV limit sw.]	SA	12502 = 16#30D6	205F/3	5FBD/36	9F/01/67	247
[Stop FW limit sw.]	LAF	11601 = 16#2D51	2056/2	5FBD/10	9B/01/02	220
[Stop FW limit sw.]	SAF	12501 = 16#30D5	205F/2	5FBD/35	9F/01/66	247
[Slowdown reverse]	dAr	12504 = 16#30D8	205F/5	5FBD/38	9F/01/69	248
[Slowdown forward]	dAF	12503 = 16#30D7	205F/4	5FBD/37	9F/01/68	248
[Engage at reversal]	bEd	10020 = 16#2724	2046/15	5FBF/41	93/01/15	223
[AI1 filter]	AI1F	4452 = 16#1164	200E/35	5FBC/44	77/01/35	169
[AI2 filter]	AI2F	4453 = 16#1165	200E/36	5FBC/45	77/01/36	170
[AI3 filter]	AI3F	4454 = 16#1166	200E/37	5FBC/46	77/01/37	172
[AI4 filter]	AI4F	4455 = 16#1167	200E/38	5FBC/47	77/01/38	174
[AO1 Filter]	AO1F	4611 = 16#1203	2010/C	5FBC/56	78/01/0C	189

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[AO2 Filter]	AO2F	4612 = 16#1204	2010/D	5FBC/57	78/01/0D	190
[AO3 Filter]	AO3F	4613 = 16#1205	2010/E	5FBC/58	78/01/0E	191
[RP filter]	PFI	13304 = 16#33F8	2067/5	5FBD/5B	A3/01/69	176
[Sharing filter]	LbF	14306 = 16#37E2	2071/7	5FBF/D9	A8/01/6B	165
[Freq. signal filter]	EFI	13312 = 16#3400	2067/D	5FBD/5E	A3/01/71	178
[Sinus filter]	OFI	3109 = 16#C25	2001/A	5FBC/0B	70/01/6E	162
[End reel]	EbO	12213 = 16#2FB5	205C/E	5FBD/27	9E/01/0E	255
[Motor fluxing]	FLU	13902 = 16#364E	206D/3	5FB4/05	A6/01/67	146
[Motor fluxing]	FLU	13902 = 16#364E	206D/3	5FB4/05	A6/01/67	219
[Ramp type]	rPt	9004 = 16#232C	203C/5	5FB3/CB	8E/01/05	201
[Reference template]	bSP	3106 = 16#C22	2001/7	5FB3/77	70/01/6B	168
[Evacuation freq.]	rSP	13833 = 16#3609	206C/22	5FB4/04	A6/01/22	258
[Brake engage freq.]	bEn	10003 = 16#2713	2046/4	5FBF/36	93/01/04	222
[Rated motor freq.]	FrS	9602 = 16#2582	2042/3	5FB3/D4	91/01/03	149
[Nominal freq sync.]	FrSS	9679 = 16#25CF	2042/50	5FC2/24	91/01/50	159
[Brake release freq]	blr	10012 = 16#271C	2046/D	5FB3/E1	93/01/0D	222
[Freq. Const Power]	FCP	14203 = 16#377B	2070/4	5FBD/82	A8/01/04	153
[Switching freq.]	SFr	3102 = 16#C1E	2001/3	5FBF/02	70/01/67	162
[I Limit. frequency]	SCL	12306 = 16#3012	205D/7	5FBF/8B	9E/01/6B	229
[Jog frequency]	JGF	11111 = 16#2B67	2051/C	5FBF/50	98/01/70	209
[Max frequency]	tFr	3103 = 16#C1F	2001/4	5FB3/74	70/01/68	149
[PID derivative gain]	rdG	11943 = 16#2EA7	2059/2C	5FB3/FC	9C/01/90	232
[ENA integral gain]	GIE	12104 = 16#2F48	205B/5	5FBF/7D	9D/01/69	161
[PID integral gain]	rIG	11942 = 16#2EA6	2059/2B	5FB3/FB	9C/01/8F	232
[ENA prop.gain]	GPE	12103 = 16#2F47	205B/4	5FBF/7C	9D/01/68	161
[PID prop. gain]	RPG	11941 = 16#2EA5	2059/2A	5FB3/FA	9C/01/8E	231
[Speed prop. gain]	SPG	9103 = 16#238F	203D/4	5FB3/D1	8E/01/68	145
[CANopen fault mgt]	COL	7011 = 16#1B63	2028/C	5FBC/8F	84/01/0C	275
[Modbus fault mgt]	SLL	7010 = 16#1B62	2028/B	5FBC/8E	84/01/0B	275
[Network fault mgt]	CLL	7015 = 16#1B67	2028/10	5FBC/93	84/01/10	275
[Torq. ctrl fault mgt]	tOb	9228 = 16#240C	203E/1D	5FBC/BD	8F/01/1D	240

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[External fault mgt]	EPL	7006 = 16#1B5E	2028/7	5FB3/C2	84/01/07	269
[Autotune fault mgt]	tnL	7012 = 16#1B64	2028/D	5FBC/90	84/01/0D	279
[UnderV. fault mgt]	USb	13803 = 16#35EB	206C/4	5FBD/72	A6/01/04	270
[Overload fault mgt]	OLL	7009 = 16#1B61	2028/A	5FBC/8D	84/01/0A	265
[Overtemp fault mgt]	OHL	7008 = 16#1B60	2028/9	5FBC/8C	84/01/09	267
[High speed]	HSP	3104 = 16#C20	2001/5	5FB3/75	70/01/69	145
[Auto DC inj. level 1]	SdC1	10403 = 16#28A3	204A/4	5FBF/45	95/01/04	208
[Auto DC inj. level 1]	SdC1	10403 = 16#28A3	204A/4	5FBF/45	95/01/04	223
[Auto DC inj. level 2]	SdC2	10405 = 16#28A5	204A/6	5FBF/47	95/01/06	208
[DC inject. level 1]	IdC	11210 = 16#2BCA	2052/B	5FB3/EC	99/01/0B	206
[DC inject. level 1]	IdC	11210 = 16#2BCA	2052/B	5FB3/EC	99/01/0B	281
[DC inject. level 2]	IdC2	11212 = 16#2BCC	2052/D	5FBF/54	99/01/0D	207
[DC inject. level 2]	IdC2	11212 = 16#2BCC	2052/D	5FBF/54	99/01/0D	281
[High speed I Limit]	CLO	12302 = 16#300E	205D/3	5FBF/87	9E/01/67	229
[Brake release I Rev]	Ird	10011 = 16#271B	2046/C	5FBF/3D	93/01/0C	222
[Brake release I FW]	Ibr	10006 = 16#2716	2046/7	5FB3/DF	93/01/07	222
[Ibr 4-20 mA loss]	IbrA	10075 = 16#275B	2046/4C	5FBC/F9	93/01/4C	227
[ldr]	IdM	9650 = 16#25B2	2042/33	5FB9/E0	91/01/33	157
[ldw]	IdA	9652 = 16#25B4	2042/35	5FC2/15	91/01/35	158
[Brake impulse]	bIP	10007 = 16#2717	2046/8	5FB3/E0	93/01/08	221
[Ramp increment]	Inr	9020 = 16#233C	203C/15	5FBF/26	8E/01/15	201
[Autotune L d-axis]	LdS	9674 = 16#25CA	2042/4B	5FC2/22	91/01/4B	154
[Autotune L q-axis]	LqS	9675 = 16#25CB	2042/4C	5FC2/23	91/01/4C	155
[RV Inhibition]	rIn	3108 = 16#C24	2001/9	5FB3/79	70/01/6D	193
[Init. traverse ctrl]	rtr	12210 = 16#2FB2	205C/B	5FBD/25	9E/01/0B	257
[Auto DC injection]	AdC	10401 = 16#28A1	204A/2	5FB3/E2	95/01/02	208
[PID correct. reverse]	PIC	11940 = 16#2EA4	2059/29	5FB3/F9	9C/01/8D	232
[K speed loop filter]	SFC	9105 = 16#2391	203D/6	5FBF/29	8E/01/6A	145
[High speed hoisting]	HSO	12301 = 16#300D	205D/2	5FBD/29	9E/01/66	228
[Lfr]	LFM	9660 = 16#25BC	2042/3D	5FB9/E1	91/01/3D	157
[Lfw]	LFA	9662 = 16#25BE	2042/3F	5FC2/19	91/01/3F	158

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[Gen. torque lim]	tLIG	9212 = 16#23FC	203E/D	5FBF/2D	8F/01/0D	241
[Motoring torque lim]	tLIM	9211 = 16#23FB	203E/C	5FBF/2C	8F/01/0C	241
[Motor surge limit.]	SUL	12601 = 16#3139	2060/2	5FBD/3C	A0/01/02	162
[+/-Speed limitation]	SrP	11505 = 16#2CF1	2055/6	5FB3/F7	9A/01/6A	216
[Current Limitation]	CLI	9201 = 16#23F1	203E/2	5FB3/D2	8F/01/02	162
[Current Limitation]	CLI	9201 = 16#23F1	203E/2	5FB3/D2	93/01/D2	243
[Current Limitation]	CLI	9201 = 16#23F1	203E/2	5FB3/D2	8F/01/02	146
[LO1 active at]	LO1S	4209 = 16#1071	200C/2D	5FBC/20	76/01/0A	184
[LO2 active at]	LO2S	4210 = 16#1072	200C/B	5FBC/21	76/01/0B	185
[LO3 active at]	LO3S	4211 = 16#1073	200C/C	5FBC/22	76/01/0C	186
[LO4 active at]	LO4S	4212 = 16#1074	200C/D	5FBC/23	76/01/0D	187
[Macro configuration]	CFG	3052 = 16#BEC	2000/35	5FBC/07	70/01/35	144
[Customized macro]	CCFG	3053 = 16#BED	2000/36	5FB9/02	70/01/36	144
[LO1 holding time]	LO1H	4229 = 16#1085	200C/1E	5FBC/28	76/01/1E	184
[LO2 holding time]	LO2H	4230 = 16#1086	200C/1F	5FBC/29	76/01/1F	185
[LO3 holding time]	LO3H	4231 = 16#1087	200C/20	5FBC/2A	76/01/20	186
[LO4 holding time]	LO4H	4232 = 16#1088	200C/21	5FBC/2B	76/01/21	187
[R1 Holding time]	r1H	4221 = 16#107D	200C/16	5FBC/24	76/01/16	180
[R2 Holding time]	r2H	4222 = 16#107E	200C/17	5FBC/25	76/01/17	181
[R3 Holding time]	r3H	4223 = 16#107F	200C/18	5FBC/26	76/01/18	182
[R4 Holding time]	r4H	4224 = 16#1080	200C/19	5FBC/27	76/01/19	183
[Reference saved]	Str	11503 = 16#2CEF	2055/4	5FBD/0C	9A/01/68	215
[Multimotors]	CHM	8025 = 16#1F59	2032/1A	5FBC/9E	89/01/1A	251
[Prevention level]	UPL	13811 = 16#35F3	206C/C	5FBD/74	A6/01/0C	271
[Undervoltage level]	USL	13802 = 16#35EA	206C/3	5FBD/71	A6/01/03	270
[Number of pulses]	PGI	5604 = 16#15E4	201A/5	5FB3/7E	7D/01/05	159
[Number of pulses]	PGI	5604 = 16#15E4	201A/5	5FB3/7E	7D/01/05	177
[Nominal motor slip]	nSL	9605 = 16#2585	2042/6	5FB9/DD	91/01/06	157
[Torque offset]	LbC3	14305 = 16#37E1	2071/6	5FBF/D8	A8/01/6A	165
[Volt surge limit. opt]	SOP	12602 = 16#313A	2060/3	5FBD/3D	A0/01/03	163
[Pole pairs]	PPnS	9672 = 16#25C8	2042/49	5FC2/20	91/01/49	154

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[Output Phase Loss]	OPL	9611 = 16#258B	2042/C	5FB3/D8	91/01/0C	266
[Input phase loss]	IPL	7002 = 16#1B5A	2028/3	5FB3/BE	84/01/03	266
[Low speed]	LSP	3105 = 16#C21	2001/6	5FB3/76	70/01/6A	145
[AI2 range]	AI2L	4483 = 16#1183	200E/54	5FBC/50	77/01/54	171
[AI3 range]	AI3L	4484 = 16#1184	200E/55	5FBC/51	77/01/55	172
[AI4 range]	AI4L	4485 = 16#1185	200E/56	5FBC/52	77/01/56	175
[Point 1 X]	LP1	10071 = 16#2757	2046/48	5FBC/F5	93/01/48	226
[Point 1Y]	CP1	10072 = 16#2758	2046/49	5FBC/F6	93/01/49	226
[Point 2 X]	LP2	10073 = 16#2759	2046/4A	5FBC/F7	93/01/4A	226
[Point 2Y]	CP2	10074 = 16#275A	2046/4B	5FBC/F8	93/01/4B	227
[AI1 Interm. point X]	AI1E	4462 = 16#116E	200E/3F	5FBC/48	77/01/3F	169
[AI2 Interm. point X]	AI2E	4463 = 16#116F	200E/40	5FBC/49	77/01/40	171
[AI3 Interm. point X]	AI3E	4464 = 16#1170	200E/41	5FBC/4A	77/01/4A	173
[AI4 Interm. point X]	AI4E	4465 = 16#1171	200E/42	5FBC/4B	77/01/42	175
[AI1 Interm. point Y]	AI1S	4472 = 16#1178	200E/49	5FBC/4C	77/01/49	169
[AI2 Interm. point Y]	AI2S	4473 = 16#1179	200E/4A	5FBC/4D	77/01/4A	171
[AI3 Interm. point Y]	AI3S	4474 = 16#117A	200E/4B	5FBC/4E	77/01/4A	173
[AI4 Interm. point Y]	AI4S	4475 = 16#117B	200E/4C	5FBC/4F	77/01/4C	175
[Pr]	PPn	9618 = 16#2592	2042/13	5FB3/DA	91/01/13	158
[UnderV. prevention]	StP	7004 = 16#1B5C	2028/5	5FB3/C0	84/01/05	271
[Stop Key priority]	PSt	64002 = 16#FA02	2262/3	5FBD/88	94/01/06	193
[Profile]	CHCF	8401 = 16#20D1	2036/2	5FBC/9F	8B/01/02	193
[DB res. protection]	brO	14111 = 16#371F	206F/C	5FBD/7C	A7/01/70	278
[Rated motor power]	nPr	9613 = 16#258D	2042/E	5FC2/0A	91/01/0E	148
[DB Resistor Power]	brP	14112 = 16#3720	206F/D	5FBD/7D	A7/01/71	278
[Quick step High]	qSH	12204 = 16#2FAC	205C/5	5FBF/81	9E/01/05	254
[Quick step Low]	qSL	12205 = 16#2FAD	205C/6	5FBF/82	9E/01/06	254
[Torque R. time out]	rtO	9229 = 16#240D	203E/1E	5FBF/32	8F/01/1E	240
[R1 Active at]	r1S	4201 = 16#1069	200C/2	5FBC/1C	76/01/02	180
[Stator R measured]	rSM	9640 = 16#25A8	2042/29	5FB9/DF	91/01/29	157
[R1rS]	rSMS	9680 = 16#25D0	2042/51	5FB9/E3	91/01/51	159

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[R1w]	rSA	9642 = 16#25AA	2042/2B	5FC2/11	91/01/2B	158
[R2 Active at]	r2S	4202 = 16#106A	200C/3	5FBC/1D	76/01/03	181
[R3 Active at]	r3S	4203 = 16#106B	200C/4	5FBC/1E	76/01/04	182
[R4 Active at]	r4S	4204 = 16#106C	200C/5	5FBC/1F	76/01/05	183
[PID ramp]	PrP	11984 = 16#2ED0	2059/55	5FBF/7B	9C/01/B9	232
[Reduction ratio]	rAP	12105 = 16#2F49	205B/6	5FBF/7E	9D/01/6A	161
[Torque ratio]	trt	9225 = 16#2409	203E/1A	5FBF/30	8F/01/1A	239
[PID integral reset]	PIS	11944 = 16#2EA8	2059/2D	5FBD/1E	9C/01/91	234
[Automatic restart]	Atr	7122 = 16#1BD2	2029/17	5FB3/C9	84/01/7B	262
[Noise reduction]	rnd	3107 = 16#C23	2001/8	5FB3/78	70/01/6C	162
[Internal PID ref.]	RPI	11920 = 16#2E90	2059/15	5FBF/6E	9C/01/79	231
[Multiplier ref. 2]	MA2	11821 = 16#2E2D	2058/16	5FBD/17	9C/01/16	200
[Multiplier ref. 3]	MA3	11822 = 16#2E2E	2058/17	5FBD/18	9C/01/17	200
[Max PID reference]	PIP2	11907 = 16#2E83	2059/8	5FBF/6D	9C/01/6C	231
[Min PID reference]	PIP1	11906 = 16#2E82	2059/7	5FBF/6C	9C/01/6B	231
[Preset ref. PID 2]	rP2	11921 = 16#2E91	2059/16	5FBF/6F	9C/01/7A	237
[Preset ref. PID 3]	rP3	11922 = 16#2E92	2059/17	5FBF/70	9C/01/7B	237
[Preset ref. PID 4]	rP4	11923 = 16#2E93	2059/18	9F/01/71	9C/01/7C	237
[Summing ref. 2]	SA2	11801 = 16#2E19	2058/2	5FBD/13	9C/01/02	199
[Summing ref. 3]	SA3	11802 = 16#2E1A	2058/3	5FBD/14	9C/01/03	199
[Subtract ref. 2]	dA2	11811 = 16#2E23	2058/C	5FBD/15	9C/01/0C	199
[Subtract ref. 3]	dA3	11812 = 16#2E24	2058/D	5FBD/16	9C/01/0D	199
[Manual reference]	PIM	11954 = 16#2EB2	2059/37	5FBD/20	9C/01/9B	235
[Catch on the fly]	FLr	3110 = 16#C26	2001/B	5FB3/7A	70/01/6F	263
[Fault reset]	rSF	7124 = 16#1BD4	2029/19	5FBC/97	84/01/7D	261
[Cust. stator R syn]	rSAS	9682 = 16#25D2	2042/53	5FC2/26	91/01/53	155
[LI1 On Delay]	L1d	4001 = 16#FA1	200A/2	5FBC/0E	75/01/02	167
[LI10 On Delay]	L10d	4010 = 16#FAA	200A/B	5FBC/17	75/01/0B	167
[LI12 On Delay]	L12d	4012 = 16#FAC	200A/D	5FBC/19	75/01/0D	167
[LI13 On Delay]	L13d	4013 = 16#FAD	200A/E	5FBC/1A	75/01/0E	167
[LI14 On Delay]	L14d	4014 = 16#FAE	200A/F	5FBC/1B	75/01/0F	167

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[LI2 On Delay]	L2d	4002 = 16#FA2	200A/3	5FBC/0F	75/01/03	167
[LI3 On Delay]	L3d	4003 = 16#FA3	200A/4	5FBC/10	75/01/04	167
[LI4 On Delay]	L4d	4004 = 16#FA4	200A/5	5FBC/11	75/01/05	167
[LI5 On Delay]	L5d	4005 = 16#FA5	200A/6	5FBC/12	75/01/06	167
[LI6 On Delay]	L6d	4006 = 16#FA6	200A/7	5FBC/13	75/01/07	167
[LI7 On Delay]	L7d	4007 = 16#FA7	200A/8	5FBC/14	75/01/08	167
[LI8 On Delay]	L8d	4008 = 16#FA8	200A/9	5FBC/15	75/01/09	167
[LI9 On Delay]	L9d	4009 = 16#FA9	200A/A	5FBC/16	75/01/0A	167
[LI11 On Delay]	L11d	4011 = 16#FAB	200A/C	5FBC/18	75/01/0C	167
[LO1 delay time]	LO1d	4249 = 16#1099	200C/32	5FBC/30	76/01/32	184
[LO2 delay time]	LO2d	4250 = 16#109A	200C/33	5FBC/31	76/01/33	185
[LO3 delay time]	LO3d	4251 = 16#109B	200C/34	5FBC/32	76/01/34	186
[LO4 delay time]	LO4d	4252 = 16#109C	200C/35	5FBC/33	76/01/35	187
[R1 Delay time]	r1d	4241 = 16#1091	200C/2A	5FBC/2C	76/01/2A	180
[R2 Delay time]	r2d	4242 = 16#1092	200C/2B	5FBC/2D	76/01/2B	181
[R3 Delay time]	r3d	4243 = 16#1093	200C/2C	5FBC/2E	76/01/2C	182
[R4 Delay time]	r4d	4244 = 16#1094	200C/2D	5FBC/2F	76/01/2D	183
[Output contact. fdbk]	rCA	13103 = 16#332F	2065/4	5FBF/56	A2/01/68	245
[Max PID feedback]	PIF2	11905 = 16#2E81	2059/6	5FBF/6B	9C/01/6A	230
[Min PID feedback]	PIF1	11904 = 16#2E80	2059/5	5FBF/6A	9C/01/69	230
[Output Ph rotation]	PHr	13401 = 16#3459	2068/2	5FBF/5F	A4/01/02	150
[Jump at reversal]	JdC	10013 = 16#271D	2046/E	5FBF/3F	93/01/0E	223
[Sensitivity]	UCb	3111 = 16#C27	2001/C	5FBF/06	70/01/70	263
[Current threshold]	Ctd	11001 = 16#2AF9	2050/2	5FB3/E3	98/01/02	146
[Freq. threshold]	Ftd	11003 = 16#2AFB	2050/4	5FB3/E5	98/01/04	147
[Braking level]	Ubr	14101 = 16#3715	206F/2	5FBF/D4	A7/01/66	164
[Freq. threshold 2]	F2d	11004 = 16#2AFC	2050/5	5FB3/E6	98/01/05	147
[Ramp 2 threshold]	FrT	9011 = 16#2333	203C/C	5FB3/D0	8E/01/0C	202
[PID wake up thresh.]	rSL	11960 = 16#2DB5	2059/3D	5FBF/21	9C/01/A1	235
[Drv therm. state al]	tHA	11009 = 16#2B01	2050/A	5FBF/4E	98/01/0A	267
[Drv therm. state al]	tHA	11009 = 16#2B01	2050/A	5FBF/4E	98/01/0A	268

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[Encoder type]	EnS	5608 = 16#15E8	201A/A	5FBC/75	7D/01/09	159
[Encoder type]	EnS	5608 = 16#15E8	201A/9	5FBC/75	7D/01/09	177
[Torque ref. sign]	tSd	9222 = 16#2406	203E/17	5FBC/BB	8F/01/17	239
[LI6 = PTC probe]	PtCL	13203 = 16#3396	2066/4	5FBF/CD	A3/01/04	260
[PTC1 probe]	PtC1	13201 = 16#3391	2066/2	5FBF/CB	A3/01/02	260
[PTC2 probe]	PtC2	13202 = 16#3392	2066/3	5FBF/CC	A3/01/03	260
[Max PID output]	POH	11953 = 16#2EB1	2059/36	5FBF/77	9C/01/9A	233
[Min PID output]	POL	11952 = 16#2EB0	2059/35	5FBF/76	9C/01/99	232
[Standard mot. freq]	bFr	3015 = 16#BC7	2000/10	5FBC/05	70/01/10	148
[Sync. wobble]	tSY	12214 = 16#2FB6	205C/F	5FBD/28	9E/01/0F	256
[Spin time]	SPt	9230 = 16#240E	203E/1F	5FBF/33	8F/01/1F	240
[Torque ramp time]	trP	9226 = 16#240A	203E/1B	5FBF/31	8F/01/1B	239
[UnderV. restart tm]	tSM	13813 = 16#35F5	206C/E	5FBF/CF	A6/01/0E	271
[T2r]	trM	9665 = 16#25C1	2042/42	5FB9/E2	91/01/42	157
[T2w]	trA	9667 = 16#25C3	2042/44	5FC2/1C	91/01/44	158
[Time to open cont.]	dAS	13102 = 16#332E	2065/3	5FB4/03	A2/01/67	246
[Time to motor run]	dbS	13101 = 16#332D	2065/2	5FB4/02	A2/01/66	245
[Undervolt. time out]	USt	13804 = 16#35EC	206C/5	5FBD/73	A6/01/05	270
[Jog delay]	JGT	11112 = 16#2B68	2051/D	5FB3/EA	98/01/71	209
[Max stop time]	StM	13814 = 16#35EC	206C/F	5FBF/D0	A6/01/0F	271
[Reel time]	tbO	12208 = 16#2FAB	205C/9	5FBF/85	9E/01/09	255
[Load measuring tm.]	tOS	12307 = 16#3013	205D/8	5FBF/8C	9E/01/6C	228
[Brake engage time]	bEt	10005 = 16#2715	2046/6	5FB3/DE	93/01/06	223
[DC injection time 1]	tdl	11213 = 16#2BCD	2052/E	5FBF/55	99/01/0E	206
[DC injection time 1]	tdl	11213 = 16#2BCD	2052/E	5FBF/55	99/01/0E	281
[DC injection time 2]	tdC	11211 = 16#2BCB	2052/C	5FBF/53	99/01/0C	207
[DC injection time 2]	tdC	11211 = 16#2BCB	2052/C	5FBF/53	99/01/0C	281
[Speed time integral]	SlT	9104 = 16#2390	203D/5	5FBF/28	8E/01/69	146
[DC bus maintain tm]	tbS	13812 = 16#35F4	206C/D	5FBF/CE	A6/01/0D	271
[Brake Release time]	brt	10004 = 16#2714	2046/5	5FB3/DD	93/01/05	222
[OutPh time detect]	Odt	7081 = 16#1BA9	2028/52	5FBF/08	84/01/52	266

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[Low speed time out]	tLS	11701 = 16#2DB5	2057/2	5FB3/F8	9B/01/66	146
[Low speed time out]	tLS	11701 = 16#2DB5	2057/2	5FB3/F8	9B/01/66	235
[Current ramp time]	brr	10015 = 16#271F	2046/10	5FBF/40	93/01/10	225
[Time to restart]	ttr	10022 = 16#2726	2046/17	5FBF/42	93/01/17	224
[Encoder check time]	ECt	5609 = 16#15E9	201A/A	5FBF/07	7D/01/0A	276
[Evacuation Input V.]	rSU	13832 = 16#3608	206C/21	5FBD/77	A6/01/21	258
[Rated motor volt.]	UnS	9601 = 16#2581	2042/2	5FB3/D3	77/01/10	148
[IGBT test]	Strt	3112 = 16#C28	2001/D	5FBC/0D	70/01/71	272
[Trq/I limit. time out]	StO	9241 = 16#2419	203E/2A	5FBF/34	8F/01/2A	277
[Mains V. time out]	LCt	13603 = 16#3523	206A/4	5FBD/6F	A5/01/04	244
[Max. restart time]	tAr	7123 = 16#1BD3	2029/18	5FBC/96	84/01/7C	262
[Auto DC inj. time 1]	tdC1	10402 = 16#28A2	204A/3	5FBF/44	95/01/03	208
[Auto DC inj. time 2]	tdC2	10404 = 16#28A4	204A/5	5FBF/46	95/01/05	208
[Traverse high]	trH	12202 = 16#2FAA	205C/3	5FBF/7F	9E/01/03	254
[Traverse Low]	trL	12203 = 16#2FAB	205C/4	5FBF/80	9E/01/04	254
[AI1 Type]	AI1t	4402 = 16#1132	200E/3	5FBC/34	77/01/03	169
[AI2 Type]	AI2t	4403 = 16#1133	200E/4	5FBC/35	77/01/04	170
[AI3 Type]	AI3t	4404 = 16#1134	200E/5	5FBC/36	77/01/05	172
[AI4 Type]	AI4t	4405 = 16#1135	200E/6	5FBC/37	77/01/06	174
[AO1 Type]	AO1t	4601 = 16#11F9	2010/2	5FBC/53	78/01/02	188
[AO2 Type]	AO2t	4602 = 16#11FA	2010/3	5FBC/54	78/01/03	190
[AO3 Type]	AO3t	4603 = 16#11FB	2010/4	5FBC/55	78/01/04	191
[2 wire type]	tCt	11102 = 16#2B5E	2051/3	5FB3/E9	98/01/67	166
[Motor control type]	Ctt	9607 = 16#2587	2042/8	5FC2/06	91/01/08	150
[Stop type]	LAS	11603 = 16#2D53	2056/4	5FBD/12	9B/01/04	220
[Stop type]	PAS	12506 = 16#30DA	205F/7	5FBD/3A	9F/01/6B	249
[Stop type]	Stt	11201 = 16#2BC1	2052/2	5FB3/EB	99/01/02	205
[Deceleration type]	dSF	12505 = 16#30D9	205F/6	5FBD/39	9F/01/6A	249
[Movement type]	bSt	10008 = 16#2718	2046/9	5FBF/3B	93/01/09	221
[Motor protect. type]	tHt	9612 = 16#258C	2042/D	5FB3/D9	91/01/0D	264
[Reference type]	PGA	13301 = 16#33F5	2067/2	5FBD/58	A3/01/66	177

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[V. constant power]	UCP	14202 = 16#377A	2070/3	5FBD/81	A8/01/03	153
[Evacuation Input V.]	UrES	13801 = 16#35E9	206C/2	5FBD/70	A6/01/02	270
[U0]	U0	12401 = 16#3071	205E/2	5FBD/2A	9F/01/02	150
[U1]	U1	12403 = 16#3073	205E/4	5FBD/2B	9F/01/04	151
[U2]	U2	12405 = 16#3075	205E/6	5FBD/2D	9F/01/06	151
[U3]	U3	12407 = 16#3077	205E/8	5FBD/2F	9F/01/08	151
[U4]	U4	12409 = 16#3079	205E/9	5FBD/31	9F/01/0A	152
[U5]	U5	12411 = 16#307B	205E/C	5FBD/33	9F/01/0C	152
[Torque unit]	Int	9260 = 16#242C	203E/3D	5FBF/35	8F/01/3D	238
[Encoder usage]	EnU	5606 = 16#15E6	201A/7	5FBC/73	7D/01/07	160
[Encoder usage]	EnU	5606 = 16#15E6	201A/7	5FBC/73	7D/01/07	177
[I Limit. 2 value]	CL2	9203 = 16#23F3	203E/4	5FBF/2B	8F/01/04	243
[AI1 max value]	UIH1	4422 = 16#1146	200E/17	5FBC/3B	77/01/17	169
[AI2 max value]	CrH2	4443 = 16#115B	200E/2C	5FBC/41	77/01/2C	170
[AI2 max value]	UIH2	4423 = 16#1147	200E/18	5FBC/3C	77/01/18	170
[AI3 max value]	CrH3	4444 = 16#115C	200E/2D	5FBC/42	77/01/2D	172
[AI4 max value]	CrH4	4445 = 16#115D	200E/2E	5FBC/43	77/01/2E	174
[AI4 max value]	UIH4	4425 = 16#1149	200E/1A	5FBC/3D	77/01/1A	174
[AO1 max Output]	AOH1	4651 = 16#122B	2010/34	5FBC/62	78/01/34	188
[AO1 max Output]	UOH1	4631 = 16#1217	2010/20	5FBC/5C	78/01/20	189
[AO2 max Output]	AOH2	4652 = 16#122C	2010/35	5FBC/63	78/01/35	190
[AO2 max Output]	UOH2	4632 = 16#1218	2010/21	5FBC/5D	78/01/21	190
[AO3 max Output]	AOH3	4653 = 16#122D	2010/36	5FBC/64	78/01/36	191
[AO3 max Output]	UOH3	4633 = 16#1219	2010/22	5FBC/5E	78/01/22	191
[Freq. max value]	EFr	13311 = 16#33FF	2067/C	5FBD/5D	A3/01/70	178
[RP max value]	PFr	13303 = 16#33F7	2067/4	5FBD/5A	A3/01/68	176
[AI1 min value]	UIL1	4412 = 16#113C	200E/D	5FBC/38	77/01/0D	169
[AI2 min value]	CrL2	4433 = 16#1151	200E/22	5FBC/3E	77/01/22	170
[AI2 min value]	UIL2	4413 = 16#113D	200E/E	5FBC/39	77/01/0E	170
[AI3 min value]	CrL3	4434 = 16#1152	200E/23	5FBC/3F	77/01/23	172
[AI4 min value]	CrL4	4435 = 16#1153	200E/24	5FBC/40	77/01/24	174

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
[AI4 min value]	UIL4	4415 = 16#113D	200E/10	5FBC/3A	77/01/10	174
[AO1 min Output]	AOL1	4641 = 16#1221	2010/2A	5FBC/5F	78/01/2A	188
[AO1 min Output]	UOL1	4621 = 16#120D	2010/16	5FBC/59	78/01/16	189
[AO2 min Output]	AOL2	4642 = 16#1222	2010/2B	5FBC/60	78/01/2B	190
[AO2 min Output]	UOL2	4622 = 16#120E	2010/17	5FBC/5A	78/01/17	190
[AO3 min Output]	AOL3	4643 = 16#1223	2010/2C	5FBC/61	78/01/2C	191
[AO3 min Output]	UOL3	4623 = 16#120F	2010/18	5FBC/5B	78/01/18	191
[Freq. min value]	EIL	13310 = 16#33FE	2067/B	5FBD/5C	A3/01/6F	178
[RP min value]	PIL	13302 = 16#33F6	2067/3	5FBD/59	A3/01/67	176
[DB Resistor value]	brU	14113 = 16#3721	206F/E	5FBD/7E	A7/01/72	278
[Encoder check]	EnC	5605 = 16#15E5	201A/6	5FB3/7F	7D/01/06	160
[Encoder check]	EnC	5605 = 16#15E5	201A/6	5FB3/7F	7D/01/06	177
[Preset speed 10]	SP10	11418 = 16#2C9A	2054/13	5FBF/60	9A/01/13	213
[Preset speed 11]	SP11	11419 = 16#2C9B	2054/14	5FBF/61	9A/01/14	213
[Preset speed 12]	SP12	11420 = 16#2C9C	2054/15	5FBF/62	9A/01/15	213
[Preset speed 13]	SP13	11421 = 16#2C9D	2054/16	9F/01/63	9A/01/16	214
[Preset speed 14]	SP14	11422 = 16#2C9E	2054/17	5FBF/64	9A/01/17	214
[Preset speed 15]	SP15	11423 = 16#2C9F	2054/18	5FBF/65	9A/01/18	214
[Preset speed 16]	SP16	11424 = 16#2CA0	2054/19	5FBF/66	9A/01/19	214
[Preset speed 2]	SP2	11410 = 16#2C92	2054/B	5FB3/F0	9A/01/0B	211
[Preset speed 3]	SP3	11411 = 16#2C93	2054/C	5FB3/F1	9A/01/0C	212
[Preset speed 4]	SP4	11412 = 16#2C94	2054/D	5FB3/F2	9A/01/0D	212
[Preset speed 5]	SP5	11413 = 16#2C95	2054/E	5FB3/F3	9A/01/0E	212
[Preset speed 6]	SP6	11414 = 16#2C96	2054/F	5FB3/F4	9A/01/0F	212
[Preset speed 7]	SP7	11415 = 16#2C97	2054/10	5FB3/F5	9A/01/10	212
[Preset speed 8]	SP8	11416 = 16#2C98	2054/11	5FB3/F6	9A/01/11	213
[Preset speed 9]	SP9	11417 = 16#2C99	2054/12	5FBF/5F	9A/01/12	213
[Measurement spd]	OSP	12305 = 16#3011	205D/6	5FBF/8A	9E/01/6A	228
[Fallback speed]	LFF	7080 = 16#1BA8	2028/51	5FB3/C5	84/01/51	279
[Nom motor speed]	nSP	9604 = 16#2584	2042/5	5FB3/D6	91/01/05	149
[Nom motor spdsync]	nSPS	9671 = 16#25C7	2042/48	5FC2/1F	91/01/48	154

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Assignment for 2 configurations	CNF1	8021 = 16#1F55	2032/16	5FBC/9C	9C/01/9C	67
Assignment for 2 sets	CHA1	8021 = 16#1F55	2032/16	5FBC/9C	9C/01/9C	70
Assignment for 3 configurations	CNF2	8022 = 16#1F56	2032/17	5FBC/9D	9C/01/9D	67
Assignment for 3 sets	CHA2	8022 = 16#1F56	2032/17	5FBC/9D	9C/01/9D	70
Drive nominal rating	nCV	3011 = 16#0BC3	2000/C	5FB0/02	70/01/0C	124
Active command channel	CCC	8442 = 16#20FA	2036/2B	5FB9/CF	8B/01/2B	86
Active command channel on fault n-1	dCC1	64301 = 16#FB2D	2265/2	5FBA/09	9A/01/09	109
Active command channel on fault n-2	dCC2	64302 = 16#FB2E	2265/3	5FBA/0A	9A/01/0A	111
Active command channel on fault n-3	dCC3	64303 = 16#FB2F	2265/4	5FBA/0B	9A/01/0B	113
Active command channel on fault n-4	dCC4	64304 = 16#FB30	2265/5	5FBA/0C	9A/01/0C	115
Active command channel on fault n-5	dCC5	64305 = 16#FB31	2265/6	5FBA/0D	9A/01/0D	117
Active command channel on fault n-6	dCC6	64306 = 16#FB32	2265/7	5FBA/0E	9A/01/0E	119
Active command channel on fault n-7	dCC7	64307 = 16#FB33	2265/8	5FBA/0F	9A/01/0F	121
Active command channel on fault n-8	dCC8	64308 = 16#FB34	2265/9	5FBA/10	9A/01/10	123
Command channel active on last fault	dCC0	64300 = 16#FB2C	2265/1	5FBA/08	9A/01/08	106
Active reference channel	CRC	8441 = 16#20F9	2036/2A	5FB9/CE	8B/01/2A	85
Active reference channel on fault n-1	drC1	64311 = 16#FB37	2265/C	5FBA/12	9A/01/12	109
Active reference channel on fault n-2	drC2	64312 = 16#FB38	2265/D	5FBA/13	9A/01/13	111
Active reference channel on fault n-3	drC3	64313 = 16#FB39	2265/E	5FBA/14	9A/01/14	113
Active reference channel on fault n-4	drC4	64314 = 16#FB3A	2265/F	5FBA/15	9A/01/15	115
Active reference channel on fault n-5	drC5	64315 = 16#FB3B	2265/10	5FBA/16	9A/01/16	117
Active reference channel on fault n-6	drC6	64316 = 16#FB3C	2265/11	5FBA/17	9A/01/17	119
Active reference channel on fault n-7	drC7	64317 = 16#FB3D	2265/12	5FBA/18	9A/01/18	121
Active reference channel on fault n-8	drC8	64318 = 16#FB3E	2265/13	5FBA/19	9A/01/19	123
Reference channel active on last fault	drC0	64310 = 16#FB36	2265/B	5FBA/11	9A/01/11	106
Active channels on fault n-1	CrP1	7291 = 16#1C7B	202A/5C	5FB9/A7	85/01/5C	109

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Active channels on fault n-2	CrP2	7292 = 16#1C7C	202A/5D	5FB9/A8	85/01/5D	111
Active channels on fault n-3	CrP3	7293 = 16#1C7D	202A/5E	5FB9/A9	85/01/5E	113
Active channels on fault n-4	CrP4	7294 = 16#1C7E	202A/5F	5FB9/AA	85/01/5F	115
Active channels on fault n-5	CrP5	7295 = 16#1C7F	202A/60	5FB9/AB	85/01/60	117
Active channels on fault n-6	CrP6	7296 = 16#1C80	202A/61	5FB9/AC	85/01/61	119
Active channels on fault n-7	CrP7	7297 = 16#1C81	202A/62	5FB9/AD	85/01/62	121
Active channels on fault n-8	CrP8	7298 = 16#1C82	202A/63	5FB9/AE	85/01/63	123
Channels active on last fault	CrP0	7290 = 16#1C7A	202A/5B	5FB9/A6	85/01/5B	107
Altivar fault code	LFt	7121 = 16#1BD1	2029/16	5FB3/C8	84/01/7A	100
Fault code on fault n-1	dP1	7201 = 16#1C21	202A/2	5FB9/56	85/01/02	107
Fault code on fault n-2	dP2	7202 = 16#1C22	202A/3	5FB9/57	85/01/03	109
Fault code on fault n-3	dP3	7203 = 16#1C23	202A/4	5FB9/58	85/01/04	111
Fault code on fault n-4	dP4	7204 = 16#1C24	202A/5	5FB9/59	85/01/05	113
Fault code on fault n-5	dP5	7205 = 16#1C25	202A/6	5FB9/5A	85/01/06	115
Fault code on fault n-6	dP6	7206 = 16#1C26	202A/7	5FB9/5B	85/01/07	117
Fault code on fault n-7	dP7	7207 = 16#1C27	202A/8	5FB9/5C	85/01/08	119
Fault code on fault n-8	dP8	7208 = 16#1C28	202A/9	5FB9/5D	85/01/09	121
Fault code on last fault	dP0	7200 = 16#1C20	202A/1	5FB9/55	85/01/01	105
"Controller Inside" card fault code	APF	7133 = 16#1BDD	2029/22	5FB0/97	84/01/86	102
Network card fault code	CnF	7132 = 16#1BDC	2029/21	5FB0/96	84/01/85	102
Option card 1 fault code	ILF1	7134 = 16#1BDE	2029/23	5FB0/98	84/01/87	103
Option card 2 fault code	ILF2	7135 = 16#1BDF	2029/24	5FB0/99	84/01/88	103
DSP402 fault code	Errd	8606 = 16#219E	603F	5FB9/D7	8C/01/07	99
Ethernet fault code	Fdrv	64233 = 16#FAE9	2264/22	-	-	103
Multiplying coefficient	MFr	11831 = 16#E37	2058/20	5FB6/3E	9C/01/20	78
Load parameter set command	VAL	12901 = 16#3265	2063/02	5FB9/ED	A1/01/66	73
Fault counter	Fnb	7393 = 16#1CE1	202B/5E	5FB9/CC	99/01/CC	104
Encoder pulse counter	PGI	5604 = 16#15E4	201A/5	5FB3/7E	7D/01/05	98
Drive encoder divided counter	PUC	5611 = 16#15EB	201A/C	5FB9/41	7D/01/0C	132
Active configuration	CNFS	8020 = 16#1F54	2032/15	5FB9/CD	89/01/15	67
Incorrect configuration	CIC	7130 = 16#1BDA	2029/1F	5FB6/1A	84/01/83	102

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Torque reference	LTR	8505 = 16#2139	6071	6071	8B/01/6A	77
Torque reference (Nm)	LtCr	9261 = 16#242D	203E/3E	5FB6/3D	2A/01/0C	77
Frequency reference	LFR	8502 = 16#2136	2037/3	5FB6/1C	8B/01/67	77
Speed reference	LFRD	8602 = 16#219A	6042	6042	2A/01/08	77
PID regulator reference	PISP	8503 = 16#2137	2037/4	5FB6/1D	8B/01/68	78
Energy consumption	APH	3230 = 16#0C9E	2002/1F	5FB9/15	71/01/1F	92
Rotor time constant	trM	9665 = 16#25C1	2042/42	5FB9/E2	91/01/42	94
Output torque	Otr	3205 = 16#0C85	6077	6077	71/01/06	87
Output torque (Nm)	Otrn	3216 = 16#0C90	2002/11	5FB9/10	2A/01/0B	88
Magnetizing current	IdM	9650 = 16#25B2	2042/33	5FB9/E0	91/01/33	93
Motor current	LCr	3204 = 16#0C84	2002/5	5FB9/06	2A/01/09	88
Motor current on fault n-1	LCP1	7241 = 16#1C49	202A/2A	5FB9/7A	85/01/2A	108
Motor current on fault n-2	LCP2	7242 = 16#1C4A	202A/2B	5FB9/7B	85/01/2B	110
Motor current on fault n-3	LCP3	7243 = 16#1C4B	202A/2C	5FB9/7C	85/01/2C	112
Motor current on fault n-4	LCP4	7244 = 16#1C4C	202A/2D	5FB9/7D	85/01/2D	114
Motor current on fault n-5	LCP5	7245 = 16#1C4D	202A/2E	5FB9/7E	85/01/2E	116
Motor current on fault n-6	LCP6	7246 = 16#1C4E	202A/2F	5FB9/7F	85/01/2F	118
Motor current on fault n-7	LCP7	7247 = 16#1C4F	202A/30	5FB9/80	85/01/30	120
Motor current on fault n-8	LCP8	7248 = 16#1C50	202A/31	5FB9/81	85/01/31	122
Motor current on last fault	LCP0	7240 = 16#1C48	202A/29	5FB9/79	85/01/29	105
Rated drive current	InV	3017 = 16#0BC9	2000/12	5FB0/07	70/01/12	124
Date	dAY	7391 = 16#1CDF	202B/5C	5FB9/CA	85/01/C0	132
Date on fault n-1	Md1	7301 = 16#1C85	202B/2	5FB9/B0	85/01/66	109
Date on fault n-2	Md2	7302 = 16#1C86	202B/3	5FB9/B1	85/01/67	111
Date on fault n-3	Md3	7303 = 16#1C87	202B/4	5FB9/B2	85/01/68	113
Date on fault n-4	Md4	7304 = 16#1C88	202B/5	5FB9/B3	85/01/69	115
Date on fault n-5	Md5	7305 = 16#1C89	202B/6	5FB9/B4	85/01/6A	117
Date on fault n-6	Md6	7306 = 16#1C8A	202B/7	5FB9/BE	85/01/6B	119
Date on fault n-7	Md7	7307 = 16#1C8B	202B/8	5FB9/B6	85/01/6C	121
Date on fault n-8	Md8	7308 = 16#1C8C	202B/9	5FB9/B7	85/01/6D	123
Date on last fault	Md0	7300 = 16#1C84	202B/1	5FB9/AF	85/01/65	107

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Acceleration time delta	SPAt	8613 = 16#21A5	6048/2	6048/2	8C/01/0E	140
Deceleration time delta	SPdt	8616 = 16#21A8	6049/2	6049/2	8C/01/11	141
Acceleration speed delta	SPAL	8611 = 16#21A3	6048/1	6048/1	8C/01/0C	140
Deceleration speed delta	SPdL	8614 = 16#21A6	6049/1	6049/1	8C/01/0F	140
Speed setpoint factor denominator	SPFd	8643 = 16#21C3	604B/2	604B/2	8C/01/2C	142
Drive encoder counter divisor	Pdl	5610 = 16#15EA	201A/B	5FBC/76	7D/01/0B	131
PID regulator discrepancy	rPE	11980 = 16#2ECC	2059/51	5FB9/E5	9C/01/B5	91
Motor thermal state	tHr	9630 = 16#259E	2042/1F	5FB9/DE	91/01/1F	92
Motor thermal state on fault n-1	tHP1	7281 = 16#1C71	202A/52	5FB9/9E	85/01/52	108
Motor thermal state on fault n-2	tHP2	7282 = 16#1C72	202A/53	5FB9/9F	85/01/53	110
Motor thermal state on fault n-3	tHP3	7283 = 16#1C73	202A/54	5FB9/A0	85/01/54	112
Motor thermal state on fault n-4	tHP4	7284 = 16#1C74	202A/55	5FB9/A1	85/01/55	114
Motor thermal state on fault n-5	tHP5	7285 = 16#1C75	202A/56	5FB9/A2	85/01/56	116
Motor thermal state on fault n-6	tHP6	7286 = 16#1C76	202A/57	5FB9/A3	85/01/57	118
Motor thermal state on fault n-7	tHP7	7287 = 16#1C77	202A/58	5FB9/A4	85/01/58	120
Motor thermal state on fault n-8	tHP8	7288 = 16#1C78	202A/59	5FB9/A5	85/01/59	122
Motor thermal state on last fault	tHP0	7280 = 16#1C70	202A/51	5FB9/9D	85/01/51	105
DBR thermal state	tHb	14114 = 16#3722	206F/F	5FBD/7F	A7/01/73	92
Drive thermal state	tHd	3209 = 16#0C89	2002/A	5FB9/0B	71/01/0A	92
Output frequency	rFr	3202 = 16#C82	2002/3	5FB9/04	71/01/03	87
Output frequency on fault n-1	rFP1	7251 = 16#1C53	202A/34	5FB9/83	85/01/34	108
Output frequency on fault n-2	rFP2	7252 = 16#1C54	202A/35	5FB9/84	85/01/35	110
Output frequency on fault n-3	rFP3	7253 = 16#1C55	202A/36	5FB9/85	85/01/36	112
Output frequency on fault n-4	rFP4	7254 = 16#1C56	202A/37	5FB9/86	85/01/37	114
Output frequency on fault n-5	rFP5	7255 = 16#1C57	202A/38	5FB9/87	85/01/38	116
Output frequency on fault n-6	rFP6	7256 = 16#1C58	202A/39	5FB9/88	85/01/39	118
Output frequency on fault n-7	rFP7	7257 = 16#1C59	202A/3A	5FB9/89	85/01/3A	120
Output frequency on fault n-8	rFP8	7258 = 16#1C5A	202A/3B	5FB9/8A	85/01/3B	122
Output frequency on last fault	rFP0	7250 = 16#1C52	202A/33	5FB9/82	85/01/33	105
Time	tIME	7392 = 16#1CE0	202B/5D	5FB9/CB	85/01/C1	132
Time on fault n-1	dM1	7311 = 16#1C8F	202B/C	5FB9/B9	85/01/70	109

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Time on fault n-2	dM2	7312 = 16#1C90	202B/D	5FB9/BA	85/01/71	111
Time on fault n-3	dM3	7313 = 16#1C91	202B/E	5FB9/BB	85/01/72	113
Time on fault n-4	dM4	7314 = 16#1C92	202B/F	5FB9/BC	85/01/73	115
Time on fault n-5	dM5	7315 = 16#1C93	202B/10	5FB9/BD	85/01/74	117
Time on fault n-6	dM6	7316 = 16#1C94	202B/11	5FB9/B5	85/01/75	119
Time on fault n-7	dM7	7317 = 16#1C95	202B/12	5FB9/BF	85/01/76	121
Time on fault n-8	dM8	7318 = 16#1C96	202B/13	5FB9/C0	85/01/77	123
Time on last fault	dM0	7310 = 16#1C8E	202B/B	5FB9/B8	85/01/6F	107
Logic input map	IL1r	5202 = 16#1452	2016/3	5FB9/28	7B/01/03	95
"Controller Inside" logic input map	PIL1	6901 = 16#1AF5	2027/2	5FB9/49	83/01/66	131
Logic output map	OL1r	5212 = 16#145C	2016/D	5FB9/2A	7B/01/0D	95
"Controller Inside" logic output map	POL1	6911 = 16#1AFF	2027/C	5FB9/4A	83/01/70	131
Standardized image of analog input 1	AI1r	5232 = 16#1470	2016/21	5FB9/2F	7B/01/21	96
Standardized image of analog input 2	AI2r	5233 = 16#1471	2016/22	5FB9/30	7B/01/22	96
Standardized image of analog input 3	AI3r	5234 = 16#1472	2016/23	5FB9/31	7B/01/23	96
Standardized image of analog input 4	AI4r	5235 = 16#1473	2016/24	5FB9/32	7B/01/24	97
Standardized image of analog output 1	AO1r	5261 = 16#148D	2016/3E	5FB9/3A	7B/01/3E	97
Standardized image of analog output 2	AO2r	5262 = 16#148E	2016/3F	5FB9/3B	7B/01/3F	97
Standardized image of analog output 3	AO3r	5263 = 16#148F	2016/40	5FB9/3C	7B/01/40	98
Physical image of analog input 1	AI1C	5242 = 16#147A	2016/2B	5FB9/33	7B/01/2B	96
Physical image of analog input 1	PAI1	6942 = 16#1B1E	2027/2B	5FB9/4B	83/01/8F	131
Physical image of analog input 2	AI2C	5243 = 16#147B	2016/2C	5FB9/34	7B/01/2C	96
Physical image of analog input 2	PAI2	6943 = 16#1B1F	2027/2C	5FB9/4C	83/01/90	131
Physical image of analog input 3	AI3C	5244 = 16#147C	2016/2D	5FB9/35	7B/01/2D	96
Physical image of analog input 4	AI4C	5245 = 16#147D	2016/2E	5FB9/36	7B/01/2E	96
Physical image of analog output 1	AO1C	5271 = 16#1497	2016/48	5FB9/3D	7B/01/48	97
Physical image of analog output 1	PAO1	6971 = 16#1B3B	2027/48	5FB9/4D	83/01/AC	131
Physical image of analog output 2	AO2C	5272 = 16#1498	2016/49	5FB9/3E	7B/01/49	97
Physical image of analog output 2	PAO2	6972 = 16#1B3C	2027/49	5FB9/4E	83/01/AD	131

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Physical image of analog output 3	AO3C	5273 = 16#1499	2016/4A	5FB9/3F	7B/01/4A	97
Leakage inductance	LFM	9660 = 16#25BC	2042/3D	5FB9/E1	91/01/3D	94
Active parameter set	CFPS	12900 = 16#3264	2063/01	5FB9/EC	A1/01/65	70
Active parameter set	CFPS	12900 = 16#3264	2063/01	5FB9/EC	A1/01/65	86
Save configuration	SCS	8001 = 16#1F41	2032/2	5FBC/9A	9C/01/9A	69
"Controller Inside" HMI exchange word 1	O01	6401 = 16#1901	2022/2	5FB3/81	81/01/02	132
"Controller Inside" HMI exchange word 10	O10	6410 = 16#190A	2022/B	5FB3/8A	81/01/0B	133
"Controller Inside" HMI exchange word 11	O11	6411 = 16#190B	2022/C	5FB3/8B	81/01/0C	133
"Controller Inside" HMI exchange word 12	O12	6412 = 16#190C	2022/D	5FB3/8C	81/01/0D	134
"Controller Inside" HMI exchange word 13	O13	6413 = 16#190D	2022/E	5FB3/8D	81/01/0E	134
"Controller Inside" HMI exchange word 14	O14	6414 = 16#190E	2022/F	5FB3/8E	81/01/0F	134
"Controller Inside" HMI exchange word 15	O15	6415 = 16#190F	2022/10	5FB3/8F	81/01/10	134
"Controller Inside" HMI exchange word 16	O16	6416 = 16#1910	2022/11	5FB3/90	81/01/11	134
"Controller Inside" HMI exchange word 17	O17	6417 = 16#1911	2022/12	5FB3/91	81/01/12	134
"Controller Inside" HMI exchange word 18	O18	6418 = 16#1912	2022/13	5FB3/92	81/01/13	134
"Controller Inside" HMI exchange word 19	O19	6419 = 16#1913	2022/14	5FB3/93	81/01/14	135
"Controller Inside" HMI exchange word 2	O02	6402 = 16#1902	2022/3	5FB3/82	81/01/03	132
"Controller Inside" HMI exchange word 20	O20	6420 = 16#1914	2022/15	5FB3/94	81/01/15	135
"Controller Inside" HMI exchange word 21	O21	6421 = 16#1915	2022/16	5FB3/95	81/01/16	135
"Controller Inside" HMI exchange word 22	O22	6422 = 16#1916	2022/17	5FB3/96	81/01/17	135
"Controller Inside" HMI exchange word 23	O23	6423 = 16#1917	2022/18	5FB3/97	81/01/18	135
"Controller Inside" HMI exchange word 24	O24	6424 = 16#1918	2022/19	5FB3/98	81/01/19	135

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
"Controller Inside" HMI exchange word 25	O25	6425 = 16#1919	2022/1A	5FB3/99	81/01/1A	135
"Controller Inside" HMI exchange word 26	O26	6426 = 16#191A	2022/1B	5FB3/9A	81/01/1B	136
"Controller Inside" HMI exchange word 27	O27	6427 = 16#191B	2022/1C	5FB3/9B	81/01/1C	136
"Controller Inside" HMI exchange word 28	O28	6428 = 16#191C	2022/1D	5FB3/9C	81/01/1D	136
"Controller Inside" HMI exchange word 29	O29	6429 = 16#191D	2022/1E	5FB3/9D	81/01/1E	136
"Controller Inside" HMI exchange word 3	O03	6403 = 16#1903	2022/4	5FB3/83	81/01/04	132
"Controller Inside" HMI exchange word 30	O30	6430 = 16#191E	2022/1F	5FB3/9E	81/01/1F	136
"Controller Inside" HMI exchange word 31	O31	6431 = 16#191F	2022/20	5FB3/9F	81/01/20	136
"Controller Inside" HMI exchange word 32	O32	6432 = 16#1920	2022/21	5FB3/A0	81/01/21	136
"Controller Inside" HMI exchange word 33	O33	6433 = 16#1921	2022/22	5FB3/A1	81/01/22	137
"Controller Inside" HMI exchange word 34	O34	6434 = 16#1922	2022/23	5FB3/A2	81/01/23	137
"Controller Inside" HMI exchange word 35	O35	6435 = 16#1923	2022/24	5FB3/A3	81/01/24	137
"Controller Inside" HMI exchange word 36	O36	6436 = 16#1924	2022/25	5FB3/A4	81/01/25	137
"Controller Inside" HMI exchange word 37	O37	6437 = 16#1925	2022/26	5FB3/A5	81/01/26	137
"Controller Inside" HMI exchange word 38	O38	6438 = 16#1926	2022/27	5FB3/A6	81/01/27	137
"Controller Inside" HMI exchange word 39	O39	6439 = 16#1927	2022/28	5FB3/A7	81/01/28	137
"Controller Inside" HMI exchange word 4	O04	6404 = 16#1904	2022/5	5FB3/84	81/01/05	132
"Controller Inside" HMI exchange word 40	O40	6440 = 16#1928	2022/29	5FB3/A8	81/01/29	138
"Controller Inside" HMI exchange word 41	O41	6441 = 16#1929	2022/2A	5FB3/A9	81/01/2A	138
"Controller Inside" HMI exchange word 42	O42	6442 = 16#192A	2022/2B	5FB3/AA	81/01/2B	138
"Controller Inside" HMI exchange word 43	O43	6443 = 16#192B	2022/2C	5FB3/AB	81/01/2C	138

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
"Controller Inside" HMI exchange word 44	O44	6444 = 16#192C	2022/2D	5FB3/AC	81/01/2D	138
"Controller Inside" HMI exchange word 45	O45	6445 = 16#192D	2022/2E	5FB3/AD	81/01/2E	138
"Controller Inside" HMI exchange word 46	O46	6446 = 16#192E	2022/2F	5FB3/AE	81/01/2F	138
"Controller Inside" HMI exchange word 47	O47	6447 = 16#192F	2022/30	5FB3/AF	81/01/30	139
"Controller Inside" HMI exchange word 48	O48	6448 = 16#1930	2022/31	5FB3/B0	81/01/31	139
"Controller Inside" HMI exchange word 49	O49	6449 = 16#1931	2022/32	5FB3/B1	81/01/32	139
"Controller Inside" HMI exchange word 5	O05	6405 = 16#1905	2022/6	5FB3/85	81/01/06	133
"Controller Inside" HMI exchange word 50	O50	6450 = 16#1932	2022/33	5FC8/32	81/01/33	139
"Controller Inside" HMI exchange word 6	O06	6406 = 16#1906	2022/7	5FB3/86	81/01/07	133
"Controller Inside" HMI exchange word 7	O07	6407 = 16#1907	2022/8	5FB3/87	81/01/08	133
"Controller Inside" HMI exchange word 8	O08	6408 = 16#1908	2022/9	5FB3/88	81/01/09	133
"Controller Inside" HMI exchange word 9	O09	6409 = 16#1909	2022/A	5FB3/89	81/01/0A	133
Status word on fault n-1	EP1	7211 = 16#1C2B	202A/C	5FB9/5F	85/01/0C	108
Status word on fault n-2	EP2	7212 = 16#1C2C	202A/D	5FB9/60	85/01/0D	110
Status word on fault n-3	EP3	7213 = 16#1C2D	202A/E	5FB9/61	85/01/0E	112
Status word on fault n-4	EP4	7214 = 16#1C2E	202A/F	5FB9/62	85/01/0F	114
Status word on fault n-5	EP5	7215 = 16#1C2F	202A/10	5FB9/63	85/01/10	116
Status word on fault n-6	EP6	7216 = 16#1C30	202A/11	5FB9/64	85/01/11	118
Status word on fault n-7	EP7	7217 = 16#1C31	202A/12	5FB9/65	85/01/12	120
Status word on fault n-8	EP8	7218 = 16#1C32	202A/13	5FB9/66	85/01/13	122
Status word on last fault	EP0	7210 = 16#1C2A	202A/B	5FB9/5E	85/01/0B	105
Extended status word 0 on last fault	IP0	7220 = 16#1C34	202A/15	5FB9/67	85/01/15	106
Extended status word on fault n-1	IP1	7221 = 16#1C35	202A/16	5FB9/68	85/01/16	108
Extended status word on fault n-2	IP2	7222 = 16#1C36	202A/17	5FB9/69	85/01/17	110
Extended status word on fault n-3	IP3	7223 = 16#1C37	202A/18	5FB9/6A	85/01/18	112

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Extended status word on fault n-4	IP4	7224 = 16#1C38	202A/19	5FB9/6B	85/01/19	114
Extended status word on fault n-5	IP5	7225 = 16#1C39	202A/1A	5FB9/6C	85/01/1A	116
Extended status word on fault n-6	IP6	7226 = 16#1C3A	202A/1B	5FB9/6D	85/01/1B	118
Extended status word on fault n-7	IP7	7227 = 16#1C3B	202A/1C	5FB9/6E	85/01/1C	120
Extended status word on fault n-8	IP8	7228 = 16#1C3C	202A/1D	5FB9/6F	85/01/1D	122
Control word	CMd	8601 = 16#2199	6040	6040	B7/01/01	74
Command word on fault n-1	CMP1	7231 = 16#1C3F	202A/20	5FB9/71	85/01/20	108
Command word on fault n-2	CMP2	7232 = 16#1C40	202A/21	5FB9/72	85/01/21	110
Command word on fault n-3	CMP3	7233 = 16#1C41	202A/22	5FB9/73	85/01/22	112
Command word on fault n-4	CMP4	7234 = 16#1C42	202A/23	5FB9/74	85/01/23	114
Command word on fault n-5	CMP5	7235 = 16#1C43	202A/24	5FB9/75	85/01/24	116
Command word on fault n-6	CMP6	7236 = 16#1C44	202A/25	5FB9/76	85/01/25	118
Command word on fault n-7	CMP7	7237 = 16#1C45	202A/26	5FB9/77	85/01/26	120
Command word on fault n-8	CMP8	7238 = 16#1C46	202A/27	5FB9/78	85/01/27	122
Command word on last fault	CMP0	7230 = 16#1C3E	202A/1F	5FB9/70	85/01/1F	106
Extended control word	CMI	8504 = 16#2138	2037/5	5FB6/1E	8B/01/69	76
Status word	ETA	8603 = 16#219B	6041	6041	71/01/02	79
Extended status word 0	ETI	3206 = 16#0C86	2002/7	5FB9/08	71/01/07	81
Extended status word 1	LRS1	3250 = 16#0CB2	2002/33	5FB9/1C	71/01/33	81
Extended status word 3	LRS3	3252 = 16#0CB4	2002/35	5FB9/1E	71/01/35	82
Extended status word 4	LRS4	3253 = 16#0CB5	2002/36	5FB9/1F	71/01/36	83
Extended status word 5	LRS5	3254 = 16#0CB6	2002/37	5FB9/20	71/01/37	83
Extended status word 6	LRS6	3255 = 16#0CB7	2002/38	5FB9/21	71/01/38	84
Extended status word 7	LRS7	3256 = 16#0CB8	2002/39	5FB9/22	71/01/39	84
Extended status word 8	LRS8	3257 = 16#0C89	2002/3A	5FB9/23	71/01/3A	85
Extended status word 2	LRS2	3251 = 16#0CB3	2002/34	5FB9/1D	71/01/34	82
Device name: characters 1 and 2	PAn0	3340 = 16#0D0C	2003/29	5FB0/25	71/01/8D	124
Device name: characters 11 and 12	PAn5	3345 = 16#0D11	2003/2E	5FB0/2A	71/01/92	125
Device name: characters 13 and 14	PAn6	3346 = 16#0D12	2003/2F	5FB0/2B	71/01/93	125
Device name: characters 15 and 16	PAn7	3346 = 16#0D12	2003/30	5FB0/2C	71/01/94	125
Device name: characters 3 and 4	PAn1	3341 = 16#0D0D	2003/2A	5FB0/26	71/01/8E	124

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Device name: characters 5 and 6	PAn2	3342 = 16#0D0E	2003/2B	5FB0/27	71/01/8F	125
Device name: characters 7 and 8	PAn3	3343 = 16#0D0F	2003/2C	5FB0/28	71/01/90	125
Device name: characters 9 and 10	PAn4	3344 = 16#0D10	2003/2D	5FB0/29	71/01/91	125
Speed setpoint factor numerator	SPFn	8642 = 16#21C2	604B/1	604B/1	8C/01/2B	141
Motor power	OPr	3211 = 16#0C8B	2002/C	5FB9/0C	71/01/0C	88
Restore configuration	FCS	8002 = 16#1F42	2032/3	5FBC/9B	9C/01/9B	69
Torque reference after ramp	trO	9232 = 16#2410	203E/21	5FB9/DC	8F/01/21	90
Torque reference before ramp	trr	9231 = 16#240F	203E/20	5FB9/DB	8F/01/20	90
Frequency reference after ramp	FrO	9021 = 16#233D	203C/16	5FB9/D9	8E/01/16	89
Frequency reference before ramp	FrH	3203 = 16#0C83	2002/4	5FB9/05	71/01/04	89
PID regulator feedback reference	rPF	11981 = 16#2ECD	2059/52	5FB9/E6	9C/01/B6	91
Speed reference after ramp	FrOd	8641 = 16#21C1	6043	5FB9/D8	8C/01/2A	89
Speed reference before ramp	FrHd	8605 = 16#219D	2038/6	5FB9/D6	8C/01/06	89
PID regulator limit output reference	rPO	11983 = 16#2ECF	2059/54	5FB9/E8	9C/01/B8	91
PID reference after ramp	rPC	11982 = 16#2ECE	2059/53	5FB9/E7	9C/01/B7	91
Asynchronous motor cold state stator resistance	rSM	9640 = 16#25A8	2042/29	5FB9/DF	91/01/29	94
Synchronous motor cold state stator resistance	rSMS	9680 = 16#25D0	2042/51	5FB9/E3	91/01/51	94
PID regulator feedback	AIU1	5281 = 16 #14A1	2016/52	5FB9/40	7B/01/52	78
Communication scanner, address of write word 1	nCA1	12721 = 16#31B1	2061/16	5FBD/46	A0/01/7A	128
Communication scanner, address of write word 2	nCA2	12722 = 16#31B2	2061/17	5FBD/47	A0/01/7B	128
Communication scanner, address of write word 3	nCA3	12723 = 16#31B3	2061/18	5FBD/48	A0/01/7C	128
Communication scanner, address of write word 4	nCA4	12724 = 16#31B4	2061/19	5FBD/49	A0/01/7D	128
Communication scanner, address of write word 5	nCA5	12725 = 16#31B5	2061/1A	5FBD/4A	A0/01/7E	128
Communication scanner, address of write word 6	nCA6	12726 = 16#31B6	2061/1B	5FBD/4B	A0/01/7F	129
Communication scanner, address of write word 7	nCA7	12727 = 16#31B7	2061/1C	5FBD/4C	A0/01/80	129
Communication scanner, address of write word 8	nCA8	12728 = 16#31B8	2061/1D	5FBD/4D	A0/01/81	129

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Communication scanner, address of read word 1	nMA1	12701 = 16#319D	2061/2	5FBD/3E	A0/01/66	129
Communication scanner, address of read word 2	nMA2	12702 = 16#319E	2061/3	5FBD/3F	A0/01/67	129
Communication scanner, address of read word 3	nMA3	12703 = 16#319F	2061/4	5FBD/40	A0/01/68	129
Communication scanner, address of read word 4	nMA4	12704 = 16#31A0	2061/5	5FBD/41	A0/01/69	129
Communication scanner, address of read word 5	nMA5	12705 = 16#31A1	2061/6	5FBD/42	A0/01/6A	130
Communication scanner, address of read word 6	nMA6	12706 = 16#31A2	2061/7	5FBD/43	A0/01/6B	130
Communication scanner, address of read word 7	nMA7	12707 = 16#31A3	2061/8	5FBD/44	A0/01/6C	130
Communication scanner, address of read word 8	nMA8	12708 = 16#31A4	2061/9	5FBD/45	A0/01/6D	130
Communication scanner, value of write word 1	nC1	12761 = 16#31D9	2061/3E	5FB6/4B	A0/01/A2	126
Communication scanner, value of write word 2	nC2	12762 = 16#31DA	2061/3F	5FB6/4C	A0/01/A3	126
Communication scanner, value of write word 3	nC3	12763 = 16#31DB	2061/40	5FB6/4D	A0/01/A4	126
Communication scanner, value of write word 4	nC4	12764 = 16#31DC	2061/41	5FB6/4E	A0/01/A5	126
Communication scanner, value of write word 5	nC5	12765 = 16#31DD	2061/42	5FB6/4F	A0/01/A6	126
Communication scanner, value of write word 6	nC6	12766 = 16#31DE	2061/43	5FB6/50	A0/01/A7	126
Communication scanner, value of write word 7	nC7	12767 = 16#31DF	2061/44	5FB6/51	A0/01/A8	126
Communication scanner, value of write word 8	nC8	12768 = 16#31E0	2061/45	5FB6/52	A0/01/A9	127
Communication scanner, value of read word 1	nM1	12741 = 16#31C5	2061/2A	5FB6/43	A0/01/8E	127
Communication scanner, value of read word 2	nM2	12742 = 16#31C6	2061/2B	5FB6/44	A0/01/8F	127
Communication scanner, value of read word 3	nM3	12743 = 16#31C7	2061/2C	5FB6/45	A0/01/90	127
Communication scanner, value of read word 4	nM4	12744 = 16#31C8	2061/2D	5FB6/46	A0/01/91	127
Communication scanner, value of read word 5	nM5	12745 = 16#31C9	2061/2E	5FB6/47	A0/01/92	127

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Communication scanner, value of read word 6	nM6	12746 = 16#31CA	2061/2F	5FB6/48	A0/01/93	127
Communication scanner, value of read word 7	nM7	12747 = 16#31CB	2061/30	5FB6/49	A0/01/94	128
Communication scanner, value of read word 8	nM8	12748 = 16#31CC	2061/31	5FB6/4A	A0/01/95	128
ODVA acceleration time	ACCd	-	-	-	2A/01/12	143
IGBT alarm time	tAC	3235 = 16#0CA3	2002/24	5FB9/1A	71/01/24	93
ODVA deceleration time	dECd	-	-	-	2A/01/13	143
Motor operating time on last fault	rtP0	7260 = 16#1C5C	202A/3D	5FB9/8B	85/01/3D	107
Internal motor operating time	rtHI	3232 = 16#0CA0	2002/21	5FB9/17	71/01/21	93
Motor operating time on fault n-1	rtP1	7261 = 16#1C5D	202A/3E	5FB9/8C	85/01/3E	109
Motor operating time on fault n-2	rtP2	7262 = 16#1C5E	202A/3F	5FB9/8D	85/01/3F	111
Motor operating time on fault n-3	rtP3	7263 = 16#1C5F	202A/40	5FB9/8E	85/01/40	113
Motor operating time on fault n-4	rtP4	7264 = 16#1C60	202A/41	5FB9/8F	85/01/41	115
Motor operating time on fault n-5	rtP5	7265 = 16#1C61	202A/42	5FB9/90	85/01/42	117
Motor operating time on fault n-6	rtP6	7266 = 16#1C62	202A/43	5FB9/91	85/01/43	119
Motor operating time on fault n-7	rtP7	7267 = 16#1C63	202A/44	5FB9/92	85/01/44	121
Motor operating time on fault n-8	rtP8	7268 = 16#1C64	202A/45	5FB9/93	85/01/45	123
Total motor operating time	rtH	3231 = 16#0C9F	2002/20	5FB9/16	71/01/20	93
Total drive operating time	PtH	3233 = 16#0CA1	2002/22	5FB9/18	71/01/22	93
Current bobbin time	EbOt	12209 = 16#2FB1	205C/A	5FB9/EB	9E/01/0A	93
Supply voltage on fault n-1	ULP1	7271 = 16#1C67	202A/48	5FB9/95	85/01/48	108
Supply voltage on fault n-2	ULP2	7272 = 16#1C68	202A/49	5FB9/96	85/01/49	110
Supply voltage on fault n-3	ULP3	7273 = 16#1C69	202A/4A	5FB9/97	85/01/4A	112
Supply voltage on fault n-4	ULP4	7274 = 16#1C6A	202A/4B	5FB9/98	85/01/4B	114
Supply voltage on fault n-5	ULP5	7275 = 16#1C6B	202A/4C	5FB9/99	85/01/4C	116
Supply voltage on fault n-6	ULP6	7276 = 16#1C6C	202A/4D	5FB9/9A	85/01/4D	118
Supply voltage on fault n-7	ULP7	7277 = 16#1C6D	202A/4E	5FB9/9B	85/01/4E	120
Supply voltage on fault n-8	ULP8	7278 = 16#1C6E	202A/4F	5FB9/9C	85/01/4F	122
Power supply voltage	ULn	3207 = 16#0C87	2002/8	5FB9/09	71/01/08	92
Power supply voltage on last fault	ULP0	7270 = 16#1C66	202A/47	5FB9/94	85/01/47	105
Drive line voltage	UCAL	3012 = 16#0BC4	2000/D	5FB0/03	70/01/0D	124

Index of parameter names

Name	Code	Logic address	CANopen index	INTERBUS index	DeviceNet path	Page
Motor voltage	UOP	3208 = 16#0C88	2002/9	5FB9/0A	71/01/09	88
Drive software version	UdP	3302 = 16#0CE6	2003/3	5FB0/0D	71/01/67	124
Output speed	rFrd	8604 = 16#219C	6044	6044	2A/01/07	87
ENA average speed	AUS	12102 = 16#2F46	205B/3	5FB9/EA	9D/01/67	88

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
10001 = 16#2711	bLC	[Brake assignment]	2046/2	5FBC/F1	93/01/02	221
10003 = 16#2713	bEn	[Brake engage freq]	2046/4	5FBF/36	93/01/04	222
10004 = 16#2714	brt	[Brake Release time]	2046/5	5FB3/DD	93/01/05	222
10005 = 16#2715	bEt	[Brake engage time]	2046/6	5FB3/DE	93/01/06	223
10006 = 16#2716	lbr	[Brake release I FW]	2046/7	5FB3/DF	93/01/07	222
10007 = 16#2717	bIP	[Brake impulse]	2046/8	5FB3/E0	93/01/08	221
10008 = 16#2718	bSt	[Movement type]	2046/9	5FBF/3B	93/01/09	221
10009 = 16#2719	bCI	[Brake contact]	2046/A	5FBC/F2	93/01/0A	221
10010 = 16#271A	tbE	[Brake engage delay]	2046/B	5FBF/3C	93/01/0B	223
10011 = 16#271B	lrd	[Brake release I Rev]	2046/C	5FBF/3D	93/01/0C	222
10012 = 16#271C	blr	[Brake release freq]	2046/D	5FB3/E1	93/01/0D	222
10013 = 16#271D	JdC	[Jump at reversal]	2046/E	5FBF/3F	93/01/0E	223
10015 = 16#271F	brr	[Current ramp time]	2046/10	5FBF/40	93/01/10	225
10020 = 16#2724	bEd	[Engage at reversal]	2046/15	5FBF/41	93/01/15	223
10022 = 16#2726	ttr	[Time to restart]	2046/17	5FBF/42	93/01/17	224
10050 = 16# 2742 (bit 0)	brH0	[BRH b0]	2046/33 (bit 0)	5FBC/F3 (bit 0)	93/01/33 (bit 0)	224
10050 = 16# 2742 (bit 1)	brH1	[BRH b1]	2046/33 (bit 1)	5FBC/F3 (bit 1)	93/01/33 (bit 1)	224
10050 = 16# 2742 (bit 2)	brH2	[BRH b2]	2046/33 (bit 2)	5FBC/F3 (bit 2)	93/01/33 (bit 2)	224
10050 = 16# 2742 (bit 3)	brH3	[BRH b3]	2046/33 (bit 3)	5FBC/F3 (bit 3)	93/01/33 (bit 3)	224
10050 = 16# 2742 (bit 4)	brH4	[BRH b4]	2046/33 (bit 4)	5FBC/F3 (bit 4)	93/01/33 (bit 4)	225
10070 = 16#2756	PES	[Weight sensor ass.]	2046/47	5FBC/F4	93/01/47	226
10071 = 16#2757	LP1	[Point 1 X]	2046/48	5FBC/F5	93/01/48	226
10072 = 16#2758	CP1	[Point 1Y]	2046/49	5FBC/F6	93/01/49	226
10073 = 16#2759	LP2	[Point 2 X]	2046/4A	5FBC/F7	93/01/4A	226
10074 = 16#275A	CP2	[Point 2Y]	2046/4B	5FBC/F8	93/01/4B	227
10075 = 16#275B	IbrA	[Ibr 4-20 mA loss]	2046/4C	5FBC/F9	93/01/4C	227
10401 = 16#28A1	AdC	[Auto DC injection]	204A/2	5FB3/E2	95/01/02	208
10402 = 16#28A2	tdC1	[Auto DC inj. time 1]	204A/3	5FBF/44	95/01/03	208

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
10403 = 16#28A3	SdC1	[Auto DC inj. level 1]	204A/4	5FBF/45	95/01/04	208
10403 = 16#28A3	SdC1	[Auto DC inj. level 1]	204A/4	5FBF/45	95/01/04	223
10404 = 16#28A4	tdC2	[Auto DC inj. time 2]	204A/5	5FBF/46	95/01/05	208
10405 = 16#28A5	SdC2	[Auto DC inj. level 2]	204A/6	5FBF/47	95/01/06	208
11001 = 16#2AF9	Ctd	[Current threshold]	2050/2	5FB3/E3	98/01/02	146
11002 = 16#2AFA	ttd	[Motor therm. level]	2050/3	5FB3/E4	98/01/03	264
11002 = 16#2AFA	ttd	[Motor therm. level]	2050 / 3	5FB3/E4	98/01/03	268
11003 = 16#2AFB	Ftd	[Freq. threshold]	2050/4	5FB3/E5	98/01/04	147
11004 = 16#2AFC	F2d	[Freq. threshold 2]	2050/5	5FB3/E6	98/01/05	147
11006 = 16#2AFE	ttd2	[Motor2 therm. level]	2050/7	5FBF/4C	98/01/07	264
11006 = 16#2AFE	ttd2	[Motor2 therm. level]	2050/7	5FBF/4C	98/01/07	268
11007 = 16#2AFF	ttd3	[Motor3 therm. level]	2050/8	5FBF/4D	98/01/08	264
11007 = 16#2AFF	ttd3	[Motor3 therm. level]	2050/8	5FBF/4D	98/01/08	268
11009 = 16#2B01	tHA	[Drv therm. state al]	2050/A	5FBF/4E	98/01/0A	267
11009 = 16#2B01	tHA	[Drv therm. state al]	2050/A	5FBF/4E	98/01/0A	268
11021 = 16#2B0D	SAt	[Thermal alarm stop]	2050/16	5FB3/E7	98/01/16	268
11101 = 16#2B5D	tCC	[2/3 wire control]	2051/2	5FB3/E8	98/01/66	166
11102 = 16#2B5E	tCt	[2 wire type]	2051/3	5FB3/E9	98/01/67	166
11105 = 16#2B61	rrS	[Reverse assign.]	2051/6	5FBC/FE	98/01/6A	166
11110 = 16#2B66	JOG	[JOG]	2051/B	5FBC/FF	98/01/6F	209
11111 = 16#2B67	JGF	[Jog frequency]	2051/C	5FBF/50	98/01/70	209
11112 = 16#2B68	JGT	[Jog delay]	2051/D	5FB3/EA	98/01/71	209
11201 = 16#2BC1	Stt	[Stop type]	2052/2	5FB3/EB	99/01/02	205
11202 = 16#2BC2	nSt	[Freewheel stop ass.]	2052/3	5FBD/02	99/01/03	205
11203 = 16#2BC3	dCI	[DC injection assign.]	2052/4	5FBD/03	99/01/04	206
11204 = 16#2BC4	FSt	[Fast stop assign.]	2052/5	5FBD/04	99/01/05	205
11210 = 16#2BCA	IdC	[DC inject. level 1]	2052/B	5FB3/EC	99/01/0B	206
11210 = 16#2BCA	IdC	[DC inject. level 1]	2052/B	5FB3/EC	99/01/0B	281
11211 = 16#2BCB	tdC	[DC injection time 2]	2052/C	5FBF/53	99/01/0C	207
11211 = 16#2BCB	tdC	[DC injection time 2]	2052/C	5FBF/53	99/01/0C	281
11212 = 16#2BCC	IdC2	[DC inject. level 2]	2052/D	5FBF/54	99/01/0D	207

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
11212 = 16#2BCC	IdC2	[DC inject. level 2]	2052/D	5FBF/54	99/01/0D	281
11213 = 16#2BCD	tdl	[DC injection time 1]	2052/E	5FBF/55	99/01/0E	206
11213 = 16#2BCD	tdl	[DC injection time 1]	2052/E	5FBF/55	99/01/0E	281
11230 = 16#2BDE	dCF	[Ramp divider]	2052/1F	5FB3/ED	99/01/1F	206
11230 = 16#2BDE	dCF	[Ramp divider]	2052/1F	5FB3/ED	99/01/1F	280
11401 = 16#2C89	PS2	[2 preset speeds]	2054/2	5FBD/06	9A/01/02	210
11402 = 16#2C8A	PS4	[4 preset speeds]	2054/3	5FBD/07	9D/01/07	210
11403 = 16#2C8B	PS8	[8 preset speeds]	2054/4	5FBD/08	9A/01/04	211
11404 = 16#2C8C	PS16	[16 preset speeds]	2054/5	5FBD/09	9A/01/05	211
11410 = 16#2C92	SP2	[Preset speed 2]	2054/B	5FB3/F0	9A/01/0B	211
11411 = 16#2C93	SP3	[Preset speed 3]	2054/C	5FB3/F1	9A/01/0C	212
11412 = 16#2C94	SP4	[Preset speed 4]	2054/D	5FB3/F2	9A/01/0D	212
11413 = 16#2C95	SP5	[Preset speed 5]	2054/E	5FB3/F3	9A/01/0E	212
11414 = 16#2C96	SP6	[Preset speed 6]	2054/F	5FB3/F4	9A/01/0F	212
11415 = 16#2C97	SP7	[Preset speed 7]	2054/10	5FB3/F5	9A/01/10	212
11416 = 16#2C98	SP8	[Preset speed 8]	2054/11	5FB3/F6	9A/01/11	213
11417 = 16#2C99	SP9	[Preset speed 9]	2054/12	5FBF/5F	9A/01/12	213
11418 = 16#2C9A	SP10	[Preset speed 10]	2054/13	5FBF/60	9A/01/13	213
11419 = 16#2C9B	SP11	[Preset speed 11]	2054/14	5FBF/61	9A/01/14	213
11420 = 16#2C9C	SP12	[Preset speed 12]	2054/15	5FBF/62	9A/01/15	213
11421 = 16#2C9D	SP13	[Preset speed 13]	2054/16	9F/01/63	9A/01/16	214
11422 = 16#2C9E	SP14	[Preset speed 14]	2054/17	5FBF/64	9A/01/17	214
11423 = 16#2C9F	SP15	[Preset speed 15]	2054/18	5FBF/65	9A/01/18	214
11424 = 16#2CA0	SP16	[Preset speed 16]	2054/19	5FBF/66	9A/01/19	214
11501 = 16#2CED	USP	[+ speed assignment]	2055/2	5FBD/0A	9A/01/66	215
11502 = 16#2CEE	dSP	[-Speed assignment]	2055/3	5FBD/0B	9A/01/67	215
11503 = 16#2CEF	Str	[Reference saved]	2055/4	5FBD/0C	9A/01/68	215
11505 = 16#2CF1	SrP	[+/-Speed limitation]	2055/6	5FB3/F7	9A/01/6A	216
11520 = 16#2D00	USI	[+ speed assignment]	2055/15	5FBD/0E	9D/01/0E	216
11521 = 16#2D01	dSI	[-Speed assignment]	2055/16	5FBD/0F	9A/01/7A	216
11601 = 16#2D51	LAF	[Stop FW limit sw.]	2056/2	5FBD/10	9B/01/02	220

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
11602 = 16#2D52	LAr	[Stop RV limit sw.]	2056/3	5FBD/11	9B/01/03	220
11603 = 16#2D53	LAS	[Stop type]	2056/4	5FBD/12	9B/01/04	220
11701 = 16#2DB5	tLS	[Low speed time out]	2057/2	5FB3/F8	9B/01/66	146
11701 = 16#2DB5	tLS	[Low speed time out]	2057/2	5FB3/F8	9B/01/66	235
11801 = 16#2E19	SA2	[Summing ref. 2]	2058/2	5FBD/13	9C/01/02	199
11802 = 16#2E1A	SA3	[Summing ref. 3]	2058/3	5FBD/14	9C/01/03	199
11811 = 16#2E23	dA2	[Subtract ref. 2]	2058/C	5FBD/15	9C/01/0C	199
11812 = 16#2E24	dA3	[Subtract ref. 3]	2058/D	5FBD/16	9C/01/0D	199
11821 = 16#2E2D	MA2	[Multiplier ref. 2]	2058/16	5FBD/17	9C/01/16	200
11822 = 16#2E2E	MA3	[Multiplier ref. 3]	2058/17	5FBD/18	9C/01/17	200
11831 = 16#2E37	MFr	Multiplying coefficient	2058/20	5FB6/3E	9C/01/20	78
11901 = 16#2E7D	PIF	[PID feedback ass.]	2059/2	5FBD/19	9C/01/66	230
11904 = 16#2E80	PIF1	[Min PID feedback]	2059/5	5FBF/6A	9C/01/69	230
11905 = 16#2E81	PIF2	[Max PID feedback]	2059/6	5FBF/6B	9C/01/6A	230
11906 = 16#2E82	PIP1	[Min PID reference]	2059/7	5FBF/6C	9C/01/6B	231
11907 = 16#2E83	PIP2	[Max PID reference]	2059/8	5FBF/6D	9C/01/6C	231
11908 = 16#2E84	PII	[Act. internal PID ref.]	2059/9	5FBD/1A	9C/01/6D	231
11909 = 16#2E85	Pr2	[2 preset PID ref.]	2059/A	5FBD/1B	9C/01/6E	236
11910 = 16#2E86	Pr4	[4 preset PID ref.]	2059/B	5FBD/1C	9C/01/6F	236
11920 = 16#2E90	RPI	[Internal PID ref.]	2059/15	5FBF/6E	9C/01/79	231
11921 = 16#2E91	rP2	[Preset ref. PID 2]	2059/16	5FBF/6F	9C/01/7A	237
11922 = 16#2E92	rP3	[Preset ref. PID 3]	2059/17	5FBF/70	9C/01/7B	237
11923 = 16#2E93	rP4	[Preset ref. PID 4]	2059/18	9F/01/71	9C/01/7C	237
11940 = 16#2EA4	PIC	[PID correct. reverse]	2059/29	5FB3/F9	9C/01/8D	232
11941 = 16#2EA5	RPG	[PID prop. gain]	2059/2A	5FB3/FA	9C/01/8E	231
11942 = 16#2EA6	rIG	[PID integral gain]	2059/2B	5FB3/FB	9C/01/8F	232
11943 = 16#2EA7	rdG	[PID derivative gain]	2059/2C	5FB3/FC	9C/01/90	232
11944 = 16#2EA8	PIS	[PID integral reset]	2059/2D	5FBD/1E	9C/01/91	234
11950 = 16#2EAE	FPI	[Speed ref. assign.]	2059/33	5FBD/1F	9C/01/97	234
11951 = 16#2EAF	PSr	[Speed input %]	2059/34	5FB3/FD	9C/01/98	234
11952 = 16#2EB0	POL	[Min PID output]	2059/35	5FBF/76	9C/01/99	232

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
11953 = 16#2EB1	POH	[Max PID output]	2059/36	5FBF/77	9C/01/9A	233
11954 = 16#2EB2	PIM	[Manual reference]	2059/37	5FBD/20	9C/01/9B	235
11960 = 16#2DB5	rSL	[PID wake up thresh.]	2059/3D	5FBD/21	9C/01/A1	235
11961 = 16#2EB9	PAL	[Min fbk alarm]	2059/3E	5FB3/FE	9C/01/A2	233
11962 = 16#2EBA	PAH	[Max fbk alarm]	2059/3F	5FB3/FF	9C/01/A3	233
11963 = 16#2EBB	PEr	[PID error Alarm]	2059/40	5FB4/01	9C/01/A4	233
11970 = 16#2EC2	PAU	[Auto/Manual assign.]	2059/47	5FBD/22	9C/01/AB	235
11980 = 16#2ECC	rPE	PID regulator discrepancy	2059/51	5FB9/E5	9C/01/B5	91
11981 = 16#2ECD	rPF	PID regulator feedback reference	2059/52	5FB9/E6	9C/01/B6	91
11982 = 16#2ECE	rPC	PID reference after ramp	2059/53	5FB9/E7	9C/01/B7	91
11983 = 16#2ECF	rPO	PID regulator limit output reference	2059/54	5FB9/E8	9C/01/B8	91
11984 = 16#2ED0	PrP	[PID ramp]	2059/55	5FBF/7B	9C/01/B9	232
12101 = 16#2F45	EnA	[ENA system]	205B/2	5FBD/23	9D/01/66	161
12102 = 16#2F46	AUS	ENA average speed	205B/3	5FB9/EA	9D/01/67	88
12103 = 16#2F47	GPE	[ENA prop.gain]	205B/4	5FBF/7C	9D/01/68	161
12104 = 16#2F48	GIE	[ENA integral gain]	205B/5	5FBF/7D	9D/01/69	161
12105 = 16#2F49	rAP	[Reduction ratio]	205B/6	5FBF/7E	9D/01/6A	161
12201 = 16#2FA9	trC	[Yarn control]	205C/2	5FBD/24	9E/01/02	254
12202 = 16#2FAA	trH	[Traverse high]	205C/3	5FBF/7F	9E/01/03	254
12203 = 16#2FAB	trL	[Traverse Low]	205C/4	5FBF/80	9E/01/04	254
12204 = 16#2FAC	qSH	[Quick step High]	205C/5	5FBF/81	9E/01/05	254
12205 = 16#2FAD	qSL	[Quick step Low]	205C/6	5FBF/82	9E/01/06	254
12206 = 16#2FAE	tUP	[Traverse ctrl. accel.]	205C/7	5FBF/83	9E/01/07	255
12207 = 16#2FAF	tdn	[Traverse ctrl. decel]	205C/8	5FBF/84	9E/01/08	255
12208 = 16#2FAB	tbO	[Reel time]	205C/9	5FBF/85	9E/01/09	255
12209 = 16#2FB1	EbOt	Current bobbin time	205C/A	5FB9/EB	9E/01/0A	93
12210 = 16#2FB2	rtr	[Init. traverse ctrl]	205C/B	5FBD/25	9E/01/0B	257
12211 = 16#2FB3	dtF	[Decrease ref. speed]	205C/C	5FBF/86	9E/01/0C	256
12212 = 16#2FB4	SnC	[Counter wobble]	205C/D	5FBD/26	9E/01/0D	256
12213 = 16#2FB5	EbO	[End reel]	205C/E	5FBD/27	9E/01/0E	255
12214 = 16#2FB6	tSY	[Sync. wobble]	205C/F	5FBD/28	9E/01/0F	256

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
12301 = 16#300D	HSO	[High speed hoisting]	205D/2	5FBD/29	9E/01/66	228
12302 = 16#300E	CLO	[High speed l Limit]	205D/3	5FBF/87	9E/01/67	229
12303 = 16#300F	COF	[Motor speed coeff.]	205D/4	5FBF/88	9E/01/68	228
12304 = 16#3010	COr	[Gen. speed coeff]	205D/5	5FBF/89	9E/01/69	228
12305 = 16#3011	OSP	[Measurement spd]	205D/6	5FBF/8A	9E/01/6A	228
12306 = 16#3012	SCL	[l Limit. frequency]	205D/7	5FBF/8B	9E/01/6B	229
12307 = 16#3013	tOS	[Load measuring tm.]	205D/8	5FBF/8C	9E/01/6C	228
12401 = 16#3071	U0	[U0]	205E/2	5FBD/2A	9F/01/02	150
12403 = 16#3073	U1	[U1]	205E/4	5FBD/2B	9F/01/04	151
12404 = 16#3074	F1	[F1]	205E/5	5FBD/2C	9F/01/05	151
12405 = 16#3075	U2	[U2]	205E/6	5FBD/2D	9F/01/06	151
12406 = 16#3076	F2	[F2]	205E/7	5FBD/2E	9F/01/07	151
12407 = 16#3077	U3	[U3]	205E/8	5FBD/2F	9F/01/08	151
12408 = 16#3078	F3	[F3]	205E/9	5FBD/30	9F/01/09	152
12409 = 16#3079	U4	[U4]	205E/9	5FBD/31	9F/01/0A	152
12410 = 16#307A	F4	[F4]	205E/B	5FBD/32	9F/01/0B	152
12411 = 16#307B	U5	[U5]	205E/C	5FBD/33	9F/01/0C	152
12412 = 16#307C	F5	[F5]	205E/D	5FBD/34	9F/01/0D	152
12501 = 16#30D5	SAF	[Stop FW limit sw.]	205F/2	5FBD/35	9F/01/66	247
12502 = 16#30D6	SAr	[Stop RV limit sw.]	205F/3	5FBD/36	9F/01/67	247
12503 = 16#30D7	dAF	[Slowdown forward]	205F/4	5FBD/37	9F/01/68	248
12504 = 16#30D8	dAr	[Slowdown reverse]	205F/5	5FBD/38	9F/01/69	248
12505 = 16#30D9	dSF	[Deceleration type]	205F/6	5FBD/39	9F/01/6A	249
12506 = 16#30DA	PAS	[Stop type]	205F/7	5FBD/3A	9F/01/6B	249
12507 = 16#30DB	CLS	[Disable limit sw.]	205F/8	5FBD/3B	9F/01/6C	248
12601 = 16#3139	SUL	[Motor surge limit.]	2060/2	5FBD/3C	A0/01/02	162
12602 = 16#313A	SOP	[Volt surge limit. opt]	2060/3	5FBD/3D	A0/01/03	163
12701 = 16#319D	nMA1	Communication scanner, address of read word 1	2061/2	5FBD/3E	A0/01/66	129
12702 = 16#319E	nMA2	Communication scanner, address of read word 2	2061/3	5FBD/3F	A0/01/67	129
12703 = 16#319F	nMA3	Communication scanner, address of read word 3	2061/4	5FBD/40	A0/01/68	129

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
12704 = 16#31A0	nMA4	Communication scanner, address of read word 4	2061/5	5FBD/41	A0/01/69	129
12705 = 16#31A1	nMA5	Communication scanner, address of read word 5	2061/6	5FBD/42	A0/01/6A	130
12706 = 16#31A2	nMA6	Communication scanner, address of read word 6	2061/7	5FBD/43	A0/01/6B	130
12707 = 16#31A3	nMA7	Communication scanner, address of read word 7	2061/8	5FBD/44	A0/01/6C	130
12708 = 16#31A4	nMA8	Communication scanner, address of read word 8	2061/9	5FBD/45	A0/01/6D	130
12721 = 16#31B1	nCA1	Communication scanner, address of write word 1	2061/16	5FBD/46	A0/01/7A	128
12722 = 16#31B2	nCA2	Communication scanner, address of write word 2	2061/17	5FBD/47	A0/01/7B	128
12723 = 16#31B3	nCA3	Communication scanner, address of write word 3	2061/18	5FBD/48	A0/01/7C	128
12724 = 16#31B4	nCA4	Communication scanner, address of write word 4	2061/19	5FBD/49	A0/01/7D	128
12725 = 16#31B5	nCA5	Communication scanner, address of write word 5	2061/1A	5FBD/4A	A0/01/7E	128
12726 = 16#31B6	nCA6	Communication scanner, address of write word 6	2061/1B	5FBD/4B	A0/01/7F	129
12727 = 16#31B7	nCA7	Communication scanner, address of write word 7	2061/1C	5FBD/4C	A0/01/80	129
12728 = 16#31B8	nCA8	Communication scanner, address of write word 8	2061/1D	5FBD/4D	A0/01/81	129
12741 = 16#31C5	nM1	Communication scanner, value of read word 1	2061/2A	5FB6/43	A0/01/8E	127
12742 = 16#31C6	nM2	Communication scanner, value of read word 2	2061/2B	5FB6/44	A0/01/8F	127
12743 = 16#31C7	nM3	Communication scanner, value of read word 3	2061/2C	5FB6/45	A0/01/90	127
12744 = 16#31C8	nM4	Communication scanner, value of read word 4	2061/2D	5FB6/46	A0/01/91	127
12745 = 16#31C9	nM5	Communication scanner, value of read word 5	2061/2E	5FB6/47	A0/01/92	127
12746 = 16#31CA	nM6	Communication scanner, value of read word 6	2061/2F	5FB6/48	A0/01/93	127
12747 = 16#31CB	nM7	Communication scanner, value of read word 7	2061/30	5FB6/49	A0/01/94	128
12748 = 16#31CC	nM8	Communication scanner, value of read word 8	2061/31	5FB6/4A	A0/01/95	128

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
12761 = 16#31D9	nC1	Communication scanner, value of write word 1	2061/3E	5FB6/4B	A0/01/A2	126
12762 = 16#31DA	nC2	Communication scanner, value of write word 2	2061/3F	5FB6/4C	A0/01/A3	126
12763 = 16#31DB	nC3	Communication scanner, value of write word 3	2061/40	5FB6/4D	A0/01/A4	126
12764 = 16#31DC	nC4	Communication scanner, value of write word 4	2061/41	5FB6/4E	A0/01/A5	126
12765 = 16#31DD	nC5	Communication scanner, value of write word 5	2061/42	5FB6/4F	A0/01/A6	126
12766 = 16#31DE	nC6	Communication scanner, value of write word 6	2061/43	5FB6/50	A0/01/A7	126
12767 = 16#31DF	nC7	Communication scanner, value of write word 7	2061/44	5FB6/51	A0/01/A8	126
12768 = 16#31E0	nC8	Communication scanner, value of write word 8	2061/45	5FB6/52	A0/01/A9	127
12900 = 16#3264	CFPS	Active parameter set	2063/01	5FB9/EC	A1/01/65	70
12900 = 16#3264	CFPS	Active parameter set	2063/01	5FB9/EC	A1/01/65	86
12901 = 16#3265	VAL	Load parameter set command	2063/02	5FB9/ED	A1/01/66	73
12902 = 16#3266	CHA1	[2 Parameter sets]	2063/3	5FB9/54	A1/01/67	250
12903 = 16#3267	CHA2	[3 Parameter sets]	2063/4	5FB9/55	A1/01/68	250
13101 = 16#332D	dbS	[Time to motor run]	2065/2	5FB4/02	A2/01/66	245
13102 = 16#332E	dAS	[Time to open cont.]	2065/3	5FB4/03	A2/01/67	246
13103 = 16#332F	rCA	[Output contact. fdbk]	2065/4	5FB4/56	A2/01/68	245
13104 = 16#3330	OCC	[Out. contactor ass.]	2065/5	5FB4/57	A2/01/69	245
13201 = 16#3391	PtC1	[PTC1 probe]	2066/2	5FBF/CB	A3/01/02	260
13202 = 16#3392	PtC2	[PTC2 probe]	2066/3	5FBF/CC	A3/01/03	260
13203 = 16#3396	PtCL	[LI6 = PTC probe]	2066/4	5FBF/CD	A3/01/04	260
13301 = 16#33F5	PGA	[Reference type]	2067/2	5FBF/58	A3/01/66	177
13302 = 16#33F6	PIL	[RP min value]	2067/3	5FBF/59	A3/01/67	176
13303 = 16#33F7	PFr	[RP max value]	2067/4	5FBF/5A	A3/01/68	176
13304 = 16#33F8	PFI	[RP filter]	2067/5	5FBF/5B	A3/01/69	176
13310 = 16#33FE	EIL	[Freq. min value]	2067/B	5FBF/5C	A3/01/6F	178
13311 = 16#33FF	EFr	[Freq. max value]	2067/C	5FBF/5D	A3/01/70	178
13312 = 16#3400	EFI	[Freq. signal filter]	2067/D	5FBF/5E	A3/01/71	178

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
13401 = 16#3459	P <small>H<small>r</small></small>	[Output Ph rotation]	2068/2	5FBD/5F	A4/01/02	150
13501 = 16#34BD	F <small>N<small>1</small></small>	[F1 key assignment]	2069/2	5FBD/60	A4/01/66	196
13502 = 16#34BE	F <small>N<small>2</small></small>	[F2 key assignment]	2069/3	5FBD/61	A4/01/67	196
13503 = 16#34BF	F <small>N<small>3</small></small>	[F3 key assignment]	2069/4	5FBD/62	A4/01/68	196
13504 = 16#34C0	F <small>N<small>4</small></small>	[F4 key assignment]	2069/5	5FBD/63	A4/01/69	197
13529 = 16#34D9	bMP	[HMI cmd.]	2069/1E	5FBD/6C	A4/01/82	197
13601 = 16#3521	L <small>E<small>S</small></small>	[Drive lock]	206A/2	5FBD/6D	A5/01/02	244
13602 = 16#3522	L <small>C<small>L</small></small>	[Line contactor ass.]	206A/3	5FBD/6E	A5/01/03	244
13603 = 16#3523	L <small>C<small>t</small></small>	[Mains V. time out]	206A/4	5FBD/6F	A5/01/04	244
13801 = 16#35E9	UrES	[Evacuation Input V.]	206C/2	5FBD/70	A6/01/02	270
13802 = 16#35EA	USL	[Undervoltage level]	206C/3	5FBD/71	A6/01/03	270
13803 = 16#35EB	USb	[UnderV. fault mgt]	206C/4	5FBD/72	A6/01/04	270
13804 = 16#35EC	USt	[Undervolt. time out]	206C/5	5FBD/73	A6/01/05	270
13811 = 16#35F3	UPL	[Prevention level]	206C/C	5FBD/74	A6/01/0C	271
13812 = 16#35F4	tbS	[DC bus maintain tm]	206C/D	5FBF/CE	A6/01/0D	271
13813 = 16#35F5	tSM	[UnderV. restart tm]	206C/E	5FBF/CF	A6/01/0E	271
13814 = 16#35EC	StM	[Max stop time]	206C/F	5FBF/D0	A6/01/0F	271
13831 = 16#3607	rFt	[Evacuation assign.]	206C/20	5FBD/76	A6/01/20	258
13832 = 16#3608	rSU	[Evacuation Input V.]	206C/21	5FBD/77	A6/01/21	258
13833 = 16#3609	rSP	[Evacuation freq.]	206C/22	5FB4/04	A6/01/22	258
13841 = 16#3611	dCO	[Precharge cont. ass.]	206C/2A	5FBD/78	A6/01/2A	259
13901 = 16#364D	FLI	[Fluxing assignment]	206D/2	5FBD/79	A6/01/66	219
13902 = 16#364E	FLU	[Motor fluxing]	206D/3	5FB4/05	A6/01/67	146
13902 = 16#364E	FLU	[Motor fluxing]	206D/3	5FB4/05	A6/01/67	219
14001 = 16#36B1	PPI	[Pairing password]	206E/2	5FBD/7A	A7/01/02	279
14101 = 16#3715	Ubr	[Braking level]	206F/2	5FBF/D4	A7/01/66	164
14102 = 16#3716	bbA	[Braking balance]	206F/3	5FBD/7B	A7/01/67	164
14111 = 16#371F	brO	[DB res. protection]	206F/C	5FBD/7C	A7/01/70	278
14112 = 16#3720	brP	[DB Resistor Power]	206F/D	5FBD/7D	A7/01/71	278
14113 = 16#3721	brU	[DB Resistor value]	206F/E	5FBD/7E	A7/01/72	278
14114 = 16#3722	tHb	DBR thermal state	206F/F	5FBD/7F	A7/01/73	92

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
14201 = 16#3779	UC2	[Vector Control 2pt]	2070/2	5FBD/80	A8/01/02	153
14202 = 16#377A	UCP	[V. constant power]	2070/3	5FBD/81	A8/01/03	153
14203 = 16#377B	FCP	[Freq. Const Power]	2070/4	5FBD/82	A8/01/04	153
14301 = 16#37DD	LbA	[Load sharing]	2071/2	5FBD/83	A8/01/66	164
14302 = 16#37DE	LbC	[Load correction]	2071/3	5FBF/D5	A8/01/67	164
14303 = 16#37DF	LbC1	[Correction min spd]	2071/4	5FBF/D6	A8/01/68	165
14304 = 16#37E0	LbC2	[Correction max spd]	2071/5	5FBF/D7	A8/01/69	165
14305 = 16#37E1	LbC3	[Torque offset]	2071/6	5FBF/D8	A8/01/6A	165
14306 = 16#37E2	LbF	[Sharing filter]	2071/7	5FBF/D9	A8/01/6B	165
3011 = 16#0BC3	nCV	Drive nominal rating	2000/C	5FB0/02	70/01/0C	124
3012 = 16#0BC4	UCAL	Drive line voltage	2000/D	5FB0/03	70/01/0D	124
3015 = 16#BC7	bFr	[Standard mot. freq]	2000/10	5FBC/05	70/01/10	148
3017 = 16#0BC9	InV	Rated drive current	2000/12	5FB0/07	70/01/12	124
3052 = 16#BEC	CFG	[Macro configuration]	2000/35	5FBC/07	70/01/35	144
3053 = 16#BED	CCFG	[Customized macro]	2000/36	5FB9/02	70/01/36	144
3102 = 16#C1E	SFr	[Switching freq.]	2001/3	5FBF/02	70/01/67	162
3103 = 16#C1F	tFr	[Max frequency]	2001/4	5FB3/74	70/01/68	149
3104 = 16#C20	HSP	[High speed]	2001/5	5FB3/75	70/01/69	145
3105 = 16#C21	LSP	[Low speed]	2001/6	5FB3/76	70/01/6A	145
3106 = 16#C22	bSP	[Reference template]	2001/7	5FB3/77	70/01/6B	168
3107 = 16#C23	ndr	[Noise reduction]	2001/8	5FB3/78	70/01/6C	162
3108 = 16#C24	rIn	[RV Inhibition]	2001/9	5FB3/79	70/01/6D	193
3109 = 16#C25	OFI	[Sinus filter]	2001/A	5FBC/0B	70/01/6E	162
3110 = 16#C26	FLr	[Catch on the fly]	2001/B	5FB3/7A	70/01/6F	263
3111 = 16#C27	UCb	[Sensitivity]	2001/C	5FBF/06	70/01/70	263
3112 = 16#C28	Strt	[IGBT test]	2001/D	5FBC/0D	70/01/71	272
3202 = 16#C82	rFr	Output frequency	2002/3	5FB9/04	71/01/03	87
3203 = 16#0C83	FrH	Frequency reference before ramp	2002/4	5FB9/05	71/01/04	89
3204 = 16#0C84	LCr	Motor current	2002/5	5FB9/06	2A/01/09	88
3205 = 16#0C85	Otr	Output torque	6077	6077	71/01/06	87
3206 = 16#0C86	ETI	Extended status word 0	2002/7	5FB9/08	71/01/07	81

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
3207 = 16#0C87	ULn	Power supply voltage	2002/8	5FB9/09	71/01/08	92
3208 = 16#0C88	UOP	Motor voltage	2002/9	5FB9/0A	71/01/09	88
3209 = 16#0C89	tHd	Drive thermal state	2002/A	5FB9/0B	71/01/0A	92
3211 = 16#0C8B	OPr	Motor power	2002/C	5FB9/0C	71/01/0C	88
3216 = 16#0C90	Otrn	Output torque (Nm)	2002/11	5FB9/10	2A/01/0B	88
3230 = 16#0C9E	APH	Energy consumption	2002/1F	5FB9/15	71/01/1F	92
3231 = 16#0C9F	rtH	Total motor operating time	2002/20	5FB9/16	71/01/20	93
3232 = 16#0CA0	rtHI	Internal motor operating time	2002/21	5FB9/17	71/01/21	93
3233 = 16#0CA1	PtH	Total drive operating time	2002/22	5FB9/18	71/01/22	93
3235 = 16#0CA3	tAC	IGBT alarm time	2002/24	5FB9/1A	71/01/24	93
3250 = 16#0CB2	LRS1	Extended status word 1	2002/33	5FB9/1C	71/01/33	81
3251 = 16#0CB3	LRS2	Extended status word 2	2002/34	5FB9/1D	71/01/34	82
3252 = 16#0CB4	LRS3	Extended status word 3	2002/35	5FB9/1E	71/01/35	82
3253 = 16#0CB5	LRS4	Extended status word 4	2002/36	5FB9/1F	71/01/36	83
3254 = 16#0CB6	LRS5	Extended status word 5	2002/37	5FB9/20	71/01/37	83
3255 = 16#0CB7	LRS6	Extended status word 6	2002/38	5FB9/21	71/01/38	84
3256 = 16#0CB8	LRS7	Extended status word 7	2002/39	5FB9/22	71/01/39	84
3257 = 16#0C89	LRS8	Extended status word 8	2002/3A	5FB9/23	71/01/3A	85
3302 = 16#0CE6	UdP	Drive software version	2003/3	5FB0/0D	71/01/67	124
3340 = 16#0D0C	PAn0	Device name: characters 1 and 2	2003/29	5FB0/25	71/01/8D	124
3341 = 16#0D0D	PAn1	Device name: characters 3 and 4	2003/2A	5FB0/26	71/01/8E	124
3342 = 16#0D0E	PAn2	Device name: characters 5 and 6	2003/2B	5FB0/27	71/01/8F	125
3343 = 16#0D0F	PAn3	Device name: characters 7 and 8	2003/2C	5FB0/28	71/01/90	125
3344 = 16#0D10	PAn4	Device name: characters 9 and 10	2003/2D	5FB0/29	71/01/91	125
3345 = 16#0D11	PAn5	Device name: characters 11 and 12	2003/2E	5FB0/2A	71/01/92	125
3346 = 16#0D12	PAn6	Device name: characters 13 and 14	2003/2F	5FB0/2B	71/01/93	125
3346 = 16#0D12	PAn7	Device name: characters 15 and 16	2003/30	5FB0/2C	71/01/94	125
4001 = 16#FA1	L1d	[L1 On Delay]	200A/2	5FBC/0E	75/01/02	167
4002 = 16#FA2	L2d	[L2 On Delay]	200A/3	5FBC/0F	75/01/03	167
4003 = 16#FA3	L3d	[L3 On Delay]	200A/4	5FBC/10	75/01/04	167
4004 = 16#FA4	L4d	[L4 On Delay]	200A/5	5FBC/11	75/01/05	167

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
4005 = 16#FA5	L5d	[LI5 On Delay]	200A/6	5FBC/12	75/01/06	167
4006 = 16#FA6	L6d	[LI6 On Delay]	200A/7	5FBC/13	75/01/07	167
4007 = 16#FA7	L7d	[LI7 On Delay]	200A/8	5FBC/14	75/01/08	167
4008 = 16#FA8	L8d	[LI8 On Delay]	200A/9	5FBC/15	75/01/09	167
4009 = 16#FA9	L9d	[LI9 On Delay]	200A/A	5FBC/16	75/01/0A	167
4010 = 16#FAA	L10d	[LI10 On Delay]	200A/B	5FBC/17	75/01/0B	167
4011 = 16#FAB	L11d	[LI11 On Delay]	200A/C	5FBC/18	75/01/0C	167
4012 = 16#FAC	L12d	[LI12 On Delay]	200A/D	5FBC/19	75/01/0D	167
4013 = 16#FAD	L13d	[LI13 On Delay]	200A/E	5FBC/1A	75/01/0E	167
4014 = 16#FAE	L14d	[LI14 On Delay]	200A/F	5FBC/1B	75/01/0F	167
4201 = 16#1069	r1S	[R1 Active at]	200C/2	5FBC/1C	76/01/02	180
4202 = 16#106A	r2S	[R2 Active at]	200C/3	5FBC/1D	76/01/03	181
4203 = 16#106B	r3S	[R3 Active at]	200C/4	5FBC/1E	76/01/04	182
4204 = 16#106C	r4S	[R4 Active at]	200C/5	5FBC/1F	76/01/05	183
4209 = 16#1071	LO1S	[LO1 active at]	200C/2D	5FBC/20	76/01/0A	184
4210 = 16#1072	LO2S	[LO2 active at]	200C/B	5FBC/21	76/01/0B	185
4211 = 16#1073	LO3S	[LO3 active at]	200C/C	5FBC/22	76/01/0C	186
4212 = 16#1074	LO4S	[LO4 active at]	200C/D	5FBC/23	76/01/0D	187
4221 = 16#107D	r1H	[R1 Holding time]	200C/16	5FBC/24	76/01/16	180
4222 = 16#107E	r2H	[R2 Holding time]	200C/17	5FBC/25	76/01/17	181
4223 = 16#107F	r3H	[R3 Holding time]	200C/18	5FBC/26	76/01/18	182
4224 = 16#1080	r4H	[R4 Holding time]	200C/19	5FBC/27	76/01/19	183
4229 = 16#1085	LO1H	[LO1 holding time]	200C/1E	5FBC/28	76/01/1E	184
4230 = 16#1086	LO2H	[LO2 holding time]	200C/1F	5FBC/29	76/01/1F	185
4231 = 16#1087	LO3H	[LO3 holding time]	200C/20	5FBC/2A	76/01/20	186
4232 = 16#1088	LO4H	[LO4 holding time]	200C/21	5FBC/2B	76/01/21	187
4241 = 16#1091	r1d	[R1 Delay time]	200C/2A	5FBC/2C	76/01/2A	180
4242 = 16#1092	r2d	[R2 Delay time]	200C/2B	5FBC/2D	76/01/2B	181
4243 = 16#1093	r3d	[R3 Delay time]	200C/2C	5FBC/2E	76/01/2C	182
4244 = 16#1094	r4d	[R4 Delay time]	200C/2D	5FBC/2F	76/01/2D	183
4249 = 16#1099	LO1d	[LO1 delay time]	200C/32	5FBC/30	76/01/32	184

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
4250 = 16#109A	LO2d	[LO2 delay time]	200C/33	5FBC/31	76/01/33	185
4251 = 16#109B	LO3d	[LO3 delay time]	200C/34	5FBC/32	76/01/34	186
4252 = 16#109C	LO4d	[LO4 delay time]	200C/35	5FBC/33	76/01/35	187
4402 = 16#1132	AI1t	[AI1 Type]	200E/3	5FBC/34	77/01/03	169
4403 = 16#1133	AI2t	[AI2 Type]	200E/4	5FBC/35	77/01/04	170
4404 = 16#1134	AI3t	[AI3 Type]	200E/5	5FBC/36	77/01/05	172
4405 = 16#1135	AI4t	[AI4 Type]	200E/6	5FBC/37	77/01/06	174
4412 = 16#113C	UIL1	[AI1 min value]	200E/D	5FBC/38	77/01/0D	169
4413 = 16#113D	UIL2	[AI2 min value]	200E/E	5FBC/39	77/01/0E	170
4415 = 16#113D	UIL4	[AI4 min value]	200E/10	5FBC/3A	77/01/10	174
4422 = 16#1146	UIH1	[AI1 max value]	200E/17	5FBC/3B	77/01/17	169
4423 = 16#1147	UIH2	[AI2 max value]	200E/18	5FBC/3C	77/01/18	170
4425 = 16#1149	UIH4	[AI4 max value]	200E/1A	5FBC/3D	77/01/1A	174
4433 = 16#1151	CrL2	[AI2 min value]	200E/22	5FBC/3E	77/01/22	170
4434 = 16#1152	CrL3	[AI3 min value]	200E/23	5FBC/3F	77/01/23	172
4435 = 16#1153	CrL4	[AI4 min value]	200E/24	5FBC/40	77/01/24	174
4443 = 16#115B	CrH2	[AI2 max value]	200E/2C	5FBC/41	77/01/2C	170
4444 = 16#115C	CrH3	[AI3 max value]	200E/2D	5FBC/42	77/01/2D	172
4445 = 16#115D	CrH4	[AI4 max value]	200E/2E	5FBC/43	77/01/2E	174
4452 = 16#1164	AI1F	[AI1 filter]	200E/35	5FBC/44	77/01/35	169
4453 = 16#1165	AI2F	[AI2 filter]	200E/36	5FBC/45	77/01/36	170
4454 = 16#1166	AI3F	[AI3 filter]	200E/37	5FBC/46	77/01/37	172
4455 = 16#1167	AI4F	[AI4 filter]	200E/38	5FBC/47	77/01/38	174
4462 = 16#116E	AI1E	[AI1 Interm. point X]	200E/3F	5FBC/48	77/01/3F	169
4463 = 16#116F	AI2E	[AI2 Interm. point X]	200E/40	5FBC/49	77/01/40	171
4464 = 16#1170	AI3E	[AI3 Interm. point X]	200E/41	5FBC/4A	77/01/4A	173
4465 = 16#1171	AI4E	[AI4 Interm. point X]	200E/42	5FBC/4B	77/01/42	175
4472 = 16#1178	AI1S	[AI1 Interm. point Y]	200E/49	5FBC/4C	77/01/49	169
4473 = 16#1179	AI2S	[AI2 Interm. point Y]	200E/4A	5FBC/4D	77/01/4A	171
4474 = 16#117A	AI3S	[AI3 Interm. point Y]	200E/4B	5FBC/4E	77/01/4A	173
4475 = 16#117B	AI4S	[AI4 Interm. point Y]	200E/4C	5FBC/4F	77/01/4C	175

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
4483 = 16#1183	AI2L	[AI2 range]	200E/54	5FBC/50	77/01/54	171
4484 = 16#1184	AI3L	[AI3 range]	200E/55	5FBC/51	77/01/55	172
4485 = 16#1185	AI4L	[AI4 range]	200E/56	5FBC/52	77/01/56	175
4601 = 16#11F9	AO1t	[AO1 Type]	2010/2	5FBC/53	78/01/02	188
4602 = 16#11FA	AO2t	[AO2 Type]	2010/3	5FBC/54	78/01/03	190
4603 = 16#11FB	AO3t	[AO3 Type]	2010/4	5FBC/55	78/01/04	191
4611 = 16#1203	AO1F	[AO1 Filter]	2010/C	5FBC/56	78/01/0C	189
4612 = 16#1204	AO2F	[AO2 Filter]	2010/D	5FBC/57	78/01/0D	190
4613 = 16#1205	AO3F	[AO3 Filter]	2010/E	5FBC/58	78/01/0E	191
4621 = 16#120D	UOL1	[AO1 min Output]	2010/16	5FBC/59	78/01/16	189
4622 = 16#120E	UOL2	[AO2 min Output]	2010/17	5FBC/5A	78/01/17	190
4623 = 16#120F	UOL3	[AO3 min Output]	2010/18	5FBC/5B	78/01/18	191
4631 = 16#1217	UOH1	[AO1 max Output]	2010/20	5FBC/5C	78/01/20	189
4632 = 16#1218	UOH2	[AO2 max Output]	2010/21	5FBC/5D	78/01/21	190
4633 = 16#1219	UOH3	[AO3 max Output]	2010/22	5FBC/5E	78/01/22	191
4641 = 16#1221	AOL1	[AO1 min Output]	2010/2A	5FBC/5F	78/01/2A	188
4642 = 16#1222	AOL2	[AO2 min Output]	2010/2B	5FBC/60	78/01/2B	190
4643 = 16#1223	AOL3	[AO3 min Output]	2010/2C	5FBC/61	78/01/2C	191
4651 = 16#122B	AOH1	[AO1 max Output]	2010/34	5FBC/62	78/01/34	188
4652 = 16#122C	AOH2	[AO2 max Output]	2010/35	5FBC/63	78/01/35	190
4653 = 16#122D	AOH3	[AO3 max Output]	2010/36	5FBC/64	78/01/36	191
5001 = 16#1389	r1	[R1 Assignment]	2014/2	5FB3/7B	7A/01/02	179
5002 = 16#138A	r2	[R2 Assignment]	2014/3	5FB3/7C	7A/01/03	181
5003 = 16#138B	r3	[R3 Assignment]	2014/4	5FBC/67	7A/01/04	182
5004 = 16#138C	r4	[R4 Assignment]	2014/5	5FBC/68	7A/01/05	183
5009 = 16#1391	LO1	[LO1 assignment]	2014/A	5FB3/7D	7A/01/0A	184
5010 = 16#1392	LO2	[LO2 assignment]	2014/B	5FBC/6A	7A/01/0B	185
5011 = 16#1393	LO3	[LO3 assignment]	2014/C	5FBC/6B	7A/01/0C	186
5012 = 16#1394	LO4	[LO4 assignment]	2014/D	5FBC/6C	7A/01/0D	187
5021 = 16#139D	AO1	[AO1 assignment]	2014/16	5FBC/6D	7A/01/16	188
5022 = 16#139E	AO2	[AO2 assignment]	2014/17	5FBC/6E	7A/01/17	190

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
5023 = 16#139F	AO3	[AO3 assignment]	2014/18	5FBC/6F	7A/01/18	191
5202 = 16#1452	IL1r	Logic input map	2016/3	5FB9/28	7B/01/03	95
5212 = 16#145C	OL1r	Logic output map	2016/D	5FB9/2A	7B/01/0D	95
5232 = 16#1470	AI1r	Standardized image of analog input 1	2016/21	5FB9/2F	7B/01/21	96
5233 = 16#1471	AI2r	Standardized image of analog input 2	2016/22	5FB9/30	7B/01/22	96
5234 = 16#1472	AI3r	Standardized image of analog input 3	2016/23	5FB9/31	7B/01/23	96
5235 = 16#1473	AI4r	Standardized image of analog input 4	2016/24	5FB9/32	7B/01/24	97
5242 = 16#147A	AI1C	Physical image of analog input 1	2016/2B	5FB9/33	7B/01/2B	96
5243 = 16#147B	AI2C	Physical image of analog input 2	2016/2C	5FB9/34	7B/01/2C	96
5244 = 16#147C	AI3C	Physical image of analog input 3	2016/2D	5FB9/35	7B/01/2D	96
5245 = 16#147D	AI4C	Physical image of analog input 4	2016/2E	5FB9/36	7B/01/2E	96
5261 = 16#148D	AO1r	Standardized image of analog output 1	2016/3E	5FB9/3A	7B/01/3E	97
5262 = 16#148E	AO2r	Standardized image of analog output 2	2016/3F	5FB9/3B	7B/01/3F	97
5263 = 16#148F	AO3r	Standardized image of analog output 3	2016/40	5FB9/3C	7B/01/40	98
5271 = 16#1497	AO1C	Physical image of analog output 1	2016/48	5FB9/3D	7B/01/48	97
5272 = 16#1498	AO2C	Physical image of analog output 2	2016/49	5FB9/3E	7B/01/49	97
5273 = 16#1499	AO3C	Physical image of analog output 3	2016/4A	5FB9/3F	7B/01/4A	97
5281 = 16 #14A1	AIU1	PID regulator feedback	2016/52	5FB9/40	7B/01/52	78
5282 = 16#14A2	AIC1	[AI net. channel]	2016/53	5FBC/70	7B/01/53	230
5604 = 16#15E4	PGI	Encoder pulse counter	201A/5	5FB3/7E	7D/01/05	98
5604 = 16#15E4	PGI	[Number of pulses]	201A/5	5FB3/7E	7D/01/05	159
5604 = 16#15E4	PGI	[Number of pulses]	201A/5	5FB3/7E	7D/01/05	177
5605 = 16#15E5	EnC	[Encoder check]	201A/6	5FB3/7F	7D/01/06	160
5605 = 16#15E5	EnC	[Encoder check]	201A/6	5FB3/7F	7D/01/06	177
5606 = 16#15E6	EnU	[Encoder usage]	201A/7	5FBC/73	7D/01/07	160
5606 = 16#15E6	EnU	[Encoder usage]	201A/7	5FBC/73	7D/01/07	177
5607 =16#15E7	ECC	[Encoder coupling]	201A/8	5FBC/74	7D/01/08	276
5608 = 16#15E8	EnS	[Encoder type]	201A/A	5FBC/75	7D/01/09	159
5608 = 16#15E8	EnS	[Encoder type]	201A/9	5FBC/75	7D/01/09	177

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
5609 = 16#15E9	ECt	[Encoder check time]	201A/A	5FBF/07	7D/01/0A	276
5610 = 16#15EA	Pdl	Drive encoder counter divisor	201A/B	5FBC/76	7D/01/0B	131
5611 = 16#15EB	PUC	Drive encoder divided counter	201A/C	5FB9/41	7D/01/0C	132
64002 = 16#FA02	PSt	[Stop Key priority]	2262/3	5FBD/88	94/01/06	193
6401 = 16#1901	O01	"Controller Inside" HMI exchange word 1	2022/2	5FB3/81	81/01/02	132
6402 = 16#1902	O02	"Controller Inside" HMI exchange word 2	2022/3	5FB3/82	81/01/03	132
6403 = 16#1903	O03	"Controller Inside" HMI exchange word 3	2022/4	5FB3/83	81/01/04	132
6404 = 16#1904	O04	"Controller Inside" HMI exchange word 4	2022/5	5FB3/84	81/01/05	132
6405 = 16#1905	O05	"Controller Inside" HMI exchange word 5	2022/6	5FB3/85	81/01/06	133
6406 = 16#1906	O06	"Controller Inside" HMI exchange word 6	2022/7	5FB3/86	81/01/07	133
6407 = 16#1907	O07	"Controller Inside" HMI exchange word 7	2022/8	5FB3/87	81/01/08	133
6408 = 16#1908	O08	"Controller Inside" HMI exchange word 8	2022/9	5FB3/88	81/01/09	133
6409 = 16#1909	O09	"Controller Inside" HMI exchange word 9	2022/A	5FB3/89	81/01/0A	133
6410 = 16#190A	O10	"Controller Inside" HMI exchange word 10	2022/B	5FB3/8A	81/01/0B	133
6411 = 16#190B	O11	"Controller Inside" HMI exchange word 11	2022/C	5FB3/8B	81/01/0C	133
6412 = 16#190C	O12	"Controller Inside" HMI exchange word 12	2022/D	5FB3/8C	81/01/0D	134
6413 = 16#190D	O13	"Controller Inside" HMI exchange word 13	2022/E	5FB3/8D	81/01/0E	134
6414 = 16#190E	O14	"Controller Inside" HMI exchange word 14	2022/F	5FB3/8E	81/01/0F	134
6415 = 16#190F	O15	"Controller Inside" HMI exchange word 15	2022/10	5FB3/8F	81/01/10	134
6416 = 16#1910	O16	"Controller Inside" HMI exchange word 16	2022/11	5FB3/90	81/01/11	134
6417 = 16#1911	O17	"Controller Inside" HMI exchange word 17	2022/12	5FB3/91	81/01/12	134
6418 = 16#1912	O18	"Controller Inside" HMI exchange word 18	2022/13	5FB3/92	81/01/13	134

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
6419 = 16#1913	O19	"Controller Inside" HMI exchange word 19	2022/14	5FB3/93	81/01/14	135
6420 = 16#1914	O20	"Controller Inside" HMI exchange word 20	2022/15	5FB3/94	81/01/15	135
6421 = 16#1915	O21	"Controller Inside" HMI exchange word 21	2022/16	5FB3/95	81/01/16	135
6422 = 16#1916	O22	"Controller Inside" HMI exchange word 22	2022/17	5FB3/96	81/01/17	135
6423 = 16#1917	O23	"Controller Inside" HMI exchange word 23	2022/18	5FB3/97	81/01/18	135
64233 = 16#FAE9	Frd	Ethernet fault code	2264/22	-	-	103
6424 = 16#1918	O24	"Controller Inside" HMI exchange word 24	2022/19	5FB3/98	81/01/19	135
6425 = 16#1919	O25	"Controller Inside" HMI exchange word 25	2022/1A	5FB3/99	81/01/1A	135
6426 = 16#191A	O26	"Controller Inside" HMI exchange word 26	2022/1B	5FB3/9A	81/01/1B	136
6427 = 16#191B	O27	"Controller Inside" HMI exchange word 27	2022/1C	5FB3/9B	81/01/1C	136
6428 = 16#191C	O28	"Controller Inside" HMI exchange word 28	2022/1D	5FB3/9C	81/01/1D	136
6429 = 16#191D	O29	"Controller Inside" HMI exchange word 29	2022/1E	5FB3/9D	81/01/1E	136
6430 = 16#191E	O30	"Controller Inside" HMI exchange word 30	2022/1F	5FB3/9E	81/01/1F	136
64300 = 16#FB2C	dCC0	Command channel active on last fault	2265/1	5FBA/08	9A/01/08	106
64301 = 16#FB2D	dCC1	Active command channel on fault n-1	2265/2	5FBA/09	9A/01/09	109
64302 = 16#FB2E	dCC2	Active command channel on fault n-2	2265/3	5FBA/0A	9A/01/0A	111
64303 = 16#FB2F	dCC3	Active command channel on fault n-3	2265/4	5FBA/0B	9A/01/0B	113
64304 = 16#FB30	dCC4	Active command channel on fault n-4	2265/5	5FBA/0C	9A/01/0C	115
64305 = 16#FB31	dCC5	Active command channel on fault n-5	2265/6	5FBA/0D	9A/01/0D	117
64306 = 16#FB32	dCC6	Active command channel on fault n-6	2265/7	5FBA/0E	9A/01/0E	119
64307 = 16#FB33	dCC7	Active command channel on fault n-7	2265/8	5FBA/0F	9A/01/0F	121

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
64308 = 16#FB34	dCC8	Active command channel on fault n-8	2265/9	5FBA/10	9A/01/10	123
6431 = 16#191F	O31	"Controller Inside" HMI exchange word 31	2022/20	5FB3/9F	81/01/20	136
64310 = 16#FB36	drC0	Reference channel active on last fault	2265/B	5FBA/11	9A/01/11	106
64311 = 16#FB37	drC1	Active reference channel on fault n-1	2265/C	5FBA/12	9A/01/12	109
64312 = 16#FB38	drC2	Active reference channel on fault n-2	2265/D	5FBA/13	9A/01/13	111
64313 = 16#FB39	drC3	Active reference channel on fault n-3	2265/E	5FBA/14	9A/01/14	113
64314 = 16#FB3A	drC4	Active reference channel on fault n-4	2265/F	5FBA/15	9A/01/15	115
64315 = 16#FB3B	drC5	Active reference channel on fault n-5	2265/10	5FBA/16	9A/01/16	117
64316 = 16#FB3C	drC6	Active reference channel on fault n-6	2265/11	5FBA/17	9A/01/17	119
64317 = 16#FB3D	drC7	Active reference channel on fault n-7	2265/12	5FBA/18	9A/01/18	121
64318 = 16#FB3E	drC8	Active reference channel on fault n-8	2265/13	5FBA/19	9A/01/19	123
6432 = 16#1920	O32	"Controller Inside" HMI exchange word 32	2022/21	5FB3/A0	81/01/21	136
6433 = 16#1921	O33	"Controller Inside" HMI exchange word 33	2022/22	5FB3/A1	81/01/22	137
6434 = 16#1922	O34	"Controller Inside" HMI exchange word 34	2022/23	5FB3/A2	81/01/23	137
6435 = 16#1923	O35	"Controller Inside" HMI exchange word 35	2022/24	5FB3/A3	81/01/24	137
6436 = 16#1924	O36	"Controller Inside" HMI exchange word 36	2022/25	5FB3/A4	81/01/25	137
6437 = 16#1925	O37	"Controller Inside" HMI exchange word 37	2022/26	5FB3/A5	81/01/26	137
6438 = 16#1926	O38	"Controller Inside" HMI exchange word 38	2022/27	5FB3/A6	81/01/27	137
6439 = 16#1927	O39	"Controller Inside" HMI exchange word 39	2022/28	5FB3/A7	81/01/28	137
6440 = 16#1928	O40	"Controller Inside" HMI exchange word 40	2022/29	5FB3/A8	81/01/29	138
6441 = 16#1929	O41	"Controller Inside" HMI exchange word 41	2022/2A	5FB3/A9	81/01/2A	138
6442 = 16#192A	O42	"Controller Inside" HMI exchange word 42	2022/2B	5FB3/AA	81/01/2B	138
6443 = 16#192B	O43	"Controller Inside" HMI exchange word 43	2022/2C	5FB3/AB	81/01/2C	138

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
6444 = 16#192C	O44	"Controller Inside" HMI exchange word 44	2022/2D	5FB3/AC	81/01/2D	138
6445 = 16#192D	O45	"Controller Inside" HMI exchange word 45	2022/2E	5FB3/AD	81/01/2E	138
6446 = 16#192E	O46	"Controller Inside" HMI exchange word 46	2022/2F	5FB3/AE	81/01/2F	138
6447 = 16#192F	O47	"Controller Inside" HMI exchange word 47	2022/30	5FB3/AF	81/01/30	139
6448 = 16#1930	O48	"Controller Inside" HMI exchange word 48	2022/31	5FB3/B0	81/01/31	139
6449 = 16#1931	O49	"Controller Inside" HMI exchange word 49	2022/32	5FB3/B1	81/01/32	139
6450 = 16#1932	O50	"Controller Inside" HMI exchange word 50	2022/33	5FC8/32	81/01/33	139
6901 = 16#1AF5	PIL1	"Controller Inside" logic input map	2027/2	5FB9/49	83/01/66	131
6911 = 16#1AFF	POL1	"Controller Inside" logic output map	2027/C	5FB9/4A	83/01/70	131
6942 = 16#1B1E	PAI1	Physical image of analog input 1	2027/2B	5FB9/4B	83/01/8F	131
6943 = 16#1B1F	PAI2	Physical image of analog input 2	2027/2C	5FB9/4C	83/01/90	131
6971 = 16#1B3B	PAO1	Physical image of analog output 1	2027/48	5FB9/4D	83/01/AC	131
6972 = 16#1B3C	PAO2	Physical image of analog output 2	2027/49	5FB9/4E	83/01/AD	131
7002 = 16#1B5A	IPL	[Input phase loss]	2028/3	5FB3/BE	84/01/03	266
7003 = 16#1B5B	LFL2	[AI2 4-20mA loss]	2028/4	5FB3/BF	84/01/04	273
7004 = 16#1B5C	StP	[UnderV. prevention]	2028/5	5FB3/C0	84/01/05	271
7005 = 16#1B5D	Sdd	[Load slip detection]	2028/6	5FB3/C1	84/01/06	276
7006 = 16#1B5E	EPL	[External fault mgt]	2028/7	5FB3/C2	84/01/07	269
7008 = 16#1B60	OHL	[Overtemp fault mgt]	2028/9	5FBC/8C	84/01/09	267
7009 = 16#1B61	OLL	[Overload fault mgt]	2028/A	5FBC/8D	84/01/0A	265
7010 = 16#1B62	SLL	[Modbus fault mgt]	2028/B	5FBC/8E	84/01/0B	275
7011 = 16#1B63	COL	[CANopen fault mgt]	2028/C	5FBC/8F	84/01/0C	275
7012 = 16#1B64	tnL	[Autotune fault mgt]	2028/D	5FBC/90	84/01/0D	279
7013 = 16#1B65	LFL3	[AI3 4-20mA loss]	2028/E	5FB3/C3	84/01/0E	273
7014 = 16#1B66	LFL4	[AI4 4-20mA loss]	2028/F	5FB3/C4	84/01/0F	273
7015 = 16#1B67	CLL	[Network fault mgt]	2028/10	5FBC/93	84/01/10	275
7080 = 16#1BA8	LFF	[Fallback speed]	2028/51	5FB3/C5	84/01/51	279

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
7081 = 16#1BA9	Odt	[OutPh time detect]	2028/52	5FBF/08	84/01/52	266
7121 = 16#1BD1	LFt	Altivar fault code	2029/16	5FB3/C8	84/01/7A	100
7122 = 16#1BD2	Atr	[Automatic restart]	2029/17	5FB3/C9	84/01/7B	262
7123 = 16#1BD3	tAr	[Max. restart time]	2029/18	5FBC/96	84/01/7C	262
7124 = 16#1BD4	rSF	[Fault reset]	2029/19	5FBC/97	84/01/7D	261
7125 = 16#1BD5	Inh	[Fault inhibit assign.]	2029/1A	5FBC/98	84/01/7E	274
7130 = 16#1BDA	CIC	Incorrect configuration	2029/1F	5FB6/1A	84/01/83	102
7131 = 16#1BDB	EtF	[External fault ass.]	2029/20	5FBC/99	84/01/84	269
7132 = 16#1BDC	CnF	Network card fault code	2029/21	5FB0/96	84/01/85	102
7133 = 16#1BDD	APF	"Controller Inside" card fault code	2029/22	5FB0/97	84/01/86	102
7134 = 16#1BDE	ILF1	Option card 1 fault code	2029/23	5FB0/98	84/01/87	103
7135 = 16#1BDF	ILF2	Option card 2 fault code	2029/24	5FB0/99	84/01/88	103
7200 = 16#1C20	dP0	Fault code on last fault	202A/1	5FB9/55	85/01/01	105
7201 = 16#1C21	dP1	Fault code on fault n-1	202A/2	5FB9/56	85/01/02	107
7202 = 16#1C22	dP2	Fault code on fault n-2	202A/3	5FB9/57	85/01/03	109
7203 = 16#1C23	dP3	Fault code on fault n-3	202A/4	5FB9/58	85/01/04	111
7204 = 16#1C24	dP4	Fault code on fault n-4	202A/5	5FB9/59	85/01/05	113
7205 = 16#1C25	dP5	Fault code on fault n-5	202A/6	5FB9/5A	85/01/06	115
7206 = 16#1C26	dP6	Fault code on fault n-6	202A/7	5FB9/5B	85/01/07	117
7207 = 16#1C27	dP7	Fault code on fault n-7	202A/8	5FB9/5C	85/01/08	119
7208 = 16#1C28	dP8	Fault code on fault n-8	202A/9	5FB9/5D	85/01/09	121
7210 = 16#1C2A	EP0	Status word on last fault	202A/B	5FB9/5E	85/01/0B	105
7211 = 16#1C2B	EP1	Status word on fault n-1	202A/C	5FB9/5F	85/01/0C	108
7212 = 16#1C2C	EP2	Status word on fault n-2	202A/D	5FB9/60	85/01/0D	110
7213 = 16#1C2D	EP3	Status word on fault n-3	202A/E	5FB9/61	85/01/0E	112
7214 = 16#1C2E	EP4	Status word on fault n-4	202A/F	5FB9/62	85/01/0F	114
7215 = 16#1C2F	EP5	Status word on fault n-5	202A/10	5FB9/63	85/01/10	116
7216 = 16#1C30	EP6	Status word on fault n-6	202A/11	5FB9/64	85/01/11	118
7217 = 16#1C31	EP7	Status word on fault n-7	202A/12	5FB9/65	85/01/12	120
7218 = 16#1C32	EP8	Status word on fault n-8	202A/13	5FB9/66	85/01/13	122
7220 = 16#1C34	IP0	Extended status word 0 on last fault	202A/15	5FB9/67	85/01/15	106

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
7221 = 16#1C35	IP1	Extended status word on fault n-1	202A/16	5FB9/68	85/01/16	108
7222 = 16#1C36	IP2	Extended status word on fault n-2	202A/17	5FB9/69	85/01/17	110
7223 = 16#1C37	IP3	Extended status word on fault n-3	202A/18	5FB9/6A	85/01/18	112
7224 = 16#1C38	IP4	Extended status word on fault n-4	202A/19	5FB9/6B	85/01/19	114
7225 = 16#1C39	IP5	Extended status word on fault n-5	202A/1A	5FB9/6C	85/01/1A	116
7226 = 16#1C3A	IP6	Extended status word on fault n-6	202A/1B	5FB9/6D	85/01/1B	118
7227 = 16#1C3B	IP7	Extended status word on fault n-7	202A/1C	5FB9/6E	85/01/1C	120
7228 = 16#1C3C	IP8	Extended status word on fault n-8	202A/1D	5FB9/6F	85/01/1D	122
7230 = 16#1C3E	CMP0	Command word on last fault	202A/1F	5FB9/70	85/01/1F	106
7231 = 16#1C3F	CMP1	Command word on fault n-1	202A/20	5FB9/71	85/01/20	108
7232 = 16#1C40	CMP2	Command word on fault n-2	202A/21	5FB9/72	85/01/21	110
7233 = 16#1C41	CMP3	Command word on fault n-3	202A/22	5FB9/73	85/01/22	112
7234 = 16#1C42	CMP4	Command word on fault n-4	202A/23	5FB9/74	85/01/23	114
7235 = 16#1C43	CMP5	Command word on fault n-5	202A/24	5FB9/75	85/01/24	116
7236 = 16#1C44	CMP6	Command word on fault n-6	202A/25	5FB9/76	85/01/25	118
7237 = 16#1C45	CMP7	Command word on fault n-7	202A/26	5FB9/77	85/01/26	120
7238 = 16#1C46	CMP8	Command word on fault n-8	202A/27	5FB9/78	85/01/27	122
7240 = 16#1C48	LCP0	Motor current on last fault	202A/29	5FB9/79	85/01/29	105
7241 = 16#1C49	LCP1	Motor current on fault n-1	202A/2A	5FB9/7A	85/01/2A	108
7242 = 16#1C4A	LCP2	Motor current on fault n-2	202A/2B	5FB9/7B	85/01/2B	110
7243 = 16#1C4B	LCP3	Motor current on fault n-3	202A/2C	5FB9/7C	85/01/2C	112
7244 = 16#1C4C	LCP4	Motor current on fault n-4	202A/2D	5FB9/7D	85/01/2D	114
7245 = 16#1C4D	LCP5	Motor current on fault n-5	202A/2E	5FB9/7E	85/01/2E	116
7246 = 16#1C4E	LCP6	Motor current on fault n-6	202A/2F	5FB9/7F	85/01/2F	118
7247 = 16#1C4F	LCP7	Motor current on fault n-7	202A/30	5FB9/80	85/01/30	120
7248 = 16#1C50	LCP8	Motor current on fault n-8	202A/31	5FB9/81	85/01/31	122
7250 = 16#1C52	rFP0	Output frequency on last fault	202A/33	5FB9/82	85/01/33	105
7251 = 16#1C53	rFP1	Output frequency on fault n-1	202A/34	5FB9/83	85/01/34	108
7252 = 16#1C54	rFP2	Output frequency on fault n-2	202A/35	5FB9/84	85/01/35	110
7253 = 16#1C55	rFP3	Output frequency on fault n-3	202A/36	5FB9/85	85/01/36	112
7254 = 16#1C56	rFP4	Output frequency on fault n-4	202A/37	5FB9/86	85/01/37	114

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
7255 = 16#1C57	rFP5	Output frequency on fault n-5	202A/38	5FB9/87	85/01/38	116
7256 = 16#1C58	rFP6	Output frequency on fault n-6	202A/39	5FB9/88	85/01/39	118
7257 = 16#1C59	rFP7	Output frequency on fault n-7	202A/3A	5FB9/89	85/01/3A	120
7258 = 16#1C5A	rFP8	Output frequency on fault n-8	202A/3B	5FB9/8A	85/01/3B	122
7260 = 16#1C5C	rtP0	Motor operating time on last fault	202A/3D	5FB9/8B	85/01/3D	107
7261 = 16#1C5D	rtP1	Motor operating time on fault n-1	202A/3E	5FB9/8C	85/01/3E	109
7262 = 16#1C5E	rtP2	Motor operating time on fault n-2	202A/3F	5FB9/8D	85/01/3F	111
7263 = 16#1C5F	rtP3	Motor operating time on fault n-3	202A/40	5FB9/8E	85/01/40	113
7264 = 16#1C60	rtP4	Motor operating time on fault n-4	202A/41	5FB9/8F	85/01/41	115
7265 = 16#1C61	rtP5	Motor operating time on fault n-5	202A/42	5FB9/90	85/01/42	117
7266 = 16#1C62	rtP6	Motor operating time on fault n-6	202A/43	5FB9/91	85/01/43	119
7267 = 16#1C63	rtP7	Motor operating time on fault n-7	202A/44	5FB9/92	85/01/44	121
7268 = 16#1C64	rtP8	Motor operating time on fault n-8	202A/45	5FB9/93	85/01/45	123
7270 = 16#1C66	ULP0	Power supply voltage on last fault	202A/47	5FB9/94	85/01/47	105
7271 = 16#1C67	ULP1	Supply voltage on fault n-1	202A/48	5FB9/95	85/01/48	108
7272 = 16#1C68	ULP2	Supply voltage on fault n-2	202A/49	5FB9/96	85/01/49	110
7273 = 16#1C69	ULP3	Supply voltage on fault n-3	202A/4A	5FB9/97	85/01/4A	112
7274 = 16#1C6A	ULP4	Supply voltage on fault n-4	202A/4B	5FB9/98	85/01/4B	114
7275 = 16#1C6B	ULP5	Supply voltage on fault n-5	202A/4C	5FB9/99	85/01/4C	116
7276 = 16#1C6C	ULP6	Supply voltage on fault n-6	202A/4D	5FB9/9A	85/01/4D	118
7277 = 16#1C6D	ULP7	Supply voltage on fault n-7	202A/4E	5FB9/9B	85/01/4E	120
7278 = 16#1C6E	ULP8	Supply voltage on fault n-8	202A/4F	5FB9/9C	85/01/4F	122
7280 = 16#1C70	tHP0	Motor thermal state on last fault	202A/51	5FB9/9D	85/01/51	105
7281 = 16#1C71	tHP1	Motor thermal state on fault n-1	202A/52	5FB9/9E	85/01/52	108
7282 = 16#1C72	tHP2	Motor thermal state on fault n-2	202A/53	5FB9/9F	85/01/53	110
7283 = 16#1C73	tHP3	Motor thermal state on fault n-3	202A/54	5FB9/A0	85/01/54	112
7284 = 16#1C74	tHP4	Motor thermal state on fault n-4	202A/55	5FB9/A1	85/01/55	114
7285 = 16#1C75	tHP5	Motor thermal state on fault n-5	202A/56	5FB9/A2	85/01/56	116
7286 = 16#1C76	tHP6	Motor thermal state on fault n-6	202A/57	5FB9/A3	85/01/57	118
7287 = 16#1C77	tHP7	Motor thermal state on fault n-7	202A/58	5FB9/A4	85/01/58	120
7288 = 16#1C78	tHP8	Motor thermal state on fault n-8	202A/59	5FB9/A5	85/01/59	122

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
7290 = 16#1C7A	CrP0	Channels active on last fault	202A/5B	5FB9/A6	85/01/5B	107
7291 = 16#1C7B	CrP1	Active channels on fault n-1	202A/5C	5FB9/A7	85/01/5C	109
7292 = 16#1C7C	CrP2	Active channels on fault n-2	202A/5D	5FB9/A8	85/01/5D	111
7293 = 16#1C7D	CrP3	Active channels on fault n-3	202A/5E	5FB9/A9	85/01/5E	113
7294 = 16#1C7E	CrP4	Active channels on fault n-4	202A/5F	5FB9/AA	85/01/5F	115
7295 = 16#1C7F	CrP5	Active channels on fault n-5	202A/60	5FB9/AB	85/01/60	117
7296 = 16#1C80	CrP6	Active channels on fault n-6	202A/61	5FB9/AC	85/01/61	119
7297 = 16#1C81	CrP7	Active channels on fault n-7	202A/62	5FB9/AD	85/01/62	121
7298 = 16#1C82	CrP8	Active channels on fault n-8	202A/63	5FB9/AE	85/01/63	123
7300 = 16#1C84	Md0	Date on last fault	202B/1	5FB9/AF	85/01/65	107
7301 = 16#1C85	Md1	Date on fault n-1	202B/2	5FB9/B0	85/01/66	109
7302 = 16#1C86	Md2	Date on fault n-2	202B/3	5FB9/B1	85/01/67	111
7303 = 16#1C87	Md3	Date on fault n-3	202B/4	5FB9/B2	85/01/68	113
7304 = 16#1C88	Md4	Date on fault n-4	202B/5	5FB9/B3	85/01/69	115
7305 = 16#1C89	Md5	Date on fault n-5	202B/6	5FB9/B4	85/01/6A	117
7306 = 16#1C8A	Md6	Date on fault n-6	202B/7	5FB9/BE	85/01/6B	119
7307 = 16#1C8B	Md7	Date on fault n-7	202B/8	5FB9/B6	85/01/6C	121
7308 = 16#1C8C	Md8	Date on fault n-8	202B/9	5FB9/B7	85/01/6D	123
7310 = 16#1C8E	dM0	Time on last fault	202B/B	5FB9/B8	85/01/6F	107
7311 = 16#1C8F	dM1	Time on fault n-1	202B/C	5FB9/B9	85/01/70	109
7312 = 16#1C90	dM2	Time on fault n-2	202B/D	5FB9/BA	85/01/71	111
7313 = 16#1C91	dM3	Time on fault n-3	202B/E	5FB9/BB	85/01/72	113
7314 = 16#1C92	dM4	Time on fault n-4	202B/F	5FB9/BC	85/01/73	115
7315 = 16#1C93	dM5	Time on fault n-5	202B/10	5FB9/BD	85/01/74	117
7316 = 16#1C94	dM6	Time on fault n-6	202B/11	5FB9/B5	85/01/75	119
7317 = 16#1C95	dM7	Time on fault n-7	202B/12	5FB9/BF	85/01/76	121
7318 = 16#1C96	dM8	Time on fault n-8	202B/13	5FB9/C0	85/01/77	123
7391 = 16#1CDF	dAY	Date	202B/5C	5FB9/CA	85/01/C0	132
7392 = 16#1CE0	tIME	Time	202B/5D	5FB9/CB	85/01/C1	132
7393 = 16#1CE1	Fnb	Fault counter	202B/5E	5FB9/CC	99/01/CC	104
8001 = 16#1F41	SCS	Save configuration	2032/2	5FBC/9A	9C/01/9A	69

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
8002 = 16#1F42	FCS	Restore configuration	2032/3	5FBC/9B	9C/01/9B	69
8020 = 16#1F54	CNFS	Active configuration	2032/15	5FB9/CD	89/01/15	67
8020 = 16#1F54	CNFS	[Config. Active]	2032/15	5FB9/CD	89/01/15	86
8021 = 16#1F55	CHA1	Assignment for 2 sets	2032/16	5FBC/9C	9C/01/9C	70
8021 = 16#1F55	CnF1	[2 Configurations]	2032/16	5FBC/9C	89/01/16	251
8021 = 16#1F55	CNF1	Assignment for 2 configurations	2032/16	5FBC/9C	9C/01/9C	67
8022 = 16#1F56	CHA2	Assignment for 3 sets	2032/17	5FBC/9D	9C/01/9D	70
8022 = 16#1F56	CnF2	[3 Configurations]	2032/17	5FBC/9D	89/01/17	252
8022 = 16#1F56	CNF2	Assignment for 3 configurations	2032/17	5FBC/9D	9C/01/9D	67
8025 = 16#1F59	CHM	[Multimotors]	2032/1A	5FBC/9E	89/01/1A	251
8401 = 16#20D1	CHCF	[Profile]	2036/2	5FBC/9F	8B/01/02	193
8402 = 16#20D2	COP	[Copy channel 1 <--> 2]	2036/3	5FBC/A0	8B/01/03	195
8411 = 16#20DB	rFC	[Ref. 2 switching]	2036/C	5FBC/A2	8B/01/0C	195
8412 = 16#20DC	rCb	[Ref 1B switching]	2036/D	5FBC/A3	8B/01/0D	198
8413 = 16#20DD	Fr1	[Ref.1 channel]	2036/E	5FBC/A4	8B/01/0E	193
8414 = 16#20DE	Fr2	[Ref.2 channel]	2036/F	5FBC/A5	8B/01/0F	195
8415 = 16#20DF	Fr1b	[Ref.1B channel]	2036/10	5FBC/A6	8B/01/10	198
8421 = 16#20D1	CCS	[Cmd switching]	2036/16	5FBC/A7	8B/01/16	194
8423 = 16#20E7	Cd1	[Cmd channel 1]	2036/18	5FBC/A8	8B/01/18	194
8424 = 16#20E8	Cd2	[Cmd channel 2]	2036/19	5FBC/A9	8B/01/19	194
8441 = 16#20F9	CRC	Active reference channel	2036/2A	5FB9/CE	8B/01/2A	85
8442 = 16#20FA	CCC	Active command channel	2036/2B	5FB9/CF	8B/01/2B	86
8491 = 16#212B	SPM	[Ref. memo ass.]	2036/5C	5FBC/AD	8B/01/5C	218
8502 = 16#2136	LFR	Frequency reference	2037/3	5FB6/1C	8B/01/67	77
8503 = 16#2137	PISP	PID regulator reference	2037/4	5FB6/1D	8B/01/68	78
8504 = 16#2138	CMI	Extended control word	2037/5	5FB6/1E	8B/01/69	76
8505 = 16#2139	LTR	Torque reference	6071	6071	8B/01/6A	77
8601 = 16#2199	CMd	Control word	6040	6040	B7/01/01	74
8602 = 16#219A	LFRD	Speed reference	6042	6042	2A/01/08	77
8603 = 16#219B	ETA	Status word	6041	6041	71/01/02	79
8604 = 16#219C	rFrd	Output speed	6044	6044	2A/01/07	87

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
8605 = 16#219D	FrHd	Speed reference before ramp	2038/6	5FB9/D6	8C/01/06	89
8606 = 16#219E	Errd	DSP402 fault code	603F	5FB9/D7	8C/01/07	99
8611 = 16#21A3	SPAL	Acceleration speed delta	6048/1	6048/1	8C/01/0C	140
8613 = 16#21A5	SPAt	Acceleration time delta	6048/2	6048/2	8C/01/0E	140
8614 = 16#21A6	SPdL	Deceleration speed delta	6049/1	6049/1	8C/01/0F	140
8616 = 16#21A8	SPdt	Deceleration time delta	6049/2	6049/2	8C/01/11	141
8641 = 16#21C1	FrOd	Speed reference after ramp	6043	5FB9/D8	8C/01/2A	89
8642 = 16#21C2	SPFn	Speed setpoint factor numerator	604B/1	604B/1	8C/01/2B	141
8643 = 16#21C3	SPFd	Speed setpoint factor denominator	604B/2	604B/2	8C/01/2C	142
9001 = 16#2329	ACC	[Acceleration]	203C/2	5FBF/1E	8E/01/02	201
9002 = 16#232A	dEC	[Deceleration]	203C/3	5FBF/1F	8E/01/03	201
9003 = 16#232B	brA	[Dec ramp adapt.]	203C/4	5FB3/CA	8E/01/04	204
9004 = 16#232C	rPt	[Ramp type]	203C/5	5FB3/CB	8E/01/05	201
9005 = 16#232D	tA1	[Begin Acc round]	203C/6	5FB3/CC	8E/01/06	202
9006 = 16#232E	tA2	[End Acc round]	203C/7	5FB3/CD	8E/01/07	202
9007 = 16#232F	tA3	[Begin Dec round]	203C/8	5FB3/CE	8E/01/08	202
9008 = 16#2330	tA4	[End Dec round]	203C/9	5FB3/CF	8E/01/09	202
9010 = 16#2332	rPS	[Ramp switching]	203C/B	5FBC/B3	8E/01/0B	203
9011 = 16#2333	Frt	[Ramp 2 threshold]	203C/C	5FB3/D0	8E/01/0C	202
9012 = 16#2334	AC2	[Acceleration 2]	203C/D	5FBF/24	8E/01/0D	217
9012 = 16#2334	AC2	[Acceleration 2]	203C/D	5FBF/24	8E/01/0D	203
9013 = 16#2335	dE2	[Deceleration 2]	203C/E	5FBF/25	8E/01/0E	203
9013 = 16#2335	dE2	[Deceleration 2]	203C/E	5FBF/25	8E/01/0E	217
9020 = 16#233C	Inr	[Ramp increment]	203C/15	5FBF/26	8E/01/15	201
9021 = 16#233D	FrO	Frequency reference after ramp	203C/16	5FB9/D9	8E/01/16	89
9103 = 16#238F	SPG	[Speed prop. gain]	203D/4	5FB3/D1	8E/01/68	145
9104 = 16#2390	Slt	[Speed time integral]	203D/5	5FBF/28	8E/01/69	146
9105 = 16#2391	SFC	[K speed loop filter]	203D/6	5FBF/29	8E/01/6A	145
9201 = 16#23F1	CLI	[Current Limitation]	203E/2	5FB3/D2	8F/01/02	146
9201 = 16#23F1	CLI	[Current Limitation]	203E/2	5FB3/D2	8F/01/02	162
9201 = 16#23F1	CLI	[Current Limitation]	203E/2	5FB3/D2	93/01/D2	243

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
9202 = 16#23F2	LC2	[Current limit 2]	203E/3	5FBC/B5	8F/01/03	243
9203 = 16#23F3	CL2	[I Limit. 2 value]	203E/4	5FBF/2B	8F/01/04	243
9210 = 16#23FA	tLA	[AI torque limit. activ.]	203E/B	5FBC/B6	8F/01/0B	241
9211 = 16#23FB	tLIM	[Motoring torque lim]	203E/C	5FBF/2C	8F/01/0C	241
9212 = 16#23FC	tLIG	[Gen. torque lim]	203E/D	5FBF/2D	8F/01/0D	241
9213 = 16#23FD	tLC	[Analog limit. act.]	203E/E	5FBC/B7	8F/01/0E	242
9214 = 16#23FE	tAA	[Torque ref. assign.]	203E/F	5FBC/B8	8F/01/0F	242
9220 = 16#2404	tSS	[Trq/spd switching]	203E/15	5FBC/B9	8F/01/15	238
9221 = 16#2405	tr1	[Torque ref. channel]	203E/16	5FBC/BA	8F/01/16	238
9222 = 16#2406	tSd	[Torque ref. sign]	203E/17	5FBC/BB	8F/01/17	239
9223 = 16#2407	dbn	[Negative deadband]	203E/18	5FBF/2E	8F/01/18	240
9224 = 16#2408	dbP	[Positive deadband]	203E/19	5FBF/2F	8F/01/19	240
9225 = 16#2409	trt	[Torque ratio]	203E/1A	5FBF/30	8F/01/1A	239
9226 = 16#240A	trP	[Torque ramp time]	203E/1B	5FBF/31	8F/01/1B	239
9227 = 16#240B	tSt	[Torque control stop]	203E/1C	5FBC/BC	8F/01/1C	239
9228 = 16#240C	tOb	[Torq. ctrl fault mgt]	203E/1D	5FBC/BD	8F/01/1D	240
9229 = 16#240D	rtO	[Torque R. time out]	203E/1E	5FBF/32	8F/01/1E	240
9230 = 16#240E	SPt	[Spin time]	203E/1F	5FBF/33	8F/01/1F	240
9231 = 16#240F	trr	Torque reference before ramp	203E/20	5FB9/DB	8F/01/20	90
9232 = 16#2410	trO	Torque reference after ramp	203E/21	5FB9/DC	8F/01/21	90
9240 = 16#2418	SSb	[Trq/I limit. stop]	203E/29	5FBC/BE	8F/01/29	277
9241 = 16#2419	StO	[Trq/I limit. time out]	203E/2A	5FBF/34	8F/01/2A	277
9260 = 16#242C	Int	[Torque unit]	203E/3D	5FBF/35	8F/01/3D	238
9261 = 16#242D	LtCr	Torque reference (Nm)	203E/3E	5FB6/3D	2A/01/0C	77
9601 = 16#2581	UnS	[Rated motor volt.]	2042/2	5FB3/D3	77/01/10	148
9602 = 16#2582	FrS	[Rated motor freq.]	2042/3	5FB3/D4	91/01/03	149
9603 = 16#2583	nCr	[Rated mot. current]	2042/4	5FB3/D5	91/01/04	148
9604 = 16#2584	nSP	[Nom motor speed]	2042/5	5FB3/D6	91/01/05	149
9605 = 16#2585	nSL	[Nominal motor slip]	2042/6	5FB9/DD	91/01/06	157
9607 = 16#2587	Ctt	[Motor control type]	2042/8	5FC2/06	91/01/08	150
9608 = 16#2588	tUn	[Auto-tuning]	2042/9	5FB3/D7	91/01/09	149

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
9609 = 16#2589	tUS	[Auto tuning status]	2042/A	5FC2/08	91/01/0A	150
9610 = 16#258A	tUL	[Auto-tune assign.]	2042/B	5FBC/EF	91/01/0B	253
9611 = 16#258B	OPL	[Output Phase Loss]	2042/C	5FB3/D8	91/01/0C	266
9612 = 16#258C	tHt	[Motor protect. type]	2042/D	5FB3/D9	91/01/0D	264
9613 = 16#258D	nPr	[Rated motor power]	2042/E	5FC2/0A	91/01/0E	148
9615 = 16#258F	AUt	[Automatic autotune]	2042/10	5FC2/0B	91/01/10	149
9618 = 16#2592	PPn	[Pr]	2042/13	5FB3/DA	91/01/13	158
9622 = 16#2596	ItH	[Mot. therm. current]	2042/17	5FB3/DB	2A/01/0A	145
9623 = 16#2597	UFr	[IR compensation]	2042/18	5FC2/0E	91/01/18	156
9625 = 16#2599	SLP	[Slip compensation]	2042/1A	5FB3/DC	91/01/1A	156
9630 = 16#259E	tHr	Motor thermal state	2042/1F	5FB9/DE	91/01/1F	92
9640 = 16#25A8	rSM	Asynchronous motor cold state stator resistance	2042/29	5FB9/DF	91/01/29	94
9640 = 16#25A8	rSM	[Stator R measured]	2042/29	5FB9/DF	91/01/29	157
9642 = 16#25AA	rSA	[R1w]	2042/2B	5FC2/11	91/01/2B	158
9650 = 16#25B2	IdM	Magnetizing current	2042/33	5FB9/E0	91/01/33	93
9650 = 16#25B2	IdM	[Idr]	2042/33	5FB9/E0	91/01/33	157
9652 = 16#25B4	IdA	[Idw]	2042/35	5FC2/15	91/01/35	158
9660 = 16#25BC	LFM	Leakage inductance	2042/3D	5FB9/E1	91/01/3D	94
9660 = 16#25BC	LFM	[Lfr]	2042/3D	5FB9/E1	91/01/3D	157
9662 = 16#25BE	LFA	[Lfw]	2042/3F	5FC2/19	91/01/3F	158
9665 = 16#25C1	trM	Rotor time constant	2042/42	5FB9/E2	91/01/42	94
9665 = 16#25C1	trM	[T2r]	2042/42	5FB9/E2	91/01/42	157
9667 = 16#25C3	trA	[T2w]	2042/44	5FC2/1C	91/01/44	158
9670 = 16#25C6	nCrS	[Nominal I sync.]	2042/47	5FC2/1E	91/01/47	154
9671 = 16#25C7	nSPS	[Nom motor spdsync]	2042/48	5FC2/1F	91/01/48	154
9672 = 16#25C8	PPnS	[Pole pairs]	2042/49	5FC2/20	91/01/49	154
9673 = 16#25C9	PHS	[Syn. EMF constant]	2042/4A	5FC2/21	91/01/4A	154
9674 = 16#25CA	LdS	[Autotune L d-axis]	2042/4B	5FC2/22	91/01/4B	154
9675 = 16#25CB	LqS	[Autotune L q-axis]	2042/4C	5FC2/23	91/01/4C	155
9679 = 16#25CF	FrSS	[Nominal freq sync.]	2042/50	5FC2/24	91/01/50	159

Index of parameter logic addresses

Logic address	Code	Name	CANopen index	INTERBUS index	DeviceNet path	Page
9680 = 16#25D0	rSMS	Synchronous motor cold state stator resistance	2042/51	5FB9/E3	91/01/51	94
9680 = 16#25D0	rSMS	[R1rS]	2042/51	5FB9/E3	91/01/51	159
9682 = 16#25D2	rSAS	[Cust. stator R syn]	2042/53	5FC2/26	91/01/53	155

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
6040	CMd	Control word	8601 = 16#2199	6040	B7/01/01	74
6041	ETA	Status word	8603 = 16#219B	6041	71/01/02	79
6042	LFRD	Speed reference	8602 = 16#219A	6042	2A/01/08	77
6043	FrOd	Speed reference after ramp	8641 = 16#21C1	5FB9/D8	8C/01/2A	89
6044	rFrd	Output speed	8604 = 16#219C	6044	2A/01/07	87
6071	LTR	Torque reference	8505 = 16#2139	6071	8B/01/6A	77
6077	Otr	Output torque	3205 = 16#0C85	6077	71/01/06	87
2000/10	bFr	[Standard mot. freq]	3015 = 16#BC7	5FBC/05	70/01/10	148
2000/12	InV	Rated drive current	3017 = 16#0BC9	5FB0/07	70/01/12	124
2000/35	CFG	[Macro configuration]	3052 = 16#BEC	5FBC/07	70/01/35	144
2000/36	CCFG	[Customized macro]	3053 = 16#BED	5FB9/02	70/01/36	144
2000/C	nCV	Drive nominal rating	3011 = 16#0BC3	5FB0/02	70/01/0C	124
2000/D	UCAL	Drive line voltage	3012 = 16#0BC4	5FB0/03	70/01/0D	124
2001/3	SFr	[Switching freq.]	3102 = 16#C1E	5FBF/02	70/01/67	162
2001/4	tFr	[Max frequency]	3103 = 16#C1F	5FB3/74	70/01/68	149
2001/5	HSP	[High speed]	3104 = 16#C20	5FB3/75	70/01/69	145
2001/6	LSP	[Low speed]	3105 = 16#C21	5FB3/76	70/01/6A	145
2001/7	bSP	[Reference template]	3106 = 16#C22	5FB3/77	70/01/6B	168
2001/8	nrd	[Noise reduction]	3107 = 16#C23	5FB3/78	70/01/6C	162
2001/9	rIn	[RV Inhibition]	3108 = 16#C24	5FB3/79	70/01/6D	193
2001/A	OFI	[Sinus filter]	3109 = 16#C25	5FBC/0B	70/01/6E	162
2001/B	FLr	[Catch on the fly]	3110 = 16#C26	5FB3/7A	70/01/6F	263
2001/C	UCb	[Sensitivity]	3111 = 16#C27	5FBF/06	70/01/70	263
2001/D	Strt	[IGBT test]	3112 = 16#C28	5FBC/0D	70/01/71	272
2002/11	Otrn	Output torque (Nm)	3216 = 16#0C90	5FB9/10	2A/01/0B	88
2002/1F	APH	Energy consumption	3230 = 16#0C9E	5FB9/15	71/01/1F	92
2002/20	rTH	Total motor operating time	3231 = 16#0C9F	5FB9/16	71/01/20	93
2002/21	rTHI	Internal motor operating time	3232 = 16#0CA0	5FB9/17	71/01/21	93
2002/22	PtH	Total drive operating time	3233 = 16#0CA1	5FB9/18	71/01/22	93
2002/24	tAC	IGBT alarm time	3235 = 16#0CA3	5FB9/1A	71/01/24	93

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2002/3	rFr	Output frequency	3202 = 16#C82	5FB9/04	71/01/03	87
2002/33	LRS1	Extended status word 1	3250 = 16#0CB2	5FB9/1C	71/01/33	81
2002/34	LRS2	Extended status word 2	3251 = 16#0CB3	5FB9/1D	71/01/34	82
2002/35	LRS3	Extended status word 3	3252 = 16#0CB4	5FB9/1E	71/01/35	82
2002/36	LRS4	Extended status word 4	3253 = 16#0CB5	5FB9/1F	71/01/36	83
2002/37	LRS5	Extended status word 5	3254 = 16#0CB6	5FB9/20	71/01/37	83
2002/38	LRS6	Extended status word 6	3255 = 16#0CB7	5FB9/21	71/01/38	84
2002/39	LRS7	Extended status word 7	3256 = 16#0CB8	5FB9/22	71/01/39	84
2002/3A	LRS8	Extended status word 8	3257 = 16#0C89	5FB9/23	71/01/3A	85
2002/4	FrH	Frequency reference before ramp	3203 = 16#0C83	5FB9/05	71/01/04	89
2002/5	LCr	Motor current	3204 = 16#0C84	5FB9/06	2A/01/09	88
2002/7	ETI	Extended status word 0	3206 = 16#0C86	5FB9/08	71/01/07	81
2002/8	ULn	Power supply voltage	3207 = 16#0C87	5FB9/09	71/01/08	92
2002/9	UOP	Motor voltage	3208 = 16#0C88	5FB9/0A	71/01/09	88
2002/A	tHd	Drive thermal state	3209 = 16#0C89	5FB9/0B	71/01/0A	92
2002/C	OPr	Motor power	3211 = 16#0C8B	5FB9/0C	71/01/0C	88
2003/29	PAn0	Device name: characters 1 and 2	3340 = 16#0D0C	5FB0/25	71/01/8D	124
2003/2A	PAn1	Device name: characters 3 and 4	3341 = 16#0D0D	5FB0/26	71/01/8E	124
2003/2B	PAn2	Device name: characters 5 and 6	3342 = 16#0D0E	5FB0/27	71/01/8F	125
2003/2C	PAn3	Device name: characters 7 and 8	3343 = 16#0D0F	5FB0/28	71/01/90	125
2003/2D	PAn4	Device name: characters 9 and 10	3344 = 16#0D10	5FB0/29	71/01/91	125
2003/2E	PAn5	Device name: characters 11 and 12	3345 = 16#0D11	5FB0/2A	71/01/92	125
2003/2F	PAn6	Device name: characters 13 and 14	3346 = 16#0D12	5FB0/2B	71/01/93	125
2003/3	UdP	Drive software version	3302 = 16#0CE6	5FB0/0D	71/01/67	124
2003/30	PAn7	Device name: characters 15 and 16	3346 = 16#0D12	5FB0/2C	71/01/94	125
200A/2	L1d	[L1 On Delay]	4001 = 16#FA1	5FBC/0E	75/01/02	167
200A/3	L2d	[L2 On Delay]	4002 = 16#FA2	5FBC/0F	75/01/03	167
200A/4	L3d	[L3 On Delay]	4003 = 16#FA3	5FBC/10	75/01/04	167
200A/5	L4d	[L4 On Delay]	4004 = 16#FA4	5FBC/11	75/01/05	167
200A/6	L5d	[L5 On Delay]	4005 = 16#FA5	5FBC/12	75/01/06	167
200A/7	L6d	[L6 On Delay]	4006 = 16#FA6	5FBC/13	75/01/07	167

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
200A/8	L7d	[LI7 On Delay]	4007 = 16#FA7	5FBC/14	75/01/08	167
200A/9	L8d	[LI8 On Delay]	4008 = 16#FA8	5FBC/15	75/01/09	167
200A/A	L9d	[LI9 On Delay]	4009 = 16#FA9	5FBC/16	75/01/0A	167
200A/B	L10d	[LI10 On Delay]	4010 = 16#FAA	5FBC/17	75/01/0B	167
200A/C	L11d	[LI11 On Delay]	4011 = 16#FAB	5FBC/18	75/01/0C	167
200A/D	L12d	[LI12 On Delay]	4012 = 16#FAC	5FBC/19	75/01/0D	167
200A/E	L13d	[LI13 On Delay]	4013 = 16#FAD	5FBC/1A	75/01/0E	167
200A/F	L14d	[LI14 On Delay]	4014 = 16#FAE	5FBC/1B	75/01/0F	167
200C/16	r1H	[R1 Holding time]	4221 = 16#107D	5FBC/24	76/01/16	180
200C/17	r2H	[R2 Holding time]	4222 = 16#107E	5FBC/25	76/01/17	181
200C/18	r3H	[R3 Holding time]	4223 = 16#107F	5FBC/26	76/01/18	182
200C/19	r4H	[R4 Holding time]	4224 = 16#1080	5FBC/27	76/01/19	183
200C/1E	LO1H	[LO1 holding time]	4229 = 16#1085	5FBC/28	76/01/1E	184
200C/1F	LO2H	[LO2 holding time]	4230 = 16#1086	5FBC/29	76/01/1F	185
200C/2	r1S	[R1 Active at]	4201 = 16#1069	5FBC/1C	76/01/02	180
200C/20	LO3H	[LO3 holding time]	4231 = 16#1087	5FBC/2A	76/01/20	186
200C/21	LO4H	[LO4 holding time]	4232 = 16#1088	5FBC/2B	76/01/21	187
200C/2A	r1d	[R1 Delay time]	4241 = 16#1091	5FBC/2C	76/01/2A	180
200C/2B	r2d	[R2 Delay time]	4242 = 16#1092	5FBC/2D	76/01/2B	181
200C/2C	r3d	[R3 Delay time]	4243 = 16#1093	5FBC/2E	76/01/2C	182
200C/2D	r4d	[R4 Delay time]	4244 = 16#1094	5FBC/2F	76/01/2D	183
200C/3	r2S	[R2 Active at]	4202 = 16#106A	5FBC/1D	76/01/03	181
200C/32	LO1d	[LO1 delay time]	4249 = 16#1099	5FBC/30	76/01/32	184
200C/33	LO2d	[LO2 delay time]	4250 = 16#109A	5FBC/31	76/01/33	185
200C/34	LO3d	[LO3 delay time]	4251 = 16#109B	5FBC/32	76/01/34	186
200C/35	LO4d	[LO4 delay time]	4252 = 16#109C	5FBC/33	76/01/35	187
200C/4	r3S	[R3 Active at]	4203 = 16#106B	5FBC/1E	76/01/04	182
200C/5	r4S	[R4 Active at]	4204 = 16#106C	5FBC/1F	76/01/05	183
200C/2D	LO1S	[LO1 active at]	4209 = 16#1071	5FBC/20	76/01/0A	184
200C/B	LO2S	[LO2 active at]	4210 = 16#1072	5FBC/21	76/01/0B	185
200C/C	LO3S	[LO3 active at]	4211 = 16#1073	5FBC/22	76/01/0C	186

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
200C/D	LO4S	[LO4 active at]	4212 = 16#1074	5FBC/23	76/01/0D	187
200E/10	UIL4	[AI4 min value]	4415 = 16#113D	5FBC/3A	77/01/10	174
200E/17	UIH1	[AI1 max value]	4422 = 16#1146	5FBC/3B	77/01/17	169
200E/18	UIH2	[AI2 max value]	4423 = 16#1147	5FBC/3C	77/01/18	170
200E/1A	UIH4	[AI4 max value]	4425 = 16#1149	5FBC/3D	77/01/1A	174
200E/22	CrL2	[AI2 min value]	4433 = 16#1151	5FBC/3E	77/01/22	170
200E/23	CrL3	[AI3 min value]	4434 = 16#1152	5FBC/3F	77/01/23	172
200E/24	CrL4	[AI4 min value]	4435 = 16#1153	5FBC/40	77/01/24	174
200E/2C	CrH2	[AI2 max value]	4443 = 16#115B	5FBC/41	77/01/2C	170
200E/2D	CrH3	[AI3 max value]	4444 = 16#115C	5FBC/42	77/01/2D	172
200E/2E	CrH4	[AI4 max value]	4445 = 16#115D	5FBC/43	77/01/2E	174
200E/3	AI1t	[AI1 Type]	4402 = 16#1132	5FBC/34	77/01/03	169
200E/35	AI1F	[AI1 filter]	4452 = 16#1164	5FBC/44	77/01/35	169
200E/36	AI2F	[AI2 filter]	4453 = 16#1165	5FBC/45	77/01/36	170
200E/37	AI3F	[AI3 filter]	4454 = 16#1166	5FBC/46	77/01/37	172
200E/38	AI4F	[AI4 filter]	4455 = 16#1167	5FBC/47	77/01/38	174
200E/3F	AI1E	[AI1 Interm. point X]	4462 = 16#116E	5FBC/48	77/01/3F	169
200E/4	AI2t	[AI2 Type]	4403 = 16#1133	5FBC/35	77/01/04	170
200E/40	AI2E	[AI2 Interm. point X]	4463 = 16#116F	5FBC/49	77/01/40	171
200E/41	AI3E	[AI3 Interm. point X]	4464 = 16#1170	5FBC/4A	77/01/4A	173
200E/42	AI4E	[AI4 Interm. point X]	4465 = 16#1171	5FBC/4B	77/01/42	175
200E/49	AI1S	[AI1 Interm. point Y]	4472 = 16#1178	5FBC/4C	77/01/49	169
200E/4A	AI2S	[AI2 Interm. point Y]	4473 = 16#1179	5FBC/4D	77/01/4A	171
200E/4B	AI3S	[AI3 Interm. point Y]	4474 = 16#117A	5FBC/4E	77/01/4A	173
200E/4C	AI4S	[AI4 Interm. point Y]	4475 = 16#117B	5FBC/4F	77/01/4C	175
200E/5	AI3t	[AI3 Type]	4404 = 16#1134	5FBC/36	77/01/05	172
200E/54	AI2L	[AI2 range]	4483 = 16#1183	5FBC/50	77/01/54	171
200E/55	AI3L	[AI3 range]	4484 = 16#1184	5FBC/51	77/01/55	172
200E/56	AI4L	[AI4 range]	4485 = 16#1185	5FBC/52	77/01/56	175
200E/6	AI4t	[AI4 Type]	4405 = 16#1135	5FBC/37	77/01/06	174
200E/D	UIL1	[AI1 min value]	4412 = 16#113C	5FBC/38	77/01/0D	169

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
200E/E	UIL2	[AI2 min value]	4413 = 16#113D	5FBC/39	77/01/0E	170
2010/16	UOL1	[AO1 min Output]	4621 = 16#120D	5FBC/59	78/01/16	189
2010/17	UOL2	[AO2 min Output]	4622 = 16#120E	5FBC/5A	78/01/17	190
2010/18	UOL3	[AO3 min Output]	4623 = 16#120F	5FBC/5B	78/01/18	191
2010/2	AO1t	[AO1 Type]	4601 = 16#11F9	5FBC/53	78/01/02	188
2010/20	UOH1	[AO1 max Output]	4631 = 16#1217	5FBC/5C	78/01/20	189
2010/21	UOH2	[AO2 max Output]	4632 = 16#1218	5FBC/5D	78/01/21	190
2010/22	UOH3	[AO3 max Output]	4633 = 16#1219	5FBC/5E	78/01/22	191
2010/2A	AOL1	[AO1 min Output]	4641 = 16#1221	5FBC/5F	78/01/2A	188
2010/2B	AOL2	[AO2 min Output]	4642 = 16#1222	5FBC/60	78/01/2B	190
2010/2C	AOL3	[AO3 min Output]	4643 = 16#1223	5FBC/61	78/01/2C	191
2010/3	AO2t	[AO2 Type]	4602 = 16#11FA	5FBC/54	78/01/03	190
2010/34	AOH1	[AO1 max Output]	4651 = 16#122B	5FBC/62	78/01/34	188
2010/35	AOH2	[AO2 max Output]	4652 = 16#122C	5FBC/63	78/01/35	190
2010/36	AOH3	[AO3 max Output]	4653 = 16#122D	5FBC/64	78/01/36	191
2010/4	AO3t	[AO3 Type]	4603 = 16#11FB	5FBC/55	78/01/04	191
2010/C	AO1F	[AO1 Filter]	4611 = 16#1203	5FBC/56	78/01/0C	189
2010/D	AO2F	[AO2 Filter]	4612 = 16#1204	5FBC/57	78/01/0D	190
2010/E	AO3F	[AO3 Filter]	4613 = 16#1205	5FBC/58	78/01/0E	191
2014/16	AO1	[AO1 assignment]	5021 = 16#139D	5FBC/6D	7A/01/16	188
2014/17	AO2	[AO2 assignment]	5022 = 16#139E	5FBC/6E	7A/01/17	190
2014/18	AO3	[AO3 assignment]	5023 = 16#139F	5FBC/6F	7A/01/18	191
2014/2	r1	[R1 Assignment]	5001 = 16#1389	5FB3/7B	7A/01/02	179
2014/3	r2	[R2 Assignment]	5002 = 16#138A	5FB3/7C	7A/01/03	181
2014/4	r3	[R3 Assignment]	5003 = 16#138B	5FBC/67	7A/01/04	182
2014/5	r4	[R4 Assignment]	5004 = 16#138C	5FBC/68	7A/01/05	183
2014/A	LO1	[LO1 assignment]	5009 = 16#1391	5FB3/7D	7A/01/0A	184
2014/B	LO2	[LO2 assignment]	5010 = 16#1392	5FBC/6A	7A/01/0B	185
2014/C	LO3	[LO3 assignment]	5011 = 16#1393	5FBC/6B	7A/01/0C	186
2014/D	LO4	[LO4 assignment]	5012 = 16#1394	5FBC/6C	7A/01/0D	187
2016/21	AI1r	Standardized image of analog input 1	5232 = 16#1470	5FB9/2F	7B/01/21	96

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2016/22	AI2r	Standardized image of analog input 2	5233 = 16#1471	5FB9/30	7B/01/22	96
2016/23	AI3r	Standardized image of analog input 3	5234 = 16#1472	5FB9/31	7B/01/23	96
2016/24	AI4r	Standardized image of analog input 4	5235 = 16#1473	5FB9/32	7B/01/24	97
2016/2B	AI1C	Physical image of analog input 1	5242 = 16#147A	5FB9/33	7B/01/2B	96
2016/2C	AI2C	Physical image of analog input 2	5243 = 16#147B	5FB9/34	7B/01/2C	96
2016/2D	AI3C	Physical image of analog input 3	5244 = 16#147C	5FB9/35	7B/01/2D	96
2016/2E	AI4C	Physical image of analog input 4	5245 = 16#147D	5FB9/36	7B/01/2E	96
2016/3	IL1r	Logic input map	5202 = 16#1452	5FB9/28	7B/01/03	95
2016/3E	AO1r	Standardized image of analog output 1	5261 = 16#148D	5FB9/3A	7B/01/3E	97
2016/3F	AO2r	Standardized image of analog output 2	5262 = 16#148E	5FB9/3B	7B/01/3F	97
2016/40	AO3r	Standardized image of analog output 3	5263 = 16#148F	5FB9/3C	7B/01/40	98
2016/48	AO1C	Physical image of analog output 1	5271 = 16#1497	5FB9/3D	7B/01/48	97
2016/49	AO2C	Physical image of analog output 2	5272 = 16#1498	5FB9/3E	7B/01/49	97
2016/4A	AO3C	Physical image of analog output 3	5273 = 16#1499	5FB9/3F	7B/01/4A	97
2016/52	AIU1	PID regulator feedback	5281 = 16 #14A1	5FB9/40	7B/01/52	78
2016/53	AIC1	[AI net. channel]	5282 = 16#14A2	5FBC/70	7B/01/53	230
2016/D	OL1r	Logic output map	5212 = 16#145C	5FB9/2A	7B/01/0D	95
201A/5	PGI	Encoder pulse counter	5604 = 16#15E4	5FB3/7E	7D/01/05	98
201A/5	PGI	[Number of pulses]	5604 = 16#15E4	5FB3/7E	7D/01/05	159
201A/5	PGI	[Number of pulses]	5604 = 16#15E4	5FB3/7E	7D/01/05	177
201A/6	EnC	[Encoder check]	5605 = 16#15E5	5FB3/7F	7D/01/06	160
201A/6	EnC	[Encoder check]	5605 = 16#15E5	5FB3/7F	7D/01/06	177
201A/7	EnU	[Encoder usage]	5606 = 16#15E6	5FBC/73	7D/01/07	160
201A/7	EnU	[Encoder usage]	5606 = 16#15E6	5FBC/73	7D/01/07	177
201A/8	ECC	[Encoder coupling]	5607 = 16#15E7	5FBC/74	7D/01/08	276
201A/9	EnS	[Encoder type]	5608 = 16#15E8	5FBC/75	7D/01/09	177
201A/A	ECt	[Encoder check time]	5609 = 16#15E9	5FBF/07	7D/01/0A	276
201A/A	EnS	[Encoder type]	5608 = 16#15E8	5FBC/75	7D/01/09	159
201A/B	Pdl	Drive encoder counter divisor	5610 = 16#15EA	5FBC/76	7D/01/0B	131
201A/C	PUC	Drive encoder divided counter	5611 = 16#15EB	5FB9/41	7D/01/0C	132

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2022/10	O15	"Controller Inside" HMI exchange word 15	6415 = 16#190F	5FB3/8F	81/01/10	134
2022/11	O16	"Controller Inside" HMI exchange word 16	6416 = 16#1910	5FB3/90	81/01/11	134
2022/12	O17	"Controller Inside" HMI exchange word 17	6417 = 16#1911	5FB3/91	81/01/12	134
2022/13	O18	"Controller Inside" HMI exchange word 18	6418 = 16#1912	5FB3/92	81/01/13	134
2022/14	O19	"Controller Inside" HMI exchange word 19	6419 = 16#1913	5FB3/93	81/01/14	135
2022/15	O20	"Controller Inside" HMI exchange word 20	6420 = 16#1914	5FB3/94	81/01/15	135
2022/16	O21	"Controller Inside" HMI exchange word 21	6421 = 16#1915	5FB3/95	81/01/16	135
2022/17	O22	"Controller Inside" HMI exchange word 22	6422 = 16#1916	5FB3/96	81/01/17	135
2022/18	O23	"Controller Inside" HMI exchange word 23	6423 = 16#1917	5FB3/97	81/01/18	135
2022/19	O24	"Controller Inside" HMI exchange word 24	6424 = 16#1918	5FB3/98	81/01/19	135
2022/1A	O25	"Controller Inside" HMI exchange word 25	6425 = 16#1919	5FB3/99	81/01/1A	135
2022/1B	O26	"Controller Inside" HMI exchange word 26	6426 = 16#191A	5FB3/9A	81/01/1B	136
2022/1C	O27	"Controller Inside" HMI exchange word 27	6427 = 16#191B	5FB3/9B	81/01/1C	136
2022/1D	O28	"Controller Inside" HMI exchange word 28	6428 = 16#191C	5FB3/9C	81/01/1D	136
2022/1E	O29	"Controller Inside" HMI exchange word 29	6429 = 16#191D	5FB3/9D	81/01/1E	136
2022/1F	O30	"Controller Inside" HMI exchange word 30	6430 = 16#191E	5FB3/9E	81/01/1F	136
2022/2	O01	"Controller Inside" HMI exchange word 1	6401 = 16#1901	5FB3/81	81/01/02	132
2022/20	O31	"Controller Inside" HMI exchange word 31	6431 = 16#191F	5FB3/9F	81/01/20	136
2022/21	O32	"Controller Inside" HMI exchange word 32	6432 = 16#1920	5FB3/A0	81/01/21	136
2022/22	O33	"Controller Inside" HMI exchange word 33	6433 = 16#1921	5FB3/A1	81/01/22	137
2022/23	O34	"Controller Inside" HMI exchange word 34	6434 = 16#1922	5FB3/A2	81/01/23	137

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2022/24	O35	"Controller Inside" HMI exchange word 35	6435 = 16#1923	5FB3/A3	81/01/24	137
2022/25	O36	"Controller Inside" HMI exchange word 36	6436 = 16#1924	5FB3/A4	81/01/25	137
2022/26	O37	"Controller Inside" HMI exchange word 37	6437 = 16#1925	5FB3/A5	81/01/26	137
2022/27	O38	"Controller Inside" HMI exchange word 38	6438 = 16#1926	5FB3/A6	81/01/27	137
2022/28	O39	"Controller Inside" HMI exchange word 39	6439 = 16#1927	5FB3/A7	81/01/28	137
2022/29	O40	"Controller Inside" HMI exchange word 40	6440 = 16#1928	5FB3/A8	81/01/29	138
2022/2A	O41	"Controller Inside" HMI exchange word 41	6441 = 16#1929	5FB3/A9	81/01/2A	138
2022/2B	O42	"Controller Inside" HMI exchange word 42	6442 = 16#192A	5FB3/AA	81/01/2B	138
2022/2C	O43	"Controller Inside" HMI exchange word 43	6443 = 16#192B	5FB3/AB	81/01/2C	138
2022/2D	O44	"Controller Inside" HMI exchange word 44	6444 = 16#192C	5FB3/AC	81/01/2D	138
2022/2E	O45	"Controller Inside" HMI exchange word 45	6445 = 16#192D	5FB3/AD	81/01/2E	138
2022/2F	O46	"Controller Inside" HMI exchange word 46	6446 = 16#192E	5FB3/AE	81/01/2F	138
2022/3	O02	"Controller Inside" HMI exchange word 2	6402 = 16#1902	5FB3/82	81/01/03	132
2022/30	O47	"Controller Inside" HMI exchange word 47	6447 = 16#192F	5FB3/AF	81/01/30	139
2022/31	O48	"Controller Inside" HMI exchange word 48	6448 = 16#1930	5FB3/B0	81/01/31	139
2022/32	O49	"Controller Inside" HMI exchange word 49	6449 = 16#1931	5FB3/B1	81/01/32	139
2022/33	O50	"Controller Inside" HMI exchange word 50	6450 = 16#1932	5FC8/32	81/01/33	139
2022/4	O03	"Controller Inside" HMI exchange word 3	6403 = 16#1903	5FB3/83	81/01/04	132
2022/5	O04	"Controller Inside" HMI exchange word 4	6404 = 16#1904	5FB3/84	81/01/05	132
2022/6	O05	"Controller Inside" HMI exchange word 5	6405 = 16#1905	5FB3/85	81/01/06	133
2022/7	O06	"Controller Inside" HMI exchange word 6	6406 = 16#1906	5FB3/86	81/01/07	133

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2022/8	O07	"Controller Inside" HMI exchange word 7	6407 = 16#1907	5FB3/87	81/01/08	133
2022/9	O08	"Controller Inside" HMI exchange word 8	6408 = 16#1908	5FB3/88	81/01/09	133
2022/A	O09	"Controller Inside" HMI exchange word 9	6409 = 16#1909	5FB3/89	81/01/0A	133
2022/B	O10	"Controller Inside" HMI exchange word 10	6410 = 16#190A	5FB3/8A	81/01/0B	133
2022/C	O11	"Controller Inside" HMI exchange word 11	6411 = 16#190B	5FB3/8B	81/01/0C	133
2022/D	O12	"Controller Inside" HMI exchange word 12	6412 = 16#190C	5FB3/8C	81/01/0D	134
2022/E	O13	"Controller Inside" HMI exchange word 13	6413 = 16#190D	5FB3/8D	81/01/0E	134
2022/F	O14	"Controller Inside" HMI exchange word 14	6414 = 16#190E	5FB3/8E	81/01/0F	134
2027/2	PIL1	"Controller Inside" logic input map	6901 = 16#1AF5	5FB9/49	83/01/66	131
2027/2B	PAI1	Physical image of analog input 1	6942 = 16#1B1E	5FB9/4B	83/01/8F	131
2027/2C	PAI2	Physical image of analog input 2	6943 = 16#1B1F	5FB9/4C	83/01/90	131
2027/48	PAO1	Physical image of analog output 1	6971 = 16#1B3B	5FB9/4D	83/01/AC	131
2027/49	PAO2	Physical image of analog output 2	6972 = 16#1B3C	5FB9/4E	83/01/AD	131
2027/C	POL1	"Controller Inside" logic output map	6911 = 16#1AFF	5FB9/4A	83/01/70	131
2028/10	CLL	[Network fault mgt]	7015 = 16#1B67	5FBC/93	84/01/10	275
2028/3	IPL	[Input phase loss]	7002 = 16#1B5A	5FB3/BE	84/01/03	266
2028/4	LFL2	[AI2 4-20mA loss]	7003 = 16#1B5B	5FB3/BF	84/01/04	273
2028/5	StP	[UnderV. prevention]	7004 = 16#1B5C	5FB3/C0	84/01/05	271
2028/51	LFF	[Fallback speed]	7080 = 16#1BA8	5FB3/C5	84/01/51	279
2028/52	Odt	[OutPh time detect]	7081 = 16#1BA9	5FBF/08	84/01/52	266
2028/6	Sdd	[Load slip detection]	7005 = 16#1B5D	5FB3/C1	84/01/06	276
2028/7	EPL	[External fault mgt]	7006 = 16#1B5E	5FB3/C2	84/01/07	269
2028/9	OHL	[Overtemp fault mgt]	7008 = 16#1B60	5FBC/8C	84/01/09	267
2028/A	OLL	[Overload fault mgt]	7009 = 16#1B61	5FBC/8D	84/01/0A	265
2028/B	SLL	[Modbus fault mgt]	7010 = 16#1B62	5FBC/8E	84/01/0B	275
2028/C	COL	[CANopen fault mgt]	7011 = 16#1B63	5FBC/8F	84/01/0C	275
2028/D	tnL	[Autotune fault mgt]	7012 = 16#1B64	5FBC/90	84/01/0D	279

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2028/E	LFL3	[AI3 4-20mA loss]	7013 =16#1B65	5FB3/C3	84/01/0E	273
2028/F	LFL4	[AI4 4-20mA loss]	7014 =16#1B66	5FB3/C4	84/01/0F	273
2029/16	LFt	Altivar fault code	7121 = 16#1BD1	5FB3/C8	84/01/7A	100
2029/17	Atr	[Automatic restart]	7122 = 16#1BD2	5FB3/C9	84/01/7B	262
2029/18	tAr	[Max. restart time]	7123 = 16#1BD3	5FBC/96	84/01/7C	262
2029/19	rSF	[Fault reset]	7124 = 16#1BD4	5FBC/97	84/01/7D	261
2029/1A	Inh	[Fault inhibit assign.]	7125 =16#1BD5	5FBC/98	84/01/7E	274
2029/1F	CIC	Incorrect configuration	7130 = 16#1BDA	5FB6/1A	84/01/83	102
2029/20	EtF	[External fault ass.]	7131 = 16#1BDB	5FBC/99	84/01/84	269
2029/21	CnF	Network card fault code	7132 = 16#1BDC	5FB0/96	84/01/85	102
2029/22	APF	"Controller Inside" card fault code	7133 = 16#1BDD	5FB0/97	84/01/86	102
2029/23	ILF1	Option card 1 fault code	7134 = 16#1BDE	5FB0/98	84/01/87	103
2029/24	ILF2	Option card 2 fault code	7135 = 16#1BDF	5FB0/99	84/01/88	103
202A/1	dP0	Fault code on last fault	7200 = 16#1C20	5FB9/55	85/01/01	105
202A/10	EP5	Status word on fault n-5	7215 = 16#1C2F	5FB9/63	85/01/10	116
202A/11	EP6	Status word on fault n-6	7216 = 16#1C30	5FB9/64	85/01/11	118
202A/12	EP7	Status word on fault n-7	7217 = 16#1C31	5FB9/65	85/01/12	120
202A/13	EP8	Status word on fault n-8	7218 = 16#1C32	5FB9/66	85/01/13	122
202A/15	IP0	Extended status word 0 on last fault	7220 = 16#1C34	5FB9/67	85/01/15	106
202A/16	IP1	Extended status word on fault n-1	7221 = 16#1C35	5FB9/68	85/01/16	108
202A/17	IP2	Extended status word on fault n-2	7222 = 16#1C36	5FB9/69	85/01/17	110
202A/18	IP3	Extended status word on fault n-3	7223 = 16#1C37	5FB9/6A	85/01/18	112
202A/19	IP4	Extended status word on fault n-4	7224 = 16#1C38	5FB9/6B	85/01/19	114
202A/1A	IP5	Extended status word on fault n-5	7225 = 16#1C39	5FB9/6C	85/01/1A	116
202A/1B	IP6	Extended status word on fault n-6	7226 = 16#1C3A	5FB9/6D	85/01/1B	118
202A/1C	IP7	Extended status word on fault n-7	7227 = 16#1C3B	5FB9/6E	85/01/1C	120
202A/1D	IP8	Extended status word on fault n-8	7228 = 16#1C3C	5FB9/6F	85/01/1D	122
202A/1F	CMP0	Command word on last fault	7230 = 16#1C3E	5FB9/70	85/01/1F	106
202A/2	dP1	Fault code on fault n-1	7201 = 16#1C21	5FB9/56	85/01/02	107
202A/20	CMP1	Command word on fault n-1	7231 = 16#1C3F	5FB9/71	85/01/20	108
202A/21	CMP2	Command word on fault n-2	7232 = 16#1C40	5FB9/72	85/01/21	110

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
202A/22	CMP3	Command word on fault n-3	7233 = 16#1C41	5FB9/73	85/01/22	112
202A/23	CMP4	Command word on fault n-4	7234 = 16#1C42	5FB9/74	85/01/23	114
202A/24	CMP5	Command word on fault n-5	7235 = 16#1C43	5FB9/75	85/01/24	116
202A/25	CMP6	Command word on fault n-6	7236 = 16#1C44	5FB9/76	85/01/25	118
202A/26	CMP7	Command word on fault n-7	7237 = 16#1C45	5FB9/77	85/01/26	120
202A/27	CMP8	Command word on fault n-8	7238 = 16#1C46	5FB9/78	85/01/27	122
202A/29	LCP0	Motor current on last fault	7240 = 16#1C48	5FB9/79	85/01/29	105
202A/2A	LCP1	Motor current on fault n-1	7241 = 16#1C49	5FB9/7A	85/01/2A	108
202A/2B	LCP2	Motor current on fault n-2	7242 = 16#1C4A	5FB9/7B	85/01/2B	110
202A/2C	LCP3	Motor current on fault n-3	7243 = 16#1C4B	5FB9/7C	85/01/2C	112
202A/2D	LCP4	Motor current on fault n-4	7244 = 16#1C4C	5FB9/7D	85/01/2D	114
202A/2E	LCP5	Motor current on fault n-5	7245 = 16#1C4D	5FB9/7E	85/01/2E	116
202A/2F	LCP6	Motor current on fault n-6	7246 = 16#1C4E	5FB9/7F	85/01/2F	118
202A/3	dP2	Fault code on fault n-2	7202 = 16#1C22	5FB9/57	85/01/03	109
202A/30	LCP7	Motor current on fault n-7	7247 = 16#1C4F	5FB9/80	85/01/30	120
202A/31	LCP8	Motor current on fault n-8	7248 = 16#1C50	5FB9/81	85/01/31	122
202A/33	rFP0	Output frequency on last fault	7250 = 16#1C52	5FB9/82	85/01/33	105
202A/34	rFP1	Output frequency on fault n-1	7251 = 16#1C53	5FB9/83	85/01/34	108
202A/35	rFP2	Output frequency on fault n-2	7252 = 16#1C54	5FB9/84	85/01/35	110
202A/36	rFP3	Output frequency on fault n-3	7253 = 16#1C55	5FB9/85	85/01/36	112
202A/37	rFP4	Output frequency on fault n-4	7254 = 16#1C56	5FB9/86	85/01/37	114
202A/38	rFP5	Output frequency on fault n-5	7255 = 16#1C57	5FB9/87	85/01/38	116
202A/39	rFP6	Output frequency on fault n-6	7256 = 16#1C58	5FB9/88	85/01/39	118
202A/3A	rFP7	Output frequency on fault n-7	7257 = 16#1C59	5FB9/89	85/01/3A	120
202A/3B	rFP8	Output frequency on fault n-8	7258 = 16#1C5A	5FB9/8A	85/01/3B	122
202A/3D	rtP0	Motor operating time on last fault	7260 = 16#1C5C	5FB9/8B	85/01/3D	107
202A/3E	rtP1	Motor operating time on fault n-1	7261 = 16#1C5D	5FB9/8C	85/01/3E	109
202A/3F	rtP2	Motor operating time on fault n-2	7262 = 16#1C5E	5FB9/8D	85/01/3F	111
202A/4	dP3	Fault code on fault n-3	7203 = 16#1C23	5FB9/58	85/01/04	111
202A/40	rtP3	Motor operating time on fault n-3	7263 = 16#1C5F	5FB9/8E	85/01/40	113
202A/41	rtP4	Motor operating time on fault n-4	7264 = 16#1C60	5FB9/8F	85/01/41	115

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
202A/42	rtP5	Motor operating time on fault n-5	7265 = 16#1C61	5FB9/90	85/01/42	117
202A/43	rtP6	Motor operating time on fault n-6	7266 = 16#1C62	5FB9/91	85/01/43	119
202A/44	rtP7	Motor operating time on fault n-7	7267 = 16#1C63	5FB9/92	85/01/44	121
202A/45	rtP8	Motor operating time on fault n-8	7268 = 16#1C64	5FB9/93	85/01/45	123
202A/47	ULP0	Power supply voltage on last fault	7270 = 16#1C66	5FB9/94	85/01/47	105
202A/48	ULP1	Supply voltage on fault n-1	7271 = 16#1C67	5FB9/95	85/01/48	108
202A/49	ULP2	Supply voltage on fault n-2	7272 = 16#1C68	5FB9/96	85/01/49	110
202A/4A	ULP3	Supply voltage on fault n-3	7273 = 16#1C69	5FB9/97	85/01/4A	112
202A/4B	ULP4	Supply voltage on fault n-4	7274 = 16#1C6A	5FB9/98	85/01/4B	114
202A/4C	ULP5	Supply voltage on fault n-5	7275 = 16#1C6B	5FB9/99	85/01/4C	116
202A/4D	ULP6	Supply voltage on fault n-6	7276 = 16#1C6C	5FB9/9A	85/01/4D	118
202A/4E	ULP7	Supply voltage on fault n-7	7277 = 16#1C6D	5FB9/9B	85/01/4E	120
202A/4F	ULP8	Supply voltage on fault n-8	7278 = 16#1C6E	5FB9/9C	85/01/4F	122
202A/5	dP4	Fault code on fault n-4	7204 = 16#1C24	5FB9/59	85/01/05	113
202A/51	tHP0	Motor thermal state on last fault	7280 = 16#1C70	5FB9/9D	85/01/51	105
202A/52	tHP1	Motor thermal state on fault n-1	7281 = 16#1C71	5FB9/9E	85/01/52	108
202A/53	tHP2	Motor thermal state on fault n-2	7282 = 16#1C72	5FB9/9F	85/01/53	110
202A/54	tHP3	Motor thermal state on fault n-3	7283 = 16#1C73	5FB9/A0	85/01/54	112
202A/55	tHP4	Motor thermal state on fault n-4	7284 = 16#1C74	5FB9/A1	85/01/55	114
202A/56	tHP5	Motor thermal state on fault n-5	7285 = 16#1C75	5FB9/A2	85/01/56	116
202A/57	tHP6	Motor thermal state on fault n-6	7286 = 16#1C76	5FB9/A3	85/01/57	118
202A/58	tHP7	Motor thermal state on fault n-7	7287 = 16#1C77	5FB9/A4	85/01/58	120
202A/59	tHP8	Motor thermal state on fault n-8	7288 = 16#1C78	5FB9/A5	85/01/59	122
202A/5B	CrP0	Channels active on last fault	7290 = 16#1C7A	5FB9/A6	85/01/5B	107
202A/5C	CrP1	Active channels on fault n-1	7291 = 16#1C7B	5FB9/A7	85/01/5C	109
202A/5D	CrP2	Active channels on fault n-2	7292 = 16#1C7C	5FB9/A8	85/01/5D	111
202A/5E	CrP3	Active channels on fault n-3	7293 = 16#1C7D	5FB9/A9	85/01/5E	113
202A/5F	CrP4	Active channels on fault n-4	7294 = 16#1C7E	5FB9/AA	85/01/5F	115
202A/6	dP5	Fault code on fault n-5	7205 = 16#1C25	5FB9/5A	85/01/06	115
202A/60	CrP5	Active channels on fault n-5	7295 = 16#1C7F	5FB9/AB	85/01/60	117
202A/61	CrP6	Active channels on fault n-6	7296 = 16#1C80	5FB9/AC	85/01/61	119

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
202A/62	CrP7	Active channels on fault n-7	7297 = 16#1C81	5FB9/AD	85/01/62	121
202A/63	CrP8	Active channels on fault n-8	7298 = 16#1C82	5FB9/AE	85/01/63	123
202A/7	dP6	Fault code on fault n-6	7206 = 16#1C26	5FB9/5B	85/01/07	117
202A/8	dP7	Fault code on fault n-7	7207 = 16#1C27	5FB9/5C	85/01/08	119
202A/9	dP8	Fault code on fault n-8	7208 = 16#1C28	5FB9/5D	85/01/09	121
202A/B	EP0	Status word on last fault	7210 = 16#1C2A	5FB9/5E	85/01/0B	105
202A/C	EP1	Status word on fault n-1	7211 = 16#1C2B	5FB9/5F	85/01/0C	108
202A/D	EP2	Status word on fault n-2	7212 = 16#1C2C	5FB9/60	85/01/0D	110
202A/E	EP3	Status word on fault n-3	7213 = 16#1C2D	5FB9/61	85/01/0E	112
202A/F	EP4	Status word on fault n-4	7214 = 16#1C2E	5FB9/62	85/01/0F	114
202B/1	Md0	Date on last fault	7300 = 16#1C84	5FB9/AF	85/01/65	107
202B/10	dM5	Time on fault n-5	7315 = 16#1C93	5FB9/BD	85/01/74	117
202B/11	dM6	Time on fault n-6	7316 = 16#1C94	5FB9/B5	85/01/75	119
202B/12	dM7	Time on fault n-7	7317 = 16#1C95	5FB9/BF	85/01/76	121
202B/13	dM8	Time on fault n-8	7318 = 16#1C96	5FB9/C0	85/01/77	123
202B/2	Md1	Date on fault n-1	7301 = 16#1C85	5FB9/B0	85/01/66	109
202B/3	Md2	Date on fault n-2	7302 = 16#1C86	5FB9/B1	85/01/67	111
202B/4	Md3	Date on fault n-3	7303 = 16#1C87	5FB9/B2	85/01/68	113
202B/5	Md4	Date on fault n-4	7304 = 16#1C88	5FB9/B3	85/01/69	115
202B/5C	dAY	Date	7391 = 16#1CDF	5FB9/CA	85/01/C0	132
202B/5D	tIME	Time	7392 = 16#1CE0	5FB9/CB	85/01/C1	132
202B/5E	Fnb	Fault counter	7393 = 16#1CE1	5FB9/CC	99/01/CC	104
202B/6	Md5	Date on fault n-5	7305 = 16#1C89	5FB9/B4	85/01/6A	117
202B/7	Md6	Date on fault n-6	7306 = 16#1C8A	5FB9/BE	85/01/6B	119
202B/8	Md7	Date on fault n-7	7307 = 16#1C8B	5FB9/B6	85/01/6C	121
202B/9	Md8	Date on fault n-8	7308 = 16#1C8C	5FB9/B7	85/01/6D	123
202B/B	dM0	Time on last fault	7310 = 16#1C8E	5FB9/B8	85/01/6F	107
202B/C	dM1	Time on fault n-1	7311 = 16#1C8F	5FB9/B9	85/01/70	109
202B/D	dM2	Time on fault n-2	7312 = 16#1C90	5FB9/BA	85/01/71	111
202B/E	dM3	Time on fault n-3	7313 = 16#1C91	5FB9/BB	85/01/72	113
202B/F	dM4	Time on fault n-4	7314 = 16#1C92	5FB9/BC	85/01/73	115

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2032/15	CNFS	Active configuration	8020 = 16#1F54	5FB9/CD	89/01/15	67
2032/15	CNFS	[Config. Active]	8020 = 16#1F54	5FB9/CD	89/01/15	86
2032/16	CHA1	Assignment for 2 sets	8021 = 16#1F55	5FBC/9C	9C/01/9C	70
2032/16	CnF1	[2 Configurations]	8021 = 16#1F55	5FBC/9C	89/01/16	251
2032/16	CNF1	Assignment for 2 configurations	8021 = 16#1F55	5FBC/9C	9C/01/9C	67
2032/17	CHA2	Assignment for 3 sets	8022 = 16#1F56	5FBC/9D	9C/01/9D	70
2032/17	CnF2	[3 Configurations]	8022 = 16#1F56	5FBC/9D	89/01/17	252
2032/17	CNF2	Assignment for 3 configurations	8022 = 16#1F56	5FBC/9D	9C/01/9D	67
2032/1A	CHM	[Multimotors]	8025 = 16#1F59	5FBC/9E	89/01/1A	251
2032/2	SCS	Save configuration	8001 = 16#1F41	5FBC/9A	9C/01/9A	69
2032/3	FCS	Restore configuration	8002 = 16#1F42	5FBC/9B	9C/01/9B	69
2036/10	Fr1b	[Ref.1B channel]	8415 = 16#20DF	5FBC/A6	8B/01/10	198
2036/16	CCS	[Cmd switching]	8421 = 16#20D1	5FBC/A7	8B/01/16	194
2036/18	Cd1	[Cmd channel 1]	8423 = 16#20E7	5FBC/A8	8B/01/18	194
2036/19	Cd2	[Cmd channel 2]	8424 = 16#20E8	5FBC/A9	8B/01/19	194
2036/2	CHCF	[Profile]	8401 = 16#20D1	5FBC/9F	8B/01/02	193
2036/2A	CRC	Active reference channel	8441 = 16#20F9	5FB9/CE	8B/01/2A	85
2036/2B	CCC	Active command channel	8442 = 16#20FA	5FB9/CF	8B/01/2B	86
2036/3	COP	[Copy channel 1 <-> 2]	8402 = 16#20D2	5FBC/A0	8B/01/03	195
2036/5C	SPM	[Ref. memo ass.]	8491 = 16#212B	5FBC/AD	8B/01/5C	218
2036/C	rFC	[Ref. 2 switching]	8411 = 16#20DB	5FBC/A2	8B/01/0C	195
2036/D	rCb	[Ref 1B switching]	8412 = 16#20DC	5FBC/A3	8B/01/0D	198
2036/E	Fr1	[Ref.1 channel]	8413 = 16#20DD	5FBC/A4	8B/01/0E	193
2036/F	Fr2	[Ref.2 channel]	8414 = 16#20DE	5FBC/A5	8B/01/0F	195
2037/3	LFR	Frequency reference	8502 = 16#2136	5FB6/1C	8B/01/67	77
2037/4	PISP	PID regulator reference	8503 = 16#2137	5FB6/1D	8B/01/68	78
2037/5	CMI	Extended control word	8504 = 16#2138	5FB6/1E	8B/01/69	76
2038/6	FrHd	Speed reference before ramp	8605 = 16#219D	5FB9/D6	8C/01/06	89
203C/15	Inr	[Ramp increment]	9020 = 16#233C	5FBF/26	8E/01/15	201
203C/16	FrO	Frequency reference after ramp	9021 = 16#233D	5FB9/D9	8E/01/16	89
203C/2	ACC	[Acceleration]	9001 = 16#2329	5FBF/1E	8E/01/02	201

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
203C/3	dEC	[Deceleration]	9002 = 16#232A	5FBF/1F	8E/01/03	201
203C/4	brA	[Dec ramp adapt.]	9003 = 16#232B	5FB3/CA	8E/01/04	204
203C/5	rPt	[Ramp type]	9004 = 16#232C	5FB3/CB	8E/01/05	201
203C/6	tA1	[Begin Acc round]	9005 = 16#232D	5FB3/CC	8E/01/06	202
203C/7	tA2	[End Acc round]	9006 = 16#232E	5FB3/CD	8E/01/07	202
203C/8	tA3	[Begin Dec round]	9007 = 16#232F	5FB3/CE	8E/01/08	202
203C/9	tA4	[End Dec round]	9008 = 16#2330	5FB3/CF	8E/01/09	202
203C/B	rPS	[Ramp switching]	9010 = 16#2332	5FBC/B3	8E/01/0B	203
203C/C	Frt	[Ramp 2 threshold]	9011 = 16#2333	5FB3/D0	8E/01/0C	202
203C/D	AC2	[Acceleration 2]	9012 = 16#2334	5FBF/24	8E/01/0D	203
203C/D	AC2	[Acceleration 2]	9012 = 16#2334	5FBF/24	8E/01/0D	217
203C/E	dE2	[Deceleration 2]	9013 = 16#2335	5FBF/25	8E/01/0E	203
203C/E	dE2	[Deceleration 2]	9013 = 16#2335	5FBF/25	8E/01/0E	217
203D/4	SPG	[Speed prop. gain]	9103 = 16#238F	5FB3/D1	8E/01/68	145
203D/5	Slt	[Speed time integral]	9104 = 16#2390	5FBF/28	8E/01/69	146
203D/6	SFC	[K speed loop filter]	9105 = 16#2391	5FBF/29	8E/01/6A	145
203E/15	tSS	[Trq/spd switching]	9220 = 16#2404	5FBC/B9	8F/01/15	238
203E/16	tr1	[Torque ref. channel]	9221 = 16#2405	5FBC/BA	8F/01/16	238
203E/17	tSd	[Torque ref. sign]	9222 = 16#2406	5FBC/BB	8F/01/17	239
203E/18	dbn	[Negative deadband]	9223 = 16#2407	5FBF/2E	8F/01/18	240
203E/19	dbP	[Positive deadband]	9224 = 16#2408	5FBF/2F	8F/01/19	240
203E/1A	trt	[Torque ratio]	9225 = 16#2409	5FBF/30	8F/01/1A	239
203E/1B	trP	[Torque ramp time]	9226 = 16#240A	5FBF/31	8F/01/1B	239
203E/1C	tSt	[Torque control stop]	9227 = 16#240B	5FBC/BC	8F/01/1C	239
203E/1D	tOb	[Torq. ctrl fault mgt]	9228 = 16#240C	5FBC/BD	8F/01/1D	240
203E/1E	rtO	[Torque R. time out]	9229 = 16#240D	5FBF/32	8F/01/1E	240
203E/1F	SPt	[Spin time]	9230 = 16#240E	5FBF/33	8F/01/1F	240
203E/2	CLI	[Current Limitation]	9201 = 16#23F1	5FB3/D2	93/01/D2	243
203E/2	CLI	[Current Limitation]	9201 = 16#23F1	5FB3/D2	8F/01/02	146
203E/2	CLI	[Current Limitation]	9201 = 16#23F1	5FB3/D2	8F/01/02	162
203E/20	trr	Torque reference before ramp	9231 = 16#240F	5FB9/DB	8F/01/20	90

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
203E/21	trO	Torque reference after ramp	9232 = 16#2410	5FB9/DC	8F/01/21	90
203E/29	SSb	[Trq/I limit. stop]	9240 = 16#2418	5FBC/BE	8F/01/29	277
203E/2A	StO	[Trq/I limit. time out]	9241 = 16#2419	5FBF/34	8F/01/2A	277
203E/3	LC2	[Current limit 2]	9202 = 16#23F2	5FBC/B5	8F/01/03	243
203E/3D	Int	[Torque unit]	9260 = 16#242C	5FBF/35	8F/01/3D	238
203E/3E	LtCr	Torque reference (Nm)	9261 = 16#242D	5FB6/3D	2A/01/0C	77
203E/4	CL2	[I Limit. 2 value]	9203 = 16#23F3	5FBF/2B	8F/01/04	243
203E/B	tLA	[AI torque limit. activ.]	9210 = 16#23FA	5FBC/B6	8F/01/0B	241
203E/C	tLIM	[Motoring torque lim]	9211 = 16#23FB	5FBF/2C	8F/01/0C	241
203E/D	tLIG	[Gen. torque lim]	9212 = 16#23FC	5FBF/2D	8F/01/0D	241
203E/E	tLC	[Analog limit. act.]	9213 = 16#23FD	5FBC/B7	8F/01/0E	242
203E/F	tAA	[Torque ref. assign.]	9214 = 16#23FE	5FBC/B8	8F/01/0F	242
2042/10	AUT	[Automatic autotune]	9615 = 16#258F	5FC2/0B	91/01/10	149
2042/13	PPn	[Pr]	9618 = 16#2592	5FB3/DA	91/01/13	158
2042/17	ItH	[Mot. therm. current]	9622 = 16#2596	5FB3/DB	2A/01/0A	145
2042/18	UFr	[IR compensation]	9623 = 16#2597	5FC2/0E	91/01/18	156
2042/1A	SLP	[Slip compensation]	9625 = 16#2599	5FB3/DC	91/01/1A	156
2042/1F	tHr	Motor thermal state	9630 = 16#259E	5FB9/DE	91/01/1F	92
2042/2	UnS	[Rated motor volt.]	9601 = 16#2581	5FB3/D3	77/01/10	148
2042/29	rSM	Asynchronous motor cold state stator resistance	9640 = 16#25A8	5FB9/DF	91/01/29	94
2042/29	rSM	[Stator R measured]	9640 = 16#25A8	5FB9/DF	91/01/29	157
2042/2B	rSA	[R1w]	9642 = 16#25AA	5FC2/11	91/01/2B	158
2042/3	FrS	[Rated motor freq.]	9602 = 16#2582	5FB3/D4	91/01/03	149
2042/33	IdM	Magnetizing current	9650 = 16#25B2	5FB9/E0	91/01/33	93
2042/33	IdM	[Idr]	9650 = 16#25B2	5FB9/E0	91/01/33	157
2042/35	IdA	[Idw]	9652 = 16#25B4	5FC2/15	91/01/35	158
2042/3D	LFM	Leakage inductance	9660 = 16#25BC	5FB9/E1	91/01/3D	94
2042/3D	LFM	[Lfr]	9660 = 16#25BC	5FB9/E1	91/01/3D	157
2042/3F	LFA	[Lfw]	9662 = 16#25BE	5FC2/19	91/01/3F	158
2042/4	nCr	[Rated mot. current]	9603 = 16#2583	5FB3/D5	91/01/04	148
2042/42	trM	Rotor time constant	9665 = 16#25C1	5FB9/E2	91/01/42	94

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2042/42	trM	[T2r]	9665 = 16#25C1	5FB9/E2	91/01/42	157
2042/44	trA	[T2w]	9667 = 16#25C3	5FC2/1C	91/01/44	158
2042/47	nCrS	[Nominal I sync.]	9670 = 16#25C6	5FC2/1E	91/01/47	154
2042/48	nSPS	[Nom motor spdsync]	9671 = 16#25C7	5FC2/1F	91/01/48	154
2042/49	PPnS	[Pole pairs]	9672 = 16#25C8	5FC2/20	91/01/49	154
2042/4A	PHS	[Syn. EMF constant]	9673 = 16#25C9	5FC2/21	91/01/4A	154
2042/4B	LdS	[Autotune L d-axis]	9674 = 16#25CA	5FC2/22	91/01/4B	154
2042/4C	LqS	[Autotune L q-axis]	9675 = 16#25CB	5FC2/23	91/01/4C	155
2042/5	nSP	[Nom motor speed]	9604 = 16#2584	5FB3/D6	91/01/05	149
2042/50	FrSS	[Nominal freq sync.]	9679 = 16#25CF	5FC2/24	91/01/50	159
2042/51	rSMS	[R1rS]	9680 = 16#25D0	5FB9/E3	91/01/51	159
2042/51	rSMS	Synchronous motor cold state stator resistance	9680 = 16#25D0	5FB9/E3	91/01/51	94
2042/53	rSAS	[Cust. stator R syn]	9682 = 16#25D2	5FC2/26	91/01/53	155
2042/6	nSL	[Nominal motor slip]	9605 = 16#2585	5FB9/DD	91/01/06	157
2042/8	Ctt	[Motor control type]	9607 = 16#2587	5FC2/06	91/01/08	150
2042/9	tUn	[Auto-tuning]	9608 = 16#2588	5FB3/D7	91/01/09	149
2042/A	tUS	[Auto tuning status]	9609 = 16#2589	5FC2/08	91/01/0A	150
2042/B	tUL	[Auto-tune assign.]	9610 = 16#258A	5FBC/EF	91/01/0B	253
2042/C	OPL	[Output Phase Loss]	9611 = 16#258B	5FB3/D8	91/01/0C	266
2042/D	tHt	[Motor protect. type]	9612 = 16#258C	5FB3/D9	91/01/0D	264
2042/E	nPr	[Rated motor power]	9613 = 16#258D	5FC2/0A	91/01/0E	148
2046/4C	IbrA	[Ibr 4-20 mA loss]	10075 = 16#275B	5FBC/F9	93/01/4C	227
2046/10	brr	[Current ramp time]	10015 = 16#271F	5FBF/40	93/01/10	225
2046/15	bEd	[Engage at reversal]	10020 = 16#2724	5FBF/41	93/01/15	223
2046/17	ttr	[Time to restart]	10022 = 16#2726	5FBF/42	93/01/17	224
2046/2	bLC	[Brake assignment]	10001 = 16#2711	5FBC/F1	93/01/02	221
2046/33 (bit 0)	brH0	[BRH b0]	10050 = 16# 2742 (bit 0)	5FBC/F3 (bit 0)	93/01/33 (bit 0)	224
2046/33 (bit 1)	brH1	[BRH b1]	10050 = 16# 2742 (bit 1)	5FBC/F3 (bit 1)	93/01/33 (bit 1)	224
2046/33 (bit 2)	brH2	[BRH b2]	10050 = 16# 2742 (bit 2)	5FBC/F3 (bit 2)	93/01/33 (bit 2)	224

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2046/33 (bit 3)	brH3	[BRH b3]	10050 = 16# 2742 (bit 3)	5FBC/F3 (bit 3)	93/01/33 (bit 3)	224
2046/33 (bit 4)	brH4	[BRH b4]	10050 = 16# 2742 (bit 4)	5FBC/F3 (bit 4)	93/01/33 (bit 4)	225
2046/4	bEn	[Brake engage freq]	10003 = 16#2713	5FBF/36	93/01/04	222
2046/47	PES	[Weight sensor ass.]	10070 = 16#2756	5FBC/F4	93/01/47	226
2046/48	LP1	[Point 1 X]	10071 = 16#2757	5FBC/F5	93/01/48	226
2046/49	CP1	[Point 1Y]	10072 = 16#2758	5FBC/F6	93/01/49	226
2046/4A	LP2	[Point 2 X]	10073 = 16#2759	5FBC/F7	93/01/4A	226
2046/4B	CP2	[Point 2Y]	10074 = 16#275A	5FBC/F8	93/01/4B	227
2046/5	brt	[Brake Release time]	10004 = 16#2714	5FB3/DD	93/01/05	222
2046/6	bEt	[Brake engage time]	10005 = 16#2715	5FB3/DE	93/01/06	223
2046/7	Ibr	[Brake release I FW]	10006 = 16#2716	5FB3/DF	93/01/07	222
2046/8	bIP	[Brake impulse]	10007 = 16#2717	5FB3/E0	93/01/08	221
2046/9	bSt	[Movement type]	10008 = 16#2718	5FBF/3B	93/01/09	221
2046/A	bCI	[Brake contact]	10009 = 16#2719	5FBC/F2	93/01/0A	221
2046/B	tbE	[Brake engage delay]	10010 = 16#271A	5FBF/3C	93/01/0B	223
2046/C	Ird	[Brake release I Rev]	10011 = 16#271B	5FBF/3D	93/01/0C	222
2046/D	blr	[Brake release freq]	10012 = 16#271C	5FB3/E1	93/01/0D	222
2046/E	JdC	[Jump at reversal]	10013 = 16#271D	5FBF/3F	93/01/0E	223
204A/2	AdC	[Auto DC injection]	10401 = 16#28A1	5FB3/E2	95/01/02	208
204A/3	tdC1	[Auto DC inj. time 1]	10402 = 16#28A2	5FBF/44	95/01/03	208
204A/4	SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	5FBF/45	95/01/04	208
204A/4	SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	5FBF/45	95/01/04	223
204A/5	tdC2	[Auto DC inj. time 2]	10404 = 16#28A4	5FBF/46	95/01/05	208
204A/6	SdC2	[Auto DC inj. level 2]	10405 = 16#28A5	5FBF/47	95/01/06	208
2050/16	SAt	[Thermal alarm stop]	11021 = 16#2B0D	5FB3/E7	98/01/16	268
2050/2	Ctd	[Current threshold]	11001 = 16#2AF9	5FB3/E3	98/01/02	146
2050/3	ttd	[Motor therm. level]	11002 = 16#2AFA	5FB3/E4	98/01/03	264
2050 / 3	ttd	[Motor therm. level]	11002 = 16#2AFA	5FB3/E4	98/01/03	268
2050/4	Ftd	[Freq. threshold]	11003 = 16#2AFB	5FB3/E5	98/01/04	147
2050/5	F2d	[Freq. threshold 2]	11004 = 16#2AFC	5FB3/E6	98/01/05	147

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2050/7	ttd2	[Motor2 therm. level]	11006 = 16#2AFE	5FBF/4C	98/01/07	264
2050/7	ttd2	[Motor2 therm. level]	11006 = 16#2AFE	5FBF/4C	98/01/07	268
2050/8	ttd3	[Motor3 therm. level]	11007 = 16#2AFF	5FBF/4D	98/01/08	264
2050/8	ttd3	[Motor3 therm. level]	11007 = 16#2AFF	5FBF/4D	98/01/08	268
2050/A	tHA	[Drv therm. state al]	11009 = 16#2B01	5FBF/4E	98/01/0A	267
2050/A	tHA	[Drv therm. state al]	11009 = 16#2B01	5FBF/4E	98/01/0A	268
2051/2	tCC	[2/3 wire control]	11101 = 16#2B5D	5FB3/E8	98/01/66	166
2051/3	tCt	[2 wire type]	11102 = 16#2B5E	5FB3/E9	98/01/67	166
2051/6	rrS	[Reverse assign.]	11105 = 16#2B61	5FBC/FE	98/01/6A	166
2051/B	JOG	[JOG]	11110 = 16#2B66	5FBC/FF	98/01/6F	209
2051/C	JGF	[Jog frequency]	11111 = 16#2B67	5FBF/50	98/01/70	209
2051/D	JGT	[Jog delay]	11112 = 16#2B68	5FB3/EA	98/01/71	209
2052/1F	dCF	[Ramp divider]	11230 = 16#2BDE	5FB3/ED	99/01/1F	206
2052/1F	dCF	[Ramp divider]	11230 = 16#2BDE	5FB3/ED	99/01/1F	280
2052/2	Stt	[Stop type]	11201 = 16#2BC1	5FB3/EB	99/01/02	205
2052/3	nSt	[Freewheel stop ass.]	11202 = 16#2BC2	5FBD/02	99/01/03	205
2052/4	dCI	[DC injection assign.]	11203 = 16#2BC3	5FBD/03	99/01/04	206
2052/5	FSt	[Fast stop assign.]	11204 = 16#2BC4	5FBD/04	99/01/05	205
2052/B	IdC	[DC inject. level 1]	11210 = 16#2BCA	5FB3/EC	99/01/0B	206
2052/B	IdC	[DC inject. level 1]	11210 = 16#2BCA	5FB3/EC	99/01/0B	281
2052/C	tdC	[DC injection time 2]	11211 = 16#2BCB	5FBF/53	99/01/0C	207
2052/C	tdC	[DC injection time 2]	11211 = 16#2BCB	5FBF/53	99/01/0C	281
2052/D	IdC2	[DC inject. level 2]	11212 = 16#2BCC	5FBF/54	99/01/0D	207
2052/D	IdC2	[DC inject. level 2]	11212 = 16#2BCC	5FBF/54	99/01/0D	281
2052/E	tdI	[DC injection time 1]	11213 = 16#2BCD	5FBF/55	99/01/0E	206
2052/E	tdI	[DC injection time 1]	11213 = 16#2BCD	5FBF/55	99/01/0E	281
2054/10	SP7	[Preset speed 7]	11415 = 16#2C97	5FB3/F5	9A/01/10	212
2054/11	SP8	[Preset speed 8]	11416 = 16#2C98	5FB3/F6	9A/01/11	213
2054/12	SP9	[Preset speed 9]	11417 = 16#2C99	5FBF/5F	9A/01/12	213
2054/13	SP10	[Preset speed 10]	11418 = 16#2C9A	5FBF/60	9A/01/13	213
2054/14	SP11	[Preset speed 11]	11419 = 16#2C9B	5FBF/61	9A/01/14	213

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2054/15	SP12	[Preset speed 12]	11420 = 16#2C9C	5FBF/62	9A/01/15	213
2054/16	SP13	[Preset speed 13]	11421 = 16#2C9D	9F/01/63	9A/01/16	214
2054/17	SP14	[Preset speed 14]	11422 = 16#2C9E	5FBF/64	9A/01/17	214
2054/18	SP15	[Preset speed 15]	11423 = 16#2C9F	5FBF/65	9A/01/18	214
2054/19	SP16	[Preset speed 16]	11424 = 16#2CA0	5FBF/66	9A/01/19	214
2054/2	PS2	[2 preset speeds]	11401 = 16#2C89	5FBD/06	9A/01/02	210
2054/3	PS4	[4 preset speeds]	11402 = 16#2C8A	5FBD/07	9D/01/07	210
2054/4	PS8	[8 preset speeds]	11403 = 16#2C8B	5FBD/08	9A/01/04	211
2054/5	PS16	[16 preset speeds]	11404 = 16#2C8C	5FBD/09	9A/01/05	211
2054/B	SP2	[Preset speed 2]	11410 = 16#2C92	5FB3/F0	9A/01/0B	211
2054/C	SP3	[Preset speed 3]	11411 = 16#2C93	5FB3/F1	9A/01/0C	212
2054/D	SP4	[Preset speed 4]	11412 = 16#2C94	5FB3/F2	9A/01/0D	212
2054/E	SP5	[Preset speed 5]	11413 = 16#2C95	5FB3/F3	9A/01/0E	212
2054/F	SP6	[Preset speed 6]	11414 = 16#2C96	5FB3/F4	9A/01/0F	212
2055/15	USI	[+ speed assignment]	11520 = 16#2D00	5FBD/0E	9D/01/0E	216
2055/16	dSI	[-Speed assignment]	11521 = 16#2D01	5FBD/0F	9A/01/7A	216
2055/2	USP	[+ speed assignment]	11501 = 16#2CED	5FBD/0A	9A/01/66	215
2055/3	dSP	[-Speed assignment]	11502 = 16#2CEE	5FBD/0B	9A/01/67	215
2055/4	Str	[Reference saved]	11503 = 16#2CEF	5FBD/0C	9A/01/68	215
2055/6	SrP	[+/-Speed limitation]	11505 = 16#2CF1	5FB3/F7	9A/01/6A	216
2056/2	LAF	[Stop FW limit sw.]	11601 = 16#2D51	5FBD/10	9B/01/02	220
2056/3	LAr	[Stop RV limit sw.]	11602 = 16#2D52	5FBD/11	9B/01/03	220
2056/4	LAS	[Stop type]	11603 = 16#2D53	5FBD/12	9B/01/04	220
2057/2	tLS	[Low speed time out]	11701 = 16#2DB5	5FB3/F8	9B/01/66	146
2057/2	tLS	[Low speed time out]	11701 = 16#2DB5	5FB3/F8	9B/01/66	235
2058/16	MA2	[Multiplier ref. 2]	11821 = 16#2E2D	5FBD/17	9C/01/16	200
2058/17	MA3	[Multiplier ref. 3]	11822 = 16#2E2E	5FBD/18	9C/01/17	200
2058/2	SA2	[Summing ref. 2]	11801 = 16#2E19	5FBD/13	9C/01/02	199
2058/20	MFr	Multiplying coefficient	11831 = 16#2E37	5FB6/3E	9C/01/20	78
2058/3	SA3	[Summing ref. 3]	11802 = 16#2E1A	5FBD/14	9C/01/03	199
2058/C	dA2	[Subtract ref. 2]	11811 = 16#2E23	5FBD/15	9C/01/0C	199

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2058/D	dA3	[Subtract ref. 3]	11812 = 16#2E24	5FBD/16	9C/01/0D	199
2059/15	RPI	[Internal PID ref.]	11920 = 16#2E90	5FBF/6E	9C/01/79	231
2059/16	rP2	[Preset ref. PID 2]	11921 = 16#2E91	5FBF/6F	9C/01/7A	237
2059/17	rP3	[Preset ref. PID 3]	11922 = 16#2E92	5FBF/70	9C/01/7B	237
2059/18	rP4	[Preset ref. PID 4]	11923 = 16#2E93	9F/01/71	9C/01/7C	237
2059/2	PIF	[PID feedback ass.]	11901 = 16#2E7D	5FBD/19	9C/01/66	230
2059/29	PIC	[PID correct. reverse]	11940 = 16#2EA4	5FB3/F9	9C/01/8D	232
2059/2A	RPG	[PID prop. gain]	11941 = 16#2EA5	5FB3/FA	9C/01/8E	231
2059/2B	rIG	[PID integral gain]	11942 = 16#2EA6	5FB3/FB	9C/01/8F	232
2059/2C	rdG	[PID derivative gain]	11943 = 16#2EA7	5FB3/FC	9C/01/90	232
2059/2D	PIS	[PID integral reset]	11944 = 16#2EA8	5FBD/1E	9C/01/91	234
2059/33	FPI	[Speed ref. assign.]	11950 = 16#2EAE	5FBD/1F	9C/01/97	234
2059/34	PSr	[Speed input %]	11951 = 16#2EAF	5FB3/FD	9C/01/98	234
2059/35	POL	[Min PID output]	11952 = 16#2EB0	5FBF/76	9C/01/99	232
2059/36	POH	[Max PID output]	11953 = 16#2EB1	5FBF/77	9C/01/9A	233
2059/37	PIM	[Manual reference]	11954 = 16#2EB2	5FBD/20	9C/01/9B	235
2059/3D	rSL	[PID wake up thresh.]	11960 = 16#2DB5	5FBD/21	9C/01/A1	235
2059/3E	PAL	[Min fbk alarm]	11961 = 16#2EB9	5FB3/FE	9C/01/A2	233
2059/3F	PAH	[Max fbk alarm]	11962 = 16#2EBA	5FB3/FF	9C/01/A3	233
2059/40	PEr	[PID error Alarm]	11963 = 16#2EBB	5FB4/01	9C/01/A4	233
2059/47	PAU	[Auto/Manual assign.]	11970 = 16#2EC2	5FBD/22	9C/01/AB	235
2059/5	PIF1	[Min PID feedback]	11904 = 16#2E80	5FBF/6A	9C/01/69	230
2059/51	rPE	PID regulator discrepancy	11980 = 16#2ECC	5FB9/E5	9C/01/B5	91
2059/52	rPF	PID regulator feedback reference	11981 = 16#2ECD	5FB9/E6	9C/01/B6	91
2059/53	rPC	PID reference after ramp	11982 = 16#2ECE	5FB9/E7	9C/01/B7	91
2059/54	rPO	PID regulator limit output reference	11983 = 16#2ECF	5FB9/E8	9C/01/B8	91
2059/55	PrP	[PID ramp]	11984 = 16#2ED0	5FBF/7B	9C/01/B9	232
2059/6	PIF2	[Max PID feedback]	11905 = 16#2E81	5FBF/6B	9C/01/6A	230
2059/7	PIP1	[Min PID reference]	11906 = 16#2E82	5FBF/6C	9C/01/6B	231
2059/8	PIP2	[Max PID reference]	11907 = 16#2E83	5FBF/6D	9C/01/6C	231
2059/9	PII	[Act. internal PID ref.]	11908 = 16#2E84	5FBD/1A	9C/01/6D	231

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2059/A	Pr2	[2 preset PID ref.]	11909 = 16#2E85	5FBD/1B	9C/01/6E	236
2059/B	Pr4	[4 preset PID ref.]	11910 = 16#2E86	5FBD/1C	9C/01/6F	236
205B/2	EnA	[ENA system]	12101 = 16#2F45	5FBD/23	9D/01/66	161
205B/3	AUS	ENA average speed	12102 = 16#2F46	5FB9/EA	9D/01/67	88
205B/4	GPE	[ENA prop.gain]	12103 = 16#2F47	5FBF/7C	9D/01/68	161
205B/5	GIE	[ENA integral gain]	12104 = 16#2F48	5FBF/7D	9D/01/69	161
205B/6	rAP	[Reduction ratio]	12105 = 16#2F49	5FBF/7E	9D/01/6A	161
205C/2	trC	[Yarn control]	12201 = 16#2FA9	5FBD/24	9E/01/02	254
205C/3	trH	[Traverse high]	12202 = 16#2FAA	5FBF/7F	9E/01/03	254
205C/4	trL	[Traverse Low]	12203 = 16#2FAB	5FBF/80	9E/01/04	254
205C/5	qSH	[Quick step High]	12204 = 16#2FAC	5FBF/81	9E/01/05	254
205C/6	qSL	[Quick step Low]	12205 = 16#2FAD	5FBF/82	9E/01/06	254
205C/7	tUP	[Traverse ctrl. accel.]	12206 = 16#2FAE	5FBF/83	9E/01/07	255
205C/8	tdn	[Traverse ctrl. decel]	12207 = 16#2FAF	5FBF/84	9E/01/08	255
205C/9	tbO	[Reel time]	12208 = 16#2FAB	5FBF/85	9E/01/09	255
205C/A	EbOt	Current bobbin time	12209 = 16#2FB1	5FB9/EB	9E/01/0A	93
205C/B	rtr	[Init. traverse ctrl]	12210 = 16#2FB2	5FBD/25	9E/01/0B	257
205C/C	dtF	[Decrease ref. speed]	12211 = 16#2FB3	5FBF/86	9E/01/0C	256
205C/D	SnC	[Counter wobble]	12212 = 16#2FB4	5FBD/26	9E/01/0D	256
205C/E	EbO	[End reel]	12213 = 16#2FB5	5FBD/27	9E/01/0E	255
205C/F	tSY	[Sync. wobble]	12214 = 16#2FB6	5FBD/28	9E/01/0F	256
205D/2	HSO	[High speed hoisting]	12301 = 16#300D	5FBD/29	9E/01/66	228
205D/3	CLO	[High speed l Limit]	12302 = 16#300E	5FBF/87	9E/01/67	229
205D/4	COF	[Motor speed coeff.]	12303 = 16#300F	5FBF/88	9E/01/68	228
205D/5	Cor	[Gen. speed coeff]	12304 = 16#3010	5FBF/89	9E/01/69	228
205D/6	OSP	[Measurement spd]	12305 = 16#3011	5FBF/8A	9E/01/6A	228
205D/7	SCL	[l Limit. frequency]	12306 = 16#3012	5FBF/8B	9E/01/6B	229
205D/8	tOS	[Load measuring tm.]	12307 = 16#3013	5FBF/8C	9E/01/6C	228
205E/2	U0	[U0]	12401 = 16#3071	5FBD/2A	9F/01/02	150
205E/4	U1	[U1]	12403 = 16#3073	5FBD/2B	9F/01/04	151
205E/5	F1	[F1]	12404 = 16#3074	5FBD/2C	9F/01/05	151

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
205E/6	U2	[U2]	12405 = 16#3075	5FBD/2D	9F/01/06	151
205E/7	F2	[F2]	12406 = 16#3076	5FBD/2E	9F/01/07	151
205E/8	U3	[U3]	12407 = 16#3077	5FBD/2F	9F/01/08	151
205E/9	F3	[F3]	12408 = 16#3078	5FBD/30	9F/01/09	152
205E/9	U4	[U4]	12409 = 16#3079	5FBD/31	9F/01/0A	152
205E/B	F4	[F4]	12410 = 16#307A	5FBD/32	9F/01/0B	152
205E/C	U5	[U5]	12411 = 16#307B	5FBD/33	9F/01/0C	152
205E/D	F5	[F5]	12412 = 16#307C	5FBD/34	9F/01/0D	152
205F/2	SAF	[Stop FW limit sw.]	12501 = 16#30D5	5FBD/35	9F/01/66	247
205F/3	SAr	[Stop RV limit sw.]	12502 = 16#30D6	5FBD/36	9F/01/67	247
205F/4	dAF	[Slowdown forward]	12503 = 16#30D7	5FBD/37	9F/01/68	248
205F/5	dAr	[Slowdown reverse]	12504 = 16#30D8	5FBD/38	9F/01/69	248
205F/6	dsF	[Deceleration type]	12505 = 16#30D9	5FBD/39	9F/01/6A	249
205F/7	PAS	[Stop type]	12506 = 16#30DA	5FBD/3A	9F/01/6B	249
205F/8	CLS	[Disable limit sw.]	12507 = 16#30DB	5FBD/3B	9F/01/6C	248
2060/2	SUL	[Motor surge limit.]	12601 = 16#3139	5FBD/3C	A0/01/02	162
2060/3	SOP	[Volt surge limit. opt]	12602 = 16#313A	5FBD/3D	A0/01/03	163
2061/16	nCA1	Communication scanner, address of write word 1	12721 = 16#31B1	5FBD/46	A0/01/7A	128
2061/17	nCA2	Communication scanner, address of write word 2	12722 = 16#31B2	5FBD/47	A0/01/7B	128
2061/18	nCA3	Communication scanner, address of write word 3	12723 = 16#31B3	5FBD/48	A0/01/7C	128
2061/19	nCA4	Communication scanner, address of write word 4	12724 = 16#31B4	5FBD/49	A0/01/7D	128
2061/1A	nCA5	Communication scanner, address of write word 5	12725 = 16#31B5	5FBD/4A	A0/01/7E	128
2061/1B	nCA6	Communication scanner, address of write word 6	12726 = 16#31B6	5FBD/4B	A0/01/7F	129
2061/1C	nCA7	Communication scanner, address of write word 7	12727 = 16#31B7	5FBD/4C	A0/01/80	129
2061/1D	nCA8	Communication scanner, address of write word 8	12728 = 16#31B8	5FBD/4D	A0/01/81	129
2061/2	nMA1	Communication scanner, address of read word 1	12701 = 16#319D	5FBD/3E	A0/01/66	129

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2061/2A	nM1	Communication scanner, value of read word 1	12741 = 16#31C5	5FB6/43	A0/01/8E	127
2061/2B	nM2	Communication scanner, value of read word 2	12742 = 16#31C6	5FB6/44	A0/01/8F	127
2061/2C	nM3	Communication scanner, value of read word 3	12743 = 16#31C7	5FB6/45	A0/01/90	127
2061/2D	nM4	Communication scanner, value of read word 4	12744 = 16#31C8	5FB6/46	A0/01/91	127
2061/2E	nM5	Communication scanner, value of read word 5	12745 = 16#31C9	5FB6/47	A0/01/92	127
2061/2F	nM6	Communication scanner, value of read word 6	12746 = 16#31CA	5FB6/48	A0/01/93	127
2061/3	nMA2	Communication scanner, address of read word 2	12702 = 16#319E	5FBD/3F	A0/01/67	129
2061/30	nM7	Communication scanner, value of read word 7	12747 = 16#31CB	5FB6/49	A0/01/94	128
2061/31	nM8	Communication scanner, value of read word 8	12748 = 16#31CC	5FB6/4A	A0/01/95	128
2061/3E	nC1	Communication scanner, value of write word 1	12761 = 16#31D9	5FB6/4B	A0/01/A2	126
2061/3F	nC2	Communication scanner, value of write word 2	12762 = 16#31DA	5FB6/4C	A0/01/A3	126
2061/4	nMA3	Communication scanner, address of read word 3	12703 = 16#319F	5FBD/40	A0/01/68	129
2061/40	nC3	Communication scanner, value of write word 3	12763 = 16#31DB	5FB6/4D	A0/01/A4	126
2061/41	nC4	Communication scanner, value of write word 4	12764 = 16#31DC	5FB6/4E	A0/01/A5	126
2061/42	nC5	Communication scanner, value of write word 5	12765 = 16#31DD	5FB6/4F	A0/01/A6	126
2061/43	nC6	Communication scanner, value of write word 6	12766 = 16#31DE	5FB6/50	A0/01/A7	126
2061/44	nC7	Communication scanner, value of write word 7	12767 = 16#31DF	5FB6/51	A0/01/A8	126
2061/45	nC8	Communication scanner, value of write word 8	12768 = 16#31E0	5FB6/52	A0/01/A9	127
2061/5	nMA4	Communication scanner, address of read word 4	12704 = 16#31A0	5FBD/41	A0/01/69	129
2061/6	nMA5	Communication scanner, address of read word 5	12705 = 16#31A1	5FBD/42	A0/01/6A	130
2061/7	nMA6	Communication scanner, address of read word 6	12706 = 16#31A2	5FBD/43	A0/01/6B	130

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2061/8	nMA7	Communication scanner, address of read word 7	12707 = 16#31A3	5FBD/44	A0/01/6C	130
2061/9	nMA8	Communication scanner, address of read word 8	12708 = 16#31A4	5FBD/45	A0/01/6D	130
2063/01	CFPS	Active parameter set	12900 = 16#3264	5FB9/EC	A1/01/65	70
2063/01	CFPS	Active parameter set	12900 = 16#3264	5FB9/EC	A1/01/65	86
2063/02	VAL	Load parameter set command	12901 = 16#3265	5FB9/ED	A1/01/66	73
2063/3	CHA1	[2 Parameter sets]	12902 = 16#3266	5FBD/54	A1/01/67	250
2063/4	CHA2	[3 Parameter sets]	12903 = 16#3267	5FBD/55	A1/01/68	250
2065/2	dBs	[Time to motor run]	13101 = 16#332D	5FB4/02	A2/01/66	245
2065/3	dAS	[Time to open cont.]	13102 = 16#332E	5FB4/03	A2/01/67	246
2065/4	rCA	[Output contact. fdbk]	13103 = 16#332F	5FBD/56	A2/01/68	245
2065/5	OCC	[Out. contactor ass.]	13104 = 16#3330	5FBD/57	A2/01/69	245
2066/2	PtC1	[PTC1 probe]	13201 = 16#3391	5FBF/CB	A3/01/02	260
2066/3	PtC2	[PTC2 probe]	13202 = 16#3392	5FBF/CC	A3/01/03	260
2066/4	PtCL	[LI6 = PTC probe]	13203 = 16#3396	5FBF/CD	A3/01/04	260
2067/2	PGA	[Reference type]	13301 = 16#33F5	5FBD/58	A3/01/66	177
2067/3	PIL	[RP min value]	13302 = 16#33F6	5FBD/59	A3/01/67	176
2067/4	PFr	[RP max value]	13303 = 16#33F7	5FBD/5A	A3/01/68	176
2067/5	PFI	[RP filter]	13304 = 16#33F8	5FBD/5B	A3/01/69	176
2067/B	EIL	[Freq. min value]	13310 = 16#33FE	5FBD/5C	A3/01/6F	178
2067/C	EFr	[Freq. max value]	13311 = 16#33FF	5FBD/5D	A3/01/70	178
2067/D	EFI	[Freq. signal filter]	13312 = 16#3400	5FBD/5E	A3/01/71	178
2068/2	PPhr	[Output Ph rotation]	13401 = 16#3459	5FBD/5F	A4/01/02	150
2069/1E	bMP	[HMI cmd.]	13529 = 16#34D9	5FBD/6C	A4/01/82	197
2069/2	FN1	[F1 key assignment]	13501 = 16#34BD	5FBD/60	A4/01/66	196
2069/3	FN2	[F2 key assignment]	13502 = 16#34BE	5FBD/61	A4/01/67	196
2069/4	FN3	[F3 key assignment]	13503 = 16#34BF	5FBD/62	A4/01/68	196
2069/5	FN4	[F4 key assignment]	13504 = 16#34C0	5FBD/63	A4/01/69	197
206A/2	LES	[Drive lock]	13601 = 16#3521	5FBD/6D	A5/01/02	244
206A/3	LLC	[Line contactor ass.]	13602 = 16#3522	5FBD/6E	A5/01/03	244
206A/4	LCt	[Mains V. time out]	13603 = 16#3523	5FBD/6F	A5/01/04	244

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
206C/2	UrES	[Evacuation Input V.]	13801 =16#35E9	5FBD/70	A6/01/02	270
206C/20	rFt	[Evacuation assign.]	13831 = 16#3607	5FBD/76	A6/01/20	258
206C/21	rSU	[Evacuation Input V.]	13832 = 16#3608	5FBD/77	A6/01/21	258
206C/22	rSP	[Evacuation freq.]	13833 = 16#3609	5FB4/04	A6/01/22	258
206C/2A	dCO	[Precharge cont. ass.]	13841 = 16#3611	5FBD/78	A6/01/2A	259
206C/3	USL	[Undervoltage level]	13802 =16#35EA	5FBD/71	A6/01/03	270
206C/4	USb	[UnderV. fault mgt]	13803 =16#35EB	5FBD/72	A6/01/04	270
206C/5	USt	[Undervolt. time out]	13804 =16#35EC	5FBD/73	A6/01/05	270
206C/C	UPL	[Prevention level]	13811 =16#35F3	5FBD/74	A6/01/0C	271
206C/D	tbS	[DC bus maintain tm]	13812 =16#35F4	5FBF/CE	A6/01/0D	271
206C/E	tSM	[UnderV. restart tm]	13813 =16#35F5	5FBF/CF	A6/01/0E	271
206C/F	StM	[Max stop time]	13814 =16#35EC	5FBF/D0	A6/01/0F	271
206D/2	FLI	[Fluxing assignment]	13901 = 16#364D	5FBD/79	A6/01/66	219
206D/3	FLU	[Motor fluxing]	13902 = 16#364E	5FB4/05	A6/01/67	146
206D/3	FLU	[Motor fluxing]	13902 = 16#364E	5FB4/05	A6/01/67	219
206E/2	PPI	[Pairing password]	14001 = 16#36B1	5FBD/7A	A7/01/02	279
206F/2	Ubr	[Braking level]	14101 = 16#3715	5FBF/D4	A7/01/66	164
206F/3	bbA	[Braking balance]	14102 = 16#3716	5FBD/7B	A7/01/67	164
206F/C	brO	[DB res. protection]	14111 =16#371F	5FBD/7C	A7/01/70	278
206F/D	brP	[DB Resistor Power]	14112 =16#3720	5FBD/7D	A7/01/71	278
206F/E	brU	[DB Resistor value]	14113 =16#3721	5FBD/7E	A7/01/72	278
206F/F	tHb	DBR thermal state	14114 = 16#3722	5FBD/7F	A7/01/73	92
2070/2	UC2	[Vector Control 2pt]	14201 = 16#3779	5FBD/80	A8/01/02	153
2070/3	UCP	[V. constant power]	14202 = 16#377A	5FBD/81	A8/01/03	153
2070/4	FCP	[Freq. Const Power]	14203 = 16#377B	5FBD/82	A8/01/04	153
2071/2	LbA	[Load sharing]	14301 = 16#37DD	5FBD/83	A8/01/66	164
2071/3	LbC	[Load correction]	14302 = 16#37DE	5FBF/D5	A8/01/67	164
2071/4	LbC1	[Correction min spd]	14303 = 16#37DF	5FBF/D6	A8/01/68	165
2071/5	LbC2	[Correction max spd]	14304 = 16#37E0	5FBF/D7	A8/01/69	165
2071/6	LbC3	[Torque offset]	14305 = 16#37E1	5FBF/D8	A8/01/6A	165
2071/7	LbF	[Sharing filter]	14306 = 16#37E2	5FBF/D9	A8/01/6B	165

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
2262/3	PSt	[Stop Key priority]	64002 = 16#FA02	5FBD/88	94/01/06	193
2264/22	Fdrv	Ethernet fault code	64233 = 16#FAE9	-	-	103
2265/1	dCC0	Command channel active on last fault	64300 = 16#FB2C	5FBA/08	9A/01/08	106
2265/10	drC5	Active reference channel on fault n-5	64315 = 16#FB3B	5FBA/16	9A/01/16	117
2265/11	drC6	Active reference channel on fault n-6	64316 = 16#FB3C	5FBA/17	9A/01/17	119
2265/12	drC7	Active reference channel on fault n-7	64317 = 16#FB3D	5FBA/18	9A/01/18	121
2265/13	drC8	Active reference channel on fault n-8	64318 = 16#FB3E	5FBA/19	9A/01/19	123
2265/2	dCC1	Active command channel on fault n-1	64301 = 16#FB2D	5FBA/09	9A/01/09	109
2265/3	dCC2	Active command channel on fault n-2	64302 = 16#FB2E	5FBA/0A	9A/01/0A	111
2265/4	dCC3	Active command channel on fault n-3	64303 = 16#FB2F	5FBA/0B	9A/01/0B	113
2265/5	dCC4	Active command channel on fault n-4	64304 = 16#FB30	5FBA/0C	9A/01/0C	115
2265/6	dCC5	Active command channel on fault n-5	64305 = 16#FB31	5FBA/0D	9A/01/0D	117
2265/7	dCC6	Active command channel on fault n-6	64306 = 16#FB32	5FBA/0E	9A/01/0E	119
2265/8	dCC7	Active command channel on fault n-7	64307 = 16#FB33	5FBA/0F	9A/01/0F	121
2265/9	dCC8	Active command channel on fault n-8	64308 = 16#FB34	5FBA/10	9A/01/10	123
2265/B	drC0	Reference channel active on last fault	64310 = 16#FB36	5FBA/11	9A/01/11	106
2265/C	drC1	Active reference channel on fault n-1	64311 = 16#FB37	5FBA/12	9A/01/12	109
2265/D	drC2	Active reference channel on fault n-2	64312 = 16#FB38	5FBA/13	9A/01/13	111
2265/E	drC3	Active reference channel on fault n-3	64313 = 16#FB39	5FBA/14	9A/01/14	113
2265/F	drC4	Active reference channel on fault n-4	64314 = 16#FB3A	5FBA/15	9A/01/15	115
603F	Errd	DSP402 fault code	8606 = 16#219E	5FB9/D7	8C/01/07	99
6048/1	SPAL	Acceleration speed delta	8611 = 16#21A3	6048/1	8C/01/0C	140
6048/2	SPAt	Acceleration time delta	8613 = 16#21A5	6048/2	8C/01/0E	140
6049/1	SPdL	Deceleration speed delta	8614 = 16#21A6	6049/1	8C/01/0F	140
6049/2	SPdt	Deceleration time delta	8616 = 16#21A8	6049/2	8C/01/11	141
604B/1	SPFn	Speed setpoint factor numerator	8642 = 16#21C2	604B/1	8C/01/2B	141

Index of parameter CANopen addresses

CANopen index	Code	Name	Logic address	INTERBUS index	DeviceNet path	Page
604B/2	SPFd	Speed setpoint factor denominator	8643 = 16#21C3	604B/2	8C/01/2C	142

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
6041	ETA	Status word	8603 = 16#219B	6041	71/01/02	79
6040	CMd	Control word	8601 = 16#2199	6040	B7/01/01	74
6042	LFRD	Speed reference	8602 = 16#219A	6042	2A/01/08	77
6044	rFrd	Output speed	8604 = 16#219C	6044	2A/01/07	87
6071	LTR	Torque reference	8505 = 16#2139	6071	8B/01/6A	77
6077	Otr	Output torque	3205 = 16#0C85	6077	71/01/06	87
5FB0/02	nCV	Drive nominal rating	3011 = 16#0BC3	2000/C	70/01/0C	124
5FB0/03	UCAL	Drive line voltage	3012 = 16#0BC4	2000/D	70/01/0D	124
5FB0/07	InV	Rated drive current	3017 = 16#0BC9	2000/12	70/01/12	124
5FB0/0D	UdP	Drive software version	3302 = 16#0CE6	2003/3	71/01/67	124
5FB0/25	PAn0	Device name: characters 1 and 2	3340 = 16#0D0C	2003/29	71/01/8D	124
5FB0/26	PAn1	Device name: characters 3 and 4	3341 = 16#0D0D	2003/2A	71/01/8E	124
5FB0/27	PAn2	Device name: characters 5 and 6	3342 = 16#0D0E	2003/2B	71/01/8F	125
5FB0/28	PAn3	Device name: characters 7 and 8	3343 = 16#0D0F	2003/2C	71/01/90	125
5FB0/29	PAn4	Device name: characters 9 and 10	3344 = 16#0D10	2003/2D	71/01/91	125
5FB0/2A	PAn5	Device name: characters 11 and 12	3345 = 16#0D11	2003/2E	71/01/92	125
5FB0/2B	PAn6	Device name: characters 13 and 14	3346 = 16#0D12	2003/2F	71/01/93	125
5FB0/2C	PAn7	Device name: characters 15 and 16	3346 = 16#0D12	2003/30	71/01/94	125
5FB0/96	CnF	Network card fault code	7132 = 16#1BDC	2029/21	84/01/85	102
5FB0/97	APF	"Controller Inside" card fault code	7133 = 16#1BDD	2029/22	84/01/86	102
5FB0/98	ILF1	Option card 1 fault code	7134 = 16#1BDE	2029/23	84/01/87	103
5FB0/99	ILF2	Option card 2 fault code	7135 = 16#1BDF	2029/24	84/01/88	103
5FB3/74	tFr	[Max frequency]	3103 = 16#C1F	2001/4	70/01/68	149
5FB3/75	HSP	[High speed]	3104 = 16#C20	2001/5	70/01/69	145
5FB3/76	LSP	[Low speed]	3105 = 16#C21	2001/6	70/01/6A	145
5FB3/77	bSP	[Reference template]	3106 = 16#C22	2001/7	70/01/6B	168
5FB3/78	nrd	[Noise reduction]	3107 = 16#C23	2001/8	70/01/6C	162
5FB3/79	rIn	[RV Inhibition]	3108 = 16#C24	2001/9	70/01/6D	193
5FB3/7A	FLr	[Catch on the fly]	3110 = 16#C26	2001/B	70/01/6F	263
5FB3/7B	r1	[R1 Assignment]	5001 = 16#1389	2014/2	7A/01/02	179

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB3/7C	r2	[R2 Assignment]	5002 = 16#138A	2014/3	7A/01/03	181
5FB3/7D	LO1	[LO1 assignment]	5009 = 16#1391	2014/A	7A/01/0A	184
5FB3/7E	PGI	Encoder pulse counter	5604 = 16#15E4	201A/5	7D/01/05	98
5FB3/7E	PGI	[Number of pulses]	5604 = 16#15E4	201A/5	7D/01/05	159
5FB3/7E	PGI	[Number of pulses]	5604 = 16#15E4	201A/5	7D/01/05	177
5FB3/7F	EnC	[Encoder check]	5605 = 16#15E5	201A/6	7D/01/06	160
5FB3/7F	EnC	[Encoder check]	5605 = 16#15E5	201A/6	7D/01/06	177
5FB3/81	O01	"Controller Inside" HMI exchange word 1	6401 = 16#1901	2022/2	81/01/02	132
5FB3/82	O02	"Controller Inside" HMI exchange word 2	6402 = 16#1902	2022/3	81/01/03	132
5FB3/83	O03	"Controller Inside" HMI exchange word 3	6403 = 16#1903	2022/4	81/01/04	132
5FB3/84	O04	"Controller Inside" HMI exchange word 4	6404 = 16#1904	2022/5	81/01/05	132
5FB3/85	O05	"Controller Inside" HMI exchange word 5	6405 = 16#1905	2022/6	81/01/06	133
5FB3/86	O06	"Controller Inside" HMI exchange word 6	6406 = 16#1906	2022/7	81/01/07	133
5FB3/87	O07	"Controller Inside" HMI exchange word 7	6407 = 16#1907	2022/8	81/01/08	133
5FB3/88	O08	"Controller Inside" HMI exchange word 8	6408 = 16#1908	2022/9	81/01/09	133
5FB3/89	O09	"Controller Inside" HMI exchange word 9	6409 = 16#1909	2022/A	81/01/0A	133
5FB3/8A	O10	"Controller Inside" HMI exchange word 10	6410 = 16#190A	2022/B	81/01/0B	133
5FB3/8B	O11	"Controller Inside" HMI exchange word 11	6411 = 16#190B	2022/C	81/01/0C	133
5FB3/8C	O12	"Controller Inside" HMI exchange word 12	6412 = 16#190C	2022/D	81/01/0D	134
5FB3/8D	O13	"Controller Inside" HMI exchange word 13	6413 = 16#190D	2022/E	81/01/0E	134
5FB3/8E	O14	"Controller Inside" HMI exchange word 14	6414 = 16#190E	2022/F	81/01/0F	134
5FB3/8F	O15	"Controller Inside" HMI exchange word 15	6415 = 16#190F	2022/10	81/01/10	134
5FB3/90	O16	"Controller Inside" HMI exchange word 16	6416 = 16#1910	2022/11	81/01/11	134

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB3/91	O17	"Controller Inside" HMI exchange word 17	6417 = 16#1911	2022/12	81/01/12	134
5FB3/92	O18	"Controller Inside" HMI exchange word 18	6418 = 16#1912	2022/13	81/01/13	134
5FB3/93	O19	"Controller Inside" HMI exchange word 19	6419 = 16#1913	2022/14	81/01/14	135
5FB3/94	O20	"Controller Inside" HMI exchange word 20	6420 = 16#1914	2022/15	81/01/15	135
5FB3/95	O21	"Controller Inside" HMI exchange word 21	6421 = 16#1915	2022/16	81/01/16	135
5FB3/96	O22	"Controller Inside" HMI exchange word 22	6422 = 16#1916	2022/17	81/01/17	135
5FB3/97	O23	"Controller Inside" HMI exchange word 23	6423 = 16#1917	2022/18	81/01/18	135
5FB3/98	O24	"Controller Inside" HMI exchange word 24	6424 = 16#1918	2022/19	81/01/19	135
5FB3/99	O25	"Controller Inside" HMI exchange word 25	6425 = 16#1919	2022/1A	81/01/1A	135
5FB3/9A	O26	"Controller Inside" HMI exchange word 26	6426 = 16#191A	2022/1B	81/01/1B	136
5FB3/9B	O27	"Controller Inside" HMI exchange word 27	6427 = 16#191B	2022/1C	81/01/1C	136
5FB3/9C	O28	"Controller Inside" HMI exchange word 28	6428 = 16#191C	2022/1D	81/01/1D	136
5FB3/9D	O29	"Controller Inside" HMI exchange word 29	6429 = 16#191D	2022/1E	81/01/1E	136
5FB3/9E	O30	"Controller Inside" HMI exchange word 30	6430 = 16#191E	2022/1F	81/01/1F	136
5FB3/9F	O31	"Controller Inside" HMI exchange word 31	6431 = 16#191F	2022/20	81/01/20	136
5FB3/A0	O32	"Controller Inside" HMI exchange word 32	6432 = 16#1920	2022/21	81/01/21	136
5FB3/A1	O33	"Controller Inside" HMI exchange word 33	6433 = 16#1921	2022/22	81/01/22	137
5FB3/A2	O34	"Controller Inside" HMI exchange word 34	6434 = 16#1922	2022/23	81/01/23	137
5FB3/A3	O35	"Controller Inside" HMI exchange word 35	6435 = 16#1923	2022/24	81/01/24	137
5FB3/A4	O36	"Controller Inside" HMI exchange word 36	6436 = 16#1924	2022/25	81/01/25	137
5FB3/A5	O37	"Controller Inside" HMI exchange word 37	6437 = 16#1925	2022/26	81/01/26	137

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB3/A6	O38	"Controller Inside" HMI exchange word 38	6438 = 16#1926	2022/27	81/01/27	137
5FB3/A7	O39	"Controller Inside" HMI exchange word 39	6439 = 16#1927	2022/28	81/01/28	137
5FB3/A8	O40	"Controller Inside" HMI exchange word 40	6440 = 16#1928	2022/29	81/01/29	138
5FB3/A9	O41	"Controller Inside" HMI exchange word 41	6441 = 16#1929	2022/2A	81/01/2A	138
5FB3/AA	O42	"Controller Inside" HMI exchange word 42	6442 = 16#192A	2022/2B	81/01/2B	138
5FB3/AB	O43	"Controller Inside" HMI exchange word 43	6443 = 16#192B	2022/2C	81/01/2C	138
5FB3/AC	O44	"Controller Inside" HMI exchange word 44	6444 = 16#192C	2022/2D	81/01/2D	138
5FB3/AD	O45	"Controller Inside" HMI exchange word 45	6445 = 16#192D	2022/2E	81/01/2E	138
5FB3/AE	O46	"Controller Inside" HMI exchange word 46	6446 = 16#192E	2022/2F	81/01/2F	138
5FB3/AF	O47	"Controller Inside" HMI exchange word 47	6447 = 16#192F	2022/30	81/01/30	139
5FB3/B0	O48	"Controller Inside" HMI exchange word 48	6448 = 16#1930	2022/31	81/01/31	139
5FB3/B1	O49	"Controller Inside" HMI exchange word 49	6449 = 16#1931	2022/32	81/01/32	139
5FB3/BE	IPL	[Input phase loss]	7002 = 16#1B5A	2028/3	84/01/03	266
5FB3/BF	LFL2	[AI2 4-20mA loss]	7003 = 16#1B5B	2028/4	84/01/04	273
5FB3/C0	StP	[UnderV. prevention]	7004 = 16#1B5C	2028/5	84/01/05	271
5FB3/C1	Sdd	[Load slip detection]	7005 = 16#1B5D	2028/6	84/01/06	276
5FB3/C2	EPL	[External fault mgt]	7006 = 16#1B5E	2028/7	84/01/07	269
5FB3/C3	LFL3	[AI3 4-20mA loss]	7013 = 16#1B65	2028/E	84/01/0E	273
5FB3/C4	LFL4	[AI4 4-20mA loss]	7014 = 16#1B66	2028/F	84/01/0F	273
5FB3/C5	LFF	[Fallback speed]	7080 = 16#1BA8	2028/51	84/01/51	279
5FB3/C8	LFt	Altivar fault code	7121 = 16#1BD1	2029/16	84/01/7A	100
5FB3/C9	Atr	[Automatic restart]	7122 = 16#1BD2	2029/17	84/01/7B	262
5FB3/CA	brA	[Dec ramp adapt.]	9003 = 16#232B	203C/4	8E/01/04	204
5FB3/CB	rPt	[Ramp type]	9004 = 16#232C	203C/5	8E/01/05	201
5FB3/CC	tA1	[Begin Acc round]	9005 = 16#232D	203C/6	8E/01/06	202

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB3/CD	tA2	[End Acc round]	9006 = 16#232E	203C/7	8E/01/07	202
5FB3/CE	tA3	[Begin Dec round]	9007 = 16#232F	203C/8	8E/01/08	202
5FB3/CF	tA4	[End Dec round]	9008 = 16#2330	203C/9	8E/01/09	202
5FB3/D0	FrT	[Ramp 2 threshold]	9011 = 16#2333	203C/C	8E/01/0C	202
5FB3/D1	SPG	[Speed prop. gain]	9103 = 16#238F	203D/4	8E/01/68	145
5FB3/D2	CLI	[Current Limitation]	9201 = 16#23F1	203E/2	8F/01/02	146
5FB3/D2	CLI	[Current Limitation]	9201 = 16#23F1	203E/2	8F/01/02	162
5FB3/D2	CLI	[Current Limitation]	9201 = 16#23F1	203E/2	93/01/D2	243
5FB3/D3	UnS	[Rated motor volt.]	9601 = 16#2581	2042/2	77/01/10	148
5FB3/D4	FrS	[Rated motor freq.]	9602 = 16#2582	2042/3	91/01/03	149
5FB3/D5	nCr	[Rated mot. current]	9603 = 16#2583	2042/4	91/01/04	148
5FB3/D6	nSP	[Nom motor speed]	9604 = 16#2584	2042/5	91/01/05	149
5FB3/D7	tUn	[Auto-tuning]	9608 = 16#2588	2042/9	91/01/09	149
5FB3/D8	OPL	[Output Phase Loss]	9611 = 16#258B	2042/C	91/01/0C	266
5FB3/D9	tHt	[Motor protect. type]	9612 = 16#258C	2042/D	91/01/0D	264
5FB3/DA	PPn	[Pr]	9618 = 16#2592	2042/13	91/01/13	158
5FB3/DB	ItH	[Mot. therm. current]	9622 = 16#2596	2042/17	2A/01/0A	145
5FB3/DC	SLP	[Slip compensation]	9625 = 16#2599	2042/1A	91/01/1A	156
5FB3/DD	brt	[Brake Release time]	10004 = 16#2714	2046/5	93/01/05	222
5FB3/DE	bEt	[Brake engage time]	10005 = 16#2715	2046/6	93/01/06	223
5FB3/DF	Ibr	[Brake release I FW]	10006 = 16#2716	2046/7	93/01/07	222
5FB3/E0	bIP	[Brake impulse]	10007 = 16#2717	2046/8	93/01/08	221
5FB3/E1	blr	[Brake release freq.]	10012 = 16#271C	2046/D	93/01/0D	222
5FB3/E2	AdC	[Auto DC injection]	10401 = 16#28A1	204A/2	95/01/02	208
5FB3/E3	Ctd	[Current threshold]	11001 = 16#2AF9	2050/2	98/01/02	146
5FB3/E4	ttd	[Motor therm. level]	11002 = 16#2AFA	2050/3	98/01/03	264
5FB3/E4	ttd	[Motor therm. level]	11002 = 16#2AFA	2050 / 3	98/01/03	268
5FB3/E5	Ftd	[Freq. threshold]	11003 = 16#2AFB	2050/4	98/01/04	147
5FB3/E6	F2d	[Freq. threshold 2]	11004 = 16#2AFC	2050/5	98/01/05	147
5FB3/E7	SAt	[Thermal alarm stop]	11021 = 16#2B0D	2050/16	98/01/16	268
5FB3/E8	tCC	[2/3 wire control]	11101 = 16#2B5D	2051/2	98/01/66	166

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB3/E9	tCt	[2 wire type]	11102 = 16#2B5E	2051/3	98/01/67	166
5FB3/EA	JGT	[Jog delay]	11112 = 16#2B68	2051/D	98/01/71	209
5FB3/EB	Stt	[Stop type]	11201 = 16#2BC1	2052/2	99/01/02	205
5FB3/EC	IdC	[DC inject. level 1]	11210 = 16#2BCA	2052/B	99/01/0B	206
5FB3/EC	IdC	[DC inject. level 1]	11210 = 16#2BCA	2052/B	99/01/0B	281
5FB3/ED	dCF	[Ramp divider]	11230 = 16#2BDE	2052/1F	99/01/1F	206
5FB3/ED	dCF	[Ramp divider]	11230 = 16#2BDE	2052/1F	99/01/1F	280
5FB3/F0	SP2	[Preset speed 2]	11410 = 16#2C92	2054/B	9A/01/0B	211
5FB3/F1	SP3	[Preset speed 3]	11411 = 16#2C93	2054/C	9A/01/0C	212
5FB3/F2	SP4	[Preset speed 4]	11412 = 16#2C94	2054/D	9A/01/0D	212
5FB3/F3	SP5	[Preset speed 5]	11413 = 16#2C95	2054/E	9A/01/0E	212
5FB3/F4	SP6	[Preset speed 6]	11414 = 16#2C96	2054/F	9A/01/0F	212
5FB3/F5	SP7	[Preset speed 7]	11415 = 16#2C97	2054/10	9A/01/10	212
5FB3/F6	SP8	[Preset speed 8]	11416 = 16#2C98	2054/11	9A/01/11	213
5FB3/F7	SrP	[+/-Speed limitation]	11505 = 16#2CF1	2055/6	9A/01/6A	216
5FB3/F8	tLS	[Low speed time out]	11701 = 16#2DB5	2057/2	9B/01/66	146
5FB3/F8	tLS	[Low speed time out]	11701 = 16#2DB5	2057/2	9B/01/66	235
5FB3/F9	PIC	[PID correct. reverse]	11940 = 16#2EA4	2059/29	9C/01/8D	232
5FB3/FA	RPG	[PID prop. gain]	11941 = 16#2EA5	2059/2A	9C/01/8E	231
5FB3/FB	rIG	[PID integral gain]	11942 = 16#2EA6	2059/2B	9C/01/8F	232
5FB3/FC	rdG	[PID derivative gain]	11943 = 16#2EA7	2059/2C	9C/01/90	232
5FB3/FD	PSr	[Speed input %]	11951 = 16#2EAF	2059/34	9C/01/98	234
5FB3/FE	PAL	[Min fbk alarm]	11961 = 16#2EB9	2059/3E	9C/01/A2	233
5FB3/FF	PAH	[Max fbk alarm]	11962 = 16#2EBA	2059/3F	9C/01/A3	233
5FB4/01	PEr	[PID error Alarm]	11963 = 16#2EBB	2059/40	9C/01/A4	233
5FB4/02	dBs	[Time to motor run]	13101 = 16#332D	2065/2	A2/01/66	245
5FB4/03	dAS	[Time to open cont.]	13102 = 16#332E	2065/3	A2/01/67	246
5FB4/04	rSP	[Evacuation freq.]	13833 = 16#3609	206C/22	A6/01/22	258
5FB4/05	FLU	[Motor fluxing]	13902 = 16#364E	206D/3	A6/01/67	146
5FB4/05	FLU	[Motor fluxing]	13902 = 16#364E	206D/3	A6/01/67	219
5FB6/1A	CIC	Incorrect configuration	7130 = 16#1BDA	2029/1F	84/01/83	102

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB6/1C	LFR	Frequency reference	8502 = 16#2136	2037/3	8B/01/67	77
5FB6/1D	PISP	PID regulator reference	8503 = 16#2137	2037/4	8B/01/68	78
5FB6/1E	CMI	Extended control word	8504 = 16#2138	2037/5	8B/01/69	76
5FB6/3D	LtCr	Torque reference (Nm)	9261 = 16#242D	203E/3E	2A/01/0C	77
5FB6/3E	MFr	Multiplying coefficient	11831 = 16#2E37	2058/20	9C/01/20	78
5FB6/43	nM1	Communication scanner, value of read word 1	12741 = 16#31C5	2061/2A	A0/01/8E	127
5FB6/44	nM2	Communication scanner, value of read word 2	12742 = 16#31C6	2061/2B	A0/01/8F	127
5FB6/45	nM3	Communication scanner, value of read word 3	12743 = 16#31C7	2061/2C	A0/01/90	127
5FB6/46	nM4	Communication scanner, value of read word 4	12744 = 16#31C8	2061/2D	A0/01/91	127
5FB6/47	nM5	Communication scanner, value of read word 5	12745 = 16#31C9	2061/2E	A0/01/92	127
5FB6/48	nM6	Communication scanner, value of read word 6	12746 = 16#31CA	2061/2F	A0/01/93	127
5FB6/49	nM7	Communication scanner, value of read word 7	12747 = 16#31CB	2061/30	A0/01/94	128
5FB6/4A	nM8	Communication scanner, value of read word 8	12748 = 16#31CC	2061/31	A0/01/95	128
5FB6/4B	nC1	Communication scanner, value of write word 1	12761 = 16#31D9	2061/3E	A0/01/A2	126
5FB6/4C	nC2	Communication scanner, value of write word 2	12762 = 16#31DA	2061/3F	A0/01/A3	126
5FB6/4D	nC3	Communication scanner, value of write word 3	12763 = 16#31DB	2061/40	A0/01/A4	126
5FB6/4E	nC4	Communication scanner, value of write word 4	12764 = 16#31DC	2061/41	A0/01/A5	126
5FB6/4F	nC5	Communication scanner, value of write word 5	12765 = 16#31DD	2061/42	A0/01/A6	126
5FB6/50	nC6	Communication scanner, value of write word 6	12766 = 16#31DE	2061/43	A0/01/A7	126
5FB6/51	nC7	Communication scanner, value of write word 7	12767 = 16#31DF	2061/44	A0/01/A8	126
5FB6/52	nC8	Communication scanner, value of write word 8	12768 = 16#31E0	2061/45	A0/01/A9	127
5FB9/02	CCFG	[Customized macro]	3053 = 16#BED	2000/36	70/01/36	144
5FB9/04	rFr	Output frequency	3202 = 16#C82	2002/3	71/01/03	87

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/05	FrH	Frequency reference before ramp	3203 = 16#0C83	2002/4	71/01/04	89
5FB9/06	LCr	Motor current	3204 = 16#0C84	2002/5	2A/01/09	88
5FB9/08	ETI	Extended status word 0	3206 = 16#0C86	2002/7	71/01/07	81
5FB9/09	ULn	Power supply voltage	3207 = 16#0C87	2002/8	71/01/08	92
5FB9/0A	UOP	Motor voltage	3208 = 16#0C88	2002/9	71/01/09	88
5FB9/0B	tHd	Drive thermal state	3209 = 16#0C89	2002/A	71/01/0A	92
5FB9/0C	OPr	Motor power	3211 = 16#0C8B	2002/C	71/01/0C	88
5FB9/10	Otrn	Output torque (Nm)	3216 = 16#0C90	2002/11	2A/01/0B	88
5FB9/15	APH	Energy consumption	3230 = 16#0C9E	2002/1F	71/01/1F	92
5FB9/16	rtH	Total motor operating time	3231 = 16#0C9F	2002/20	71/01/20	93
5FB9/17	rtHI	Internal motor operating time	3232 = 16#0CA0	2002/21	71/01/21	93
5FB9/18	PtH	Total drive operating time	3233 = 16#0CA1	2002/22	71/01/22	93
5FB9/1A	tAC	IGBT alarm time	3235 = 16#0CA3	2002/24	71/01/24	93
5FB9/1C	LRS1	Extended status word 1	3250 = 16#0CB2	2002/33	71/01/33	81
5FB9/1D	LRS2	Extended status word 2	3251 = 16#0CB3	2002/34	71/01/34	82
5FB9/1E	LRS3	Extended status word 3	3252 = 16#0CB4	2002/35	71/01/35	82
5FB9/1F	LRS4	Extended status word 4	3253 = 16#0CB5	2002/36	71/01/36	83
5FB9/20	LRS5	Extended status word 5	3254 = 16#0CB6	2002/37	71/01/37	83
5FB9/21	LRS6	Extended status word 6	3255 = 16#0CB7	2002/38	71/01/38	84
5FB9/22	LRS7	Extended status word 7	3256 = 16#0CB8	2002/39	71/01/39	84
5FB9/23	LRS8	Extended status word 8	3257 = 16#0C89	2002/3A	71/01/3A	85
5FB9/28	IL1r	Logic input map	5202 = 16#1452	2016/3	7B/01/03	95
5FB9/2A	OL1r	Logic output map	5212 = 16#145C	2016/D	7B/01/0D	95
5FB9/2F	AI1r	Standardized image of analog input 1	5232 = 16#1470	2016/21	7B/01/21	96
5FB9/30	AI2r	Standardized image of analog input 2	5233 = 16#1471	2016/22	7B/01/22	96
5FB9/31	AI3r	Standardized image of analog input 3	5234 = 16#1472	2016/23	7B/01/23	96
5FB9/32	AI4r	Standardized image of analog input 4	5235 = 16#1473	2016/24	7B/01/24	97
5FB9/33	AI1C	Physical image of analog input 1	5242 = 16#147A	2016/2B	7B/01/2B	96
5FB9/34	AI2C	Physical image of analog input 2	5243 = 16#147B	2016/2C	7B/01/2C	96

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/35	AI3C	Physical image of analog input 3	5244 = 16#147C	2016/2D	7B/01/2D	96
5FB9/36	AI4C	Physical image of analog input 4	5245 = 16#147D	2016/2E	7B/01/2E	96
5FB9/3A	AO1r	Standardized image of analog output 1	5261 = 16#148D	2016/3E	7B/01/3E	97
5FB9/3B	AO2r	Standardized image of analog output 2	5262 = 16#148E	2016/3F	7B/01/3F	97
5FB9/3C	AO3r	Standardized image of analog output 3	5263 = 16#148F	2016/40	7B/01/40	98
5FB9/3D	AO1C	Physical image of analog output 1	5271 = 16#1497	2016/48	7B/01/48	97
5FB9/3E	AO2C	Physical image of analog output 2	5272 = 16#1498	2016/49	7B/01/49	97
5FB9/3F	AO3C	Physical image of analog output 3	5273 = 16#1499	2016/4A	7B/01/4A	97
5FB9/40	AIU1	PID regulator feedback	5281 = 16 #14A1	2016/52	7B/01/52	78
5FB9/41	PUC	Drive encoder divided counter	5611 = 16#15EB	201A/C	7D/01/0C	132
5FB9/49	PIL1	"Controller Inside" logic input map	6901 = 16#1AF5	2027/2	83/01/66	131
5FB9/4A	POL1	"Controller Inside" logic output map	6911 = 16#1AFF	2027/C	83/01/70	131
5FB9/4B	PAI1	Physical image of analog input 1	6942 = 16#1B1E	2027/2B	83/01/8F	131
5FB9/4C	PAI2	Physical image of analog input 2	6943 = 16#1B1F	2027/2C	83/01/90	131
5FB9/4D	PAO1	Physical image of analog output 1	6971 = 16#1B3B	2027/48	83/01/AC	131
5FB9/4E	PAO2	Physical image of analog output 2	6972 = 16#1B3C	2027/49	83/01/AD	131
5FB9/55	dP0	Fault code on last fault	7200 = 16#1C20	202A/1	85/01/01	105
5FB9/56	dP1	Fault code on fault n-1	7201 = 16#1C21	202A/2	85/01/02	107
5FB9/57	dP2	Fault code on fault n-2	7202 = 16#1C22	202A/3	85/01/03	109
5FB9/58	dP3	Fault code on fault n-3	7203 = 16#1C23	202A/4	85/01/04	111
5FB9/59	dP4	Fault code on fault n-4	7204 = 16#1C24	202A/5	85/01/05	113
5FB9/5A	dP5	Fault code on fault n-5	7205 = 16#1C25	202A/6	85/01/06	115
5FB9/5B	dP6	Fault code on fault n-6	7206 = 16#1C26	202A/7	85/01/07	117
5FB9/5C	dP7	Fault code on fault n-7	7207 = 16#1C27	202A/8	85/01/08	119
5FB9/5D	dP8	Fault code on fault n-8	7208 = 16#1C28	202A/9	85/01/09	121
5FB9/5E	EP0	Status word on last fault	7210 = 16#1C2A	202A/B	85/01/0B	105
5FB9/5F	EP1	Status word on fault n-1	7211 = 16#1C2B	202A/C	85/01/0C	108
5FB9/60	EP2	Status word on fault n-2	7212 = 16#1C2C	202A/D	85/01/0D	110
5FB9/61	EP3	Status word on fault n-3	7213 = 16#1C2D	202A/E	85/01/0E	112
5FB9/62	EP4	Status word on fault n-4	7214 = 16#1C2E	202A/F	85/01/0F	114
5FB9/63	EP5	Status word on fault n-5	7215 = 16#1C2F	202A/10	85/01/10	116

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/64	EP6	Status word on fault n-6	7216 = 16#1C30	202A/11	85/01/11	118
5FB9/65	EP7	Status word on fault n-7	7217 = 16#1C31	202A/12	85/01/12	120
5FB9/66	EP8	Status word on fault n-8	7218 = 16#1C32	202A/13	85/01/13	122
5FB9/67	IP0	Extended status word 0 on last fault	7220 = 16#1C34	202A/15	85/01/15	106
5FB9/68	IP1	Extended status word on fault n-1	7221 = 16#1C35	202A/16	85/01/16	108
5FB9/69	IP2	Extended status word on fault n-2	7222 = 16#1C36	202A/17	85/01/17	110
5FB9/6A	IP3	Extended status word on fault n-3	7223 = 16#1C37	202A/18	85/01/18	112
5FB9/6B	IP4	Extended status word on fault n-4	7224 = 16#1C38	202A/19	85/01/19	114
5FB9/6C	IP5	Extended status word on fault n-5	7225 = 16#1C39	202A/1A	85/01/1A	116
5FB9/6D	IP6	Extended status word on fault n-6	7226 = 16#1C3A	202A/1B	85/01/1B	118
5FB9/6E	IP7	Extended status word on fault n-7	7227 = 16#1C3B	202A/1C	85/01/1C	120
5FB9/6F	IP8	Extended status word on fault n-8	7228 = 16#1C3C	202A/1D	85/01/1D	122
5FB9/70	CMP0	Command word on last fault	7230 = 16#1C3E	202A/1F	85/01/1F	106
5FB9/71	CMP1	Command word on fault n-1	7231 = 16#1C3F	202A/20	85/01/20	108
5FB9/72	CMP2	Command word on fault n-2	7232 = 16#1C40	202A/21	85/01/21	110
5FB9/73	CMP3	Command word on fault n-3	7233 = 16#1C41	202A/22	85/01/22	112
5FB9/74	CMP4	Command word on fault n-4	7234 = 16#1C42	202A/23	85/01/23	114
5FB9/75	CMP5	Command word on fault n-5	7235 = 16#1C43	202A/24	85/01/24	116
5FB9/76	CMP6	Command word on fault n-6	7236 = 16#1C44	202A/25	85/01/25	118
5FB9/77	CMP7	Command word on fault n-7	7237 = 16#1C45	202A/26	85/01/26	120
5FB9/78	CMP8	Command word on fault n-8	7238 = 16#1C46	202A/27	85/01/27	122
5FB9/79	LCP0	Motor current on last fault	7240 = 16#1C48	202A/29	85/01/29	105
5FB9/7A	LCP1	Motor current on fault n-1	7241 = 16#1C49	202A/2A	85/01/2A	108
5FB9/7B	LCP2	Motor current on fault n-2	7242 = 16#1C4A	202A/2B	85/01/2B	110
5FB9/7C	LCP3	Motor current on fault n-3	7243 = 16#1C4B	202A/2C	85/01/2C	112
5FB9/7D	LCP4	Motor current on fault n-4	7244 = 16#1C4C	202A/2D	85/01/2D	114
5FB9/7E	LCP5	Motor current on fault n-5	7245 = 16#1C4D	202A/2E	85/01/2E	116
5FB9/7F	LCP6	Motor current on fault n-6	7246 = 16#1C4E	202A/2F	85/01/2F	118
5FB9/80	LCP7	Motor current on fault n-7	7247 = 16#1C4F	202A/30	85/01/30	120
5FB9/81	LCP8	Motor current on fault n-8	7248 = 16#1C50	202A/31	85/01/31	122
5FB9/82	rFP0	Output frequency on last fault	7250 = 16#1C52	202A/33	85/01/33	105

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/83	rFP1	Output frequency on fault n-1	7251 = 16#1C53	202A/34	85/01/34	108
5FB9/84	rFP2	Output frequency on fault n-2	7252 = 16#1C54	202A/35	85/01/35	110
5FB9/85	rFP3	Output frequency on fault n-3	7253 = 16#1C55	202A/36	85/01/36	112
5FB9/86	rFP4	Output frequency on fault n-4	7254 = 16#1C56	202A/37	85/01/37	114
5FB9/87	rFP5	Output frequency on fault n-5	7255 = 16#1C57	202A/38	85/01/38	116
5FB9/88	rFP6	Output frequency on fault n-6	7256 = 16#1C58	202A/39	85/01/39	118
5FB9/89	rFP7	Output frequency on fault n-7	7257 = 16#1C59	202A/3A	85/01/3A	120
5FB9/8A	rFP8	Output frequency on fault n-8	7258 = 16#1C5A	202A/3B	85/01/3B	122
5FB9/8B	rtP0	Motor operating time on last fault	7260 = 16#1C5C	202A/3D	85/01/3D	107
5FB9/8C	rtP1	Motor operating time on fault n-1	7261 = 16#1C5D	202A/3E	85/01/3E	109
5FB9/8D	rtP2	Motor operating time on fault n-2	7262 = 16#1C5E	202A/3F	85/01/3F	111
5FB9/8E	rtP3	Motor operating time on fault n-3	7263 = 16#1C5F	202A/40	85/01/40	113
5FB9/8F	rtP4	Motor operating time on fault n-4	7264 = 16#1C60	202A/41	85/01/41	115
5FB9/90	rtP5	Motor operating time on fault n-5	7265 = 16#1C61	202A/42	85/01/42	117
5FB9/91	rtP6	Motor operating time on fault n-6	7266 = 16#1C62	202A/43	85/01/43	119
5FB9/92	rtP7	Motor operating time on fault n-7	7267 = 16#1C63	202A/44	85/01/44	121
5FB9/93	rtP8	Motor operating time on fault n-8	7268 = 16#1C64	202A/45	85/01/45	123
5FB9/94	ULP0	Power supply voltage on last fault	7270 = 16#1C66	202A/47	85/01/47	105
5FB9/95	ULP1	Supply voltage on fault n-1	7271 = 16#1C67	202A/48	85/01/48	108
5FB9/96	ULP2	Supply voltage on fault n-2	7272 = 16#1C68	202A/49	85/01/49	110
5FB9/97	ULP3	Supply voltage on fault n-3	7273 = 16#1C69	202A/4A	85/01/4A	112
5FB9/98	ULP4	Supply voltage on fault n-4	7274 = 16#1C6A	202A/4B	85/01/4B	114
5FB9/99	ULP5	Supply voltage on fault n-5	7275 = 16#1C6B	202A/4C	85/01/4C	116
5FB9/9A	ULP6	Supply voltage on fault n-6	7276 = 16#1C6C	202A/4D	85/01/4D	118
5FB9/9B	ULP7	Supply voltage on fault n-7	7277 = 16#1C6D	202A/4E	85/01/4E	120
5FB9/9C	ULP8	Supply voltage on fault n-8	7278 = 16#1C6E	202A/4F	85/01/4F	122
5FB9/9D	tHP0	Motor thermal state on last fault	7280 = 16#1C70	202A/51	85/01/51	105
5FB9/9E	tHP1	Motor thermal state on fault n-1	7281 = 16#1C71	202A/52	85/01/52	108
5FB9/9F	tHP2	Motor thermal state on fault n-2	7282 = 16#1C72	202A/53	85/01/53	110
5FB9/A0	tHP3	Motor thermal state on fault n-3	7283 = 16#1C73	202A/54	85/01/54	112
5FB9/A1	tHP4	Motor thermal state on fault n-4	7284 = 16#1C74	202A/55	85/01/55	114

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/A2	tHP5	Motor thermal state on fault n-5	7285 = 16#1C75	202A/56	85/01/56	116
5FB9/A3	tHP6	Motor thermal state on fault n-6	7286 = 16#1C76	202A/57	85/01/57	118
5FB9/A4	tHP7	Motor thermal state on fault n-7	7287 = 16#1C77	202A/58	85/01/58	120
5FB9/A5	tHP8	Motor thermal state on fault n-8	7288 = 16#1C78	202A/59	85/01/59	122
5FB9/A6	CrP0	Channels active on last fault	7290 = 16#1C7A	202A/5B	85/01/5B	107
5FB9/A7	CrP1	Active channels on fault n-1	7291 = 16#1C7B	202A/5C	85/01/5C	109
5FB9/A8	CrP2	Active channels on fault n-2	7292 = 16#1C7C	202A/5D	85/01/5D	111
5FB9/A9	CrP3	Active channels on fault n-3	7293 = 16#1C7D	202A/5E	85/01/5E	113
5FB9/AA	CrP4	Active channels on fault n-4	7294 = 16#1C7E	202A/5F	85/01/5F	115
5FB9/AB	CrP5	Active channels on fault n-5	7295 = 16#1C7F	202A/60	85/01/60	117
5FB9/AC	CrP6	Active channels on fault n-6	7296 = 16#1C80	202A/61	85/01/61	119
5FB9/AD	CrP7	Active channels on fault n-7	7297 = 16#1C81	202A/62	85/01/62	121
5FB9/AE	CrP8	Active channels on fault n-8	7298 = 16#1C82	202A/63	85/01/63	123
5FB9/AF	Md0	Date on last fault	7300 = 16#1C84	202B/1	85/01/65	107
5FB9/B0	Md1	Date on fault n-1	7301 = 16#1C85	202B/2	85/01/66	109
5FB9/B1	Md2	Date on fault n-2	7302 = 16#1C86	202B/3	85/01/67	111
5FB9/B2	Md3	Date on fault n-3	7303 = 16#1C87	202B/4	85/01/68	113
5FB9/B3	Md4	Date on fault n-4	7304 = 16#1C88	202B/5	85/01/69	115
5FB9/B4	Md5	Date on fault n-5	7305 = 16#1C89	202B/6	85/01/6A	117
5FB9/B5	dM6	Time on fault n-6	7316 = 16#1C94	202B/11	85/01/75	119
5FB9/B6	Md7	Date on fault n-7	7307 = 16#1C8B	202B/8	85/01/6C	121
5FB9/B7	Md8	Date on fault n-8	7308 = 16#1C8C	202B/9	85/01/6D	123
5FB9/B8	dM0	Time on last fault	7310 = 16#1C8E	202B/B	85/01/6F	107
5FB9/B9	dM1	Time on fault n-1	7311 = 16#1C8F	202B/C	85/01/70	109
5FB9/BA	dM2	Time on fault n-2	7312 = 16#1C90	202B/D	85/01/71	111
5FB9/BB	dM3	Time on fault n-3	7313 = 16#1C91	202B/E	85/01/72	113
5FB9/BC	dM4	Time on fault n-4	7314 = 16#1C92	202B/F	85/01/73	115
5FB9/BD	dM5	Time on fault n-5	7315 = 16#1C93	202B/10	85/01/74	117
5FB9/BE	Md6	Date on fault n-6	7306 = 16#1C8A	202B/7	85/01/6B	119
5FB9/BF	dM7	Time on fault n-7	7317 = 16#1C95	202B/12	85/01/76	121
5FB9/C0	dM8	Time on fault n-8	7318 = 16#1C96	202B/13	85/01/77	123

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/CA	dAY	Date	7391 = 16#1CDF	202B/5C	85/01/C0	132
5FB9/CB	tIME	Time	7392 = 16#1CE0	202B/5D	85/01/C1	132
5FB9/CC	Fnb	Fault counter	7393 = 16#1CE1	202B/5E	99/01/CC	104
5FB9/CD	CNFS	Active configuration	8020 = 16#1F54	2032/15	89/01/15	67
5FB9/CD	CNFS	[Config. Active]	8020 = 16#1F54	2032/15	89/01/15	86
5FB9/CE	CRC	Active reference channel	8441 = 16#20F9	2036/2A	8B/01/2A	85
5FB9/CF	CCC	Active command channel	8442 = 16#20FA	2036/2B	8B/01/2B	86
5FB9/D6	FrHd	Speed reference before ramp	8605 = 16#219D	2038/6	8C/01/06	89
5FB9/D7	Errd	DSP402 fault code	8606 = 16#219E	603F	8C/01/07	99
5FB9/D8	FrOd	Speed reference after ramp	8641 = 16#21C1	6043	8C/01/2A	89
5FB9/D9	FrO	Frequency reference after ramp	9021 = 16#233D	203C/16	8E/01/16	89
5FB9/DB	trr	Torque reference before ramp	9231 = 16#240F	203E/20	8F/01/20	90
5FB9/DC	trO	Torque reference after ramp	9232 = 16#2410	203E/21	8F/01/21	90
5FB9/DD	nSL	[Nominal motor slip]	9605 = 16#2585	2042/6	91/01/06	157
5FB9/DE	tHr	Motor thermal state	9630 = 16#259E	2042/1F	91/01/1F	92
5FB9/DF	rSM	Asynchronous motor cold state stator resistance	9640 = 16#25A8	2042/29	91/01/29	94
5FB9/DF	rSM	[Stator R measured]	9640 = 16#25A8	2042/29	91/01/29	157
5FB9/E0	IdM	Magnetizing current	9650 = 16#25B2	2042/33	91/01/33	93
5FB9/E0	IdM	[Idr]	9650 = 16#25B2	2042/33	91/01/33	157
5FB9/E1	LFM	Leakage inductance	9660 = 16#25BC	2042/3D	91/01/3D	94
5FB9/E1	LFM	[Lfr]	9660 = 16#25BC	2042/3D	91/01/3D	157
5FB9/E2	trM	Rotor time constant	9665 = 16#25C1	2042/42	91/01/42	94
5FB9/E2	trM	[T2r]	9665 = 16#25C1	2042/42	91/01/42	157
5FB9/E3	rSMS	Synchronous motor cold state stator resistance	9680 = 16#25D0	2042/51	91/01/51	94
5FB9/E3	rSMS	[R1rS]	9680 = 16#25D0	2042/51	91/01/51	159
5FB9/E5	rPE	PID regulator discrepancy	11980 = 16#2ECC	2059/51	9C/01/B5	91
5FB9/E6	rPF	PID regulator feedback reference	11981 = 16#2ECD	2059/52	9C/01/B6	91
5FB9/E7	rPC	PID reference after ramp	11982 = 16#2ECE	2059/53	9C/01/B7	91
5FB9/E8	rPO	PID regulator limit output reference	11983 = 16#2ECF	2059/54	9C/01/B8	91
5FB9/EA	AUS	ENA average speed	12102 = 16#2F46	205B/3	9D/01/67	88

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FB9/EB	EbOt	Current bobbin time	12209 = 16#2FB1	205C/A	9E/01/0A	93
5FB9/EC	CFPS	Active parameter set	12900 = 16#3264	2063/01	A1/01/65	70
5FB9/EC	CFPS	Active parameter set	12900 = 16#3264	2063/01	A1/01/65	86
5FB9/ED	VAL	Load parameter set command	12901 = 16#3265	2063/02	A1/01/66	73
5FBA/08	dCC0	Command channel active on last fault	64300 = 16#FB2C	2265/1	9A/01/08	106
5FBA/09	dCC1	Active command channel on fault n-1	64301 = 16#FB2D	2265/2	9A/01/09	109
5FBA/0A	dCC2	Active command channel on fault n-2	64302 = 16#FB2E	2265/3	9A/01/0A	111
5FBA/0B	dCC3	Active command channel on fault n-3	64303 = 16#FB2F	2265/4	9A/01/0B	113
5FBA/0C	dCC4	Active command channel on fault n-4	64304 = 16#FB30	2265/5	9A/01/0C	115
5FBA/0D	dCC5	Active command channel on fault n-5	64305 = 16#FB31	2265/6	9A/01/0D	117
5FBA/0E	dCC6	Active command channel on fault n-6	64306 = 16#FB32	2265/7	9A/01/0E	119
5FBA/0F	dCC7	Active command channel on fault n-7	64307 = 16#FB33	2265/8	9A/01/0F	121
5FBA/10	dCC8	Active command channel on fault n-8	64308 = 16#FB34	2265/9	9A/01/10	123
5FBA/11	drC0	Reference channel active on last fault	64310 = 16#FB36	2265/B	9A/01/11	106
5FBA/12	drC1	Active reference channel on fault n-1	64311 = 16#FB37	2265/C	9A/01/12	109
5FBA/13	drC2	Active reference channel on fault n-2	64312 = 16#FB38	2265/D	9A/01/13	111
5FBA/14	drC3	Active reference channel on fault n-3	64313 = 16#FB39	2265/E	9A/01/14	113
5FBA/15	drC4	Active reference channel on fault n-4	64314 = 16#FB3A	2265/F	9A/01/15	115
5FBA/16	drC5	Active reference channel on fault n-5	64315 = 16#FB3B	2265/10	9A/01/16	117
5FBA/17	drC6	Active reference channel on fault n-6	64316 = 16#FB3C	2265/11	9A/01/17	119
5FBA/18	drC7	Active reference channel on fault n-7	64317 = 16#FB3D	2265/12	9A/01/18	121
5FBA/19	drC8	Active reference channel on fault n-8	64318 = 16#FB3E	2265/13	9A/01/19	123
5FBC/05	bFr	[Standard mot. freq]	3015 = 16#BC7	2000/10	70/01/10	148
5FBC/07	CFG	[Macro configuration]	3052 = 16#BEC	2000/35	70/01/35	144
5FBC/0B	OFI	[Sinus filter]	3109 = 16#C25	2001/A	70/01/6E	162
5FBC/0D	Strt	[IGBT test]	3112 = 16#C28	2001/D	70/01/71	272

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBC/0E	L1d	[LI1 On Delay]	4001 = 16#FA1	200A/2	75/01/02	167
5FBC/0F	L2d	[LI2 On Delay]	4002 = 16#FA2	200A/3	75/01/03	167
5FBC/10	L3d	[LI3 On Delay]	4003 = 16#FA3	200A/4	75/01/04	167
5FBC/11	L4d	[LI4 On Delay]	4004 = 16#FA4	200A/5	75/01/05	167
5FBC/12	L5d	[LI5 On Delay]	4005 = 16#FA5	200A/6	75/01/06	167
5FBC/13	L6d	[LI6 On Delay]	4006 = 16#FA6	200A/7	75/01/07	167
5FBC/14	L7d	[LI7 On Delay]	4007 = 16#FA7	200A/8	75/01/08	167
5FBC/15	L8d	[LI8 On Delay]	4008 = 16#FA8	200A/9	75/01/09	167
5FBC/16	L9d	[LI9 On Delay]	4009 = 16#FA9	200A/A	75/01/0A	167
5FBC/17	L10d	[LI10 On Delay]	4010 = 16#FAA	200A/B	75/01/0B	167
5FBC/18	L11d	[LI11 On Delay]	4011 = 16#FAB	200A/C	75/01/0C	167
5FBC/19	L12d	[LI12 On Delay]	4012 = 16#FAC	200A/D	75/01/0D	167
5FBC/1A	L13d	[LI13 On Delay]	4013 = 16#FAD	200A/E	75/01/0E	167
5FBC/1B	L14d	[LI14 On Delay]	4014 = 16#FAE	200A/F	75/01/0F	167
5FBC/1C	r1S	[R1 Active at]	4201 = 16#1069	200C/2	76/01/02	180
5FBC/1D	r2S	[R2 Active at]	4202 = 16#106A	200C/3	76/01/03	181
5FBC/1E	r3S	[R3 Active at]	4203 = 16#106B	200C/4	76/01/04	182
5FBC/1F	r4S	[R4 Active at]	4204 = 16#106C	200C/5	76/01/05	183
5FBC/20	LO1S	[LO1 active at]	4209 = 16#1071	200C/2D	76/01/0A	184
5FBC/21	LO2S	[LO2 active at]	4210 = 16#1072	200C/B	76/01/0B	185
5FBC/22	LO3S	[LO3 active at]	4211 = 16#1073	200C/C	76/01/0C	186
5FBC/23	LO4S	[LO4 active at]	4212 = 16#1074	200C/D	76/01/0D	187
5FBC/24	r1H	[R1 Holding time]	4221 = 16#107D	200C/16	76/01/16	180
5FBC/25	r2H	[R2 Holding time]	4222 = 16#107E	200C/17	76/01/17	181
5FBC/26	r3H	[R3 Holding time]	4223 = 16#107F	200C/18	76/01/18	182
5FBC/27	r4H	[R4 Holding time]	4224 = 16#1080	200C/19	76/01/19	183
5FBC/28	LO1H	[LO1 holding time]	4229 = 16#1085	200C/1E	76/01/1E	184
5FBC/29	LO2H	[LO2 holding time]	4230 = 16#1086	200C/1F	76/01/1F	185
5FBC/2A	LO3H	[LO3 holding time]	4231 = 16#1087	200C/20	76/01/20	186
5FBC/2B	LO4H	[LO4 holding time]	4232 = 16#1088	200C/21	76/01/21	187
5FBC/2C	r1d	[R1 Delay time]	4241 = 16#1091	200C/2A	76/01/2A	180

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBC/2D	r2d	[R2 Delay time]	4242 = 16#1092	200C/2B	76/01/2B	181
5FBC/2E	r3d	[R3 Delay time]	4243 = 16#1093	200C/2C	76/01/2C	182
5FBC/2F	r4d	[R4 Delay time]	4244 = 16#1094	200C/2D	76/01/2D	183
5FBC/30	LO1d	[LO1 delay time]	4249 = 16#1099	200C/32	76/01/32	184
5FBC/31	LO2d	[LO2 delay time]	4250 = 16#109A	200C/33	76/01/33	185
5FBC/32	LO3d	[LO3 delay time]	4251 = 16#109B	200C/34	76/01/34	186
5FBC/33	LO4d	[LO4 delay time]	4252 = 16#109C	200C/35	76/01/35	187
5FBC/34	AI1t	[AI1 Type]	4402 = 16#1132	200E/3	77/01/03	169
5FBC/35	AI2t	[AI2 Type]	4403 = 16#1133	200E/4	77/01/04	170
5FBC/36	AI3t	[AI3 Type]	4404 = 16#1134	200E/5	77/01/05	172
5FBC/37	AI4t	[AI4 Type]	4405 = 16#1135	200E/6	77/01/06	174
5FBC/38	UIL1	[AI1 min value]	4412 = 16#113C	200E/D	77/01/0D	169
5FBC/39	UIL2	[AI2 min value]	4413 = 16#113D	200E/E	77/01/0E	170
5FBC/3A	UIL4	[AI4 min value]	4415 = 16#113D	200E/10	77/01/10	174
5FBC/3B	UIH1	[AI1 max value]	4422 = 16#1146	200E/17	77/01/17	169
5FBC/3C	UIH2	[AI2 max value]	4423 = 16#1147	200E/18	77/01/18	170
5FBC/3D	UIH4	[AI4 max value]	4425 = 16#1149	200E/1A	77/01/1A	174
5FBC/3E	CrL2	[AI2 min value]	4433 = 16#1151	200E/22	77/01/22	170
5FBC/3F	CrL3	[AI3 min value]	4434 = 16#1152	200E/23	77/01/23	172
5FBC/40	CrL4	[AI4 min value]	4435 = 16#1153	200E/24	77/01/24	174
5FBC/41	CrH2	[AI2 max value]	4443 = 16#115B	200E/2C	77/01/2C	170
5FBC/42	CrH3	[AI3 max value]	4444 = 16#115C	200E/2D	77/01/2D	172
5FBC/43	CrH4	[AI4 max value]	4445 = 16#115D	200E/2E	77/01/2E	174
5FBC/44	AI1F	[AI1 filter]	4452 = 16#1164	200E/35	77/01/35	169
5FBC/45	AI2F	[AI2 filter]	4453 = 16#1165	200E/36	77/01/36	170
5FBC/46	AI3F	[AI3 filter]	4454 = 16#1166	200E/37	77/01/37	172
5FBC/47	AI4F	[AI4 filter]	4455 = 16#1167	200E/38	77/01/38	174
5FBC/48	AI1E	[AI1 Interm. point X]	4462 = 16#116E	200E/3F	77/01/3F	169
5FBC/49	AI2E	[AI2 Interm. point X]	4463 = 16#116F	200E/40	77/01/40	171
5FBC/4A	AI3E	[AI3 Interm. point X]	4464 = 16#1170	200E/41	77/01/4A	173
5FBC/4B	AI4E	[AI4 Interm. point X]	4465 = 16#1171	200E/42	77/01/42	175

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBC/4C	AI1S	[AI1 Interm. point Y]	4472 = 16#1178	200E/49	77/01/49	169
5FBC/4D	AI2S	[AI2 Interm. point Y]	4473 = 16#1179	200E/4A	77/01/4A	171
5FBC/4E	AI3S	[AI3 Interm. point Y]	4474 = 16#117A	200E/4B	77/01/4A	173
5FBC/4F	AI4S	[AI4 Interm. point Y]	4475 = 16#117B	200E/4C	77/01/4C	175
5FBC/50	AI2L	[AI2 range]	4483 = 16#1183	200E/54	77/01/54	171
5FBC/51	AI3L	[AI3 range]	4484 = 16#1184	200E/55	77/01/55	172
5FBC/52	AI4L	[AI4 range]	4485 = 16#1185	200E/56	77/01/56	175
5FBC/53	AO1t	[AO1 Type]	4601 = 16#11F9	2010/2	78/01/02	188
5FBC/54	AO2t	[AO2 Type]	4602 = 16#11FA	2010/3	78/01/03	190
5FBC/55	AO3t	[AO3 Type]	4603 = 16#11FB	2010/4	78/01/04	191
5FBC/56	AO1F	[AO1 Filter]	4611 = 16#1203	2010/C	78/01/0C	189
5FBC/57	AO2F	[AO2 Filter]	4612 = 16#1204	2010/D	78/01/0D	190
5FBC/58	AO3F	[AO3 Filter]	4613 = 16#1205	2010/E	78/01/0E	191
5FBC/59	UOL1	[AO1 min Output]	4621 = 16#120D	2010/16	78/01/16	189
5FBC/5A	UOL2	[AO2 min Output]	4622 = 16#120E	2010/17	78/01/17	190
5FBC/5B	UOL3	[AO3 min Output]	4623 = 16#120F	2010/18	78/01/18	191
5FBC/5C	UOH1	[AO1 max Output]	4631 = 16#1217	2010/20	78/01/20	189
5FBC/5D	UOH2	[AO2 max Output]	4632 = 16#1218	2010/21	78/01/21	190
5FBC/5E	UOH3	[AO3 max Output]	4633 = 16#1219	2010/22	78/01/22	191
5FBC/5F	AOL1	[AO1 min Output]	4641 = 16#1221	2010/2A	78/01/2A	188
5FBC/60	AOL2	[AO2 min Output]	4642 = 16#1222	2010/2B	78/01/2B	190
5FBC/61	AOL3	[AO3 min Output]	4643 = 16#1223	2010/2C	78/01/2C	191
5FBC/62	AOH1	[AO1 max Output]	4651 = 16#122B	2010/34	78/01/34	188
5FBC/63	AOH2	[AO2 max Output]	4652 = 16#122C	2010/35	78/01/35	190
5FBC/64	AOH3	[AO3 max Output]	4653 = 16#122D	2010/36	78/01/36	191
5FBC/67	r3	[R3 Assignment]	5003 = 16#138B	2014/4	7A/01/04	182
5FBC/68	r4	[R4 Assignment]	5004 = 16#138C	2014/5	7A/01/05	183
5FBC/6A	LO2	[LO2 assignment]	5010 = 16#1392	2014/B	7A/01/0B	185
5FBC/6B	LO3	[LO3 assignment]	5011 = 16#1393	2014/C	7A/01/0C	186
5FBC/6C	LO4	[LO4 assignment]	5012 = 16#1394	2014/D	7A/01/0D	187
5FBC/6D	AO1	[AO1 assignment]	5021 = 16#139D	2014/16	7A/01/16	188

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBC/6E	AO2	[AO2 assignment]	5022 = 16#139E	2014/17	7A/01/17	190
5FBC/6F	AO3	[AO3 assignment]	5023 = 16#139F	2014/18	7A/01/18	191
5FBC/70	AIC1	[AI net. channel]	5282 = 16#14A2	2016/53	7B/01/53	230
5FBC/73	EnU	[Encoder usage]	5606 = 16#15E6	201A/7	7D/01/07	160
5FBC/73	EnU	[Encoder usage]	5606 = 16#15E6	201A/7	7D/01/07	177
5FBC/74	ECC	[Encoder coupling]	5607 = 16#15E7	201A/8	7D/01/08	276
5FBC/75	EnS	[Encoder type]	5608 = 16#15E8	201A/A	7D/01/09	159
5FBC/75	EnS	[Encoder type]	5608 = 16#15E8	201A/9	7D/01/09	177
5FBC/76	Pdl	Drive encoder counter divisor	5610 = 16#15EA	201A/B	7D/01/0B	131
5FBC/8C	OHL	[Overtemp fault mgt]	7008 = 16#1B60	2028/9	84/01/09	267
5FBC/8D	OLL	[Overload fault mgt]	7009 = 16#1B61	2028/A	84/01/0A	265
5FBC/8E	SLL	[Modbus fault mgt]	7010 = 16#1B62	2028/B	84/01/0B	275
5FBC/8F	COL	[CANopen fault mgt]	7011 = 16#1B63	2028/C	84/01/0C	275
5FBC/90	tnL	[Autotune fault mgt]	7012 = 16#1B64	2028/D	84/01/0D	279
5FBC/93	CLL	[Network fault mgt]	7015 = 16#1B67	2028/10	84/01/10	275
5FBC/96	tAr	[Max. restart time]	7123 = 16#1BD3	2029/18	84/01/7C	262
5FBC/97	rSF	[Fault reset]	7124 = 16#1BD4	2029/19	84/01/7D	261
5FBC/98	Inh	[Fault inhibit assign.]	7125 = 16#1BD5	2029/1A	84/01/7E	274
5FBC/99	EtF	[External fault ass.]	7131 = 16#1BDB	2029/20	84/01/84	269
5FBC/9A	SCS	Save configuration	8001 = 16#1F41	2032/2	9C/01/9A	69
5FBC/9B	FCS	Restore configuration	8002 = 16#1F42	2032/3	9C/01/9B	69
5FBC/9C	CHA1	Assignment for 2 sets	8021 = 16#1F55	2032/16	9C/01/9C	70
5FBC/9C	CnF1	[2 Configurations]	8021 = 16#1F55	2032/16	89/01/16	251
5FBC/9C	CNF1	Assignment for 2 configurations	8021 = 16#1F55	2032/16	9C/01/9C	67
5FBC/9D	CHA2	Assignment for 3 sets	8022 = 16#1F56	2032/17	9C/01/9D	70
5FBC/9D	CnF2	[3 Configurations]	8022 = 16#1F56	2032/17	89/01/17	252
5FBC/9D	CNF2	Assignment for 3 configurations	8022 = 16#1F56	2032/17	9C/01/9D	67
5FBC/9E	CHM	[Multimotors]	8025 = 16#1F59	2032/1A	89/01/1A	251
5FBC/9F	CHCF	[Profile]	8401 = 16#20D1	2036/2	8B/01/02	193
5FBC/A0	COP	[Copy channel 1 <-> 2]	8402 = 16#20D2	2036/3	8B/01/03	195
5FBC/A2	rFC	[Ref. 2 switching]	8411 = 16#20DB	2036/C	8B/01/0C	195

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBC/A3	rCb	[Ref.1B switching]	8412 = 16#20DC	2036/D	8B/01/0D	198
5FBC/A4	Fr1	[Ref.1 channel]	8413 = 16#20DD	2036/E	8B/01/0E	193
5FBC/A5	Fr2	[Ref.2 channel]	8414 = 16#20DE	2036/F	8B/01/0F	195
5FBC/A6	Fr1b	[Ref.1B channel]	8415 = 16#20DF	2036/10	8B/01/10	198
5FBC/A7	CCS	[Cmd switching]	8421 = 16#20D1	2036/16	8B/01/16	194
5FBC/A8	Cd1	[Cmd channel 1]	8423 = 16#20E7	2036/18	8B/01/18	194
5FBC/A9	Cd2	[Cmd channel 2]	8424 = 16#20E8	2036/19	8B/01/19	194
5FBC/AD	SPM	[Ref. memo ass.]	8491 = 16#212B	2036/5C	8B/01/5C	218
5FBC/B3	rPS	[Ramp switching]	9010 = 16#2332	203C/B	8E/01/0B	203
5FBC/B5	LC2	[Current limit 2]	9202 = 16#23F2	203E/3	8F/01/03	243
5FBC/B6	tLA	[AI torque limit. activ.]	9210 = 16#23FA	203E/B	8F/01/0B	241
5FBC/B7	tLC	[Analog limit. act.]	9213 = 16#23FD	203E/E	8F/01/0E	242
5FBC/B8	tAA	[Torque ref. assign.]	9214 = 16#23FE	203E/F	8F/01/0F	242
5FBC/B9	tSS	[Trq/spd switching]	9220 = 16#2404	203E/15	8F/01/15	238
5FBC/BA	tr1	[Torque ref. channel]	9221 = 16#2405	203E/16	8F/01/16	238
5FBC/BB	tSd	[Torque ref. sign]	9222 = 16#2406	203E/17	8F/01/17	239
5FBC/BC	tSt	[Torque control stop]	9227 = 16#240B	203E/1C	8F/01/1C	239
5FBC/BD	tOb	[Torq. ctrl fault mgt]	9228 = 16#240C	203E/1D	8F/01/1D	240
5FBC/BE	SSb	[Trq/l limit. stop]	9240 = 16#2418	203E/29	8F/01/29	277
5FBC/EF	tUL	[Auto-tune assign.]	9610 = 16#258A	2042/B	91/01/0B	253
5FBC/F1	bLC	[Brake assignment]	10001 = 16#2711	2046/2	93/01/02	221
5FBC/F2	bCI	[Brake contact]	10009 = 16#2719	2046/A	93/01/0A	221
5FBC/F3 (bit 0)	brH0	[BRH b0]	10050 = 16# 2742 (bit 0)	2046/33 (bit 0)	93/01/33 (bit 0)	224
5FBC/F3 (bit 1)	brH1	[BRH b1]	10050 = 16# 2742 (bit 1)	2046/33 (bit 1)	93/01/33 (bit 1)	224
5FBC/F3 (bit 2)	brH2	[BRH b2]	10050 = 16# 2742 (bit 2)	2046/33 (bit 2)	93/01/33 (bit 2)	224
5FBC/F3 (bit 3)	brH3	[BRH b3]	10050 = 16# 2742 (bit 3)	2046/33 (bit 3)	93/01/33 (bit 3)	224
5FBC/F3 (bit 4)	brH4	[BRH b4]	10050 = 16# 2742 (bit 4)	2046/33 (bit 4)	93/01/33 (bit 4)	225
5FBC/F4	PES	[Weight sensor ass.]	10070 = 16#2756	2046/47	93/01/47	226
5FBC/F5	LP1	[Point 1 X]	10071 = 16#2757	2046/48	93/01/48	226

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBC/F6	CP1	[Point 1Y]	10072 = 16#2758	2046/49	93/01/49	226
5FBC/F7	LP2	[Point 2 X]	10073 = 16#2759	2046/4A	93/01/4A	226
5FBC/F8	CP2	[Point 2Y]	10074 = 16#275A	2046/4B	93/01/4B	227
5FBC/F9	IbrA	[Ibr 4-20 mA loss]	10075 = 16#275B	2046/4C	93/01/4C	227
5FBC/FE	rrS	[Reverse assign.]	11105 = 16#2B61	2051/6	98/01/6A	166
5FBC/FF	JOG	[JOG]	11110 = 16#2B66	2051/B	98/01/6F	209
5FBD/02	nSt	[Freewheel stop ass.]	11202 = 16#2BC2	2052/3	99/01/03	205
5FBD/03	dCI	[DC injection assign.]	11203 = 16#2BC3	2052/4	99/01/04	206
5FBD/04	FSt	[Fast stop assign.]	11204 = 16#2BC4	2052/5	99/01/05	205
5FBD/06	PS2	[2 preset speeds]	11401 = 16#2C89	2054/2	9A/01/02	210
5FBD/07	PS4	[4 preset speeds]	11402 = 16#2C8A	2054/3	9D/01/07	210
5FBD/08	PS8	[8 preset speeds]	11403 = 16#2C8B	2054/4	9A/01/04	211
5FBD/09	PS16	[16 preset speeds]	11404 = 16#2C8C	2054/5	9A/01/05	211
5FBD/0A	USP	[+ speed assignment]	11501 = 16#2CED	2055/2	9A/01/66	215
5FBD/0B	dSP	[-Speed assignment]	11502 = 16#2CEE	2055/3	9A/01/67	215
5FBD/0C	Str	[Reference saved]	11503 = 16#2CEF	2055/4	9A/01/68	215
5FBD/0E	USI	[+ speed assignment]	11520 = 16#2D00	2055/15	9D/01/0E	216
5FBD/0F	dSI	[-Speed assignment]	11521 = 16#2D01	2055/16	9A/01/7A	216
5FBD/10	LAF	[Stop FW limit sw.]	11601 = 16#2D51	2056/2	9B/01/02	220
5FBD/11	LAx	[Stop RV limit sw.]	11602 = 16#2D52	2056/3	9B/01/03	220
5FBD/12	LAS	[Stop type]	11603 = 16#2D53	2056/4	9B/01/04	220
5FBD/13	SA2	[Summing ref. 2]	11801 = 16#2E19	2058/2	9C/01/02	199
5FBD/14	SA3	[Summing ref. 3]	11802 = 16#2E1A	2058/3	9C/01/03	199
5FBD/15	da2	[Subtract ref. 2]	11811 = 16#2E23	2058/C	9C/01/0C	199
5FBD/16	da3	[Subtract ref. 3]	11812 = 16#2E24	2058/D	9C/01/0D	199
5FBD/17	MA2	[Multiplier ref. 2]	11821 = 16#2E2D	2058/16	9C/01/16	200
5FBD/18	MA3	[Multiplier ref. 3]	11822 = 16#2E2E	2058/17	9C/01/17	200
5FBD/19	PIF	[PID feedback ass.]	11901 = 16#2E7D	2059/2	9C/01/66	230
5FBD/1A	PII	[Act. internal PID ref.]	11908 = 16#2E84	2059/9	9C/01/6D	231
5FBD/1B	Pr2	[2 preset PID ref.]	11909 = 16#2E85	2059/A	9C/01/6E	236
5FBD/1C	Pr4	[4 preset PID ref.]	11910 = 16#2E86	2059/B	9C/01/6F	236

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBD/1E	PIS	[PID integral reset]	11944 = 16#2EA8	2059/2D	9C/01/91	234
5FBD/1F	FPI	[Speed ref. assign.]	11950 = 16#2EAE	2059/33	9C/01/97	234
5FBD/20	PIM	[Manual reference]	11954 = 16#2EB2	2059/37	9C/01/9B	235
5FBD/21	rSL	[PID wake up thresh.]	11960 = 16#2DB5	2059/3D	9C/01/A1	235
5FBD/22	PAU	[Auto/Manual assign.]	11970 = 16#2EC2	2059/47	9C/01/AB	235
5FBD/23	EnA	[ENA system]	12101 = 16#2F45	205B/2	9D/01/66	161
5FBD/24	trC	[Yarn control]	12201 = 16#2FA9	205C/2	9E/01/02	254
5FBD/25	rtr	[Init. traverse ctrl]	12210 = 16#2FB2	205C/B	9E/01/0B	257
5FBD/26	SnC	[Counter wobble]	12212 = 16#2FB4	205C/D	9E/01/0D	256
5FBD/27	EbO	[End reel]	12213 = 16#2FB5	205C/E	9E/01/0E	255
5FBD/28	tSY	[Sync. wobble]	12214 = 16#2FB6	205C/F	9E/01/0F	256
5FBD/29	HSO	[High speed hoisting]	12301 = 16#300D	205D/2	9E/01/66	228
5FBD/2A	U0	[U0]	12401 = 16#3071	205E/2	9F/01/02	150
5FBD/2B	U1	[U1]	12403 = 16#3073	205E/4	9F/01/04	151
5FBD/2C	F1	[F1]	12404 = 16#3074	205E/5	9F/01/05	151
5FBD/2D	U2	[U2]	12405 = 16#3075	205E/6	9F/01/06	151
5FBD/2E	F2	[F2]	12406 = 16#3076	205E/7	9F/01/07	151
5FBD/2F	U3	[U3]	12407 = 16#3077	205E/8	9F/01/08	151
5FBD/30	F3	[F3]	12408 = 16#3078	205E/9	9F/01/09	152
5FBD/31	U4	[U4]	12409 = 16#3079	205E/9	9F/01/0A	152
5FBD/32	F4	[F4]	12410 = 16#307A	205E/B	9F/01/0B	152
5FBD/33	U5	[U5]	12411 = 16#307B	205E/C	9F/01/0C	152
5FBD/34	F5	[F5]	12412 = 16#307C	205E/D	9F/01/0D	152
5FBD/35	SAF	[Stop FW limit sw.]	12501 = 16#30D5	205F/2	9F/01/66	247
5FBD/36	SAr	[Stop RV limit sw.]	12502 = 16#30D6	205F/3	9F/01/67	247
5FBD/37	dAF	[Slowdown forward]	12503 = 16#30D7	205F/4	9F/01/68	248
5FBD/38	dAr	[Slowdown reverse]	12504 = 16#30D8	205F/5	9F/01/69	248
5FBD/39	dSF	[Deceleration type]	12505 = 16#30D9	205F/6	9F/01/6A	249
5FBD/3A	PAS	[Stop type]	12506 = 16#30DA	205F/7	9F/01/6B	249
5FBD/3B	CLS	[Disable limit sw.]	12507 = 16#30DB	205F/8	9F/01/6C	248
5FBD/3C	SUL	[Motor surge limit.]	12601 = 16#3139	2060/2	A0/01/02	162

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBD/3D	SOP	[Volt surge limit. opt]	12602 = 16#313A	2060/3	A0/01/03	163
5FBD/3E	nMA1	Communication scanner, address of read word 1	12701 = 16#319D	2061/2	A0/01/66	129
5FBD/3F	nMA2	Communication scanner, address of read word 2	12702 = 16#319E	2061/3	A0/01/67	129
5FBD/40	nMA3	Communication scanner, address of read word 3	12703 = 16#319F	2061/4	A0/01/68	129
5FBD/41	nMA4	Communication scanner, address of read word 4	12704 = 16#31A0	2061/5	A0/01/69	129
5FBD/42	nMA5	Communication scanner, address of read word 5	12705 = 16#31A1	2061/6	A0/01/6A	130
5FBD/43	nMA6	Communication scanner, address of read word 6	12706 = 16#31A2	2061/7	A0/01/6B	130
5FBD/44	nMA7	Communication scanner, address of read word 7	12707 = 16#31A3	2061/8	A0/01/6C	130
5FBD/45	nMA8	Communication scanner, address of read word 8	12708 = 16#31A4	2061/9	A0/01/6D	130
5FBD/46	nCA1	Communication scanner, address of write word 1	12721 = 16#31B1	2061/16	A0/01/7A	128
5FBD/47	nCA2	Communication scanner, address of write word 2	12722 = 16#31B2	2061/17	A0/01/7B	128
5FBD/48	nCA3	Communication scanner, address of write word 3	12723 = 16#31B3	2061/18	A0/01/7C	128
5FBD/49	nCA4	Communication scanner, address of write word 4	12724 = 16#31B4	2061/19	A0/01/7D	128
5FBD/4A	nCA5	Communication scanner, address of write word 5	12725 = 16#31B5	2061/1A	A0/01/7E	128
5FBD/4B	nCA6	Communication scanner, address of write word 6	12726 = 16#31B6	2061/1B	A0/01/7F	129
5FBD/4C	nCA7	Communication scanner, address of write word 7	12727 = 16#31B7	2061/1C	A0/01/80	129
5FBD/4D	nCA8	Communication scanner, address of write word 8	12728 = 16#31B8	2061/1D	A0/01/81	129
5FBD/54	CHA1	[2 Parameter sets]	12902 = 16#3266	2063/3	A1/01/67	250
5FBD/55	CHA2	[3 Parameter sets]	12903 = 16#3267	2063/4	A1/01/68	250
5FBD/56	rCA	[Output contact. fdbk]	13103 = 16#332F	2065/4	A2/01/68	245
5FBD/57	OCC	[Out. contactor ass.]	13104 = 16#3330	2065/5	A2/01/69	245
5FBD/58	PGA	[Reference type]	13301 = 16#33F5	2067/2	A3/01/66	177
5FBD/59	PIL	[RP min value]	13302 = 16#33F6	2067/3	A3/01/67	176

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBD/5A	PFr	[RP max value]	13303 = 16#33F7	2067/4	A3/01/68	176
5FBD/5B	PFI	[RP filter]	13304 = 16#33F8	2067/5	A3/01/69	176
5FBD/5C	EIL	[Freq. min value]	13310 = 16#33FE	2067/B	A3/01/6F	178
5FBD/5D	EFr	[Freq. max value]	13311 = 16#33FF	2067/C	A3/01/70	178
5FBD/5E	EFI	[Freq. signal filter]	13312 = 16#3400	2067/D	A3/01/71	178
5FBD/5F	PHr	[Output Ph rotation]	13401 = 16#3459	2068/2	A4/01/02	150
5FBD/60	FN1	[F1 key assignment]	13501 = 16#34BD	2069/2	A4/01/66	196
5FBD/61	FN2	[F2 key assignment]	13502 = 16#34BE	2069/3	A4/01/67	196
5FBD/62	FN3	[F3 key assignment]	13503 = 16#34BF	2069/4	A4/01/68	196
5FBD/63	FN4	[F4 key assignment]	13504 = 16#34C0	2069/5	A4/01/69	197
5FBD/6C	bMP	[HMI cmd.]	13529 = 16#34D9	2069/1E	A4/01/82	197
5FBD/6D	LES	[Drive lock]	13601 = 16#3521	206A/2	A5/01/02	244
5FBD/6E	LLC	[Line contactor ass.]	13602 = 16#3522	206A/3	A5/01/03	244
5FBD/6F	LCT	[Mains V. time out]	13603 = 16#3523	206A/4	A5/01/04	244
5FBD/70	UrES	[Evacuation Input V.]	13801 = 16#35E9	206C/2	A6/01/02	270
5FBD/71	USL	[Undervoltage level]	13802 = 16#35EA	206C/3	A6/01/03	270
5FBD/72	USb	[UnderV. fault mgt]	13803 = 16#35EB	206C/4	A6/01/04	270
5FBD/73	USt	[Undervolt. time out]	13804 = 16#35EC	206C/5	A6/01/05	270
5FBD/74	UPL	[Prevention level]	13811 = 16#35F3	206C/C	A6/01/0C	271
5FBD/76	rFt	[Evacuation assign.]	13831 = 16#3607	206C/20	A6/01/20	258
5FBD/77	rSU	[Evacuation Input V.]	13832 = 16#3608	206C/21	A6/01/21	258
5FBD/78	dCO	[Precharge cont. ass.]	13841 = 16#3611	206C/2A	A6/01/2A	259
5FBD/79	FLI	[Fluxing assignment]	13901 = 16#364D	206D/2	A6/01/66	219
5FBD/7A	PPI	[Pairing password]	14001 = 16#36B1	206E/2	A7/01/02	279
5FBD/7B	bbA	[Braking balance]	14102 = 16#3716	206F/3	A7/01/67	164
5FBD/7C	brO	[DB res. protection]	14111 = 16#371F	206F/C	A7/01/70	278
5FBD/7D	brP	[DB Resistor Power]	14112 = 16#3720	206F/D	A7/01/71	278
5FBD/7E	brU	[DB Resistor value]	14113 = 16#3721	206F/E	A7/01/72	278
5FBD/7F	tHb	DBR thermal state	14114 = 16#3722	206F/F	A7/01/73	92
5FBD/80	UC2	[Vector Control 2pt]	14201 = 16#3779	2070/2	A8/01/02	153
5FBD/81	UCP	[V. constant power]	14202 = 16#377A	2070/3	A8/01/03	153

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBD/82	FCP	[Freq. Const Power]	14203 = 16#377B	2070/4	A8/01/04	153
5FBD/83	LbA	[Load sharing]	14301 = 16#37DD	2071/2	A8/01/66	164
5FBD/88	PSt	[Stop Key priority]	64002 = 16#FA02	2262/3	94/01/06	193
5FBF/02	SFr	[Switching freq.]	3102 = 16#C1E	2001/3	70/01/67	162
5FBF/06	UCb	[Sensitivity]	3111 = 16#C27	2001/C	70/01/70	263
5FBF/07	ECt	[Encoder check time]	5609 = 16#15E9	201A/A	7D/01/0A	276
5FBF/08	Odt	[OutPh time detect]	7081 = 16#1BA9	2028/52	84/01/52	266
5FBF/1E	ACC	[Acceleration]	9001 = 16#2329	203C/2	8E/01/02	201
5FBF/1F	dEC	[Deceleration]	9002 = 16#232A	203C/3	8E/01/03	201
5FBF/24	AC2	[Acceleration 2]	9012 = 16#2334	203C/D	8E/01/0D	203
5FBF/24	AC2	[Acceleration 2]	9012 = 16#2334	203C/D	8E/01/0D	217
5FBF/25	dE2	[Deceleration 2]	9013 = 16#2335	203C/E	8E/01/0E	203
5FBF/25	dE2	[Deceleration 2]	9013 = 16#2335	203C/E	8E/01/0E	217
5FBF/26	Inr	[Ramp increment]	9020 = 16#233C	203C/15	8E/01/15	201
5FBF/28	SlT	[Speed time integral]	9104 = 16#2390	203D/5	8E/01/69	146
5FBF/29	SFC	[K speed loop filter]	9105 = 16#2391	203D/6	8E/01/6A	145
5FBF/2B	CL2	[I Limit. 2 value]	9203 = 16#23F3	203E/4	8F/01/04	243
5FBF/2C	tLIM	[Motoring torque lim]	9211 = 16#23FB	203E/C	8F/01/0C	241
5FBF/2D	tLIG	[Gen. torque lim]	9212 = 16#23FC	203E/D	8F/01/0D	241
5FBF/2E	dbn	[Negative deadband]	9223 = 16#2407	203E/18	8F/01/18	240
5FBF/2F	dbP	[Positive deadband]	9224 = 16#2408	203E/19	8F/01/19	240
5FBF/30	trt	[Torque ratio]	9225 = 16#2409	203E/1A	8F/01/1A	239
5FBF/31	trP	[Torque ramp time]	9226 = 16#240A	203E/1B	8F/01/1B	239
5FBF/32	rtO	[Torque R. time out]	9229 = 16#240D	203E/1E	8F/01/1E	240
5FBF/33	SPt	[Spin time]	9230 = 16#240E	203E/1F	8F/01/1F	240
5FBF/34	StO	[Trq/I limit. time out]	9241 = 16#2419	203E/2A	8F/01/2A	277
5FBF/35	Int	[Torque unit]	9260 = 16#242C	203E/3D	8F/01/3D	238
5FBF/36	bEn	[Brake engage freq]	10003 = 16#2713	2046/4	93/01/04	222
5FBF/3B	bSt	[Movement type]	10008 = 16#2718	2046/9	93/01/09	221
5FBF/3C	tbE	[Brake engage delay]	10010 = 16#271A	2046/B	93/01/0B	223
5FBF/3D	Ird	[Brake release I Rev]	10011 = 16#271B	2046/C	93/01/0C	222

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBF/3F	JdC	[Jump at reversal]	10013 = 16#271D	2046/E	93/01/0E	223
5FBF/40	brr	[Current ramp time]	10015 = 16#271F	2046/10	93/01/10	225
5FBF/41	bEd	[Engage at reversal]	10020 = 16#2724	2046/15	93/01/15	223
5FBF/42	ttr	[Time to restart]	10022 = 16#2726	2046/17	93/01/17	224
5FBF/44	tdC1	[Auto DC inj. time 1]	10402 = 16#28A2	204A/3	95/01/03	208
5FBF/45	SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	204A/4	95/01/04	208
5FBF/45	SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	204A/4	95/01/04	223
5FBF/46	tdC2	[Auto DC inj. time 2]	10404 = 16#28A4	204A/5	95/01/05	208
5FBF/47	SdC2	[Auto DC inj. level 2]	10405 = 16#28A5	204A/6	95/01/06	208
5FBF/4C	ttd2	[Motor2 therm. level]	11006 = 16#2AFE	2050/7	98/01/07	264
5FBF/4C	ttd2	[Motor2 therm. level]	11006 = 16#2AFE	2050/7	98/01/07	268
5FBF/4D	ttd3	[Motor3 therm. level]	11007 = 16#2AFF	2050/8	98/01/08	264
5FBF/4D	ttd3	[Motor3 therm. level]	11007 = 16#2AFF	2050/8	98/01/08	268
5FBF/4E	tHA	[Drv therm. state al]	11009 = 16#2B01	2050/A	98/01/0A	267
5FBF/4E	tHA	[Drv therm. state al]	11009 = 16#2B01	2050/A	98/01/0A	268
5FBF/50	JGF	[Jog frequency]	11111 = 16#2B67	2051/C	98/01/70	209
5FBF/53	tdC	[DC injection time 2]	11211 = 16#2BCB	2052/C	99/01/0C	207
5FBF/53	tdC	[DC injection time 2]	11211 = 16#2BCB	2052/C	99/01/0C	281
5FBF/54	IdC2	[DC inject. level 2]	11212 = 16#2BCC	2052/D	99/01/0D	207
5FBF/54	IdC2	[DC inject. level 2]	11212 = 16#2BCC	2052/D	99/01/0D	281
5FBF/55	tdI	[DC injection time 1]	11213 = 16#2BCD	2052/E	99/01/0E	206
5FBF/55	tdI	[DC injection time 1]	11213 = 16#2BCD	2052/E	99/01/0E	281
5FBF/5F	SP9	[Preset speed 9]	11417 = 16#2C99	2054/12	9A/01/12	213
5FBF/60	SP10	[Preset speed 10]	11418 = 16#2C9A	2054/13	9A/01/13	213
5FBF/61	SP11	[Preset speed 11]	11419 = 16#2C9B	2054/14	9A/01/14	213
5FBF/62	SP12	[Preset speed 12]	11420 = 16#2C9C	2054/15	9A/01/15	213
5FBF/64	SP14	[Preset speed 14]	11422 = 16#2C9E	2054/17	9A/01/17	214
5FBF/65	SP15	[Preset speed 15]	11423 = 16#2C9F	2054/18	9A/01/18	214
5FBF/66	SP16	[Preset speed 16]	11424 = 16#2CA0	2054/19	9A/01/19	214
5FBF/6A	PIF1	[Min PID feedback]	11904 = 16#2E80	2059/5	9C/01/69	230
5FBF/6B	PIF2	[Max PID feedback]	11905 = 16#2E81	2059/6	9C/01/6A	230

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBF/6C	PIP1	[Min PID reference]	11906 = 16#2E82	2059/7	9C/01/6B	231
5FBF/6D	PIP2	[Max PID reference]	11907 = 16#2E83	2059/8	9C/01/6C	231
5FBF/6E	RPI	[Internal PID ref.]	11920 = 16#2E90	2059/15	9C/01/79	231
5FBF/6F	rP2	[Preset ref. PID 2]	11921 = 16#2E91	2059/16	9C/01/7A	237
5FBF/70	rP3	[Preset ref. PID 3]	11922 = 16#2E92	2059/17	9C/01/7B	237
5FBF/76	POL	[Min PID output]	11952 = 16#2EB0	2059/35	9C/01/99	232
5FBF/77	POH	[Max PID output]	11953 = 16#2EB1	2059/36	9C/01/9A	233
5FBF/7B	PrP	[PID ramp]	11984 = 16#2ED0	2059/55	9C/01/B9	232
5FBF/7C	GPE	[ENA prop.gain]	12103 = 16#2F47	205B/4	9D/01/68	161
5FBF/7D	GIE	[ENA integral gain]	12104 = 16#2F48	205B/5	9D/01/69	161
5FBF/7E	rAP	[Reduction ratio]	12105 = 16#2F49	205B/6	9D/01/6A	161
5FBF/7F	trH	[Traverse high]	12202 = 16#2FAA	205C/3	9E/01/03	254
5FBF/80	trL	[Traverse Low]	12203 = 16#2FAB	205C/4	9E/01/04	254
5FBF/81	qSH	[Quick step High]	12204 = 16#2FAC	205C/5	9E/01/05	254
5FBF/82	qSL	[Quick step Low]	12205 = 16#2FAD	205C/6	9E/01/06	254
5FBF/83	tUP	[Traverse ctrl. accel.]	12206 = 16#2FAE	205C/7	9E/01/07	255
5FBF/84	tdn	[Traverse ctrl. decel]	12207 = 16#2FAF	205C/8	9E/01/08	255
5FBF/85	tbO	[Reel time]	12208 = 16#2FAB	205C/9	9E/01/09	255
5FBF/86	dtF	[Decrease ref. speed]	12211 = 16#2FB3	205C/C	9E/01/0C	256
5FBF/87	CLO	[High speed l Limit]	12302 = 16#300E	205D/3	9E/01/67	229
5FBF/88	COF	[Motor speed coeff.]	12303 = 16#300F	205D/4	9E/01/68	228
5FBF/89	COr	[Gen. speed coeff]	12304 = 16#3010	205D/5	9E/01/69	228
5FBF/8A	OSP	[Measurement spd]	12305 = 16#3011	205D/6	9E/01/6A	228
5FBF/8B	SCL	[l Limit. frequency]	12306 = 16#3012	205D/7	9E/01/6B	229
5FBF/8C	tOS	[Load measuring tm.]	12307 = 16#3013	205D/8	9E/01/6C	228
5FBF/CB	PtC1	[PTC1 probe]	13201 = 16#3391	2066/2	A3/01/02	260
5FBF/CC	PtC2	[PTC2 probe]	13202 = 16#3392	2066/3	A3/01/03	260
5FBF/CD	PtCL	[L16 = PTC probe]	13203 = 16#3396	2066/4	A3/01/04	260
5FBF/CE	tbS	[DC bus maintain tm]	13812 = 16#35F4	206C/D	A6/01/0D	271
5FBF/CF	tSM	[UnderV. restart tm]	13813 = 16#35F5	206C/E	A6/01/0E	271
5FBF/D0	StM	[Max stop time]	13814 = 16#35EC	206C/F	A6/01/0F	271

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
5FBF/D4	Ubr	[Braking level]	14101 = 16#3715	206F/2	A7/01/66	164
5FBF/D5	LbC	[Load correction]	14302 = 16#37DE	2071/3	A8/01/67	164
5FBF/D6	LbC1	[Correction min spd]	14303 = 16#37DF	2071/4	A8/01/68	165
5FBF/D7	LbC2	[Correction max spd]	14304 = 16#37E0	2071/5	A8/01/69	165
5FBF/D8	LbC3	[Torque offset]	14305 = 16#37E1	2071/6	A8/01/6A	165
5FBF/D9	LbF	[Sharing filter]	14306 = 16#37E2	2071/7	A8/01/6B	165
5FC2/06	Ctt	[Motor control type]	9607 = 16#2587	2042/8	91/01/08	150
5FC2/08	tUS	[Auto tuning status]	9609 = 16#2589	2042/A	91/01/0A	150
5FC2/0A	nPr	[Rated motor power]	9613 = 16#258D	2042/E	91/01/0E	148
5FC2/0B	AUt	[Automatic autotune]	9615 = 16#258F	2042/10	91/01/10	149
5FC2/0E	UFr	[IIR compensation]	9623 = 16#2597	2042/18	91/01/18	156
5FC2/11	rSA	[R1w]	9642 = 16#25AA	2042/2B	91/01/2B	158
5FC2/15	IdA	[Idw]	9652 = 16#25B4	2042/35	91/01/35	158
5FC2/19	LFA	[Lfw]	9662 = 16#25BE	2042/3F	91/01/3F	158
5FC2/1C	trA	[T2w]	9667 = 16#25C3	2042/44	91/01/44	158
5FC2/1E	nCrS	[Nominal I sync.]	9670 = 16#25C6	2042/47	91/01/47	154
5FC2/1F	nSPS	[Nom motor spdsync]	9671 = 16#25C7	2042/48	91/01/48	154
5FC2/20	PPnS	[Pole pairs]	9672 = 16#25C8	2042/49	91/01/49	154
5FC2/21	PHS	[Syn. EMF constant]	9673 = 16#25C9	2042/4A	91/01/4A	154
5FC2/22	LdS	[Autotune L d-axis]	9674 = 16#25CA	2042/4B	91/01/4B	154
5FC2/23	LqS	[Autotune L q-axis]	9675 = 16#25CB	2042/4C	91/01/4C	155
5FC2/24	FrSS	[Nominal freq sync.]	9679 = 16#25CF	2042/50	91/01/50	159
5FC2/26	rSAS	[Cust. stator R syn]	9682 = 16#25D2	2042/53	91/01/53	155
5FC8/32	O50	"Controller Inside" HMI exchange word 50	6450 = 16#1932	2022/33	81/01/33	139
6048/1	SPAL	Acceleration speed delta	8611 = 16#21A3	6048/1	8C/01/0C	140
6048/2	SPAt	Acceleration time delta	8613 = 16#21A5	6048/2	8C/01/0E	140
6049/1	SPdL	Deceleration speed delta	8614 = 16#21A6	6049/1	8C/01/0F	140
6049/2	SPdt	Deceleration time delta	8616 = 16#21A8	6049/2	8C/01/11	141
604B/1	SPFn	Speed setpoint factor numerator	8642 = 16#21C2	604B/1	8C/01/2B	141
604B/2	SPFd	Speed setpoint factor denominator	8643 = 16#21C3	604B/2	8C/01/2C	142
9F/01/63	SP13	[Preset speed 13]	11421 = 16#2C9D	2054/16	9A/01/16	214

Index of parameter INTERBUS addresses

INTERBUS index	Code	Name	Logic address	CANopen index	DeviceNet path	Page
9F/01/71	rP4	[Preset ref. PID 4]	11923 = 16#2E93	2059/18	9C/01/7C	237

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
2A/01/07	rFrd	Output speed	8604 = 16#219C	6044	6044	87
2A/01/08	LFRD	Speed reference	8602 = 16#219A	6042	6042	77
2A/01/09	LCr	Motor current	3204 = 16#0C84	2002/5	5FB9/06	88
2A/01/0A	ItH	[Mot. therm. current]	9622 = 16#2596	2042/17	5FB3/DB	145
2A/01/0B	Otrn	Output torque (Nm)	3216 = 16#0C90	2002/11	5FB9/10	88
2A/01/0C	LtCr	Torque reference (Nm)	9261 = 16#242D	203E/3E	5FB6/3D	77
2A/01/12	ACCd	ODVA acceleration time	-	-	-	143
2A/01/13	dECd	ODVA deceleration time	-	-	-	143
70/01/0C	nCV	Drive nominal rating	3011 = 16#0BC3	2000/C	5FB0/02	124
70/01/0D	UCAL	Drive line voltage	3012 = 16#0BC4	2000/D	5FB0/03	124
70/01/10	bFr	[Standard mot. freq.]	3015 = 16#BC7	2000/10	5FBC/05	148
70/01/12	InV	Rated drive current	3017 = 16#0BC9	2000/12	5FB0/07	124
70/01/35	CFG	[Macro configuration]	3052 = 16#BEC	2000/35	5FBC/07	144
70/01/36	CCFG	[Customized macro]	3053 = 16#BED	2000/36	5FB9/02	144
70/01/67	SFr	[Switching freq.]	3102 = 16#C1E	2001/3	5FBF/02	162
70/01/68	tFr	[Max frequency]	3103 = 16#C1F	2001/4	5FB3/74	149
70/01/69	HSP	[High speed]	3104 = 16#C20	2001/5	5FB3/75	145
70/01/6A	LSP	[Low speed]	3105 = 16#C21	2001/6	5FB3/76	145
70/01/6B	bSP	[Reference template]	3106 = 16#C22	2001/7	5FB3/77	168
70/01/6C	nrd	[Noise reduction]	3107 = 16#C23	2001/8	5FB3/78	162
70/01/6D	rIn	[RV Inhibition]	3108 = 16#C24	2001/9	5FB3/79	193
70/01/6E	OFI	[Sinus filter]	3109 = 16#C25	2001/A	5FBC/0B	162
70/01/6F	FLr	[Catch on the fly]	3110 = 16#C26	2001/B	5FB3/7A	263
70/01/70	UCb	[Sensitivity]	3111 = 16#C27	2001/C	5FBF/06	263
70/01/71	Strt	[IGBT test]	3112 = 16#C28	2001/D	5FBC/0D	272
71/01/02	ETA	Status word	8603 = 16#219B	6041	6041	79
71/01/03	rFr	Output frequency	3202 = 16#C82	2002/3	5FB9/04	87
71/01/04	FrH	Frequency reference before ramp	3203 = 16#0C83	2002/4	5FB9/05	89
71/01/06	Otr	Output torque	3205 = 16#0C85	6077	6077	87
71/01/07	ETI	Extended status word 0	3206 = 16#0C86	2002/7	5FB9/08	81

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
71/01/08	ULn	Power supply voltage	3207 = 16#0C87	2002/8	5FB9/09	92
71/01/09	UOP	Motor voltage	3208 = 16#0C88	2002/9	5FB9/0A	88
71/01/0A	tHd	Drive thermal state	3209 = 16#0C89	2002/A	5FB9/0B	92
71/01/0C	OPr	Motor power	3211 = 16#0C8B	2002/C	5FB9/0C	88
71/01/1F	APH	Energy consumption	3230 = 16#0C9E	2002/1F	5FB9/15	92
71/01/20	rtH	Total motor operating time	3231 = 16#0C9F	2002/20	5FB9/16	93
71/01/21	rtHI	Internal motor operating time	3232 = 16#0CA0	2002/21	5FB9/17	93
71/01/22	PtH	Total drive operating time	3233 = 16#0CA1	2002/22	5FB9/18	93
71/01/24	tAC	IGBT alarm time	3235 = 16#0CA3	2002/24	5FB9/1A	93
71/01/33	LRS1	Extended status word 1	3250 = 16#0CB2	2002/33	5FB9/1C	81
71/01/34	LRS2	Extended status word 2	3251 = 16#0CB3	2002/34	5FB9/1D	82
71/01/35	LRS3	Extended status word 3	3252 = 16#0CB4	2002/35	5FB9/1E	82
71/01/36	LRS4	Extended status word 4	3253 = 16#0CB5	2002/36	5FB9/1F	83
71/01/37	LRS5	Extended status word 5	3254 = 16#0CB6	2002/37	5FB9/20	83
71/01/38	LRS6	Extended status word 6	3255 = 16#0CB7	2002/38	5FB9/21	84
71/01/39	LRS7	Extended status word 7	3256 = 16#0CB8	2002/39	5FB9/22	84
71/01/3A	LRS8	Extended status word 8	3257 = 16#0C89	2002/3A	5FB9/23	85
71/01/67	UdP	Drive software version	3302 = 16#0CE6	2003/3	5FB0/0D	124
71/01/8D	PAn0	Device name: characters 1 and 2	3340 = 16#0D0C	2003/29	5FB0/25	124
71/01/8E	PAn1	Device name: characters 3 and 4	3341 = 16#0D0D	2003/2A	5FB0/26	124
71/01/8F	PAn2	Device name: characters 5 and 6	3342 = 16#0D0E	2003/2B	5FB0/27	125
71/01/90	PAn3	Device name: characters 7 and 8	3343 = 16#0D0F	2003/2C	5FB0/28	125
71/01/91	PAn4	Device name: characters 9 and 10	3344 = 16#0D10	2003/2D	5FB0/29	125
71/01/92	PAn5	Device name: characters 11 and 12	3345 = 16#0D11	2003/2E	5FB0/2A	125
71/01/93	PAn6	Device name: characters 13 and 14	3346 = 16#0D12	2003/2F	5FB0/2B	125
71/01/94	PAn7	Device name: characters 15 and 16	3346 = 16#0D12	2003/30	5FB0/2C	125
75/01/02	L1d	[L1 On Delay]	4001 = 16#FA1	200A/2	5FBC/0E	167
75/01/03	L2d	[L2 On Delay]	4002 = 16#FA2	200A/3	5FBC/0F	167
75/01/04	L3d	[L3 On Delay]	4003 = 16#FA3	200A/4	5FBC/10	167
75/01/05	L4d	[L4 On Delay]	4004 = 16#FA4	200A/5	5FBC/11	167
75/01/06	L5d	[L5 On Delay]	4005 = 16#FA5	200A/6	5FBC/12	167

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
75/01/07	L6d	[LI6 On Delay]	4006 = 16#FA6	200A/7	5FBC/13	167
75/01/08	L7d	[LI7 On Delay]	4007 = 16#FA7	200A/8	5FBC/14	167
75/01/09	L8d	[LI8 On Delay]	4008 = 16#FA8	200A/9	5FBC/15	167
75/01/0A	L9d	[LI9 On Delay]	4009 = 16#FA9	200A/A	5FBC/16	167
75/01/0B	L10d	[LI10 On Delay]	4010 = 16#FAA	200A/B	5FBC/17	167
75/01/0C	L11d	[LI11 On Delay]	4011 = 16#FAB	200A/C	5FBC/18	167
75/01/0D	L12d	[LI12 On Delay]	4012 = 16#FAC	200A/D	5FBC/19	167
75/01/0E	L13d	[LI13 On Delay]	4013 = 16#FAD	200A/E	5FBC/1A	167
75/01/0F	L14d	[LI14 On Delay]	4014 = 16#FAE	200A/F	5FBC/1B	167
76/01/02	r1S	[R1 Active at]	4201 = 16#1069	200C/2	5FBC/1C	180
76/01/03	r2S	[R2 Active at]	4202 = 16#106A	200C/3	5FBC/1D	181
76/01/04	r3S	[R3 Active at]	4203 = 16#106B	200C/4	5FBC/1E	182
76/01/05	r4S	[R4 Active at]	4204 = 16#106C	200C/5	5FBC/1F	183
76/01/0A	LO1S	[LO1 active at]	4209 = 16#1071	200C/2D	5FBC/20	184
76/01/0B	LO2S	[LO2 active at]	4210 = 16#1072	200C/B	5FBC/21	185
76/01/0C	LO3S	[LO3 active at]	4211 = 16#1073	200C/C	5FBC/22	186
76/01/0D	LO4S	[LO4 active at]	4212 = 16#1074	200C/D	5FBC/23	187
76/01/16	r1H	[R1 Holding time]	4221 = 16#107D	200C/16	5FBC/24	180
76/01/17	r2H	[R2 Holding time]	4222 = 16#107E	200C/17	5FBC/25	181
76/01/18	r3H	[R3 Holding time]	4223 = 16#107F	200C/18	5FBC/26	182
76/01/19	r4H	[R4 Holding time]	4224 = 16#1080	200C/19	5FBC/27	183
76/01/1E	LO1H	[LO1 holding time]	4229 = 16#1085	200C/1E	5FBC/28	184
76/01/1F	LO2H	[LO2 holding time]	4230 = 16#1086	200C/1F	5FBC/29	185
76/01/20	LO3H	[LO3 holding time]	4231 = 16#1087	200C/20	5FBC/2A	186
76/01/21	LO4H	[LO4 holding time]	4232 = 16#1088	200C/21	5FBC/2B	187
76/01/2A	r1d	[R1 Delay time]	4241 = 16#1091	200C/2A	5FBC/2C	180
76/01/2B	r2d	[R2 Delay time]	4242 = 16#1092	200C/2B	5FBC/2D	181
76/01/2C	r3d	[R3 Delay time]	4243 = 16#1093	200C/2C	5FBC/2E	182
76/01/2D	r4d	[R4 Delay time]	4244 = 16#1094	200C/2D	5FBC/2F	183
76/01/32	LO1d	[LO1 delay time]	4249 = 16#1099	200C/32	5FBC/30	184
76/01/33	LO2d	[LO2 delay time]	4250 = 16#109A	200C/33	5FBC/31	185

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
76/01/34	LO3d	[LO3 delay time]	4251 = 16#109B	200C/34	5FBC/32	186
76/01/35	LO4d	[LO4 delay time]	4252 = 16#109C	200C/35	5FBC/33	187
77/01/03	AI1t	[AI1 Type]	4402 = 16#1132	200E/3	5FBC/34	169
77/01/04	AI2t	[AI2 Type]	4403 = 16#1133	200E/4	5FBC/35	170
77/01/05	AI3t	[AI3 Type]	4404 = 16#1134	200E/5	5FBC/36	172
77/01/06	AI4t	[AI4 Type]	4405 = 16#1135	200E/6	5FBC/37	174
77/01/0D	UIL1	[AI1 min value]	4412 = 16#113C	200E/D	5FBC/38	169
77/01/0E	UIL2	[AI2 min value]	4413 = 16#113D	200E/E	5FBC/39	170
77/01/10	UIL4	[AI4 min value]	4415 = 16#113D	200E/10	5FBC/3A	174
77/01/10	UnS	[Rated motor volt.]	9601 = 16#2581	2042/2	5FB3/D3	148
77/01/17	UIH1	[AI1 max value]	4422 = 16#1146	200E/17	5FBC/3B	169
77/01/18	UIH2	[AI2 max value]	4423 = 16#1147	200E/18	5FBC/3C	170
77/01/1A	UIH4	[AI4 max value]	4425 = 16#1149	200E/1A	5FBC/3D	174
77/01/22	CrL2	[AI2 min value]	4433 = 16#1151	200E/22	5FBC/3E	170
77/01/23	CrL3	[AI3 min value]	4434 = 16#1152	200E/23	5FBC/3F	172
77/01/24	CrL4	[AI4 min value]	4435 = 16#1153	200E/24	5FBC/40	174
77/01/2C	CrH2	[AI2 max value]	4443 = 16#115B	200E/2C	5FBC/41	170
77/01/2D	CrH3	[AI3 max value]	4444 = 16#115C	200E/2D	5FBC/42	172
77/01/2E	CrH4	[AI4 max value]	4445 = 16#115D	200E/2E	5FBC/43	174
77/01/35	AI1F	[AI1 filter]	4452 = 16#1164	200E/35	5FBC/44	169
77/01/36	AI2F	[AI2 filter]	4453 = 16#1165	200E/36	5FBC/45	170
77/01/37	AI3F	[AI3 filter]	4454 = 16#1166	200E/37	5FBC/46	172
77/01/38	AI4F	[AI4 filter]	4455 = 16#1167	200E/38	5FBC/47	174
77/01/3F	AI1E	[AI1 Interm. point X]	4462 = 16#116E	200E/3F	5FBC/48	169
77/01/40	AI2E	[AI2 Interm. point X]	4463 = 16#116F	200E/40	5FBC/49	171
77/01/42	AI4E	[AI4 Interm. point X]	4465 = 16#1171	200E/42	5FBC/4B	175
77/01/49	AI1S	[AI1 Interm. point Y]	4472 = 16#1178	200E/49	5FBC/4C	169
77/01/4A	AI2S	[AI2 Interm. point Y]	4473 = 16#1179	200E/4A	5FBC/4D	171
77/01/4A	AI3E	[AI3 Interm. point X]	4464 = 16#1170	200E/41	5FBC/4A	173
77/01/4A	AI3S	[AI3 Interm. point Y]	4474 = 16#117A	200E/4B	5FBC/4E	173
77/01/4C	AI4S	[AI4 Interm. point Y]	4475 = 16#117B	200E/4C	5FBC/4F	175

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
77/01/54	AI2L	[AI2 range]	4483 = 16#1183	200E/54	5FBC/50	171
77/01/55	AI3L	[AI3 range]	4484 = 16#1184	200E/55	5FBC/51	172
77/01/56	AI4L	[AI4 range]	4485 = 16#1185	200E/56	5FBC/52	175
78/01/02	AO1t	[AO1 Type]	4601 = 16#11F9	2010/2	5FBC/53	188
78/01/03	AO2t	[AO2 Type]	4602 = 16#11FA	2010/3	5FBC/54	190
78/01/04	AO3t	[AO3 Type]	4603 = 16#11FB	2010/4	5FBC/55	191
78/01/0C	AO1F	[AO1 Filter]	4611 = 16#1203	2010/C	5FBC/56	189
78/01/0D	AO2F	[AO2 Filter]	4612 = 16#1204	2010/D	5FBC/57	190
78/01/0E	AO3F	[AO3 Filter]	4613 = 16#1205	2010/E	5FBC/58	191
78/01/16	UOL1	[AO1 min Output]	4621 = 16#120D	2010/16	5FBC/59	189
78/01/17	UOL2	[AO2 min Output]	4622 = 16#120E	2010/17	5FBC/5A	190
78/01/18	UOL3	[AO3 min Output]	4623 = 16#120F	2010/18	5FBC/5B	191
78/01/20	UOH1	[AO1 max Output]	4631 = 16#1217	2010/20	5FBC/5C	189
78/01/21	UOH2	[AO2 max Output]	4632 = 16#1218	2010/21	5FBC/5D	190
78/01/22	UOH3	[AO3 max Output]	4633 = 16#1219	2010/22	5FBC/5E	191
78/01/2A	AOL1	[AO1 min Output]	4641 = 16#1221	2010/2A	5FBC/5F	188
78/01/2B	AOL2	[AO2 min Output]	4642 = 16#1222	2010/2B	5FBC/60	190
78/01/2C	AOL3	[AO3 min Output]	4643 = 16#1223	2010/2C	5FBC/61	191
78/01/34	AOH1	[AO1 max Output]	4651 = 16#122B	2010/34	5FBC/62	188
78/01/35	AOH2	[AO2 max Output]	4652 = 16#122C	2010/35	5FBC/63	190
78/01/36	AOH3	[AO3 max Output]	4653 = 16#122D	2010/36	5FBC/64	191
7A/01/02	r1	[R1 Assignment]	5001 = 16#1389	2014/2	5FB3/7B	179
7A/01/03	r2	[R2 Assignment]	5002 = 16#138A	2014/3	5FB3/7C	181
7A/01/04	r3	[R3 Assignment]	5003 = 16#138B	2014/4	5FBC/67	182
7A/01/05	r4	[R4 Assignment]	5004 = 16#138C	2014/5	5FBC/68	183
7A/01/0A	LO1	[LO1 assignment]	5009 = 16#1391	2014/A	5FB3/7D	184
7A/01/0B	LO2	[LO2 assignment]	5010 = 16#1392	2014/B	5FBC/6A	185
7A/01/0C	LO3	[LO3 assignment]	5011 = 16#1393	2014/C	5FBC/6B	186
7A/01/0D	LO4	[LO4 assignment]	5012 = 16#1394	2014/D	5FBC/6C	187
7A/01/16	AO1	[AO1 assignment]	5021 = 16#139D	2014/16	5FBC/6D	188
7A/01/17	AO2	[AO2 assignment]	5022 = 16#139E	2014/17	5FBC/6E	190

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
7A/01/18	AO3	[AO3 assignment]	5023 = 16#139F	2014/18	5FBC/6F	191
7B/01/03	IL1r	Logic input map	5202 = 16#1452	2016/3	5FB9/28	95
7B/01/0D	OL1r	Logic output map	5212 = 16#145C	2016/D	5FB9/2A	95
7B/01/21	AI1r	Standardized image of analog input 1	5232 = 16#1470	2016/21	5FB9/2F	96
7B/01/22	AI2r	Standardized image of analog input 2	5233 = 16#1471	2016/22	5FB9/30	96
7B/01/23	AI3r	Standardized image of analog input 3	5234 = 16#1472	2016/23	5FB9/31	96
7B/01/24	AI4r	Standardized image of analog input 4	5235 = 16#1473	2016/24	5FB9/32	97
7B/01/2B	AI1C	Physical image of analog input 1	5242 = 16#147A	2016/2B	5FB9/33	96
7B/01/2C	AI2C	Physical image of analog input 2	5243 = 16#147B	2016/2C	5FB9/34	96
7B/01/2D	AI3C	Physical image of analog input 3	5244 = 16#147C	2016/2D	5FB9/35	96
7B/01/2E	AI4C	Physical image of analog input 4	5245 = 16#147D	2016/2E	5FB9/36	96
7B/01/3E	AO1r	Standardized image of analog output 1	5261 = 16#148D	2016/3E	5FB9/3A	97
7B/01/3F	AO2r	Standardized image of analog output 2	5262 = 16#148E	2016/3F	5FB9/3B	97
7B/01/40	AO3r	Standardized image of analog output 3	5263 = 16#148F	2016/40	5FB9/3C	98
7B/01/48	AO1C	Physical image of analog output 1	5271 = 16#1497	2016/48	5FB9/3D	97
7B/01/49	AO2C	Physical image of analog output 2	5272 = 16#1498	2016/49	5FB9/3E	97
7B/01/4A	AO3C	Physical image of analog output 3	5273 = 16#1499	2016/4A	5FB9/3F	97
7B/01/52	AIU1	PID regulator feedback	5281 = 16 #14A1	2016/52	5FB9/40	78
7B/01/53	AIC1	[AI net. channel]	5282 = 16#14A2	2016/53	5FBC/70	230
7D/01/05	PGI	Encoder pulse counter	5604 = 16#15E4	201A/5	5FB3/7E	98
7D/01/05	PGI	[Number of pulses]	5604 = 16#15E4	201A/5	5FB3/7E	159
7D/01/05	PGI	[Number of pulses]	5604 = 16#15E4	201A/5	5FB3/7E	177
7D/01/06	EnC	[Encoder check]	5605 = 16#15E5	201A/6	5FB3/7F	160
7D/01/06	EnC	[Encoder check]	5605 = 16#15E5	201A/6	5FB3/7F	177
7D/01/07	EnU	[Encoder usage]	5606 = 16#15E6	201A/7	5FBC/73	160
7D/01/07	EnU	[Encoder usage]	5606 = 16#15E6	201A/7	5FBC/73	177
7D/01/08	ECC	[Encoder coupling]	5607 = 16#15E7	201A/8	5FBC/74	276
7D/01/09	EnS	[Encoder type]	5608 = 16#15E8	201A/A	5FBC/75	159
7D/01/09	EnS	[Encoder type]	5608 = 16#15E8	201A/9	5FBC/75	177

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
7D/01/0A	ECt	[Encoder check time]	5609 = 16#15E9	201A/A	5FBF/07	276
7D/01/0B	Pdl	Drive encoder counter divisor	5610 = 16#15EA	201A/B	5FBC/76	131
7D/01/0C	PUC	Drive encoder divided counter	5611 = 16#15EB	201A/C	5FB9/41	132
81/01/02	O01	"Controller Inside" HMI exchange word 1	6401 = 16#1901	2022/2	5FB3/81	132
81/01/03	O02	"Controller Inside" HMI exchange word 2	6402 = 16#1902	2022/3	5FB3/82	132
81/01/04	O03	"Controller Inside" HMI exchange word 3	6403 = 16#1903	2022/4	5FB3/83	132
81/01/05	O04	"Controller Inside" HMI exchange word 4	6404 = 16#1904	2022/5	5FB3/84	132
81/01/06	O05	"Controller Inside" HMI exchange word 5	6405 = 16#1905	2022/6	5FB3/85	133
81/01/07	O06	"Controller Inside" HMI exchange word 6	6406 = 16#1906	2022/7	5FB3/86	133
81/01/08	O07	"Controller Inside" HMI exchange word 7	6407 = 16#1907	2022/8	5FB3/87	133
81/01/09	O08	"Controller Inside" HMI exchange word 8	6408 = 16#1908	2022/9	5FB3/88	133
81/01/0A	O09	"Controller Inside" HMI exchange word 9	6409 = 16#1909	2022/A	5FB3/89	133
81/01/0B	O10	"Controller Inside" HMI exchange word 10	6410 = 16#190A	2022/B	5FB3/8A	133
81/01/0C	O11	"Controller Inside" HMI exchange word 11	6411 = 16#190B	2022/C	5FB3/8B	133
81/01/0D	O12	"Controller Inside" HMI exchange word 12	6412 = 16#190C	2022/D	5FB3/8C	134
81/01/0E	O13	"Controller Inside" HMI exchange word 13	6413 = 16#190D	2022/E	5FB3/8D	134
81/01/0F	O14	"Controller Inside" HMI exchange word 14	6414 = 16#190E	2022/F	5FB3/8E	134
81/01/10	O15	"Controller Inside" HMI exchange word 15	6415 = 16#190F	2022/10	5FB3/8F	134
81/01/11	O16	"Controller Inside" HMI exchange word 16	6416 = 16#1910	2022/11	5FB3/90	134
81/01/12	O17	"Controller Inside" HMI exchange word 17	6417 = 16#1911	2022/12	5FB3/91	134
81/01/13	O18	"Controller Inside" HMI exchange word 18	6418 = 16#1912	2022/13	5FB3/92	134
81/01/14	O19	"Controller Inside" HMI exchange word 19	6419 = 16#1913	2022/14	5FB3/93	135

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
81/01/15	O20	"Controller Inside" HMI exchange word 20	6420 = 16#1914	2022/15	5FB3/94	135
81/01/16	O21	"Controller Inside" HMI exchange word 21	6421 = 16#1915	2022/16	5FB3/95	135
81/01/17	O22	"Controller Inside" HMI exchange word 22	6422 = 16#1916	2022/17	5FB3/96	135
81/01/18	O23	"Controller Inside" HMI exchange word 23	6423 = 16#1917	2022/18	5FB3/97	135
81/01/19	O24	"Controller Inside" HMI exchange word 24	6424 = 16#1918	2022/19	5FB3/98	135
81/01/1A	O25	"Controller Inside" HMI exchange word 25	6425 = 16#1919	2022/1A	5FB3/99	135
81/01/1B	O26	"Controller Inside" HMI exchange word 26	6426 = 16#191A	2022/1B	5FB3/9A	136
81/01/1C	O27	"Controller Inside" HMI exchange word 27	6427 = 16#191B	2022/1C	5FB3/9B	136
81/01/1D	O28	"Controller Inside" HMI exchange word 28	6428 = 16#191C	2022/1D	5FB3/9C	136
81/01/1E	O29	"Controller Inside" HMI exchange word 29	6429 = 16#191D	2022/1E	5FB3/9D	136
81/01/1F	O30	"Controller Inside" HMI exchange word 30	6430 = 16#191E	2022/1F	5FB3/9E	136
81/01/20	O31	"Controller Inside" HMI exchange word 31	6431 = 16#191F	2022/20	5FB3/9F	136
81/01/21	O32	"Controller Inside" HMI exchange word 32	6432 = 16#1920	2022/21	5FB3/A0	136
81/01/22	O33	"Controller Inside" HMI exchange word 33	6433 = 16#1921	2022/22	5FB3/A1	137
81/01/23	O34	"Controller Inside" HMI exchange word 34	6434 = 16#1922	2022/23	5FB3/A2	137
81/01/24	O35	"Controller Inside" HMI exchange word 35	6435 = 16#1923	2022/24	5FB3/A3	137
81/01/25	O36	"Controller Inside" HMI exchange word 36	6436 = 16#1924	2022/25	5FB3/A4	137
81/01/26	O37	"Controller Inside" HMI exchange word 37	6437 = 16#1925	2022/26	5FB3/A5	137
81/01/27	O38	"Controller Inside" HMI exchange word 38	6438 = 16#1926	2022/27	5FB3/A6	137
81/01/28	O39	"Controller Inside" HMI exchange word 39	6439 = 16#1927	2022/28	5FB3/A7	137
81/01/29	O40	"Controller Inside" HMI exchange word 40	6440 = 16#1928	2022/29	5FB3/A8	138

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
81/01/2A	O41	"Controller Inside" HMI exchange word 41	6441 = 16#1929	2022/2A	5FB3/A9	138
81/01/2B	O42	"Controller Inside" HMI exchange word 42	6442 = 16#192A	2022/2B	5FB3/AA	138
81/01/2C	O43	"Controller Inside" HMI exchange word 43	6443 = 16#192B	2022/2C	5FB3/AB	138
81/01/2D	O44	"Controller Inside" HMI exchange word 44	6444 = 16#192C	2022/2D	5FB3/AC	138
81/01/2E	O45	"Controller Inside" HMI exchange word 45	6445 = 16#192D	2022/2E	5FB3/AD	138
81/01/2F	O46	"Controller Inside" HMI exchange word 46	6446 = 16#192E	2022/2F	5FB3/AE	138
81/01/30	O47	"Controller Inside" HMI exchange word 47	6447 = 16#192F	2022/30	5FB3/AF	139
81/01/31	O48	"Controller Inside" HMI exchange word 48	6448 = 16#1930	2022/31	5FB3/B0	139
81/01/32	O49	"Controller Inside" HMI exchange word 49	6449 = 16#1931	2022/32	5FB3/B1	139
81/01/33	O50	"Controller Inside" HMI exchange word 50	6450 = 16#1932	2022/33	5FC8/32	139
83/01/66	PIL1	"Controller Inside" logic input map	6901 = 16#1AF5	2027/2	5FB9/49	131
83/01/70	POL1	"Controller Inside" logic output map	6911 = 16#1AFF	2027/C	5FB9/4A	131
83/01/8F	PAI1	Physical image of analog input 1	6942 = 16#1B1E	2027/2B	5FB9/4B	131
83/01/90	PAI2	Physical image of analog input 2	6943 = 16#1B1F	2027/2C	5FB9/4C	131
83/01/AC	PAO1	Physical image of analog output 1	6971 = 16#1B3B	2027/48	5FB9/4D	131
83/01/AD	PAO2	Physical image of analog output 2	6972 = 16#1B3C	2027/49	5FB9/4E	131
84/01/03	IPL	[Input phase loss]	7002 = 16#1B5A	2028/3	5FB3/BE	266
84/01/04	LFL2	[AI2 4-20mA loss]	7003 = 16#1B5B	2028/4	5FB3/BF	273
84/01/05	StP	[UnderV. prevention]	7004 = 16#1B5C	2028/5	5FB3/C0	271
84/01/06	Sdd	[Load slip detection]	7005 = 16#1B5D	2028/6	5FB3/C1	276
84/01/07	EPL	[External fault mgt]	7006 = 16#1B5E	2028/7	5FB3/C2	269
84/01/09	OHL	[Overtemp fault mgt]	7008 = 16#1B60	2028/9	5FBC/8C	267
84/01/0A	OLL	[Overload fault mgt]	7009 = 16#1B61	2028/A	5FBC/8D	265
84/01/0B	SLL	[Modbus fault mgt]	7010 = 16#1B62	2028/B	5FBC/8E	275
84/01/0C	COL	[CANopen fault mgt]	7011 = 16#1B63	2028/C	5FBC/8F	275
84/01/0D	tnL	[Autotune fault mgt]	7012 = 16#1B64	2028/D	5FBC/90	279

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
84/01/0E	LFL3	[AI3 4-20mA loss]	7013 =16#1B65	2028/E	5FB3/C3	273
84/01/0F	LFL4	[AI4 4-20mA loss]	7014 =16#1B66	2028/F	5FB3/C4	273
84/01/10	CLL	[Network fault mgt]	7015 =16#1B67	2028/10	5FBC/93	275
84/01/51	LFF	[Fallback speed]	7080 =16#1BA8	2028/51	5FB3/C5	279
84/01/52	Odt	[OutPh time detect]	7081 = 16#1BA9	2028/52	5FBF/08	266
84/01/7A	LFt	Altivar fault code	7121 = 16#1BD1	2029/16	5FB3/C8	100
84/01/7B	Atr	[Automatic restart]	7122 = 16#1BD2	2029/17	5FB3/C9	262
84/01/7C	tAr	[Max. restart time]	7123 = 16#1BD3	2029/18	5FBC/96	262
84/01/7D	rSF	[Fault reset]	7124 = 16#1BD4	2029/19	5FBC/97	261
84/01/7E	Inh	[Fault inhibit assign.]	7125 =16#1BD5	2029/1A	5FBC/98	274
84/01/83	CIC	Incorrect configuration	7130 = 16#1BDA	2029/1F	5FB6/1A	102
84/01/84	EtF	[External fault ass.]	7131 = 16#1BDB	2029/20	5FBC/99	269
84/01/85	CnF	Network card fault code	7132 = 16#1BDC	2029/21	5FB0/96	102
84/01/86	APF	"Controller Inside" card fault code	7133 = 16#1BDD	2029/22	5FB0/97	102
84/01/87	ILF1	Option card 1 fault code	7134 = 16#1BDE	2029/23	5FB0/98	103
84/01/88	ILF2	Option card 2 fault code	7135 = 16#1BDF	2029/24	5FB0/99	103
85/01/01	dP0	Fault code on last fault	7200 = 16#1C20	202A/1	5FB9/55	105
85/01/02	dP1	Fault code on fault n-1	7201 = 16#1C21	202A/2	5FB9/56	107
85/01/03	dP2	Fault code on fault n-2	7202 = 16#1C22	202A/3	5FB9/57	109
85/01/04	dP3	Fault code on fault n-3	7203 = 16#1C23	202A/4	5FB9/58	111
85/01/05	dP4	Fault code on fault n-4	7204 = 16#1C24	202A/5	5FB9/59	113
85/01/06	dP5	Fault code on fault n-5	7205 = 16#1C25	202A/6	5FB9/5A	115
85/01/07	dP6	Fault code on fault n-6	7206 = 16#1C26	202A/7	5FB9/5B	117
85/01/08	dP7	Fault code on fault n-7	7207 = 16#1C27	202A/8	5FB9/5C	119
85/01/09	dP8	Fault code on fault n-8	7208 = 16#1C28	202A/9	5FB9/5D	121
85/01/0B	EP0	Status word on last fault	7210 = 16#1C2A	202A/B	5FB9/5E	105
85/01/0C	EP1	Status word on fault n-1	7211 = 16#1C2B	202A/C	5FB9/5F	108
85/01/0D	EP2	Status word on fault n-2	7212 = 16#1C2C	202A/D	5FB9/60	110
85/01/0E	EP3	Status word on fault n-3	7213 = 16#1C2D	202A/E	5FB9/61	112
85/01/0F	EP4	Status word on fault n-4	7214 = 16#1C2E	202A/F	5FB9/62	114
85/01/10	EP5	Status word on fault n-5	7215 = 16#1C2F	202A/10	5FB9/63	116

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
85/01/11	EP6	Status word on fault n-6	7216 = 16#1C30	202A/11	5FB9/64	118
85/01/12	EP7	Status word on fault n-7	7217 = 16#1C31	202A/12	5FB9/65	120
85/01/13	EP8	Status word on fault n-8	7218 = 16#1C32	202A/13	5FB9/66	122
85/01/15	IP0	Extended status word 0 on last fault	7220 = 16#1C34	202A/15	5FB9/67	106
85/01/16	IP1	Extended status word on fault n-1	7221 = 16#1C35	202A/16	5FB9/68	108
85/01/17	IP2	Extended status word on fault n-2	7222 = 16#1C36	202A/17	5FB9/69	110
85/01/18	IP3	Extended status word on fault n-3	7223 = 16#1C37	202A/18	5FB9/6A	112
85/01/19	IP4	Extended status word on fault n-4	7224 = 16#1C38	202A/19	5FB9/6B	114
85/01/1A	IP5	Extended status word on fault n-5	7225 = 16#1C39	202A/1A	5FB9/6C	116
85/01/1B	IP6	Extended status word on fault n-6	7226 = 16#1C3A	202A/1B	5FB9/6D	118
85/01/1C	IP7	Extended status word on fault n-7	7227 = 16#1C3B	202A/1C	5FB9/6E	120
85/01/1D	IP8	Extended status word on fault n-8	7228 = 16#1C3C	202A/1D	5FB9/6F	122
85/01/1F	CMP0	Command word on last fault	7230 = 16#1C3E	202A/1F	5FB9/70	106
85/01/20	CMP1	Command word on fault n-1	7231 = 16#1C3F	202A/20	5FB9/71	108
85/01/21	CMP2	Command word on fault n-2	7232 = 16#1C40	202A/21	5FB9/72	110
85/01/22	CMP3	Command word on fault n-3	7233 = 16#1C41	202A/22	5FB9/73	112
85/01/23	CMP4	Command word on fault n-4	7234 = 16#1C42	202A/23	5FB9/74	114
85/01/24	CMP5	Command word on fault n-5	7235 = 16#1C43	202A/24	5FB9/75	116
85/01/25	CMP6	Command word on fault n-6	7236 = 16#1C44	202A/25	5FB9/76	118
85/01/26	CMP7	Command word on fault n-7	7237 = 16#1C45	202A/26	5FB9/77	120
85/01/27	CMP8	Command word on fault n-8	7238 = 16#1C46	202A/27	5FB9/78	122
85/01/29	LCP0	Motor current on last fault	7240 = 16#1C48	202A/29	5FB9/79	105
85/01/2A	LCP1	Motor current on fault n-1	7241 = 16#1C49	202A/2A	5FB9/7A	108
85/01/2B	LCP2	Motor current on fault n-2	7242 = 16#1C4A	202A/2B	5FB9/7B	110
85/01/2C	LCP3	Motor current on fault n-3	7243 = 16#1C4B	202A/2C	5FB9/7C	112
85/01/2D	LCP4	Motor current on fault n-4	7244 = 16#1C4C	202A/2D	5FB9/7D	114
85/01/2E	LCP5	Motor current on fault n-5	7245 = 16#1C4D	202A/2E	5FB9/7E	116
85/01/2F	LCP6	Motor current on fault n-6	7246 = 16#1C4E	202A/2F	5FB9/7F	118
85/01/30	LCP7	Motor current on fault n-7	7247 = 16#1C4F	202A/30	5FB9/80	120
85/01/31	LCP8	Motor current on fault n-8	7248 = 16#1C50	202A/31	5FB9/81	122
85/01/33	rFP0	Output frequency on last fault	7250 = 16#1C52	202A/33	5FB9/82	105

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
85/01/34	rFP1	Output frequency on fault n-1	7251 = 16#1C53	202A/34	5FB9/83	108
85/01/35	rFP2	Output frequency on fault n-2	7252 = 16#1C54	202A/35	5FB9/84	110
85/01/36	rFP3	Output frequency on fault n-3	7253 = 16#1C55	202A/36	5FB9/85	112
85/01/37	rFP4	Output frequency on fault n-4	7254 = 16#1C56	202A/37	5FB9/86	114
85/01/38	rFP5	Output frequency on fault n-5	7255 = 16#1C57	202A/38	5FB9/87	116
85/01/39	rFP6	Output frequency on fault n-6	7256 = 16#1C58	202A/39	5FB9/88	118
85/01/3A	rFP7	Output frequency on fault n-7	7257 = 16#1C59	202A/3A	5FB9/89	120
85/01/3B	rFP8	Output frequency on fault n-8	7258 = 16#1C5A	202A/3B	5FB9/8A	122
85/01/3D	rtP0	Motor operating time on last fault	7260 = 16#1C5C	202A/3D	5FB9/8B	107
85/01/3E	rtP1	Motor operating time on fault n-1	7261 = 16#1C5D	202A/3E	5FB9/8C	109
85/01/3F	rtP2	Motor operating time on fault n-2	7262 = 16#1C5E	202A/3F	5FB9/8D	111
85/01/40	rtP3	Motor operating time on fault n-3	7263 = 16#1C5F	202A/40	5FB9/8E	113
85/01/41	rtP4	Motor operating time on fault n-4	7264 = 16#1C60	202A/41	5FB9/8F	115
85/01/42	rtP5	Motor operating time on fault n-5	7265 = 16#1C61	202A/42	5FB9/90	117
85/01/43	rtP6	Motor operating time on fault n-6	7266 = 16#1C62	202A/43	5FB9/91	119
85/01/44	rtP7	Motor operating time on fault n-7	7267 = 16#1C63	202A/44	5FB9/92	121
85/01/45	rtP8	Motor operating time on fault n-8	7268 = 16#1C64	202A/45	5FB9/93	123
85/01/47	ULP0	Power supply voltage on last fault	7270 = 16#1C66	202A/47	5FB9/94	105
85/01/48	ULP1	Supply voltage on fault n-1	7271 = 16#1C67	202A/48	5FB9/95	108
85/01/49	ULP2	Supply voltage on fault n-2	7272 = 16#1C68	202A/49	5FB9/96	110
85/01/4A	ULP3	Supply voltage on fault n-3	7273 = 16#1C69	202A/4A	5FB9/97	112
85/01/4B	ULP4	Supply voltage on fault n-4	7274 = 16#1C6A	202A/4B	5FB9/98	114
85/01/4C	ULP5	Supply voltage on fault n-5	7275 = 16#1C6B	202A/4C	5FB9/99	116
85/01/4D	ULP6	Supply voltage on fault n-6	7276 = 16#1C6C	202A/4D	5FB9/9A	118
85/01/4E	ULP7	Supply voltage on fault n-7	7277 = 16#1C6D	202A/4E	5FB9/9B	120
85/01/4F	ULP8	Supply voltage on fault n-8	7278 = 16#1C6E	202A/4F	5FB9/9C	122
85/01/51	tHP0	Motor thermal state on last fault	7280 = 16#1C70	202A/51	5FB9/9D	105
85/01/52	tHP1	Motor thermal state on fault n-1	7281 = 16#1C71	202A/52	5FB9/9E	108
85/01/53	tHP2	Motor thermal state on fault n-2	7282 = 16#1C72	202A/53	5FB9/9F	110
85/01/54	tHP3	Motor thermal state on fault n-3	7283 = 16#1C73	202A/54	5FB9/A0	112
85/01/55	tHP4	Motor thermal state on fault n-4	7284 = 16#1C74	202A/55	5FB9/A1	114

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
85/01/56	tHP5	Motor thermal state on fault n-5	7285 = 16#1C75	202A/56	5FB9/A2	116
85/01/57	tHP6	Motor thermal state on fault n-6	7286 = 16#1C76	202A/57	5FB9/A3	118
85/01/58	tHP7	Motor thermal state on fault n-7	7287 = 16#1C77	202A/58	5FB9/A4	120
85/01/59	tHP8	Motor thermal state on fault n-8	7288 = 16#1C78	202A/59	5FB9/A5	122
85/01/5B	CrP0	Channels active on last fault	7290 = 16#1C7A	202A/5B	5FB9/A6	107
85/01/5C	CrP1	Active channels on fault n-1	7291 = 16#1C7B	202A/5C	5FB9/A7	109
85/01/5D	CrP2	Active channels on fault n-2	7292 = 16#1C7C	202A/5D	5FB9/A8	111
85/01/5E	CrP3	Active channels on fault n-3	7293 = 16#1C7D	202A/5E	5FB9/A9	113
85/01/5F	CrP4	Active channels on fault n-4	7294 = 16#1C7E	202A/5F	5FB9/AA	115
85/01/60	CrP5	Active channels on fault n-5	7295 = 16#1C7F	202A/60	5FB9/AB	117
85/01/61	CrP6	Active channels on fault n-6	7296 = 16#1C80	202A/61	5FB9/AC	119
85/01/62	CrP7	Active channels on fault n-7	7297 = 16#1C81	202A/62	5FB9/AD	121
85/01/63	CrP8	Active channels on fault n-8	7298 = 16#1C82	202A/63	5FB9/AE	123
85/01/65	Md0	Date on last fault	7300 = 16#1C84	202B/1	5FB9/AF	107
85/01/66	Md1	Date on fault n-1	7301 = 16#1C85	202B/2	5FB9/B0	109
85/01/67	Md2	Date on fault n-2	7302 = 16#1C86	202B/3	5FB9/B1	111
85/01/68	Md3	Date on fault n-3	7303 = 16#1C87	202B/4	5FB9/B2	113
85/01/69	Md4	Date on fault n-4	7304 = 16#1C88	202B/5	5FB9/B3	115
85/01/6A	Md5	Date on fault n-5	7305 = 16#1C89	202B/6	5FB9/B4	117
85/01/6B	Md6	Date on fault n-6	7306 = 16#1C8A	202B/7	5FB9/BE	119
85/01/6C	Md7	Date on fault n-7	7307 = 16#1C8B	202B/8	5FB9/B6	121
85/01/6D	Md8	Date on fault n-8	7308 = 16#1C8C	202B/9	5FB9/B7	123
85/01/6F	dM0	Time on last fault	7310 = 16#1C8E	202B/B	5FB9/B8	107
85/01/70	dM1	Time on fault n-1	7311 = 16#1C8F	202B/C	5FB9/B9	109
85/01/71	dM2	Time on fault n-2	7312 = 16#1C90	202B/D	5FB9/BA	111
85/01/72	dM3	Time on fault n-3	7313 = 16#1C91	202B/E	5FB9/BB	113
85/01/73	dM4	Time on fault n-4	7314 = 16#1C92	202B/F	5FB9/BC	115
85/01/74	dM5	Time on fault n-5	7315 = 16#1C93	202B/10	5FB9/BD	117
85/01/75	dM6	Time on fault n-6	7316 = 16#1C94	202B/11	5FB9/B5	119
85/01/76	dM7	Time on fault n-7	7317 = 16#1C95	202B/12	5FB9/BF	121
85/01/77	dM8	Time on fault n-8	7318 = 16#1C96	202B/13	5FB9/C0	123

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
85/01/C0	dAY	Date	7391 = 16#1CDF	202B/5C	5FB9/CA	132
85/01/C1	tIME	Time	7392 = 16#1CE0	202B/5D	5FB9/CB	132
89/01/15	CNFS	[Config. Active]	8020 = 16#1F54	2032/15	5FB9/CD	86
89/01/15	CNFS	Active configuration	8020 = 16#1F54	2032/15	5FB9/CD	67
89/01/16	CnF1	[2 Configurations]	8021 = 16#1F55	2032/16	5FBC/9C	251
89/01/17	CnF2	[3 Configurations]	8022 = 16#1F56	2032/17	5FBC/9D	252
89/01/1A	CHM	[Multimotors]	8025 = 16#1F59	2032/1A	5FBC/9E	251
8B/01/02	CHCF	[Profile]	8401 = 16#20D1	2036/2	5FBC/9F	193
8B/01/03	COP	[Copy channel 1 <-> 2]	8402 = 16#20D2	2036/3	5FBC/A0	195
8B/01/0C	rFC	[Ref. 2 switching]	8411 = 16#20DB	2036/C	5FBC/A2	195
8B/01/0D	rCb	[Ref 1B switching]	8412 = 16#20DC	2036/D	5FBC/A3	198
8B/01/0E	Fr1	[Ref.1 channel]	8413 = 16#20DD	2036/E	5FBC/A4	193
8B/01/0F	Fr2	[Ref.2 channel]	8414 = 16#20DE	2036/F	5FBC/A5	195
8B/01/10	Fr1b	[Ref.1B channel]	8415 = 16#20DF	2036/10	5FBC/A6	198
8B/01/16	CCS	[Cmd switching]	8421 = 16#20D1	2036/16	5FBC/A7	194
8B/01/18	Cd1	[Cmd channel 1]	8423 = 16#20E7	2036/18	5FBC/A8	194
8B/01/19	Cd2	[Cmd channel 2]	8424 = 16#20E8	2036/19	5FBC/A9	194
8B/01/2A	CRC	Active reference channel	8441 = 16#20F9	2036/2A	5FB9/CE	85
8B/01/2B	CCC	Active command channel	8442 = 16#20FA	2036/2B	5FB9/CF	86
8B/01/5C	SPM	[Ref. memo ass.]	8491 = 16#212B	2036/5C	5FBC/AD	218
8B/01/67	LFR	Frequency reference	8502 = 16#2136	2037/3	5FB6/1C	77
8B/01/68	PISP	PID regulator reference	8503 = 16#2137	2037/4	5FB6/1D	78
8B/01/69	CMI	Extended control word	8504 = 16#2138	2037/5	5FB6/1E	76
8B/01/6A	LTR	Torque reference	8505 = 16#2139	6071	6071	77
8C/01/06	FrHd	Speed reference before ramp	8605 = 16#219D	2038/6	5FB9/D6	89
8C/01/07	Errd	DSP402 fault code	8606 = 16#219E	603F	5FB9/D7	99
8C/01/0C	SPAL	Acceleration speed delta	8611 = 16#21A3	6048/1	6048/1	140
8C/01/0E	SPAt	Acceleration time delta	8613 = 16#21A5	6048/2	6048/2	140
8C/01/0F	SPdL	Deceleration speed delta	8614 = 16#21A6	6049/1	6049/1	140
8C/01/11	SPdt	Deceleration time delta	8616 = 16#21A8	6049/2	6049/2	141
8C/01/2A	FrOd	Speed reference after ramp	8641 = 16#21C1	6043	5FB9/D8	89

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
8C/01/2B	SPFn	Speed setpoint factor numerator	8642 = 16#21C2	604B/1	604B/1	141
8C/01/2C	SPFd	Speed setpoint factor denominator	8643 = 16#21C3	604B/2	604B/2	142
8E/01/02	ACC	[Acceleration]	9001 = 16#2329	203C/2	5FBF/1E	201
8E/01/03	dEC	[Deceleration]	9002 = 16#232A	203C/3	5FBF/1F	201
8E/01/04	brA	[Dec ramp adapt.]	9003 = 16#232B	203C/4	5FB3/CA	204
8E/01/05	rPt	[Ramp type]	9004 = 16#232C	203C/5	5FB3/CB	201
8E/01/06	tA1	[Begin Acc round]	9005 = 16#232D	203C/6	5FB3/CC	202
8E/01/07	tA2	[End Acc round]	9006 = 16#232E	203C/7	5FB3/CD	202
8E/01/08	tA3	[Begin Dec round]	9007 = 16#232F	203C/8	5FB3/CE	202
8E/01/09	tA4	[End Dec round]	9008 = 16#2330	203C/9	5FB3/CF	202
8E/01/0B	rPS	[Ramp switching]	9010 = 16#2332	203C/B	5FBC/B3	203
8E/01/0C	Frt	[Ramp 2 threshold]	9011 = 16#2333	203C/C	5FB3/D0	202
8E/01/0D	AC2	[Acceleration 2]	9012 = 16#2334	203C/D	5FBF/24	203
8E/01/0D	AC2	[Acceleration 2]	9012 = 16#2334	203C/D	5FBF/24	217
8E/01/0E	dE2	[Deceleration 2]	9013 = 16#2335	203C/E	5FBF/25	203
8E/01/0E	dE2	[Deceleration 2]	9013 = 16#2335	203C/E	5FBF/25	217
8E/01/15	Inr	[Ramp increment]	9020 = 16#233C	203C/15	5FBF/26	201
8E/01/16	FrO	Frequency reference after ramp	9021 = 16#233D	203C/16	5FB9/D9	89
8E/01/68	SPG	[Speed prop. gain]	9103 = 16#238F	203D/4	5FB3/D1	145
8E/01/69	Slt	[Speed time integral]	9104 = 16#2390	203D/5	5FBF/28	146
8E/01/6A	SFC	[K speed loop filter]	9105 = 16#2391	203D/6	5FBF/29	145
8F/01/02	CLI	[Current Limitation]	9201 = 16#23F1	203E/2	5FB3/D2	146
8F/01/02	CLI	[Current Limitation]	9201 = 16#23F1	203E/2	5FB3/D2	162
8F/01/03	LC2	[Current limit 2]	9202 = 16#23F2	203E/3	5FBC/B5	243
8F/01/04	CL2	[I Limit. 2 value]	9203 = 16#23F3	203E/4	5FBF/2B	243
8F/01/0B	tLA	[AI torque limit. activ.]	9210 = 16#23FA	203E/B	5FBC/B6	241
8F/01/0C	tLIM	[Motoring torque lim]	9211 = 16#23FB	203E/C	5FBF/2C	241
8F/01/0D	tLIG	[Gen. torque lim]	9212 = 16#23FC	203E/D	5FBF/2D	241
8F/01/0E	tLC	[Analog limit. act.]	9213 = 16#23FD	203E/E	5FBC/B7	242
8F/01/0F	tAA	[Torque ref. assign.]	9214 = 16#23FE	203E/F	5FBC/B8	242
8F/01/15	tSS	[Trq/spd switching]	9220 = 16#2404	203E/15	5FBC/B9	238

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
8F/01/16	tr1	[Torque ref. channel]	9221 = 16#2405	203E/16	5FBC/BA	238
8F/01/17	tSd	[Torque ref. sign]	9222 = 16#2406	203E/17	5FBC/BB	239
8F/01/18	dbn	[Negative deadband]	9223 = 16#2407	203E/18	5FBF/2E	240
8F/01/19	dBp	[Positive deadband]	9224 = 16#2408	203E/19	5FBF/2F	240
8F/01/1A	trt	[Torque ratio]	9225 = 16#2409	203E/1A	5FBF/30	239
8F/01/1B	trP	[Torque ramp time]	9226 = 16#240A	203E/1B	5FBF/31	239
8F/01/1C	tSt	[Torque control stop]	9227 = 16#240B	203E/1C	5FBC/BC	239
8F/01/1D	tOb	[Torq. ctrl fault mgt]	9228 = 16#240C	203E/1D	5FBC/BD	240
8F/01/1E	rtO	[Torque R. time out]	9229 = 16#240D	203E/1E	5FBF/32	240
8F/01/1F	SPt	[Spin time]	9230 = 16#240E	203E/1F	5FBF/33	240
8F/01/20	trr	Torque reference before ramp	9231 = 16#240F	203E/20	5FB9/DB	90
8F/01/21	trO	Torque reference after ramp	9232 = 16#2410	203E/21	5FB9/DC	90
8F/01/29	SSb	[Trq/I limit. stop]	9240 = 16#2418	203E/29	5FBC/BE	277
8F/01/2A	StO	[Trq/I limit. time out]	9241 = 16#2419	203E/2A	5FBF/34	277
8F/01/3D	Int	[Torque unit]	9260 = 16#242C	203E/3D	5FBF/35	238
91/01/03	FrS	[Rated motor freq.]	9602 = 16#2582	2042/3	5FB3/D4	149
91/01/04	nCr	[Rated mot. current]	9603 = 16#2583	2042/4	5FB3/D5	148
91/01/05	nSP	[Nom motor speed]	9604 = 16#2584	2042/5	5FB3/D6	149
91/01/06	nSL	[Nominal motor slip]	9605 = 16#2585	2042/6	5FB9/DD	157
91/01/08	Ctt	[Motor control type]	9607 = 16#2587	2042/8	5FC2/06	150
91/01/09	tUn	[Auto-tuning]	9608 = 16#2588	2042/9	5FB3/D7	149
91/01/0A	tUS	[Auto tuning status]	9609 = 16#2589	2042/A	5FC2/08	150
91/01/0B	tUL	[Auto-tune assign.]	9610 = 16#258A	2042/B	5FBC/EF	253
91/01/0C	OPL	[Output Phase Loss]	9611 = 16#258B	2042/C	5FB3/D8	266
91/01/0D	tHt	[Motor protect. type]	9612 = 16#258C	2042/D	5FB3/D9	264
91/01/0E	nPr	[Rated motor power]	9613 = 16#258D	2042/E	5FC2/0A	148
91/01/10	AUt	[Automatic autotune]	9615 = 16#258F	2042/10	5FC2/0B	149
91/01/13	PPn	[Pr]	9618 = 16#2592	2042/13	5FB3/DA	158
91/01/18	UFr	[IR compensation]	9623 = 16#2597	2042/18	5FC2/0E	156
91/01/1A	SLP	[Slip compensation]	9625 = 16#2599	2042/1A	5FB3/DC	156
91/01/1F	tHr	Motor thermal state	9630 = 16#259E	2042/1F	5FB9/DE	92

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
91/01/29	rSM	Asynchronous motor cold state stator resistance	9640 = 16#25A8	2042/29	5FB9/DF	94
91/01/29	rSM	[Stator R measured]	9640 = 16#25A8	2042/29	5FB9/DF	157
91/01/2B	rSA	[R1w]	9642 = 16#25AA	2042/2B	5FC2/11	158
91/01/33	IdM	Magnetizing current	9650 = 16#25B2	2042/33	5FB9/E0	93
91/01/33	IdM	[Idr]	9650 = 16#25B2	2042/33	5FB9/E0	157
91/01/35	IdA	[Idw]	9652 = 16#25B4	2042/35	5FC2/15	158
91/01/3D	LFM	Leakage inductance	9660 = 16#25BC	2042/3D	5FB9/E1	94
91/01/3D	LFM	[Lfr]	9660 = 16#25BC	2042/3D	5FB9/E1	157
91/01/3F	LFA	[Lfw]	9662 = 16#25BE	2042/3F	5FC2/19	158
91/01/42	trM	Rotor time constant	9665 = 16#25C1	2042/42	5FB9/E2	94
91/01/42	trM	[T2r]	9665 = 16#25C1	2042/42	5FB9/E2	157
91/01/44	trA	[T2w]	9667 = 16#25C3	2042/44	5FC2/1C	158
91/01/47	nCrS	[Nominal I sync.]	9670 = 16#25C6	2042/47	5FC2/1E	154
91/01/48	nSPS	[Nom motor spdsync]	9671 = 16#25C7	2042/48	5FC2/1F	154
91/01/49	PPnS	[Pole pairs]	9672 = 16#25C8	2042/49	5FC2/20	154
91/01/4A	PHS	[Syn. EMF constant]	9673 = 16#25C9	2042/4A	5FC2/21	154
91/01/4B	LdS	[Autotune L d-axis]	9674 = 16#25CA	2042/4B	5FC2/22	154
91/01/4C	LqS	[Autotune L q-axis]	9675 = 16#25CB	2042/4C	5FC2/23	155
91/01/50	FrSS	[Nominal freq sync.]	9679 = 16#25CF	2042/50	5FC2/24	159
91/01/51	rSMS	Synchronous motor cold state stator resistance	9680 = 16#25D0	2042/51	5FB9/E3	94
91/01/51	rSMS	[R1rS]	9680 = 16#25D0	2042/51	5FB9/E3	159
91/01/53	rSAS	[Cust. stator R syn]	9682 = 16#25D2	2042/53	5FC2/26	155
93/01/02	bLC	[Brake assignment]	10001 = 16#2711	2046/2	5FBC/F1	221
93/01/04	bEn	[Brake engage freq]	10003 = 16#2713	2046/4	5FBF/36	222
93/01/05	brt	[Brake Release time]	10004 = 16#2714	2046/5	5FB3/DD	222
93/01/06	bEt	[Brake engage time]	10005 = 16#2715	2046/6	5FB3/DE	223
93/01/07	Ibr	[Brake release I FW]	10006 = 16#2716	2046/7	5FB3/DF	222
93/01/08	bIP	[Brake impulse]	10007 = 16#2717	2046/8	5FB3/E0	221
93/01/09	bSt	[Movement type]	10008 = 16#2718	2046/9	5FBF/3B	221
93/01/0A	bCI	[Brake contact]	10009 = 16#2719	2046/A	5FBC/F2	221

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
93/01/0B	tbE	[Brake engage delay]	10010 = 16#271A	2046/B	5FBF/3C	223
93/01/0C	lrd	[Brake release l Rev]	10011 = 16#271B	2046/C	5FBF/3D	222
93/01/0D	blr	[Brake release freq]	10012 = 16#271C	2046/D	5FB3/E1	222
93/01/0E	JdC	[Jump at reversal]	10013 = 16#271D	2046/E	5FBF/3F	223
93/01/10	brr	[Current ramp time]	10015 = 16#271F	2046/10	5FBF/40	225
93/01/15	bEd	[Engage at reversal]	10020 = 16#2724	2046/15	5FBF/41	223
93/01/17	ttr	[Time to restart]	10022 = 16#2726	2046/17	5FBF/42	224
93/01/33 (bit 0)	brH0	[BRH b0]	10050 = 16# 2742 (bit 0)	2046/33 (bit 0)	5FBC/F3 (bit 0)	224
93/01/33 (bit 1)	brH1	[BRH b1]	10050 = 16# 2742 (bit 1)	2046/33 (bit 1)	5FBC/F3 (bit 1)	224
93/01/33 (bit 2)	brH2	[BRH b2]	10050 = 16# 2742 (bit 2)	2046/33 (bit 2)	5FBC/F3 (bit 2)	224
93/01/33 (bit 3)	brH3	[BRH b3]	10050 = 16# 2742 (bit 3)	2046/33 (bit 3)	5FBC/F3 (bit 3)	224
93/01/33 (bit 4)	brH4	[BRH b4]	10050 = 16# 2742 (bit 4)	2046/33 (bit 4)	5FBC/F3 (bit 4)	225
93/01/47	PES	[Weight sensor ass.]	10070 = 16#2756	2046/47	5FBC/F4	226
93/01/48	LP1	[Point 1 X]	10071 = 16#2757	2046/48	5FBC/F5	226
93/01/49	CP1	[Point 1Y]	10072 = 16#2758	2046/49	5FBC/F6	226
93/01/4A	LP2	[Point 2 X]	10073 = 16#2759	2046/4A	5FBC/F7	226
93/01/4B	CP2	[Point 2Y]	10074 = 16#275A	2046/4B	5FBC/F8	227
93/01/4C	IbrA	[Ibr 4-20 mA loss]	10075 = 16#275B	2046/4C	5FBC/F9	227
93/01/D2	CLI	[Current Limitation]	9201 = 16#23F1	203E/2	5FB3/D2	243
94/01/06	PSt	[Stop Key priority]	64002 = 16#FA02	2262/3	5FBD/88	193
95/01/02	AdC	[Auto DC injection]	10401 = 16#28A1	204A/2	5FB3/E2	208
95/01/03	tdC1	[Auto DC inj. time 1]	10402 = 16#28A2	204A/3	5FBF/44	208
95/01/04	SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	204A/4	5FBF/45	208
95/01/04	SdC1	[Auto DC inj. level 1]	10403 = 16#28A3	204A/4	5FBF/45	223
95/01/05	tdC2	[Auto DC inj. time 2]	10404 = 16#28A4	204A/5	5FBF/46	208
95/01/06	SdC2	[Auto DC inj. level 2]	10405 = 16#28A5	204A/6	5FBF/47	208
98/01/02	Ctd	[Current threshold]	11001 = 16#2AF9	2050/2	5FB3/E3	146
98/01/03	ttd	[Motor therm. level]	11002 = 16#2AFA	2050/3	5FB3/E4	264
98/01/03	ttd	[Motor therm. level]	11002 = 16#2AFA	2050 / 3	5FB3/E4	268

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
98/01/04	Ftd	[Freq. threshold]	11003 = 16#2AFB	2050/4	5FB3/E5	147
98/01/05	F2d	[Freq. threshold 2]	11004 = 16#2AFC	2050/5	5FB3/E6	147
98/01/07	ttd2	[Motor2 therm. level]	11006 = 16#2AFE	2050/7	5FBF/4C	264
98/01/07	ttd2	[Motor2 therm. level]	11006 = 16#2AFE	2050/7	5FBF/4C	268
98/01/08	ttd3	[Motor3 therm. level]	11007 = 16#2AFF	2050/8	5FBF/4D	268
98/01/08	ttd3	[Motor3 therm. level]	11007 = 16#2AFF	2050/8	5FBF/4D	264
98/01/0A	tHA	[Drv therm. state al]	11009 = 16#2B01	2050/A	5FBF/4E	267
98/01/0A	tHA	[Drv therm. state al]	11009 = 16#2B01	2050/A	5FBF/4E	268
98/01/16	SAt	[Thermal alarm stop]	11021 = 16#2B0D	2050/16	5FB3/E7	268
98/01/66	tCC	[2/3 wire control]	11101 = 16#2B5D	2051/2	5FB3/E8	166
98/01/67	tCt	[2 wire type]	11102 = 16#2B5E	2051/3	5FB3/E9	166
98/01/6A	rrS	[Reverse assign.]	11105 = 16#2B61	2051/6	5FBC/FE	166
98/01/6F	JOG	[JOG]	11110 = 16#2B66	2051/B	5FBC/FF	209
98/01/70	JGF	[Jog frequency]	11111 = 16#2B67	2051/C	5FBF/50	209
98/01/71	JGT	[Jog delay]	11112 = 16#2B68	2051/D	5FB3/EA	209
99/01/02	Stt	[Stop type]	11201 = 16#2BC1	2052/2	5FB3/EB	205
99/01/03	nSt	[Freewheel stop ass.]	11202 = 16#2BC2	2052/3	5FBD/02	205
99/01/04	dCI	[DC injection assign.]	11203 = 16#2BC3	2052/4	5FBD/03	206
99/01/05	FSt	[Fast stop assign.]	11204 = 16#2BC4	2052/5	5FBD/04	205
99/01/0B	IdC	[DC inject. level 1]	11210 = 16#2BCA	2052/B	5FB3/EC	206
99/01/0B	IdC	[DC inject. level 1]	11210 = 16#2BCA	2052/B	5FB3/EC	281
99/01/0C	tdC	[DC injection time 2]	11211 = 16#2BCB	2052/C	5FBF/53	207
99/01/0C	tdC	[DC injection time 2]	11211 = 16#2BCB	2052/C	5FBF/53	281
99/01/0D	IdC2	[DC inject. level 2]	11212 = 16#2BCC	2052/D	5FBF/54	207
99/01/0D	IdC2	[DC inject. level 2]	11212 = 16#2BCC	2052/D	5FBF/54	281
99/01/0E	tdl	[DC injection time 1]	11213 = 16#2BCD	2052/E	5FBF/55	206
99/01/0E	tdl	[DC injection time 1]	11213 = 16#2BCD	2052/E	5FBF/55	281
99/01/1F	dCF	[Ramp divider]	11230 = 16#2BDE	2052/1F	5FB3/ED	280
99/01/1F	dCF	[Ramp divider]	11230 = 16#2BDE	2052/1F	5FB3/ED	206
99/01/CC	Fnb	Fault counter	7393 = 16#1CE1	202B/5E	5FB9/CC	104
9A/01/02	PS2	[2 preset speeds]	11401 = 16#2C89	2054/2	5FBD/06	210

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
9A/01/04	PS8	[8 preset speeds]	11403 = 16#2C8B	2054/4	5FBD/08	211
9A/01/05	PS16	[16 preset speeds]	11404 = 16#2C8C	2054/5	5FBD/09	211
9A/01/08	dCC0	Command channel active on last fault	64300 = 16#FB2C	2265/1	5FBA/08	106
9A/01/09	dCC1	Active command channel on fault n-1	64301 = 16#FB2D	2265/2	5FBA/09	109
9A/01/0A	dCC2	Active command channel on fault n-2	64302 = 16#FB2E	2265/3	5FBA/0A	111
9A/01/0B	dCC3	Active command channel on fault n-3	64303 = 16#FB2F	2265/4	5FBA/0B	113
9A/01/0B	SP2	[Preset speed 2]	11410 = 16#2C92	2054/B	5FB3/F0	211
9A/01/0C	dCC4	Active command channel on fault n-4	64304 = 16#FB30	2265/5	5FBA/0C	115
9A/01/0C	SP3	[Preset speed 3]	11411 = 16#2C93	2054/C	5FB3/F1	212
9A/01/0D	dCC5	Active command channel on fault n-5	64305 = 16#FB31	2265/6	5FBA/0D	117
9A/01/0D	SP4	[Preset speed 4]	11412 = 16#2C94	2054/D	5FB3/F2	212
9A/01/0E	dCC6	Active command channel on fault n-6	64306 = 16#FB32	2265/7	5FBA/0E	119
9A/01/0E	SP5	[Preset speed 5]	11413 = 16#2C95	2054/E	5FB3/F3	212
9A/01/0F	dCC7	Active command channel on fault n-7	64307 = 16#FB33	2265/8	5FBA/0F	121
9A/01/0F	SP6	[Preset speed 6]	11414 = 16#2C96	2054/F	5FB3/F4	212
9A/01/10	dCC8	Active command channel on fault n-8	64308 = 16#FB34	2265/9	5FBA/10	123
9A/01/10	SP7	[Preset speed 7]	11415 = 16#2C97	2054/10	5FB3/F5	212
9A/01/11	drC0	Reference channel active on last fault	64310 = 16#FB36	2265/B	5FBA/11	106
9A/01/11	SP8	[Preset speed 8]	11416 = 16#2C98	2054/11	5FB3/F6	213
9A/01/12	drC1	Active reference channel on fault n-1	64311 = 16#FB37	2265/C	5FBA/12	109
9A/01/12	SP9	[Preset speed 9]	11417 = 16#2C99	2054/12	5FBF/5F	213
9A/01/13	drC2	Active reference channel on fault n-2	64312 = 16#FB38	2265/D	5FBA/13	111
9A/01/13	SP10	[Preset speed 10]	11418 = 16#2C9A	2054/13	5FBF/60	213
9A/01/14	drC3	Active reference channel on fault n-3	64313 = 16#FB39	2265/E	5FBA/14	113
9A/01/14	SP11	[Preset speed 11]	11419 = 16#2C9B	2054/14	5FBF/61	213
9A/01/15	drC4	Active reference channel on fault n-4	64314 = 16#FB3A	2265/F	5FBA/15	115

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
9A/01/15	SP12	[Preset speed 12]	11420 = 16#2C9C	2054/15	5FBF/62	213
9A/01/16	drC5	Active reference channel on fault n-5	64315 = 16#FB3B	2265/10	5FBA/16	117
9A/01/16	SP13	[Preset speed 13]	11421 = 16#2C9D	2054/16	9F/01/63	214
9A/01/17	drC6	Active reference channel on fault n-6	64316 = 16#FB3C	2265/11	5FBA/17	119
9A/01/17	SP14	[Preset speed 14]	11422 = 16#2C9E	2054/17	5FBF/64	214
9A/01/18	drC7	Active reference channel on fault n-7	64317 = 16#FB3D	2265/12	5FBA/18	121
9A/01/18	SP15	[Preset speed 15]	11423 = 16#2C9F	2054/18	5FBF/65	214
9A/01/19	drC8	Active reference channel on fault n-8	64318 = 16#FB3E	2265/13	5FBA/19	123
9A/01/19	SP16	[Preset speed 16]	11424 = 16#2CA0	2054/19	5FBF/66	214
9A/01/66	USP	[+ speed assignment]	11501 = 16#2CED	2055/2	5FBD/0A	215
9A/01/67	dSP	[-Speed assignment]	11502 = 16#2CEE	2055/3	5FBD/0B	215
9A/01/68	Str	[Reference saved]	11503 = 16#2CEF	2055/4	5FBD/0C	215
9A/01/6A	SrP	[+/-Speed limitation]	11505 = 16#2CF1	2055/6	5FB3/F7	216
9A/01/7A	dSI	[-Speed assignment]	11521 = 16#2D01	2055/16	5FBD/0F	216
9B/01/02	LAF	[Stop FW limit sw.]	11601 = 16#2D51	2056/2	5FBD/10	220
9B/01/03	LAr	[Stop RV limit sw.]	11602 = 16#2D52	2056/3	5FBD/11	220
9B/01/04	LAS	[Stop type]	11603 = 16#2D53	2056/4	5FBD/12	220
9B/01/66	tLS	[Low speed time out]	11701 = 16#2DB5	2057/2	5FB3/F8	146
9B/01/66	tLS	[Low speed time out]	11701 = 16#2DB5	2057/2	5FB3/F8	235
9C/01/02	SA2	[Summing ref. 2]	11801 = 16#2E19	2058/2	5FBD/13	199
9C/01/03	SA3	[Summing ref. 3]	11802 = 16#2E1A	2058/3	5FBD/14	199
9C/01/0C	dA2	[Subtract ref. 2]	11811 = 16#2E23	2058/C	5FBD/15	199
9C/01/0D	dA3	[Subtract ref. 3]	11812 = 16#2E24	2058/D	5FBD/16	199
9C/01/16	MA2	[Multiplier ref. 2]	11821 = 16#2E2D	2058/16	5FBD/17	200
9C/01/17	MA3	[Multiplier ref. 3]	11822 = 16#2E2E	2058/17	5FBD/18	200
9C/01/20	MFr	Multiplying coefficient	11831 = 16#2E37	2058/20	5FB6/3E	78
9C/01/66	PIF	[PID feedback ass.]	11901 = 16#2E7D	2059/2	5FBD/19	230
9C/01/69	PIF1	[Min PID feedback]	11904 = 16#2E80	2059/5	5FBF/6A	230
9C/01/6A	PIF2	[Max PID feedback]	11905 = 16#2E81	2059/6	5FBF/6B	230
9C/01/6B	PIP1	[Min PID reference]	11906 = 16#2E82	2059/7	5FBF/6C	231
9C/01/6C	PIP2	[Max PID reference]	11907 = 16#2E83	2059/8	5FBF/6D	231

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
9C/01/6D	PII	[Act. internal PID ref.]	11908 = 16#2E84	2059/9	5FBD/1A	231
9C/01/6E	Pr2	[2 preset PID ref.]	11909 = 16#2E85	2059/A	5FBD/1B	236
9C/01/6F	Pr4	[4 preset PID ref.]	11910 = 16#2E86	2059/B	5FBD/1C	236
9C/01/79	RPI	[Internal PID ref.]	11920 = 16#2E90	2059/15	5FBF/6E	231
9C/01/7A	rP2	[Preset ref. PID 2]	11921 = 16#2E91	2059/16	5FBF/6F	237
9C/01/7B	rP3	[Preset ref. PID 3]	11922 = 16#2E92	2059/17	5FBF/70	237
9C/01/7C	rP4	[Preset ref. PID 4]	11923 = 16#2E93	2059/18	9F/01/71	237
9C/01/8D	PIC	[PID correct. reverse]	11940 = 16#2EA4	2059/29	5FB3/F9	232
9C/01/8E	RPG	[PID prop. gain]	11941 = 16#2EA5	2059/2A	5FB3/FA	231
9C/01/8F	rIG	[PID integral gain]	11942 = 16#2EA6	2059/2B	5FB3/FB	232
9C/01/90	rdG	[PID derivative gain]	11943 = 16#2EA7	2059/2C	5FB3/FC	232
9C/01/91	PIS	[PID integral reset]	11944 = 16#2EA8	2059/2D	5FBD/1E	234
9C/01/97	FPI	[Speed ref. assign.]	11950 = 16#2EAE	2059/33	5FBD/1F	234
9C/01/98	PSr	[Speed input %]	11951 = 16#2EAF	2059/34	5FB3/FD	234
9C/01/99	POL	[Min PID output]	11952 = 16#2EB0	2059/35	5FBF/76	232
9C/01/9A	POH	[Max PID output]	11953 = 16#2EB1	2059/36	5FBF/77	233
9C/01/9A	SCS	Save configuration	8001 = 16#1F41	2032/2	5FBC/9A	69
9C/01/9B	FCS	Restore configuration	8002 = 16#1F42	2032/3	5FBC/9B	69
9C/01/9B	PIM	[Manual reference]	11954 = 16#2EB2	2059/37	5FBD/20	235
9C/01/9C	CHA1	Assignment for 2 sets	8021 = 16#1F55	2032/16	5FBC/9C	70
9C/01/9C	CNF1	Assignment for 2 configurations	8021 = 16#1F55	2032/16	5FBC/9C	67
9C/01/9D	CHA2	Assignment for 3 sets	8022 = 16#1F56	2032/17	5FBC/9D	70
9C/01/9D	CNF2	Assignment for 3 configurations	8022 = 16#1F56	2032/17	5FBC/9D	67
9C/01/A1	rSL	[PID wake up thresh.]	11960 = 16#2DB5	2059/3D	5FBD/21	235
9C/01/A2	PAL	[Min fbk alarm]	11961 = 16#2EB9	2059/3E	5FB3/FE	233
9C/01/A3	PAH	[Max fbk alarm]	11962 = 16#2EBA	2059/3F	5FB3/FF	233
9C/01/A4	PEr	[PID error Alarm]	11963 = 16#2EBB	2059/40	5FB4/01	233
9C/01/AB	PAU	[Auto/Manual assign.]	11970 = 16#2EC2	2059/47	5FBD/22	235
9C/01/B5	rPE	PID regulator discrepancy	11980 = 16#2ECC	2059/51	5FB9/E5	91
9C/01/B6	rPF	PID regulator feedback reference	11981 = 16#2ECD	2059/52	5FB9/E6	91
9C/01/B7	rPC	PID reference after ramp	11982 = 16#2ECE	2059/53	5FB9/E7	91

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
9C/01/B8	rPO	PID regulator limit output reference	11983 = 16#2ECF	2059/54	5FB9/E8	91
9C/01/B9	PrP	[PID ramp]	11984 = 16#2ED0	2059/55	5FBF/7B	232
9D/01/07	PS4	[4 preset speeds]	11402 = 16#2C8A	2054/3	5FBD/07	210
9D/01/0E	USI	[+ speed assignment]	11520 = 16#2D00	2055/15	5FBD/0E	216
9D/01/66	EnA	[ENA system]	12101 = 16#2F45	205B/2	5FBD/23	161
9D/01/67	AUS	ENA average speed	12102 = 16#2F46	205B/3	5FB9/EA	88
9D/01/68	GPE	[ENA prop.gain]	12103 = 16#2F47	205B/4	5FBF/7C	161
9D/01/69	GIE	[ENA integral gain]	12104 = 16#2F48	205B/5	5FBF/7D	161
9D/01/6A	rAP	[Reduction ratio]	12105 = 16#2F49	205B/6	5FBF/7E	161
9E/01/02	trC	[Yarn control]	12201 = 16#2FA9	205C/2	5FBD/24	254
9E/01/03	trH	[Traverse high]	12202 = 16#2FAA	205C/3	5FBF/7F	254
9E/01/04	trL	[Traverse Low]	12203 = 16#2FAB	205C/4	5FBF/80	254
9E/01/05	qSH	[Quick step High]	12204 = 16#2FAC	205C/5	5FBF/81	254
9E/01/06	qSL	[Quick step Low]	12205 = 16#2FAD	205C/6	5FBF/82	254
9E/01/07	tUP	[Traverse ctrl. accel.]	12206 = 16#2FAE	205C/7	5FBF/83	255
9E/01/08	tdn	[Traverse ctrl. decel]	12207 = 16#2FAF	205C/8	5FBF/84	255
9E/01/09	tbO	[Reel time]	12208 = 16#2FAB	205C/9	5FBF/85	255
9E/01/0A	EbOt	Current bobbin time	12209 = 16#2FB1	205C/A	5FB9/EB	93
9E/01/0B	rtr	[Init. traverse ctrl]	12210 = 16#2FB2	205C/B	5FBD/25	257
9E/01/0C	dtF	[Decrease ref. speed]	12211 = 16#2FB3	205C/C	5FBF/86	256
9E/01/0D	SnC	[Counter wobble]	12212 = 16#2FB4	205C/D	5FBD/26	256
9E/01/0E	EbO	[End reel]	12213 = 16#2FB5	205C/E	5FBD/27	255
9E/01/0F	tSY	[Sync. wobble]	12214 = 16#2FB6	205C/F	5FBD/28	256
9E/01/66	HSO	[High speed hoisting]	12301 = 16#300D	205D/2	5FBD/29	228
9E/01/67	CLO	[High speed l Limit]	12302 = 16#300E	205D/3	5FBF/87	229
9E/01/68	COF	[Motor speed coeff.]	12303 = 16#300F	205D/4	5FBF/88	228
9E/01/69	COOr	[Gen. speed coeff]	12304 = 16#3010	205D/5	5FBF/89	228
9E/01/6A	OSP	[Measurement spd]	12305 = 16#3011	205D/6	5FBF/8A	228
9E/01/6B	SCL	[l Limit. frequency]	12306 = 16#3012	205D/7	5FBF/8B	229
9E/01/6C	tOS	[Load measuring tm.]	12307 = 16#3013	205D/8	5FBF/8C	228
9F/01/02	U0	[U0]	12401 = 16#3071	205E/2	5FBD/2A	150

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
9F/01/04	U1	[U1]	12403 = 16#3073	205E/4	5FBD/2B	151
9F/01/05	F1	[F1]	12404 = 16#3074	205E/5	5FBD/2C	151
9F/01/06	U2	[U2]	12405 = 16#3075	205E/6	5FBD/2D	151
9F/01/07	F2	[F2]	12406 = 16#3076	205E/7	5FBD/2E	151
9F/01/08	U3	[U3]	12407 = 16#3077	205E/8	5FBD/2F	151
9F/01/09	F3	[F3]	12408 = 16#3078	205E/9	5FBD/30	152
9F/01/0A	U4	[U4]	12409 = 16#3079	205E/9	5FBD/31	152
9F/01/0B	F4	[F4]	12410 = 16#307A	205E/B	5FBD/32	152
9F/01/0C	U5	[U5]	12411 = 16#307B	205E/C	5FBD/33	152
9F/01/0D	F5	[F5]	12412 = 16#307C	205E/D	5FBD/34	152
9F/01/66	SAF	[Stop FW limit sw.]	12501 = 16#30D5	205F/2	5FBD/35	247
9F/01/67	SAr	[Stop RV limit sw.]	12502 = 16#30D6	205F/3	5FBD/36	247
9F/01/68	dAF	[Slowdown forward]	12503 = 16#30D7	205F/4	5FBD/37	248
9F/01/69	dAr	[Slowdown reverse]	12504 = 16#30D8	205F/5	5FBD/38	248
9F/01/6A	dsF	[Deceleration type]	12505 = 16#30D9	205F/6	5FBD/39	249
9F/01/6B	PAS	[Stop type]	12506 = 16#30DA	205F/7	5FBD/3A	249
9F/01/6C	CLS	[Disable limit sw.]	12507 = 16#30DB	205F/8	5FBD/3B	248
A0/01/02	SUL	[Motor surge limit.]	12601 = 16#3139	2060/2	5FBD/3C	162
A0/01/03	SOP	[Volt surge limit. opt]	12602 = 16#313A	2060/3	5FBD/3D	163
A0/01/66	nMA1	Communication scanner, address of read word 1	12701 = 16#319D	2061/2	5FBD/3E	129
A0/01/67	nMA2	Communication scanner, address of read word 2	12702 = 16#319E	2061/3	5FBD/3F	129
A0/01/68	nMA3	Communication scanner, address of read word 3	12703 = 16#319F	2061/4	5FBD/40	129
A0/01/69	nMA4	Communication scanner, address of read word 4	12704 = 16#31A0	2061/5	5FBD/41	129
A0/01/6A	nMA5	Communication scanner, address of read word 5	12705 = 16#31A1	2061/6	5FBD/42	130
A0/01/6B	nMA6	Communication scanner, address of read word 6	12706 = 16#31A2	2061/7	5FBD/43	130
A0/01/6C	nMA7	Communication scanner, address of read word 7	12707 = 16#31A3	2061/8	5FBD/44	130
A0/01/6D	nMA8	Communication scanner, address of read word 8	12708 = 16#31A4	2061/9	5FBD/45	130

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
A0/01/7A	nCA1	Communication scanner, address of write word 1	12721 = 16#31B1	2061/16	5FBD/46	128
A0/01/7B	nCA2	Communication scanner, address of write word 2	12722 = 16#31B2	2061/17	5FBD/47	128
A0/01/7C	nCA3	Communication scanner, address of write word 3	12723 = 16#31B3	2061/18	5FBD/48	128
A0/01/7D	nCA4	Communication scanner, address of write word 4	12724 = 16#31B4	2061/19	5FBD/49	128
A0/01/7E	nCA5	Communication scanner, address of write word 5	12725 = 16#31B5	2061/1A	5FBD/4A	128
A0/01/7F	nCA6	Communication scanner, address of write word 6	12726 = 16#31B6	2061/1B	5FBD/4B	129
A0/01/80	nCA7	Communication scanner, address of write word 7	12727 = 16#31B7	2061/1C	5FBD/4C	129
A0/01/81	nCA8	Communication scanner, address of write word 8	12728 = 16#31B8	2061/1D	5FBD/4D	129
A0/01/8E	nM1	Communication scanner, value of read word 1	12741 = 16#31C5	2061/2A	5FB6/43	127
A0/01/8F	nM2	Communication scanner, value of read word 2	12742 = 16#31C6	2061/2B	5FB6/44	127
A0/01/90	nM3	Communication scanner, value of read word 3	12743 = 16#31C7	2061/2C	5FB6/45	127
A0/01/91	nM4	Communication scanner, value of read word 4	12744 = 16#31C8	2061/2D	5FB6/46	127
A0/01/92	nM5	Communication scanner, value of read word 5	12745 = 16#31C9	2061/2E	5FB6/47	127
A0/01/93	nM6	Communication scanner, value of read word 6	12746 = 16#31CA	2061/2F	5FB6/48	127
A0/01/94	nM7	Communication scanner, value of read word 7	12747 = 16#31CB	2061/30	5FB6/49	128
A0/01/95	nM8	Communication scanner, value of read word 8	12748 = 16#31CC	2061/31	5FB6/4A	128
A0/01/A2	nC1	Communication scanner, value of write word 1	12761 = 16#31D9	2061/3E	5FB6/4B	126
A0/01/A3	nC2	Communication scanner, value of write word 2	12762 = 16#31DA	2061/3F	5FB6/4C	126
A0/01/A4	nC3	Communication scanner, value of write word 3	12763 = 16#31DB	2061/40	5FB6/4D	126
A0/01/A5	nC4	Communication scanner, value of write word 4	12764 = 16#31DC	2061/41	5FB6/4E	126
A0/01/A6	nC5	Communication scanner, value of write word 5	12765 = 16#31DD	2061/42	5FB6/4F	126

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
A0/01/A7	nC6	Communication scanner, value of write word 6	12766 = 16#31DE	2061/43	5FB6/50	126
A0/01/A8	nC7	Communication scanner, value of write word 7	12767 = 16#31DF	2061/44	5FB6/51	126
A0/01/A9	nC8	Communication scanner, value of write word 8	12768 = 16#31E0	2061/45	5FB6/52	127
A1/01/65	CFPS	Active parameter set	12900 = 16#3264	2063/01	5FB9/EC	86
A1/01/65	CFPS	Active parameter set	12900 = 16#3264	2063/01	5FB9/EC	70
A1/01/66	VAL	Load parameter set command	12901 = 16#3265	2063/02	5FB9/ED	73
A1/01/67	CHA1	[2 Parameter sets]	12902 = 16#3266	2063/3	5FBD/54	250
A1/01/68	CHA2	[3 Parameter sets]	12903 = 16#3267	2063/4	5FBD/55	250
A2/01/66	dbS	[Time to motor run]	13101 = 16#332D	2065/2	5FB4/02	245
A2/01/67	dAS	[Time to open cont.]	13102 = 16#332E	2065/3	5FB4/03	246
A2/01/68	rCA	[Output contact. fdbk]	13103 = 16#332F	2065/4	5FBD/56	245
A2/01/69	OCC	[Out. contactor ass.]	13104 = 16#3330	2065/5	5FBD/57	245
A3/01/02	PtC1	[PTC1 probe]	13201 = 16#3391	2066/2	5FBF/CB	260
A3/01/03	PtC2	[PTC2 probe]	13202 = 16#3392	2066/3	5FBF/CC	260
A3/01/04	PtCL	[LI6 = PTC probe]	13203 = 16#3396	2066/4	5FBF/CD	260
A3/01/66	PGA	[Reference type]	13301 = 16#33F5	2067/2	5FBD/58	177
A3/01/67	PIL	[RP min value]	13302 = 16#33F6	2067/3	5FBD/59	176
A3/01/68	PFr	[RP max value]	13303 = 16#33F7	2067/4	5FBD/5A	176
A3/01/69	PFI	[RP filter]	13304 = 16#33F8	2067/5	5FBD/5B	176
A3/01/6F	EIL	[Freq. min value]	13310 = 16#33FE	2067/B	5FBD/5C	178
A3/01/70	EFr	[Freq. max value]	13311 = 16#33FF	2067/C	5FBD/5D	178
A3/01/71	EFI	[Freq. signal filter]	13312 = 16#3400	2067/D	5FBD/5E	178
A4/01/02	PPhr	[Output Ph rotation]	13401 = 16#3459	2068/2	5FBD/5F	150
A4/01/66	FN1	[F1 key assignment]	13501 = 16#34BD	2069/2	5FBD/60	196
A4/01/67	FN2	[F2 key assignment]	13502 = 16#34BE	2069/3	5FBD/61	196
A4/01/68	FN3	[F3 key assignment]	13503 = 16#34BF	2069/4	5FBD/62	196
A4/01/69	FN4	[F4 key assignment]	13504 = 16#34C0	2069/5	5FBD/63	197
A4/01/82	bMP	[HMI cmd.]	13529 = 16#34D9	2069/1E	5FBD/6C	197
A5/01/02	LES	[Drive lock]	13601 = 16#3521	206A/2	5FBD/6D	244
A5/01/03	LLC	[Line contactor ass.]	13602 = 16#3522	206A/3	5FBD/6E	244

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
A5/01/04	LCt	[Mains V. time out]	13603 = 16#3523	206A/4	5FBD/6F	244
A6/01/02	UrES	[Evacuation Input V.]	13801 = 16#35E9	206C/2	5FBD/70	270
A6/01/03	USL	[Undervoltage level]	13802 = 16#35EA	206C/3	5FBD/71	270
A6/01/04	USb	[UnderV. fault mgt]	13803 = 16#35EB	206C/4	5FBD/72	270
A6/01/05	USt	[Undervolt. time out]	13804 = 16#35EC	206C/5	5FBD/73	270
A6/01/0C	UPL	[Prevention level]	13811 = 16#35F3	206C/C	5FBD/74	271
A6/01/0D	tbS	[DC bus maintain tm]	13812 = 16#35F4	206C/D	5FBF/CE	271
A6/01/0E	tSM	[UnderV. restart tm]	13813 = 16#35F5	206C/E	5FBF/CF	271
A6/01/0F	StM	[Max stop time]	13814 = 16#35EC	206C/F	5FBF/D0	271
A6/01/20	rFt	[Evacuation assign.]	13831 = 16#3607	206C/20	5FBD/76	258
A6/01/21	rSU	[Evacuation Input V.]	13832 = 16#3608	206C/21	5FBD/77	258
A6/01/22	rSP	[Evacuation freq.]	13833 = 16#3609	206C/22	5FB4/04	258
A6/01/2A	dCO	[Precharge cont. ass.]	13841 = 16#3611	206C/2A	5FBD/78	259
A6/01/66	FLI	[Fluxing assignment]	13901 = 16#364D	206D/2	5FBD/79	219
A6/01/67	FLU	[Motor fluxing]	13902 = 16#364E	206D/3	5FB4/05	146
A6/01/67	FLU	[Motor fluxing]	13902 = 16#364E	206D/3	5FB4/05	219
A7/01/02	PPI	[Pairing password]	14001 = 16#36B1	206E/2	5FBD/7A	279
A7/01/66	Ubr	[Braking level]	14101 = 16#3715	206F/2	5FBF/D4	164
A7/01/67	bbA	[Braking balance]	14102 = 16#3716	206F/3	5FBD/7B	164
A7/01/70	brO	[DB res. protection]	14111 = 16#371F	206F/C	5FBD/7C	278
A7/01/71	brP	[DB Resistor Power]	14112 = 16#3720	206F/D	5FBD/7D	278
A7/01/72	brU	[DB Resistor value]	14113 = 16#3721	206F/E	5FBD/7E	278
A7/01/73	tHb	DBR thermal state	14114 = 16#3722	206F/F	5FBD/7F	92
A8/01/02	UC2	[Vector Control 2pt]	14201 = 16#3779	2070/2	5FBD/80	153
A8/01/03	UCP	[V. constant power]	14202 = 16#377A	2070/3	5FBD/81	153
A8/01/04	FCP	[Freq. Const Power]	14203 = 16#377B	2070/4	5FBD/82	153
A8/01/66	LbA	[Load sharing]	14301 = 16#37DD	2071/2	5FBD/83	164
A8/01/67	LbC	[Load correction]	14302 = 16#37DE	2071/3	5FBF/D5	164
A8/01/68	LbC1	[Correction min spd]	14303 = 16#37DF	2071/4	5FBF/D6	165
A8/01/69	LbC2	[Correction max spd]	14304 = 16#37E0	2071/5	5FBF/D7	165
A8/01/6A	LbC3	[Torque offset]	14305 = 16#37E1	2071/6	5FBF/D8	165

Index of parameter DeviceNet addresses

DeviceNet path	Code	Name	Logic address	CANopen index	INTERBUS index	Page
A8/01/6B	LbF	[Sharing filter]	14306 = 16#37E2	2071/7	5FBF/D9	165
B7/01/01	CMd	Control word	8601 = 16#2199	6040	6040	74



atv71_Parameters_EN_V1

2005-01