Generalizable IC Routing using Deep RL with a GNN-based Policy Network

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Abstract

Routing in integrated circuits (IC) design is often modeled as a pathfinding problem and has consistently represented one of the most challenging aspects due to its complexity and time-consuming nature. Even with state-of-the-art commercial CAD tools, routing complex designs can take hours, if not days. Consequently, there is a growing interest in applying machine learning-based approaches to overcome these challenges. In previous work, we introduced a novel reinforcement learning (RL) approach for routing. While this RL router outperformed the baseline, it was limited by poor generalizability, requiring a new policy to be trained from scratch for each problem. In this project, we aim to address this limitation by introducing a novel graph neural network (GNN) as the policy network for the RL router. This enhancement allows the agents to generalize their learned routing policies to new, unseen designs, reducing the computation time significantly.

1 Introduction

Physical design is a crucial phase in the IC design process, wherein the logical representation of the chip is converted into a physical geometric layout essential for fabrication foundries. This stage is subdivided, with routing being a key step were pins specified by netlists are connected using wires and vias. The quality of the routing, often evaluated by wirelength and egde overflow, impacts vital chip metrics like performance, power, and area [1].

For 2-dimensional circuit layouts, routing can be modeled as a pathfinding problem in a 2D grid, where the objective is to establish paths connecting pins placed on different coordinates of the said grid. This grid can be treated like a graph, with the circuit's pins placed on its nodes and the connections between them occupying its edges. Each edge possesses a capacity feature, indicating the maximum number of permissible wires due to the physical constraints of wire thickness and finite space within a chip region. An optimal routing solution seeks to minimize total wire length and avoid edge overflow (usage of an edge exceeding its capacity), ensuring all connections are efficient and within capacity.

Two important terminologies here are net and netlist. A *net* refers to a set of pins requiring connection, and a *netlist* is a set consisting of multiple nets that need routing during the process. For clarity, we will use the terms nodes and pins interchangeably throughout this paper.

Complexities of traditional routing algorithms have inspired researchers to use machine-learning-based techniques instead. One such technique is RL, and specifically, its deep variant (DRL), which is a fusion of RL and deep neural networks. It has showcased capabilities exceeding human performance in various contexts [2].

In prior research, we introduced a multi-agent RL router to address the IC routing challenge. Each agent, tasked with routing one individual net, operated concurrently under a shared super policy, as we designed the agents to be homogeneous and fully-cooperative. Trained with proximal policy optimization (PPO) [3] and evaluated against benchmarks, this RL router demonstrated superior performance compared to the A* baseline. However, a limitation emerged: the policy, though effective for specific benchmarks, lacked generalizability for other, albeit similar, problems. We attributed this shortfall to the policy neural network's architecture, comprising a multilayer perceptron (MLP), which struggled to extract rich information from the state encodings.

Contrastingly, GNNs exhibit robust generalization capabilities when processing graphical data [4, 5], aligning with the routing problem's grid graph model. In this project, we introduce a novel message-passing GNN architecture integrated into the RL router's policy and value networks. The GNN accepts the routing grid graph from the environment as state input and determines actions accordingly. This innovation aims to bolster the RL router's ability to generalize trained policy to problems not encountered during training, promising substantial reductions in computation time.

2 Related work

Numerous studies in existing literature have verified the effectiveness of employing GNNs within the policy/value network of RL models to enhance generalization, especially when the agent's environment is graph-representable. However, to our knowledge, no existing study has directly explored the use of GNN equipped RL for IC routing.

For instance, [6] introduced an RL IC router trained via a Deep Q-Network (DQN), utilizing an MLP as the RL agent's value network. However, similar to our previous work, it showed a lack of generalization. [7] proposed an attention-based RL router, which was built on top of a genetic algorithm (GA) router. Although the authors claimed it had significant performance and generalization capabilities, it was only compared against the very GA router that acted as its baseline, and the performance gain could only be described as marginal.

Contrastingly, [4] incorporated a GNN as the value network of an RL agent, also trained by DQN. This approach manifested notable generalization capabilities in network routing—a field bearing similarities to IC routing—and serves as the principal inspiration for this paper. In parallel, both [8] and [5] echoed the integration of GNNs with RL models wherein the operational problems are graphs. Furthermore, [9] and [10] adopted GNNs as encoder layers for their respective RL policy and value networks. [11] introduced a hybrid RL agent, which was designed to solve both IC placement and routing simultaneously. The authors incorporated both CNN and GNN models in the policy network of their RL agent to capture as much information about the netlists as possible; however, their methodology put more focus on placement than routing, and they didn't evaluate their agent in terms of generalizability.

3 Methodology

3.1 IC routing as a pathfinding problem

Figure 1 illustrates a simplified IC routing problem, framed as a pathfinding task on a grid graph. The objective is to route a net between two pins at coordinates (0,0) and (3,3). Each edge in this graph has a capacity of one, indicating that it can be traversed no more than once. The challenge, therefore, lies in finding the shortest path between these points while adhering to the edge traversal constraints.

3.2 GNN-based RL framework for IC routing

An illustration of the proposed RL routing framework is displayed in Figure 2. The framework involves an agent engaging repeatedly with an environment. At each time step t, the agent is presented with an observation S_t and a reward R_t , and then at the subsequent time step, t+1, it executes an action A_{t+1} in the environment. This process is directed by a policy network and a value network. The policy network approximates the policy $\pi_{\theta_1}(a|s) = P(A_{t+1} = a|S_t = s)$, signifying the probability of choosing action a given the environment is in state s. This function is represented by the proposed GNN, parameterized by θ_1 , which will be detailed later. The value

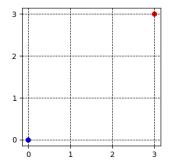


Figure 1: The grid graph of a simple IC routing problem.

network approximates the value function $V_{\theta_2}(s)$ and is instrumental in updating the policy network's parameters θ_1 . It employs a similar GNN architecture as the policy network but operates with distinct parameters, ensuring $\theta_1 \neq \theta_2$.

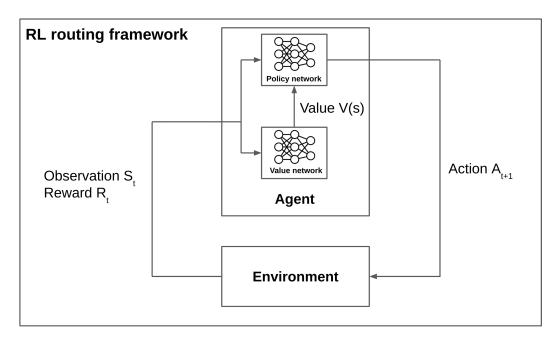


Figure 2: The overview of the proposed DRL routing framework.

3.3 Environment

The environment encapsulates the grid pathfinding problem. It processes actions from the agent, updates its internal states, and subsequently provides new observation states and rewards to the agent.

3.3.1 Observations

Observations are the states discernible by an agent from the environment, containing essential details about the environment. These include a node feature matrix $\mathbf{X} \in \mathbb{R}^{N \times 2}$, an edge feature matrix $\mathbf{E} \in \mathbb{R}^{N \times N}$, and an adjacency matrix $\mathbf{A} \in \mathbb{R}^{N \times N}$. In a grid comprising N nodes, each node is represented by a 2D feature vector x, with both elements being binary. The first element, x_1 , signifies that the agent is currently at this node if it equals 1, while x_2 denotes the target position. The matrix \mathbf{E} holds the scalar capacity of each edge, which decreases by 1 each time the agent traverses a specific edge.

3.3.2 Reward Function

The environment imposes a -1 penalty on the agent for each step taken not leading to the target, thereby incentivizing the discovery of the shortest path. Successfully reaching the target, characterized by the agent and target occupying the same node, grants a +100 reward. This event is indicated when a vector x in \mathbf{X} has both elements equal to 1. The reward function $R(\mathbf{X})$ is defined as:

$$R(\mathbf{X}) = \begin{cases} 100 & \text{if } \exists i \in \{1, 2, \dots, N\}, x_{i_1} = x_{i_2} = 1, \\ -1 & \text{otherwise.} \end{cases}$$
 (1)

This reward structure encourages the agent to minimize wirelength, synonymous with the distance traveled to reach the target. Any unnecessary deviations result in reduced cumulative rewards.

3.4 Agent

The agent, upon receiving observations and rewards from the environment, inputs these into the policy and value networks. The former generate probability distributions of actions. During training, the parameters of these networks are optimized to produce a sequence of actions that maximizes cumulative reward.

3.4.1 Actions

Actions within this framework are represented by integers ranging from 0 to 3, corresponding to the agent moving up, right, down, or left from its current position via an edge. Following each action, the environment transitions to a new state, resulting in updates to $\bf E$ and $\bf X$.

3.4.2 GNN Design

The Graph Neural Network (GNN) utilizes A, E, and X as inputs. Initially, it encodes the node feature matrix X into a hidden representation matrix H^t . This process is represented as:

$$h_i^0 = F_{\text{encode}}(x_i) \tag{2}$$

where F_{encode} is an MLP. The superscript t in \mathbf{H} denotes the number of message-passing iterations. At the outset, when no message passing has occurred, t=0.

The subsequent step involves computing messages for each node, formulated as:

$$m_{ji} = F_{\text{message}}([h_j^t, h_i^t, e_{ji}]) \tag{3}$$

Here, F_{message} is another MLP. The vector $[h_j^t, h_i^t, e_{ji}]$ is a concatenation of the hidden states h_j^t and h_i^t from $\mathbf{H^t}$, and the scalar capacity $e_{ji} \in \mathbf{E}$ of the edge connecting nodes i and j. The third step aggregates messages for each node from all neighboring nodes:

$$\bar{m}_i = \sum_{j \in N_{\rm in}} m_{ji} \tag{4}$$

The aggregated message \bar{m}_i for node i is the sum of messages received from all its $N_{\rm in}$ neighbors. The fourth step updates the hidden states of all nodes using their current states and aggregated messages:

$$h_i^{t+1} = F_{\text{update}}(h_i^t, \bar{m}_i) \tag{5}$$

In this step, $F_{\rm update}$ is implemented using a gated recurrent unit (GRU). The message-passing process, encompassing steps 2 to 4, is repeated T times, where T is the diameter of the input grid graph.

The final step involves reading out the policy logits for the policy network, or the value for the value network. The policy logits are then passed through a softmax layer to obtain the probability distribution of actions. This step aggregates all node states h_i^T and forwards the result to another MLP for further processing.

3.5 Training

The proposed GNN is integrated into our RL routing framework, acting as both the policy and value network. Training is conducted using PPO [3] with RLlib. The GNN's parameters are optimized through stochastic gradient descent, influenced by the feedback from the value network.

4 Experiments

An MLP-based RL framework serves as a baseline for comparing the performance of our GNN-based RL network. Both frameworks underwent 40 training iterations with benchmark A. Following this, three evaluations were carried out:

- (1) Policies trained with benchmark A were tested on the same benchmark to compare the performance and training efficiency between GNN and MLP.
- (2) Zero-shot policies from benchmark A, were applied on benchmark B to assess the generalizability of the two frameworks. Details of these benchmarks are provided in the subsequent table.
- (3) The policies were further trained on benchmark B for an additional 40 iterations (fine-tuning). The performances of GNN and MLP during fine-tuning were then compared.

Parameter	Benchmark A	Benchmark B	
Length	4	4	
Width	4	4	
Source node coordinates	(0,0)	(3,0)	
Target node coordinates	(3,2)	(0,1)	
Edge capacity	2	2	

Table 1: Two benchmarks used for training and testing.

4.1 Comparing training efficiency

Figure 3 displays the training efficiencies (reward versus training iteration) of both the GNN and MLP-based frameworks. Notably, the GNN-based framework demonstrates an ability to attain a higher reward after 40 iterations of training. Figure 4 illustrates the routing solutions generated by both frameworks. It is observed that each framework successfully identifies an optimal path between the source and the target node. This outcome is attributed to the simplicity of the benchmark. Consequently, it can be concluded that both GNN and MLP are capable of learning a policy from one benchmark, which then performs optimally when tested with the same benchmark utilized for training.

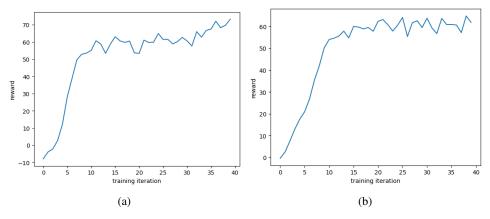


Figure 3: Training efficiencies for (a) GNN-based framework, (b) MLP-based framework.

4.2 Comparing zero-shot performance

Figure 5 presents the zero-shot performance of both the GNN and MLP frameworks on benchmark B, utilizing policies acquired from benchmark A. The results indicate that neither framework is able to produce valid routing results, despite the benchmarks being quite similar and straightforward in nature. This leads to the conclusion that substituting MLP with GNN in the RL framework does not enhance its zero-shot generalizability.

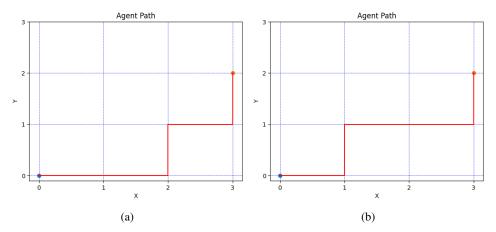


Figure 4: Inference results for (a) GNN-based framework, (b) MLP-based framework on benchmark A.

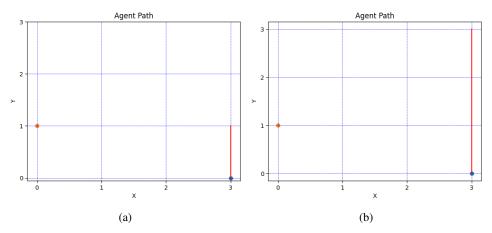


Figure 5: Zero-shot performance of (a) GNN-based framework, (b) MLP-based framework on benchmark B, using policies trained with benchmark A.

4.3 Comparing fine-tuning performance

Figure 6 displays the fine-tuning outcomes for both RL agents. Unfortunately, due to issues originating from the RLlib library, neither agent could undergo effective fine-tuning when exposed to benchmarks different from those used in their initial training. This is evident from the reward versus time curves, which remain flat, signifying that the policy networks in both RL agents did not undergo any updates. Consequently, they remained in a static state, unable to adapt or improve.

5 Conclusion

In this project, a message-passing GNN is proposed for integration into an RL framework for IC routing. The aim is to enhance the generalizability of RL policies, allowing for policies trained in one environment to be effectively transferred to or fine-tuned for unseen environments. However, challenges such as implementation limitations and bugs in the RLlib library prevented the attainment of conclusive results. These issues underscore the need for future work, particularly in the manual development and implementation of the RL training policy, to re-evaluate the generalizability of the GNN-based RL framework.

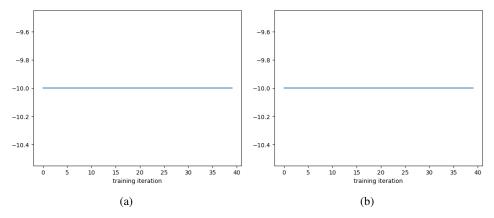


Figure 6: Fine-tuning of (a) GNN-based framework, (b) MLP-based framework on benchmark B, based on policies trained with benchmark A.

References

- [1] J. Hu and S. S. Sapatnekar, "A survey on multi-net global routing for integrated circuits," *Integration*, vol. 31, pp. 1–49, 11 2001.
- [2] V. Mnih, K. Kavukcuoglu, D. Silver, A. Graves, I. Antonoglou, D. Wierstra, and M. Riedmiller, "Playing atari with deep reinforcement learning," 2013.
- [3] J. Schulman, F. Wolski, P. Dhariwal, A. Radford, and O. Klimov, "Proximal policy optimization algorithms," 7 2017. [Online]. Available: http://arxiv.org/abs/1707.06347
- [4] P. Almasan, J. Suárez-Varela, K. Rusek, P. Barlet-Ros, and A. Cabellos-Aparicio, "Deep reinforcement learning meets graph neural networks: Exploring a routing optimization use case," *Computer Communications*, vol. 196, pp. 184–194, 12 2022.
- [5] T. Wang, R. Liao, J. Ba, and S. Fidler, "Nervenet: Learning structured policy with graph neural networks," 2018. [Online]. Available: http://www.cs.toronto.edu/
- [6] H. Liao, W. Zhang, X. Dong, B. Poczos, K. Shimada, and L. B. Kara, "A deep reinforcement learning approach for global routing," *Journal of Mechanical Design, Transactions of the ASME*, vol. 142, 6 2020.
- [7] H. Liao, Q. Dong, X. Dong, W. Zhang, W. Zhang, W. Qi, E. Fallon, and L. B. Kara, "Attention routing: track-assignment detailed routing using attention-based reinforcement learning," 2020.
- [8] H. Chen, K. C. Hsu, W. J. Turner, P. H. Wei, K. Zhu, D. Z. Pan, and H. Ren, "Reinforcement learning guided detailed routing for custom circuits." Association for Computing Machinery, 3 2023, pp. 26–34.
- [9] A. Mirhoseini, A. Goldie, M. Yazgan, J. W. Jiang, E. Songhori, S. Wang, Y. J. Lee, E. Johnson, O. Pathak, A. Nazi, J. Pak, A. Tong, K. Srinivasa, W. Hang, E. Tuncer, Q. V. Le, J. Laudon, R. Ho, R. Carpenter, and J. Dean, "A graph placement methodology for fast chip design," *Nature*, vol. 594, pp. 207–212, 6 2021.
- [10] S. Yue, E. M. Songhori, J. W. Jiang, T. Boyd, A. Goldie, A. Mirhoseini, and S. Guadarrama, "Scalability and generalization of circuit training for chip floorplanning." Association for Computing Machinery, 4 2022, pp. 65–70.
- [11] R. Cheng and J. Yan, "On joint learning for solving placement and routing in chip design," Advances in Neural Information Processing Systems, vol. 20, pp. 16508–16519, 10 2021. [Online]. Available: https://arxiv.org/abs/2111.00234v2