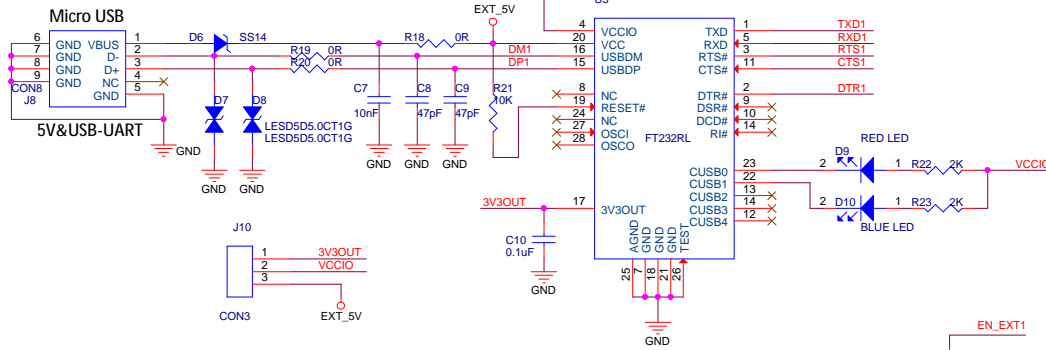
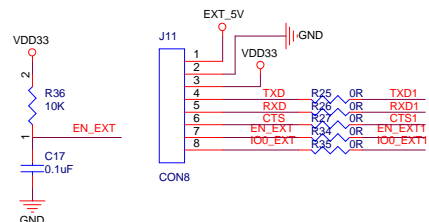


Flow control: short J14 and J67

UART for ESP_Test board

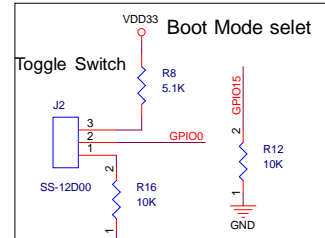
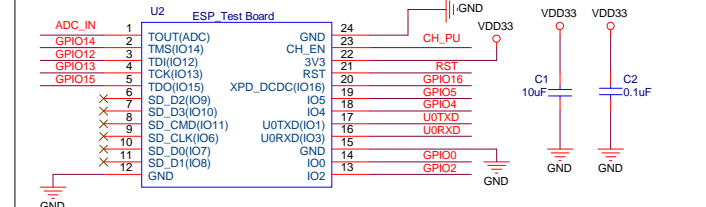


VCCIO->3V3OUT : 3.3V TTL level for UART
VCCIO->EXT_5V : 5V TTL level for UART

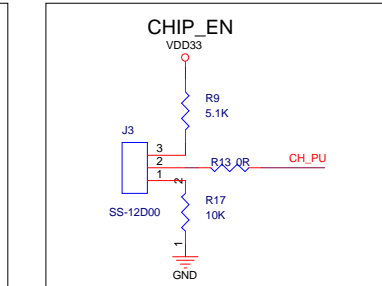
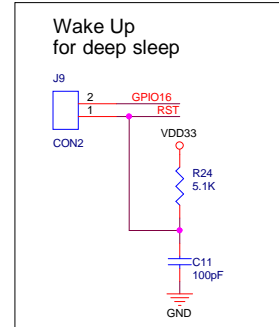
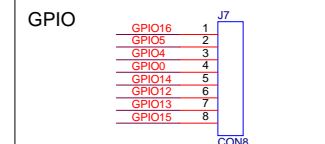


UART for external test module

Auto program			
DTR1	RTS1-->EN_EXT1	IO0_EXT1	
1	1	1	1
0	0	1	1
1	0	0	1
0	1	1	0



- 1.UART Download : J66 Push Down
- 2.Flash Boot : J66 Pull Up



Toggle Switch
Pull Up by default

CHIP_EN:
can also be controlled by external gpio.

