

NAMDEV PAVANKUMAR KALE

SYSTEM ENGINEER

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INDIAN

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PROFESSIONAL SUMMARY

Problem-solving Systems Engineer with success collaborating on client projects and learning through hands-on experiences. Promoting strong work ethic and exemplary organizational skills.

WORK HISTORY

SOFTWARE ENGINEER, 10/2022 - Current

KPIT Technologies, Pune, India

Currently working in the Onboard component Vehicle Analytics in OBD project as Systems Engineer, responsible for SysML artifacts development and requirements authoring/management.

Professional Experience:

- Total 1+ years of experience in the System Engineering and Model based development.
- Good Knowledge Of SysML, and UML.
- Knowledge of MBSE.
- Knowledge of System Engineering.
- Knowledge of Functional Safety (Level1).
- Working under Agile & Scrum Methodology.
- Requirements Engineering.
- Functional Safety, FMEA, ASIL etc.

Tools Used:

- IBM Rational Rhapsody
- MSOSA
- Dassault System TRM (Traceability Requirement Management)
- Confluence/JIRA
- Lucid

Project Experience:

- Worked on Architecture Tool Catia Magic (MSOSA Dassault Systems), modelling language SysML to design requirements and system behavioral flow.
- Experience in creating System Requirements from Customer requirements and knowledge of INCOSE and IEEE guidelines for requirement authoring.
- Working in Agile workflow and Knowledge of Issues/ Bug reporting and management in JIRA.

Model Based Systems Engineering:

 Hands on experience in creating structural and behavioral diagrams as per MagicGrid Framework. Worked on Architecture Tool CATIA No Magic, modelling language SysML

Requirement Engineering:

- Hands on experience in creating System and Software Requirements from Stakeholder Needs. Worked on Architecture Tool Magic Systems of Systems Architect, modeling language SysML, UML to design requirement. Experience in ISO 26262, INCOSE, IEEE Standards, EARS.
- knowledge of Software Development life cycle, and SW development models (V model).
- Writing functional and non-functional requirements following INCOSE/IEEE Standards.
- Functional/Software requirement analysis and decomposition at the component or unit level.
- Creating SysML diagrams and developing logical & technical-level architecture.
- Creating SRD, and ICD in TRM, Baselining of the requirements in TRM.
- Worked On a Fingerprint Based Security System Project during Academics.
- Currently Working in the Onboard component Vehicle Analytics in the OBD project as a Systems Engineer, responsible for SysML artifacts development and requirements authoring/management.
- Worked on Block Definition Diagram [BDD], Internal Block Diagram [IBD], State Flow Diagram, Activity and Sequence Diagrams.
- Project specific stakeholder requirement documents, feature system requirement document, interface control document, feature system description document is directly imported into TRM.

ASSOCIATE SYSTEM ENGINEER, 10/2021 - 10/2022

KPIT Technologies, Pune, India

- As part of System Engineer Trainee,
- Have completed GENESIS training at KPIT.
- During training Enhanced knowledge on,
- UML, SysML, Requirements writing, Software development lifecycle, Autosar, Functional Safety, Embedded C etc..
- And, got allocated to OBD Project as a System Engineer.
- as part of my work in OBD project as a System Engineer.
- I have used following Tool's,
- MSOSA (Catia no magic)
- JIRA/Confluence
- TRM
- IBM Rational Rhapsody
- Lucid

here worked on Requirements writing, designing of "Activity diagrams, BDD,IBD, Use Cases, State machine diagram, sequence diagram, re-authoring already written requirements etc....

LABVIEW TRAINEE, 06/2021 - 10/2022

Ceryle Innovative Technologies, Pune, India

I have worked on LabVIEW software and also learned the Basics of LabVIEW also got onsite experience during my time there. also,

- Collaborated with team members to achieve target results.
- Increased customer satisfaction by resolving issues.
- Demonstrated respect, friendliness and willingness to help wherever needed.
- Proved successful working within tight deadlines and fast-paced atmosphere.

SKILLS

MBSE

FMEA

• UML

• Requirements

Systems Engineering
 SplC
 Requirements engineering
 Functional Safety

Government College of Engineering&Research Avasari, Pune, India, 07/2021
Bachelor of Engineering: Instrumentation And Control Engineering

Yeshwant Mahavidyalaya Nanded, Nanded, India, 02/2015
CLASS XII: HSC

Nagnath Vidyalaya Aundha Nagnath, Aundha Nagnath, India, 03/2013
CLASS X: SSC

CERTIFICATIONS

EDUCATION

- ISO 26262 Functional Safety Level 1 (KPIT internal Training).
- Project Management Foundation: Requirements (PMI) LinkedIn.
- Software Design: Modeling with UML LinkedIn.
- Software Development Life Cycle (SDLC) LinkedIn.
- Understanding Manual Testing LinkedIn.
- Agile Software Development LinkedIn.
- THE HIGH FLYERS (KPIT internal Award 2 Times).
- Writing Professional Emails in English (Coursera).
- English for Common Interactions in the Workplace: Basic Level (Coursera).
- Succesful Presentation (Coursera).

INTERESTS

- Playing Cricket
- Playing Soccer
- Talking
- Exploring new Things
- Travelling
- Hiking etc..

LANGUAGES	English	Hindi	
	Advanced	Fluent	
	Marathi		
	Native		