

NAYAN MAHADEO BHENDE

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CAREER OBJECTIVE

To have a growth oriented and challenging career, where I can contribute my knowledge and skills to the organization and enhance my experience through continuous learning and teamwork.

PROJECT PROFILE-

- ❖ Working as Senior Software Engineer Quest Global Limited (2021-Till date)
- ❖ Worked as Software Engineer Cyient Ltd (2018-2021)

PROJECT PROFILE-

- ❖ 4+ Yr. of professional experience in Locomotive and Automotive Domain.
- ❖ Experience in C ,Embedded C and PLC for Development and testing in Windows environment.
- ❖ Hands on Experience in SDLC and SDTC
- ❖ Proficiency in working on Embedded application
- ❖ Good knowledge of various tools like RTS, Visual Studio, JIRA and SVN
- ❖ Having knowledge on Linux Operating System concepts.
- ❖ Good understanding in Agile way of working and software development cycle.
- ❖ Having knowledge on scrum related projects.
- ❖ Experience in Functionality testing and Debugging.
- ❖ Experience in problem solving and bug fixing tasks.
- ❖ Experience in Black box Testing (RBT) and White box testing (SBT).
- ❖ Working experience in Integration testing and target testing, Unit testing.
- ❖ Good exposure with (HMI and Simulator testing, Emulator testing) and testing tools like NI TESTSTAND, CONTROL BUILD.
- ❖ Working knowledge for Matlab and simulink
- ❖ Involve in SW Technical defect analysis and provide solution.

SOFTWARE SKILLS

- ❖ **Embedded and tools:** - CATIA Control Build, VTD, DCU terms, Wire shark, BT-Soft-TCMS tools, Soda-Gen tool, Clear case and Clear-Quest, Jira, Jazz RTC, SVN Repository.
- ❖ **Guideline/Standards:** - EN50128, EN50129, ISO26262, IEC1131-3
- ❖ **Communication Protocols:** UART, SPI, I2C, CAN, Ethernet, and other Railway protocols.
- ❖ **Programming:** C, Basic C++, Embedded C for BT-driven tools.
- ❖ **Other Tools:-** DSPACE Control Desk ,MWT-IEC61131-3 , MTPE and MTVD SWM-regestry .

CERTIFICATION

- ❖ Completed MATLAB Simulink for System and Algorithm Modelling
- ❖ Completed Simulation-Based Testing with Simulink. (V and V)

ACADEMIC QUALIFICATION

- ❖ Pg-Diploma - Embedded System design CDAC Knowledge Park- .Bengaluru Kar
- ❖ B.E - Electronics and telecommunication - RTMNU, Nagpur Maha

- ❖ Intermediate - MS-Board Nagpur Maha
- ❖ High School - MS-Board Nagpur Maha

PROJECTS

■ MCX and PSW Project:- (Traction Control Engineer)

- ❖ Implements SW with respect to appropriate design principles and standards.
- ❖ Builds SW application and Maintains the built SW under change and configuration control
- ❖ Prepares release notes and Maintains traceability to and from design
- ❖ Assures that correct SW baselines are used during tests
- ❖ Develops the SW and SW/HW integration test specification based on designer's SW architecture .Assures that functionality is traceable and testable
- ❖ Develops integration report providing the outcome of the integration testing
- ❖ Develops SW Test Procedure and Develops component test specification and reports
- ❖ Develops Overall test specifications and reports
- ❖ Performs SW Component Test, Integration Test, Overall tests
- ❖ Identifies potential deviations from expected results, record them in test reports and communicate to responsible for evaluation and decision making
- ❖ Ensures traceability of test objectives and results against the specified SW requirements and vice versa
- ❖ Performing Model Debugging. Software module implementation as per customer requirement. Analysis of Technical defects. Functionality testing. Fault analysis. specific Functionality and review of Test results
- ❖ Implementation and Testing for the model as per Change Request in SWM for different Projects/Platform (Ex.SH9, BJ16, etc.)
- ❖ Analyzing simulation results to verify model behavior for Requirement Specification Analysis.
- ❖ Done Load test Cycle for Project/ platform to check behavior.
- ❖ Created Baseline for different versions (Ex:- 0.0.8.3 Gen2 ,0.0.1.0 for Gen 3) for released
- ❖ Analyses of log for different parameters and modules to understand as well as analysis
- ❖ Participating in test-report generation process with issue list and correct justification.
- ❖ One-on-one interaction between Clients and Managements
- ❖ Analysis CR from Jazz etc for Model-based requirement.
- ❖ Create Test harness and worked on test sequence block. Debugged model for Code coverage Analysis.

■ SCC and FCA projects:- (MBD Engineer)

- ❖ Performing Unit testing for SCC and FCA different modules .
- ❖ Model in Loop (MIL) for requirement based model
- ❖ Requirement based Simulink and Stateflow modelling Forward Collision avoidance (FCA) and Smart Cruise control (SCC) system of ADAS system.
- ❖ Analysis and debugging the functionality and related issues. Development of Test Cases and Test Procedures for Requirements for Module, Unit and Integration Testing.
- ❖ Working Experience for MATLAB module-based design with Simulink and testing for V and V activities
- ❖ Collecting Model Coverage and Performing requirements-based assessments
- ❖ Development of test cases and test procedures for requirements for the module.
- ❖ Analyzing simulation results to verify model behavior
- ❖ Create time-based and logic-based test cases for a Simulink model.
- ❖ Analyze test results of a Simulink simulation, both during and after the simulation.
- ❖ Model Debugging. Raising defects and tracking them to closure through Jira.
- ❖ One-on-one interaction between Clients and Managements
- ❖ Participating in test-report generation process with issue list and correct justification.

■ TALENT-2-and E-LOCO projects (V and V Eng)

- ❖ To Create Script in JScript format. Need to create Source file as well as Updated Library file as per software requirement.
- ❖ Done Regression and Validation activities for different ASV releases and supported Integration activities in TCMS Lab.
- ❖ Experience as an SIT (System Integration Test) Engineer for V and V projects
- ❖ Used to work on Real-Time hardware Devices (MITRAC device,) for TALENT-2 train with the respective Car configuration (2-car,4, and5 car).
- ❖ Debug software issue in LAB for the Car configuration and updated finding
- ❖ Validation Activities for ASV6910, ASV6_7_10_0, and ASV6_9_3_1.
- ❖ Support for Integration Activities in Lab Work on HMI for manual execution for script in the lab as per Configuration.
- ❖ Analysis of FRS (Functional requirement and specification) and Analysis of SwADS
- ❖ Responsible for Designing modules in CATIA with the help of ladder diagrams and PLC programming.CATIA Control build(Dassault System) for development and testing.
- ❖ Raising defects against the build. Discussing issues with customer
- ❖ Responsible for designing Test cases and their execution using Control build and MTF tools
- ❖ Responsible for preparing deliverable for function for (SIL2) by doing coverage analysis and checking boundary conditions for the respected variables in different datatype.
- ❖ Responsible for test reports as well as coverage analysis. Raising defects.
- ❖ High Level Testing (HSIT), Low Level Testing (Unit Testing), Integration Testing (VTD).
- ❖ Development of Test Cases and Test Procedures for Requirements for Unit Testing. Debugging and analysis the requirement issues.
- ❖ Development of Test Cases and Test Procedures for Requirements for HSIT. Debugging hardware issue on test bench. Raising defects and tracking them to closure. Involved in review activity. Technical guidance to the team and resolution of the issues.

PERSONAL PROFILE

- ❖ **Father's Name:** Mr. M P Bhende
- ❖ **Passport:** Yes
- ❖ **Marital Status:** Single
- ❖ **Language Known:** English, Hindi, Marathi

The above facts are true to the best of my knowledge and belief.

Date: - 03/01/2023

Place: Bengaluru, India

Nayan Mahadeo Bhende