Kishore Subramani

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CAREER OBJECTIVE

To work in a stimulating and challenging environment that would facilitate the maximum utilization and application of my broad skills and expertise in making a positive difference to the organization

EXPERIENCE SUMMARY

- Having 7 years 10 Months of experience in Automotive Domain.
- Experience in HIL System Development and Testing, Embedded Hardware Design and Testing.
- Experience in Vehicle Level Testing and Black Box Level Testing, Manual Testing, Automation Testing, Function Testing and Regression Testing.
- Experience in LABCAR Development, Testing and Complete LABCAR Architecture Modelling.
- Experience in creating Test System Plan, Test cases development, Test case Execution and Test reports preparation.
- Hands on experience in Designing & Testing of HIL System.
- Hands on experience in HIL System hardware verification and product validation.
- Hands on experience in Modelling, GUI Creation etc.
- Hands on Experience in 16-bit and 32-bit microcontroller testing.
- Prepared deliverables like New HIL System Level Proposal, SOW, BOM, Design document.
- Component's selection (Resistor, Capacitor, Inductor, Transistor, Diode etc.).
- Experience in Communication Protocol: CAN, LIN, Automotive Ethernet etc.
- Having knowledge in collecting alternate specifications for all the electrical & electronics like IC packages, Experience in PCB Reverse Engineering Activities.
- Business Visit to China for Project Needs.

WORK SUMMARY

- Working as **Associate Architect** in **Vitesco Technologies India Private Limited**, Bangalore from Sep 2021 to Till Date.
- Worked as **Senior HIL Testing Engineer** in **Comstar Automotive Private limited**, Chennai from Sep 2020 to Sep2021.
- Worked as **System Engineer** in **BlueBinaries Engineering and Solutions Pvt Ltd**, Chennai from Jan 2018 to Dec 2019.
- Worked as **Hardware Testing Engineer** at **Ather Energy Private Limited**, Bangalore from Oct 2017 to Jan 2018.
- Worked as **Engineer** at **Darien Electric Madras**, Chennai from June 2014 to May 2017.

TECHNICAL SUMMARY

- Protocols — SPI, I2C, CAN.
- Testing Tools — CANape, CANoe, CANalyzer, INCA, MATLAB, dSPACE Configuration desk, dSPACE control desk, dSPACE Automation Desk and ETAS FIU.
- Hardware Tools — Digital Storage Oscilloscope, Function generator, CRO.
- Simulation — MATLAB Simulink and Basics of NI LabVIEW and NI Max.
- Schematics Design Tools — ORCAD 17.2, Proteus, Express Schematic.
- General Tools — DOORS, JIRA, IMS.

- MS-Office MS-Word, MS-Excel, MS-Power point.

CERTIFICATION AND AWARDS

- Certified in **ISTQB Foundation Level in Software Testing**.
- Excellent Performance "**SPOT AWARD"** towards technical hour presentation for 8 speed Automatic Transmission project System Overview.
- Excellent Performance "**SPOT AWARD"** towards completing regression test for 8AT SA TCU Project.

Projects Profile

Vitesco Technologies India Private Limited, Bangalore.

1. **Project** : Automatic Transmission System.

   Designation : Associate Architect

   Description

   The product involves the validation of 8 speed Automatic transmission system feature and other technology related features and performing various level of Testing like Level2 Software Requirement Testing, Functional safety testing, Fault Injection Testing, Electrical testing, Automation testing and Manual testing with TCU and dSPACE HiL System.

   Roles and Responsibilities:

   - Level 2 Requirement Analysis and Test case preparation in doors and Test case execution and GUI Creation.
   - Handling on Test planning preparation and execution in Automatic transmission system.
   - Handling on complete MATLAB Plant modelling, Configuration desk modelling, Control desk modelling and Automation desk in Automatic Transmission System.
   - Automation Test script preparation and Test script execution of Level2 functional safety in AT TCU System.
   - Execution of complete regression testing and preparation of test summary reports in Automatic transmission system.
   - 8AT TCU System - Manual testing and Automation testing using DSPACE HIL system.

Comstar Automotive Private limited, Chennai.

1. **Project** : BSG ECU System

   Designation : Senior HiL Test Engineer

   Description

   The product involves the validation of BSG ECU with Engine system Feature and other technology related features

   Performing various level of Testing like electrical testing, system integration testing, manual testing And automation testing with BSG ECU and dSPACE HiL System.

   Roles and Responsibilities:

   - Requirement Specification Document Preparation, Test case preparation, Test case execution, BOM Creation and GUI Creation
   - Handling on Complete BSG Plant Modelling, Configuration desk modelling, Control desk Modelling and Automation desk.
   - Relay Selection for ECUs
   - BSG ECU System - Manual testing and Automation testing using DSpace HiL system.

- Verifying the intended clamping concept, harness and hardware signals
- Validation of wiring connection in dSPACE BSG System.
- Verification of the physical and logical behavior of installed ECUs.

BlueBinaries Engineering and Solutions Private Limited, Chennai.

1. **Project** : LABCAR HIL System Architecture

   Designation : System Engineer

   Description

   LABCAR specific hardware can be assembled according to the customer's demands and performing various level of Testing like electrical testing, system integration testing, manual testing with original vehicle wiring harness, sensor , actuators and ECU's.

   System integration or architecture level tests can be done using LABCAR Module without vehicle.

   Roles and Responsibilities:

   - Requirement Specification Document Preparation, BOM Creation and GUI Creation
   - Complete LABCAR Architecture Modelling.
   - Relay Selection for various ECUs
   - LABCAR- Manual testing, Automation testing and Network testing
   - Verifying the intended clamping concept, harness and hardware signals
   - Validation of wiring connection in LABCAR & ensured for quality of E&E architecture.
   - Verification of the physical and logical behavior of all installed ECUs

2. **Project** : In-Vehicle Network System

   Designation : System Engineer

   Description

   In-Vehicle Networking is a method of transferring data to multiple ECU's in the vehicle network using communication protocol like CAN, LIN.

   Roles and Responsibilities:

   - Testing on CAN Communication in different ECU's
   - Test case preparation for CAN Features

Ather Energy private Limited, Bangalore.

1. **Project** : IHB Dashboard

   Designation : Hardware Testing Engineer

   Description

   The product involves the validation of all driver related warnings and other technology related features.

   Roles and Responsibilities:

   - Validation of the cluster features: Telltales, Warnings, Gauges, Odometer, GPS, GSM, Bluetooth, IMU, BMS, ALS etc.
   - Performing hardware level testing like timing parameter analysis.
   - HW Tools: Test Box, Decade resistance boxes, Function generator, Digital Storage Oscilloscopes, PCAN .

Darien Electricals Ltd, Chennai.

1. **Project** : TVS Instrument cluster

   Designation : Hardware Design Engineer

   Description

   Instrument cluster is for Drive Information System

   Roles and Responsibilities:

   - Requirement Specification Document Preparation
   - Components selection (Resistor, Capacitor, Inductor, Transistor, Diode etc…..).
   - Power supply module design using Linear and Switching Regulator
   - Microcontroller Selection and Schematic Entry using ORCAD
   - Preparation of EBOM and releasing it for Manufacturing
   - Board testing and Hardware troubleshooting.

2. **Project** : Strain Gauge Amplifier

   Designation : Hardware Design Engineer

   Description

   Strain gauges amplifies are mainly used to measure the strain on an object by using strain (350 ohm,120 ohm)

   Strain gauge is basically operated in amplifier circuit.

   Bridge Balanced concept is the input of instrumental amplifier circuit.

   Roles and Responsibilities:

   - Preparation of Requirement Specification Document, Design Calculation.
   - Component Selection and Schematic Capturing
   - Hardware Design Documentation preparation.
   - Board testing and Hardware troubleshooting.

3. **Project** : RCPT

   Role : Hardware Design Engineer

   Description

   RCPT is used to measures the ability of chloride ions concrete to penetrate a concrete mix.
   RCPT based on ASTM C1202 Standard Test Method concept.
   RCPT operates on + 60V DC POWER SUPPLY by using SCR-SCR thyristor module.
   Gate driver circuit is mainly used to produce the gate pulse, in the trigger input of SCR module.

   Roles and Responsibilities:

   - Component Selection and Schematic Capturing
   - Hardware circuit simulation using Simulation Tool.
   - Hardware Design Documentation preparation.
   - Board bring up testing and Hardware troubleshooting.

Projects Profile

- Bachelor of Engineering (Electronics and Communication Engineering) - 2014 - Kumaraguru College of Technology, Anna University, Coimbatore (76.7 %).
- Diploma (Electronics and Communication Engineering) - 2011 - Muthayammal Polytechnic college, TNDOTE – Rasipuram (96.17%).

PERSONAL DETAILS

- **Date of Birth** 15th October 1992
- **Marital Status** Married
- **Languages Known** English, Tamil

DECLARATION

I do hereby declare that the particulars of information and facts stated herein above are true, correct and complete to the best of my knowledge and belief.

Date:

Place: