Natarajan A V

- 18th March 1998
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Automation engineer with a strong and healthy appetite for growth and knowledge, with good experience in HIL Testing, verification and validation with complete automation process. Worked in challenging deadlines successfully and delivered quality work products. A good team player with very good communication and multitasking skills.



Educational Details

Higher Secondary Education:

Shri Ahobila math oriental higher secondary school, Tamil Nadu *Year 2015 – 16.*

B. Tech – Electronics and Instrumentation engineering:

SRM Institute of Science and technologies , Tamil Nadu *Year 2016 – 2020*



Skills and Technical expertise	
•	Analysis Tools:
 Programming Languages: 	IMC FAMOS
Python	Vector CANape
C/C++	Vector CANoe
Embedded C	 Process and Tools:
 Model based design tools: 	SVN
MATLAB	Git Repositories
MATLAB Simulink.	IBM DOORS
Xylinx FPGA block set.	Methodologies:
• Automation Tools:	Agile
Control Desk	V-Model
Configuration Desk	
ETAS INCA	

Work Experience



KPIT Technologies Pvt Ltd - Pune



April 2021 to Present.

Test Automation:



- Creating fully automated test scripts using Python Programming from test procedures for Hardware In-Loop testing.
- Execution of the Automated scripts in the HIL Environment and generation of test reports.



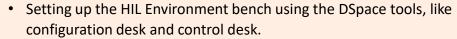
- Analysis of the test reports and communicating/discussing the failures with clients.
- Reviewing automated test scripts.

Test specification creations:



- Analyzing the software requirements and creating a well curated test strategy for the testing activities
- Creating testcases and test procedure with full test coverage and expected results.
- Understanding and discussing the test constraints with the Clients.

HIL Configuration Activities:





- Configuring the wiring Harness between the target hardware and DSpace HIL Rack.
- Rest bus simulation for communication protocols such as CAN and LIN using DSpace - MATLAB Simulink toolbox.
- Setting up SENT (Single Edge nibble Transmission) protocol messages for pressure and position sensors.
- Worked with Xilinx FPGA MATLAB Simulink modeling tool.