

# PAINENI VENKATESH

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## CAREER OBJECTIVE:

To be associated with an esteemed organization that provides an opportunity to utilize skills to my full potential, improve knowledge with the latest technologies and to be a part of the team that works dynamically towards the growth of the organization and the country.

## WORKING EXPERIENCE:

4+ years of experience in Real Time Embedded Software Safety Critical Systems in Avionics and Aerospace-V&V Domain.

Senior Software Engineer  
Alten Global Technology Solutions Pvt. Ltd., Bangalore.  
Deputed at ADE, DRDO Bangalore from June 2020 – Present.

Oct 2019 – Present

## PROJECTS:

### “Software Development of Flight Control Software for LCA”

- Development of FTI & NVM tool using M-Scripting.
- Developed the Simulink, GUI and State-Flow models as per design requirements for “AUTO PILOT” functionalities.
- Development of the Simulink model of the “CLAW” and “AUTO PILOT” functionalities (DFCC) as per the FRD (Flight requirement document).
- Design, develop, and maintain state-flow models for dynamic systems, control algorithms, and signal processing applications.
- Developed Simulink model will be compared with the DFCC Code to verify and validate the code.

### “Software Testing of Flight Control Software for LCA”

- System Integration Testing (SIT) on existing OFP as per new requirements and verified the HSI files as per the FRD/SRS. Raise the issue if any test cases are missing to cover any requirement.
- Debugging of Advance engineering test station (AETS) using real time Air data computers (ADC).
- Review of DFFC ADA code as per the FRD/SRS. Raise the issue if any mismatch with the FRD and noticed to the ADA developer (Preliminary step).
- Test cases are generated by using MATLAB based application.

## SKILL SET:

- Languages : Basics of C,ADA
- Operating Systems : Windows, Linux

- Tools known : MATLAB & Simulink, AITESS, RTTRT, Understand
- Version control tools : SCM, tortoise SVN

### **EDUCATIONAL QUALIFICATION:**

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- **Bachelor of Technology in Electronics and Communication Engineering** during the year 2014-2018 with 68% from Sree Vaidyanathan Engineering College, Tirupati, Andhra Pradesh.
- **Intermediate** during the year 2012-2014 with 85.3% from Narayana Junior College, Rayachoti, Andhra Pradesh.
- **SSC** during the year 2011-2012 with 87% from Sri Sai Concept School, Rayachoti, Andhra Pradesh.

### **PERSONAL DETAILS:**

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Languages: English and Telugu.

Date of Birth : 25-07-1997

Address : Indiranagar, Bangalore, Karanataka-560038.

### **DECLARATION:**

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I hereby declare that all the details furnished above are true to the best of my knowledge.

**PAINENI VENKATESH**