

proc0\_main<sub>0</sub> ———

proc0\_main<sub>1</sub> ———

 $proc1\_main_0 - H proc1\_main_1 -$ 

figures-2: single-qubit-gate-global-to-local-mapping

alice\_main<sub>0</sub> —

 $alice\_main_1$  —

alice\_teleport —

alice\_entanglement —

Processor #0 (Alice) qubits

bob\_main<sub>0</sub> —

bob\_main<sub>1</sub> —

bob\_teleport ----

bob\_entanglement —

Processor #1 (Bob) qubits

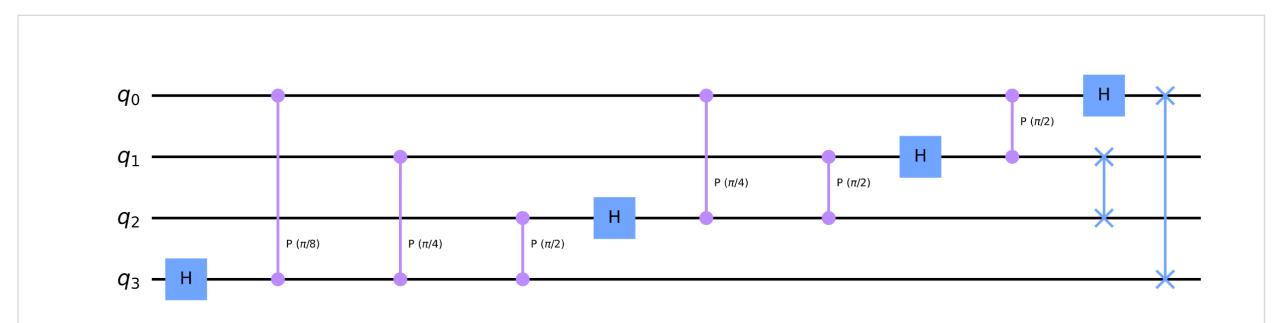
alice\_measure  $\stackrel{2}{=}$ 

Processor #0 (Alice) classical bits

bob\_measure  $\stackrel{2}{=}$ 

Processor #1 (Bob) classical bits

figures-4 : quantum processor registers



figures-5 : 4-bit local quantum Fourier transformation

