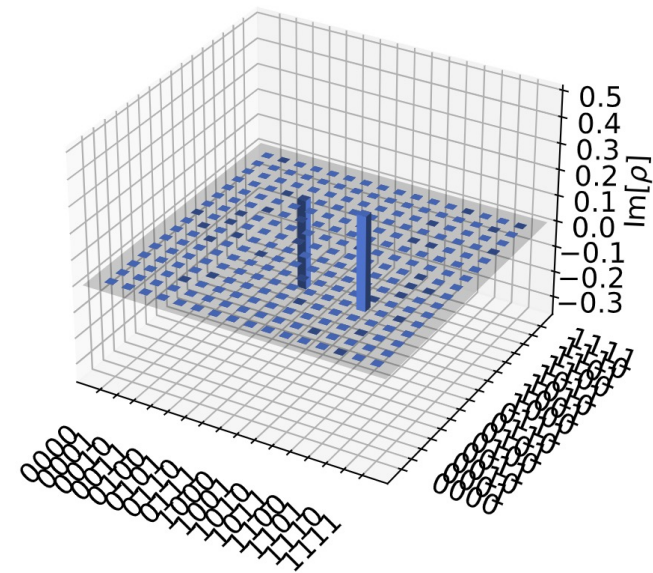



figures-0 : monolithic-quantum-computer-circuit-example



figures-1 : monolithic-quantum-computer-city-plot

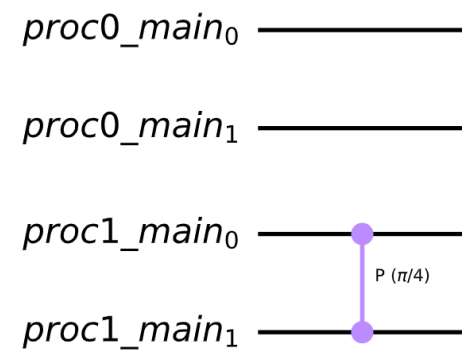
*proc0_main*₀ ———

*proc0_main*₁ ———

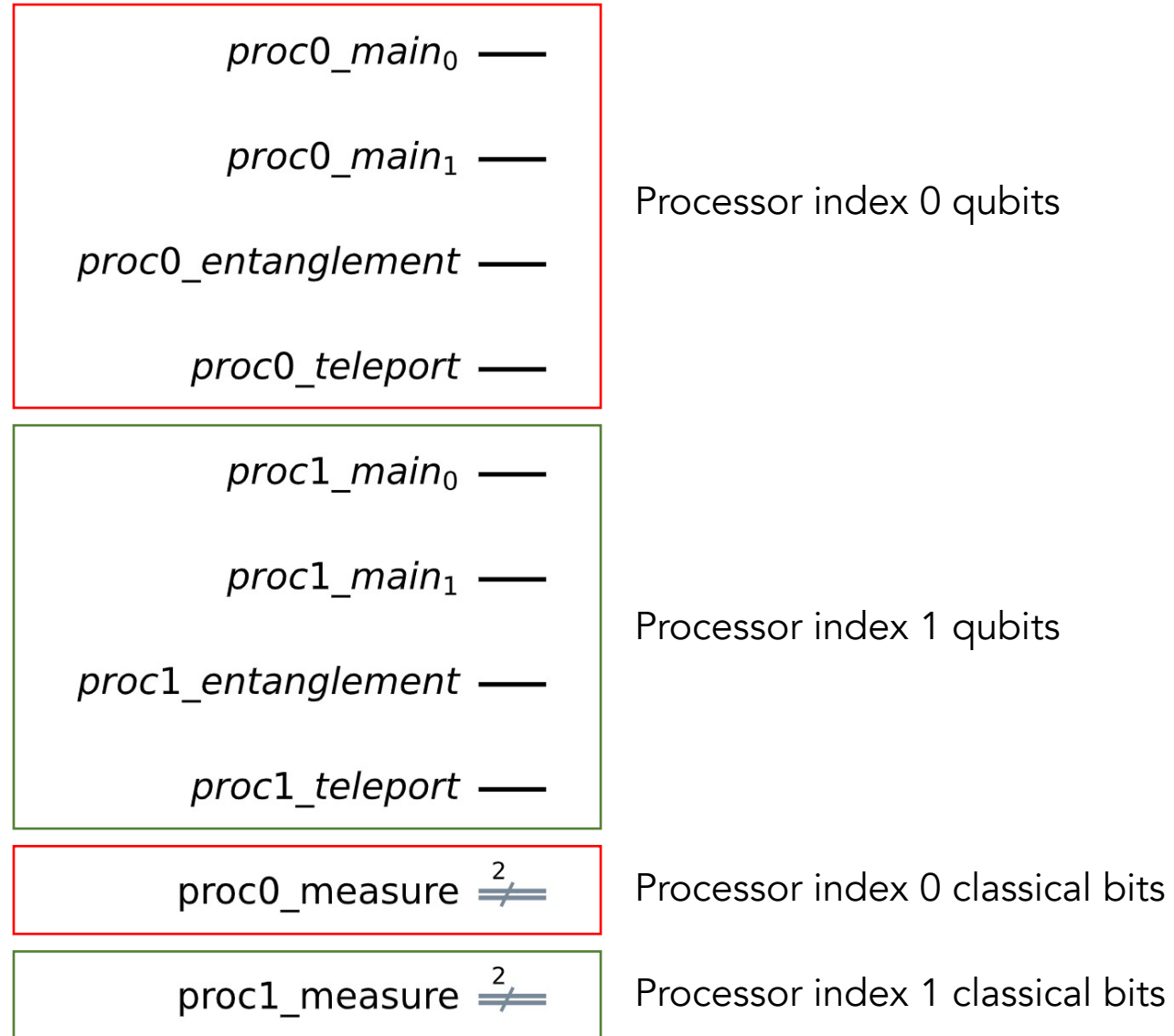
*proc1_main*₀ —  —

*proc1_main*₁ ———

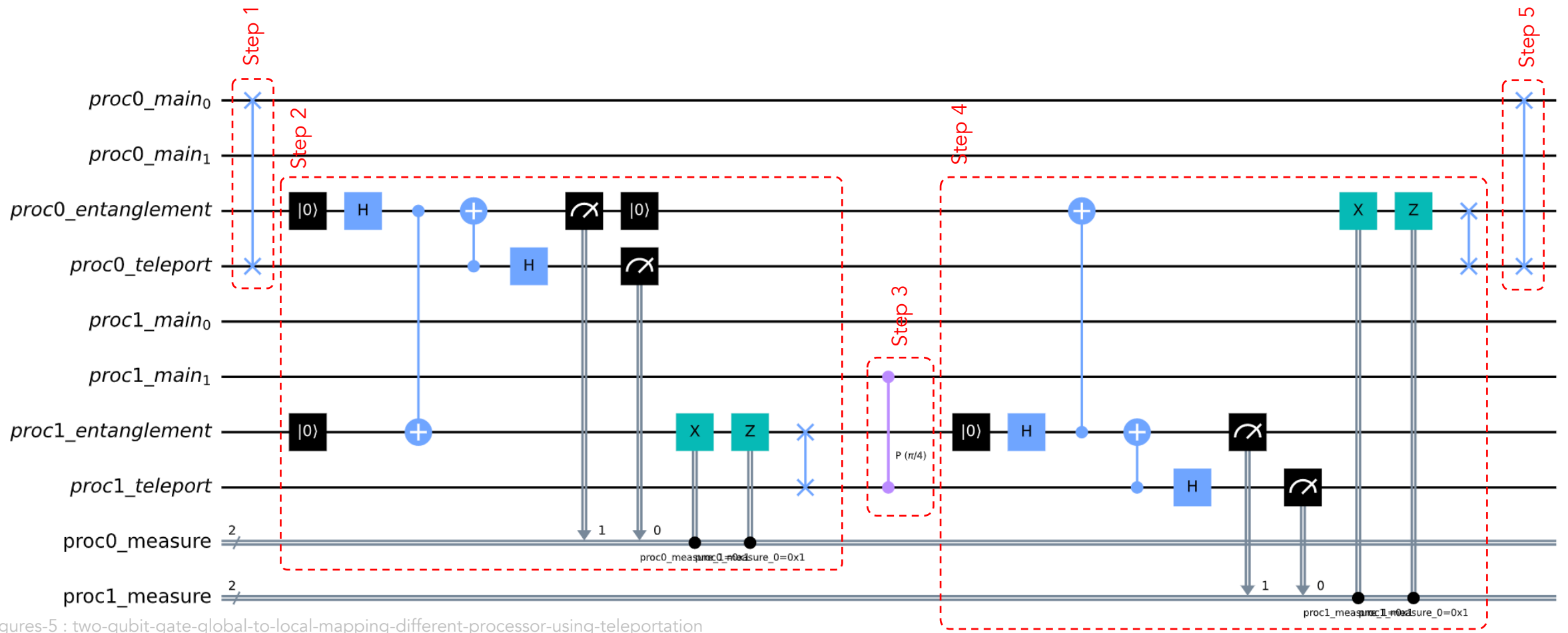
figures-2 : single-qubit-gate-global-to-local-mapping



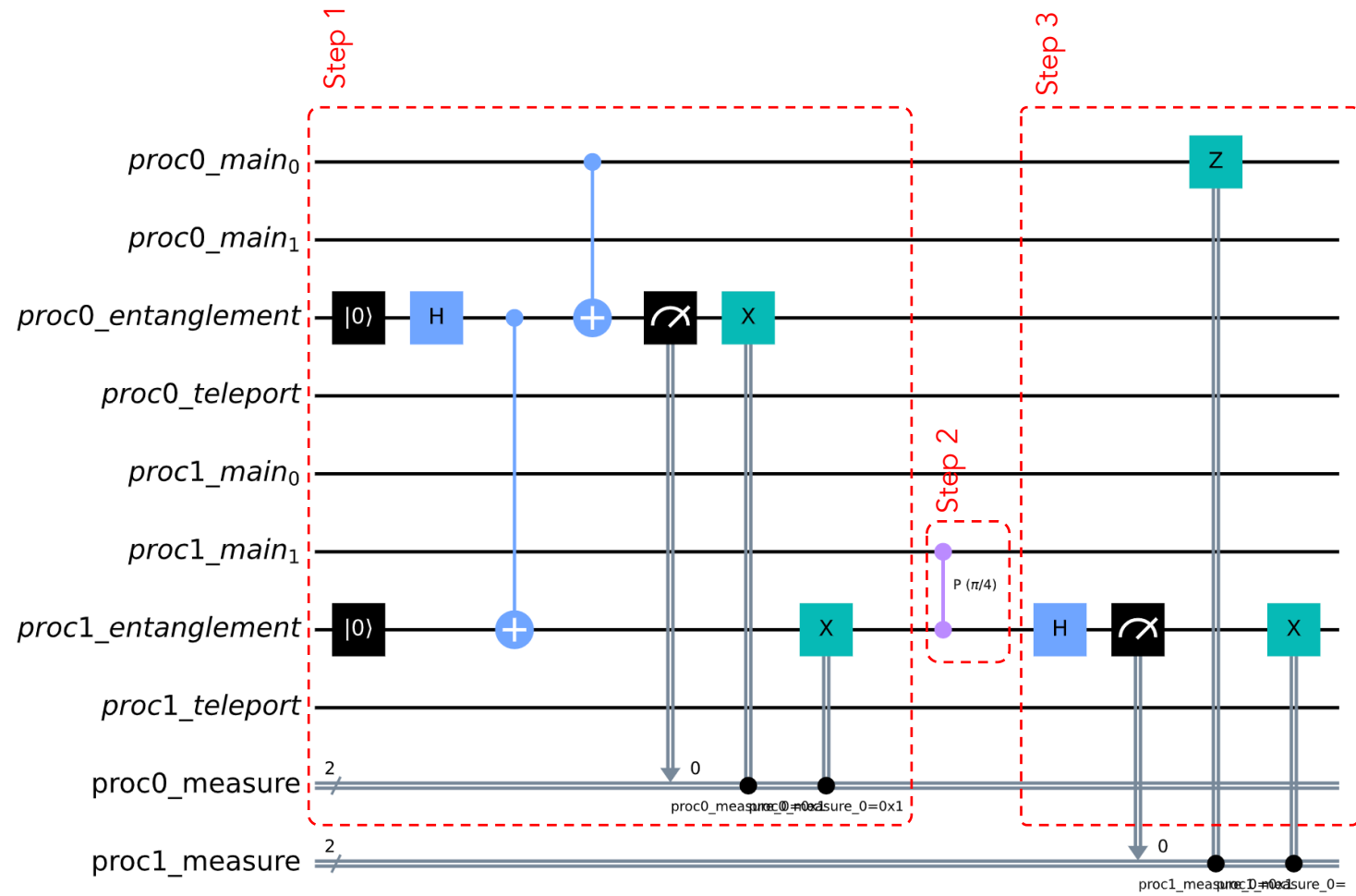
figures-3 : two-qubit-gate-global-to-local-mapping-same-processor



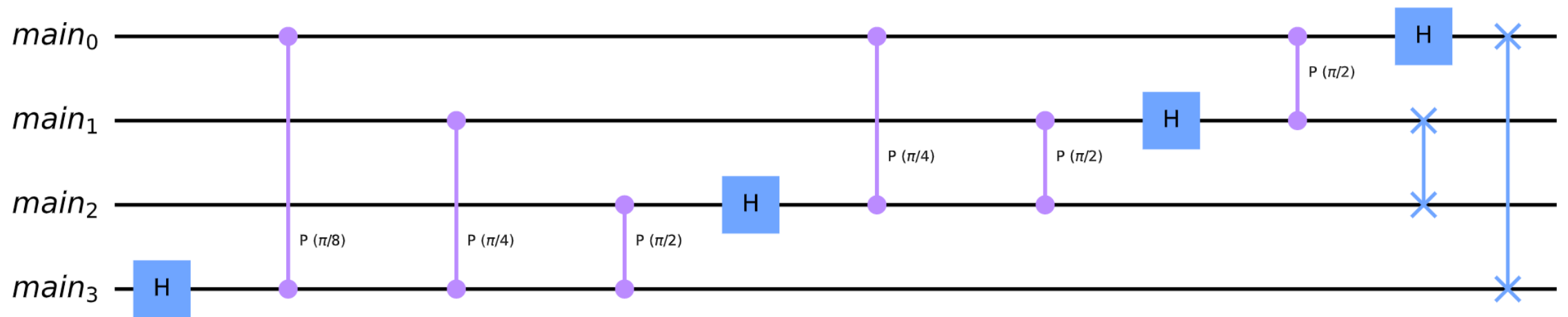
figures-4 : quantum processor registers



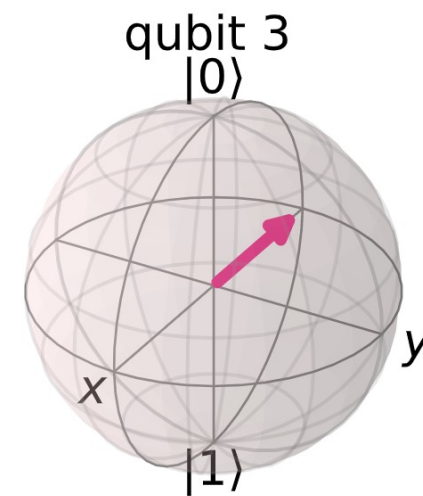
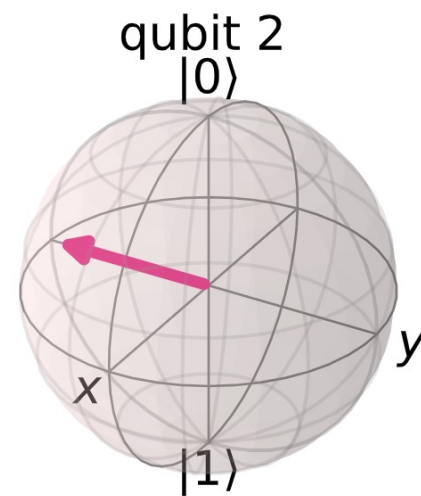
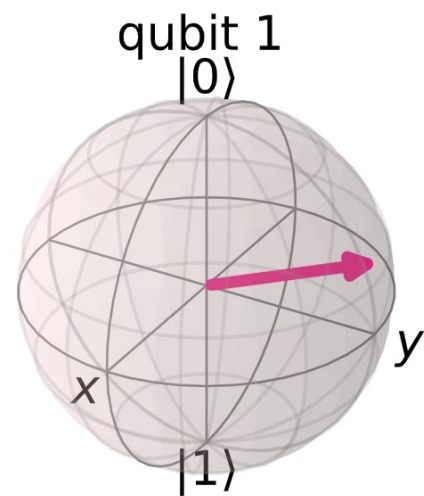
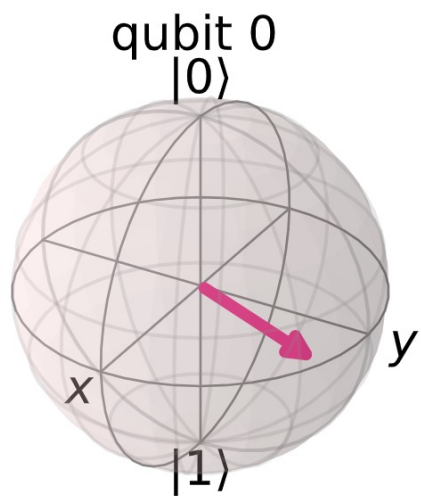
figures-5 : two-qubit-gate-global-to-local-mapping-different-processor-using-teleportation



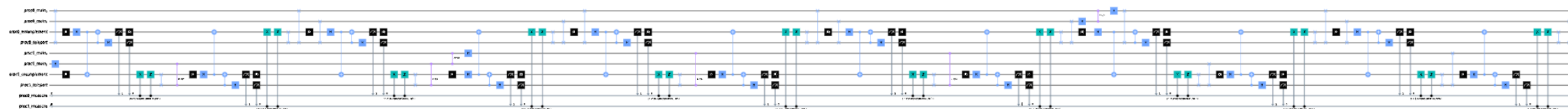
figures-6 : two-qubit-gate-global-to-local-mapping-different-processor-using-cat-state



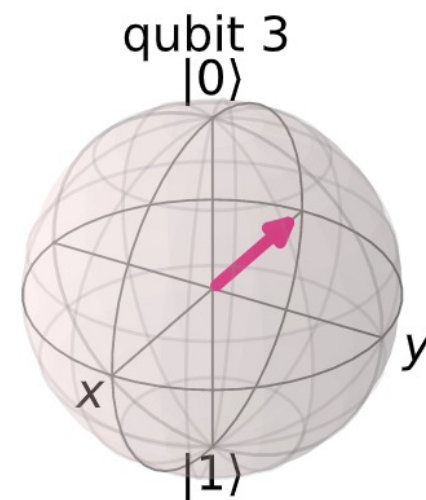
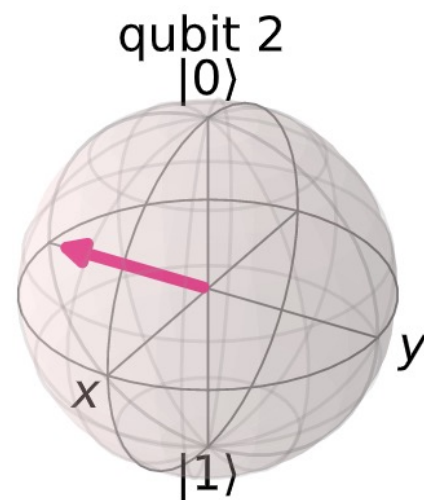
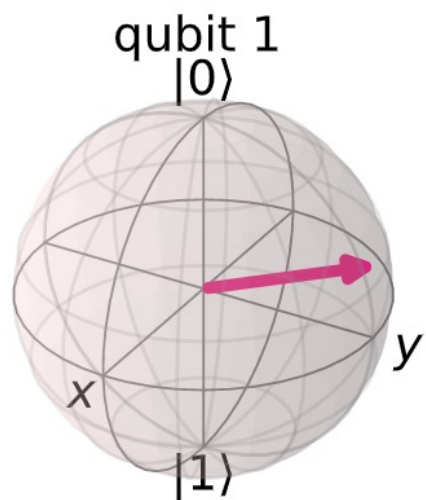
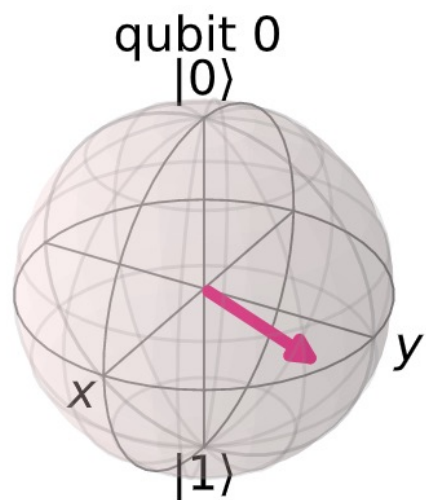
figures-7 : 4-bit-local-quantum-fourier-transformation



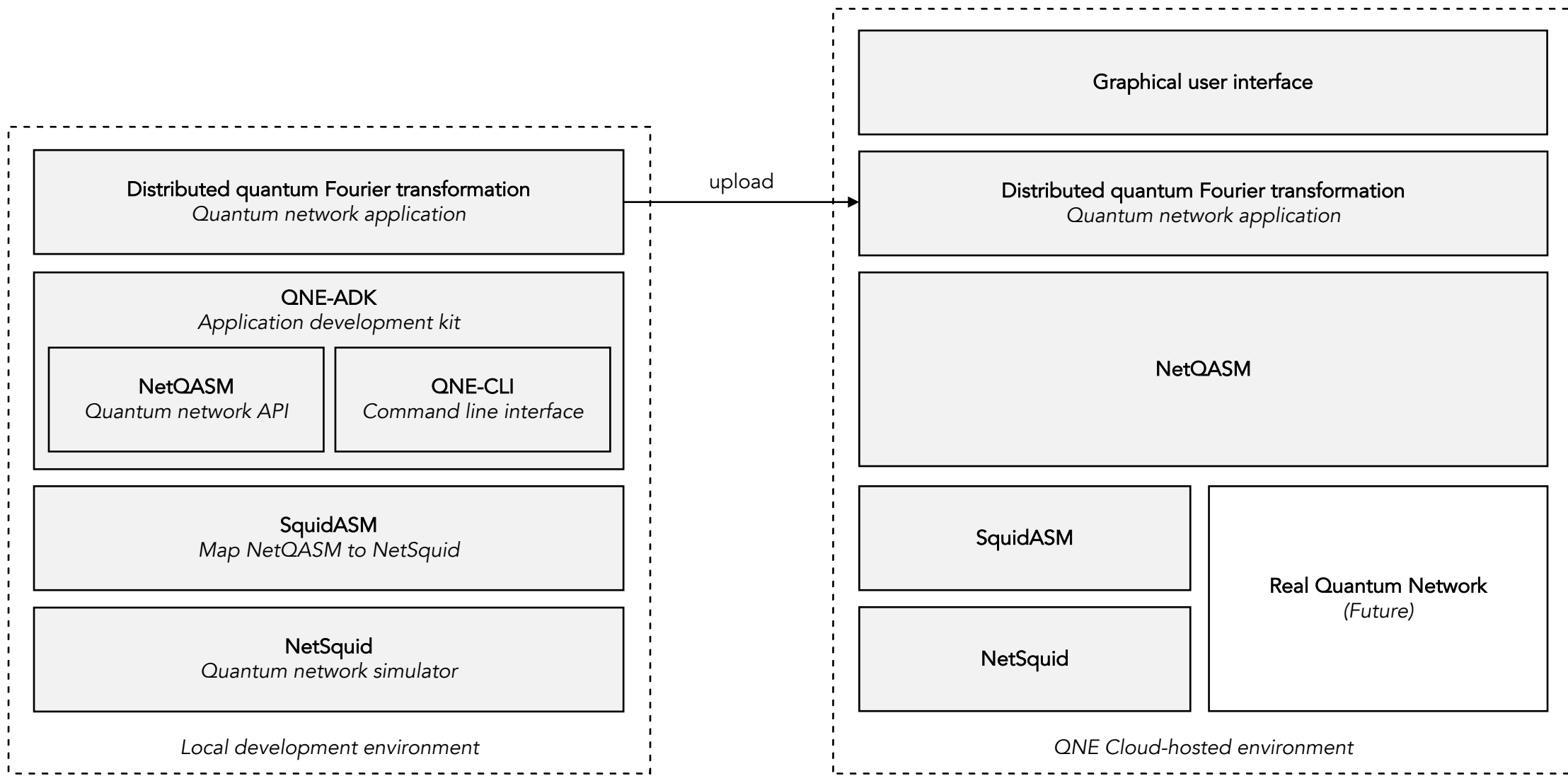
figures-8 : Bloch multi-vector for 4-bit QFT with input 3



figures-9 : 4-bit-distributed-quantum-fourier-transformation-using-teleportation



figures-10 : bloch-multi-vector-for-4-bit-distributed-qft-teleport-with-input-3.png



figures-11 :Components of QNE