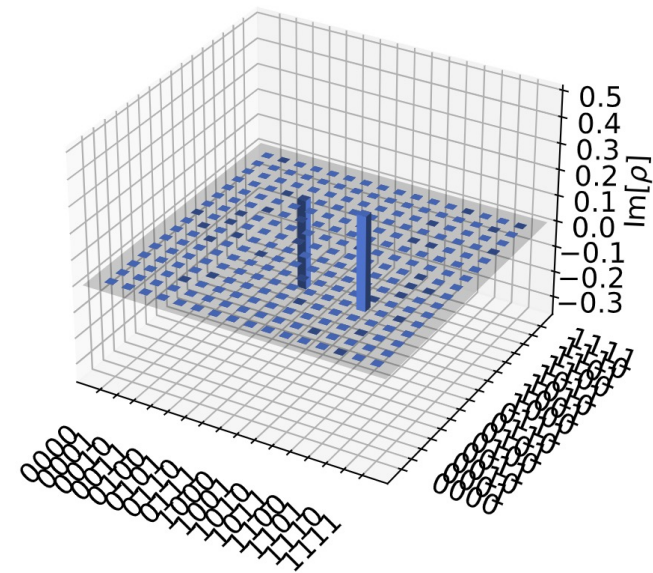



figures-0 : monolithic-quantum-computer-circuit-example



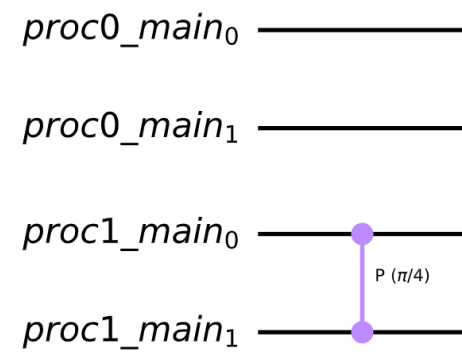
*proc0\_main*<sub>0</sub> ———

*proc0\_main*<sub>1</sub> ———

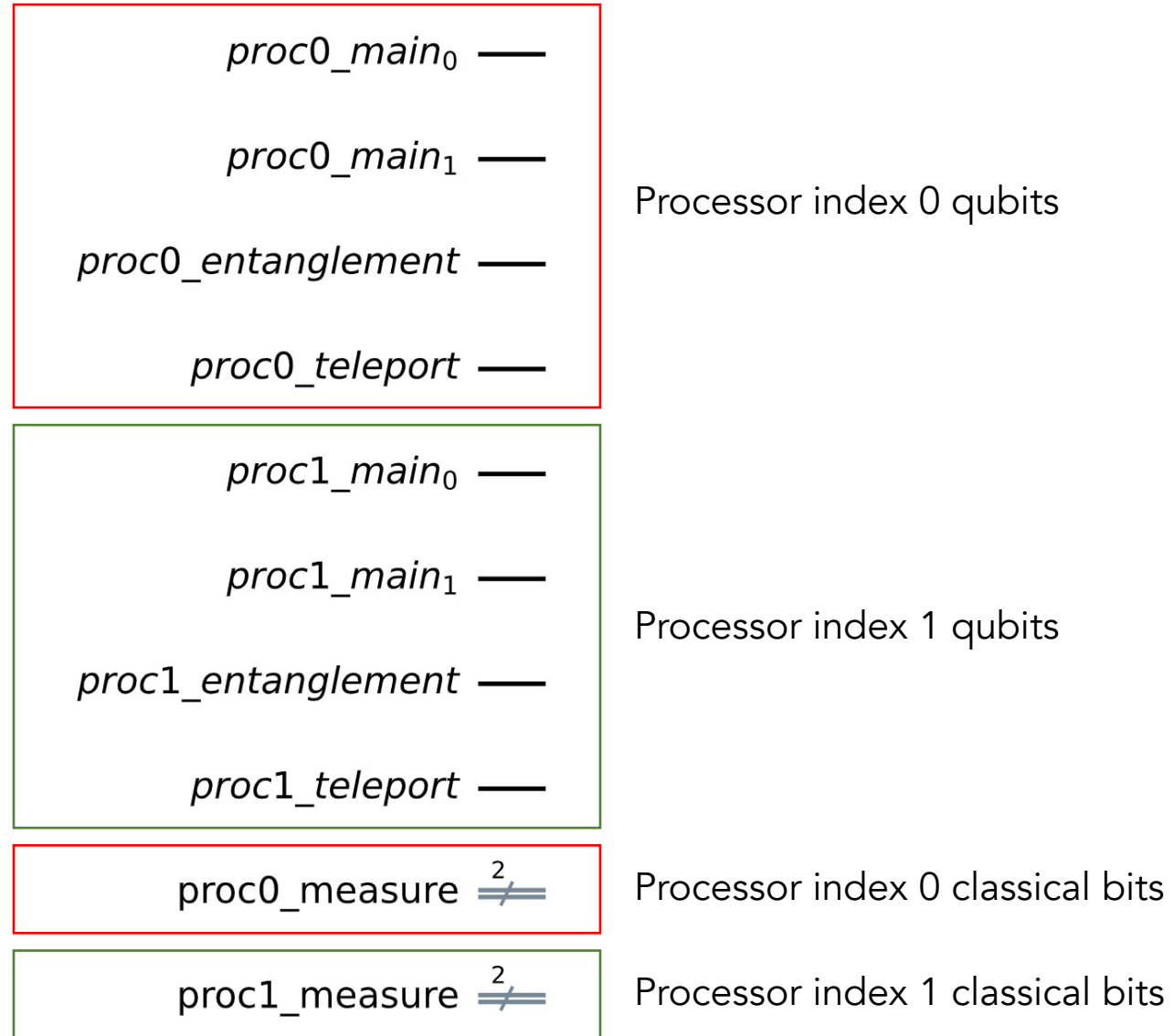
*proc1\_main*<sub>0</sub> —  —

*proc1\_main*<sub>1</sub> ———

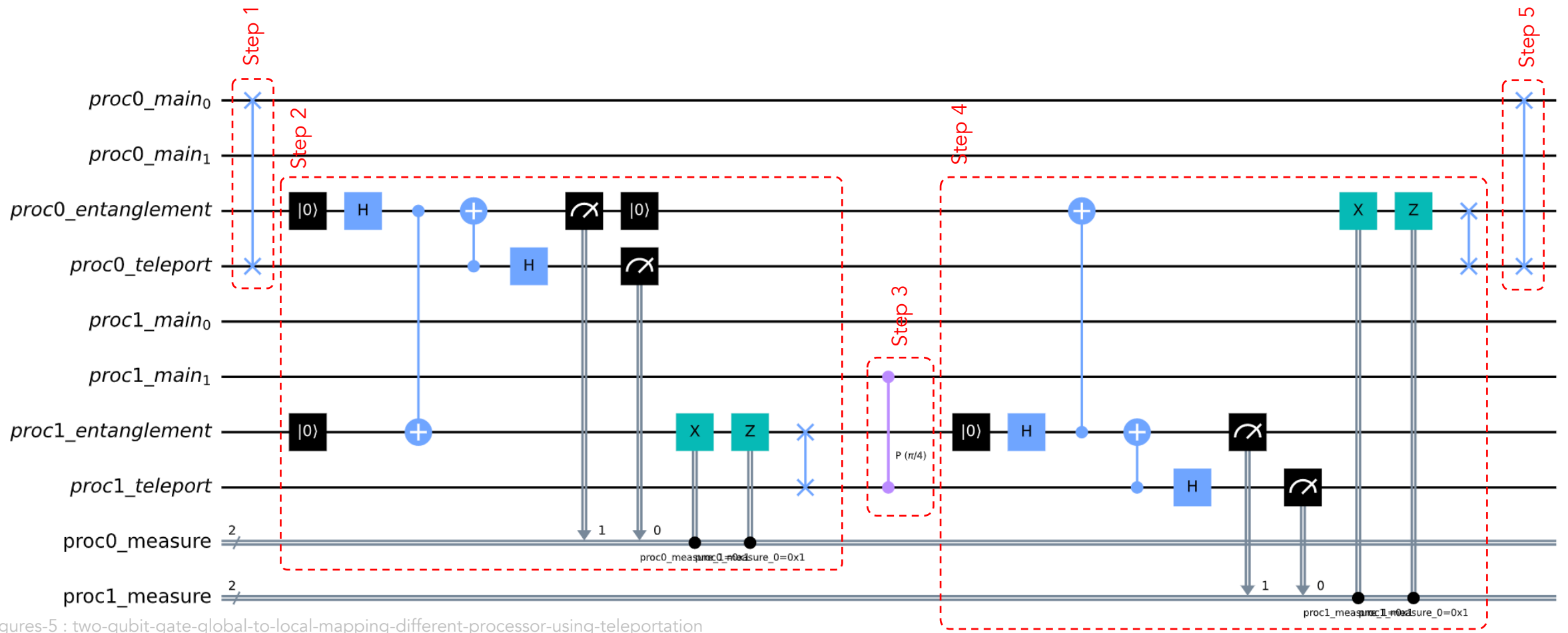
figures-2 : single-qubit-gate-global-to-local-mapping



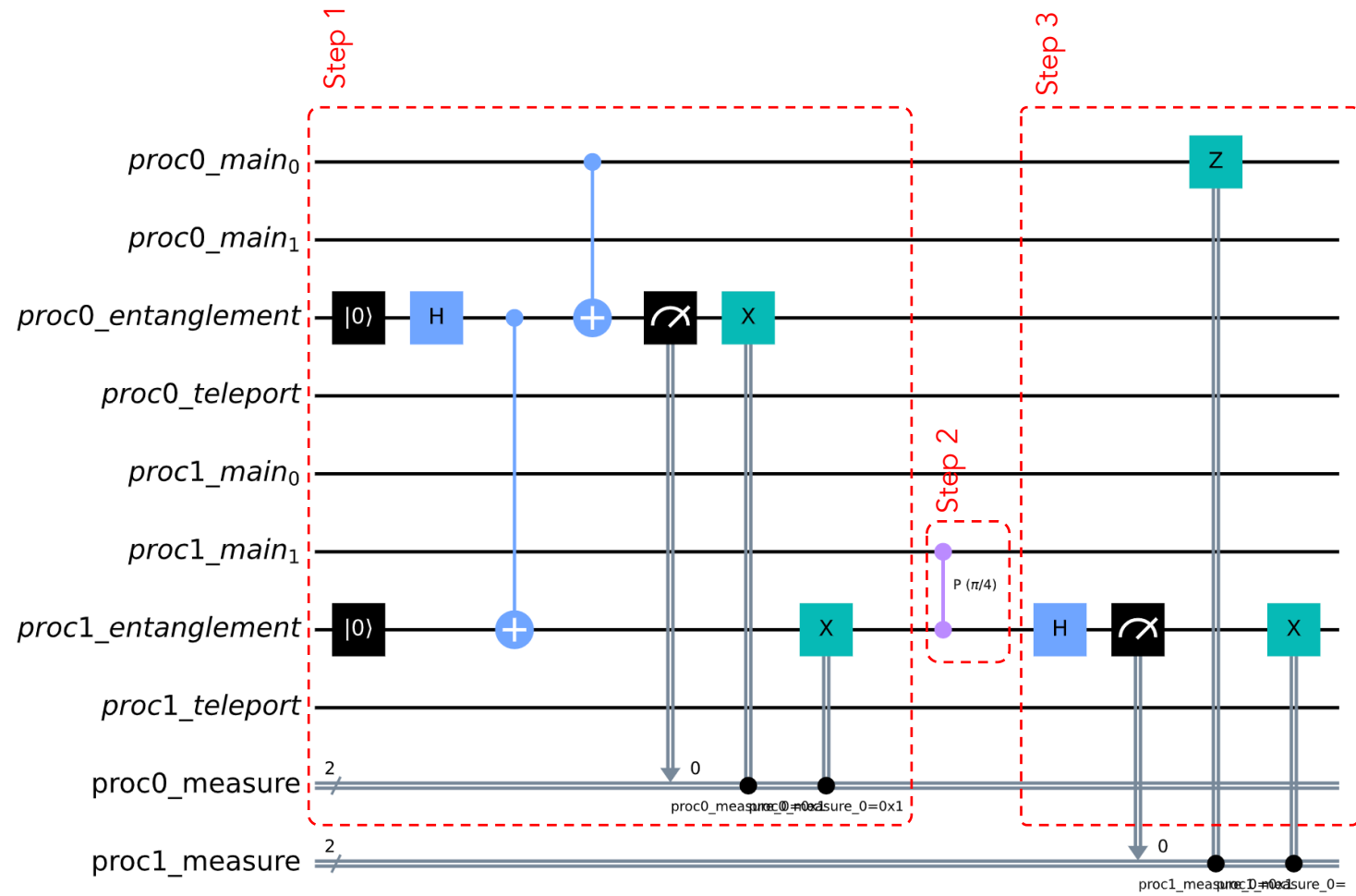
figures-3 : two-qubit-gate-global-to-local-mapping-same-processor



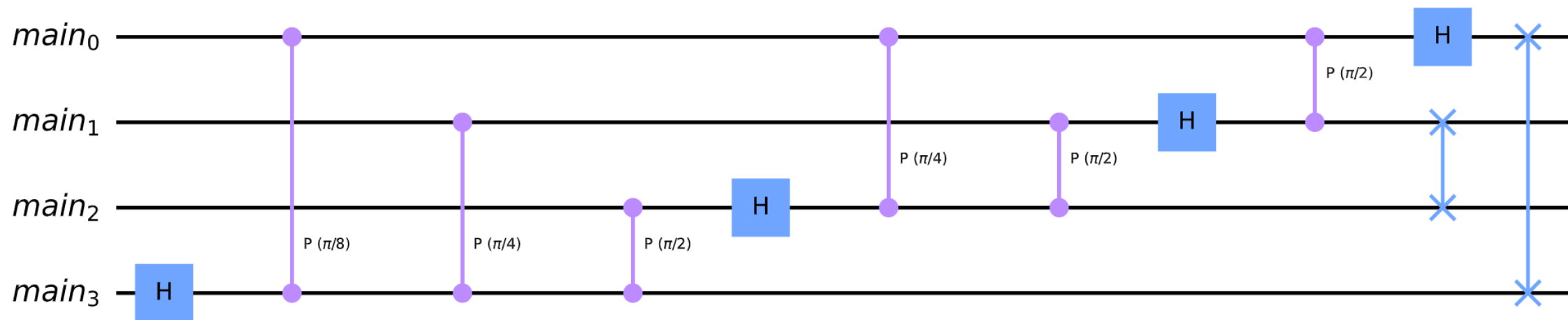
figures-4 : quantum processor registers



figures-5 : two-qubit-gate-global-to-local-mapping-different-processor-using-teleportation

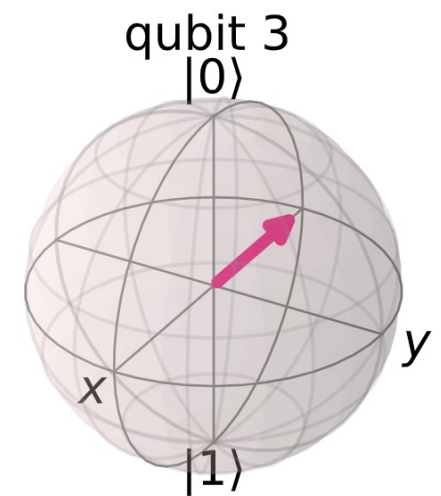
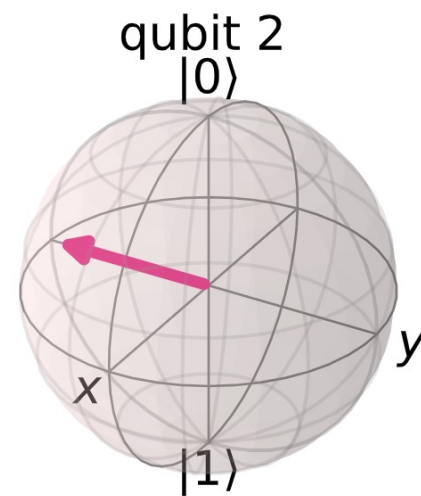
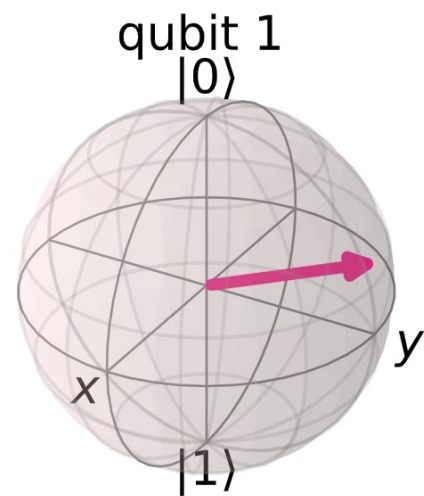
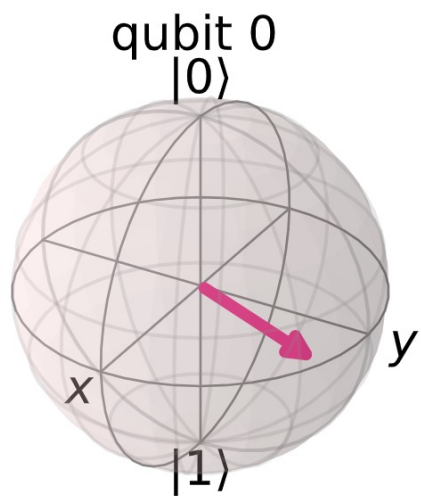


figures-6 : two-qubit-gate-global-to-local-mapping-different-processor-using-cat-state



figures-7 : 4-bit local quantum Fourier transformation





figures-8 : Bloch multi-vector for 4-bit QFT with input 3