

proc0_main₀ ———

proc0_main₁ ———

 $proc1_main_0 - H proc1_main_1 -$

figures-2: single-qubit-gate-global-to-local-mapping

proc0_main₀ —

proc0_main₁ —

proc0_entanglement —

proc0_teleport —

Processor index 0 qubits

 $proc1_main_0$ —

 $proc1_main_1$ —

proc1_entanglement ——

proc1_teleport —

Processor index 1 qubits

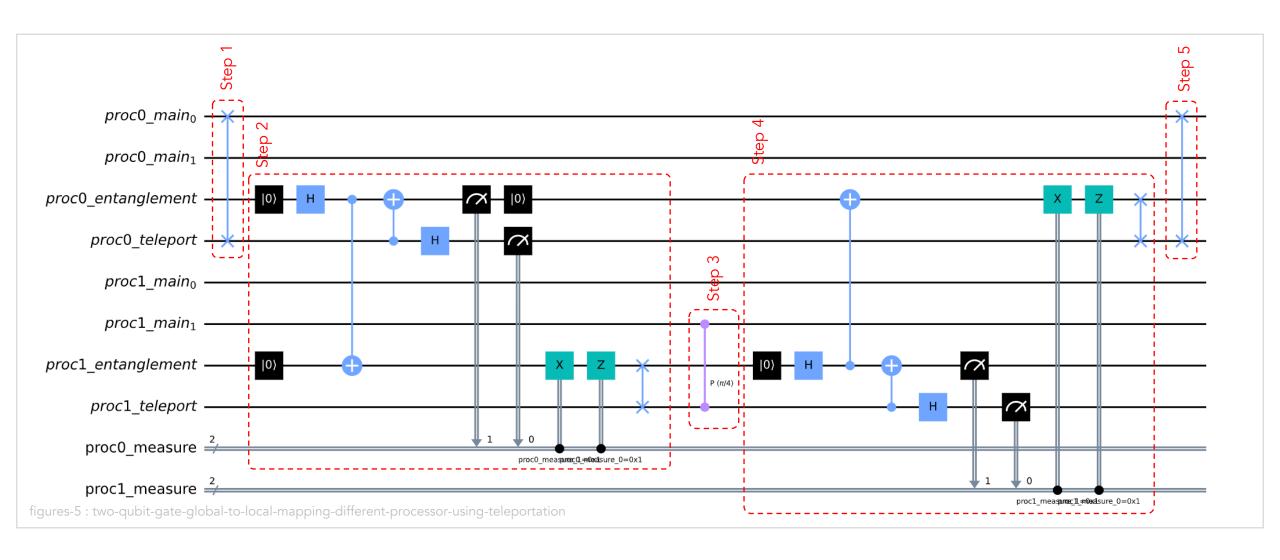
proc0_measure $\stackrel{2}{=}$

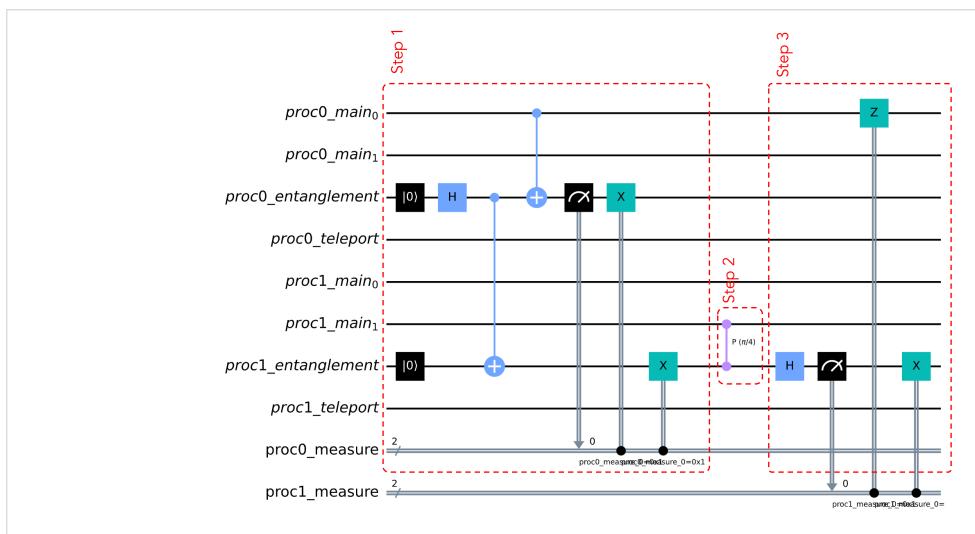
Processor index 0 classical bits

proc1_measure $\stackrel{2}{=}$

Processor index 1 classical bits

figures-4: quantum processor registers





figures-6: two-qubit-gate-global-to-local-mapping-different-processor-using-cat-state

