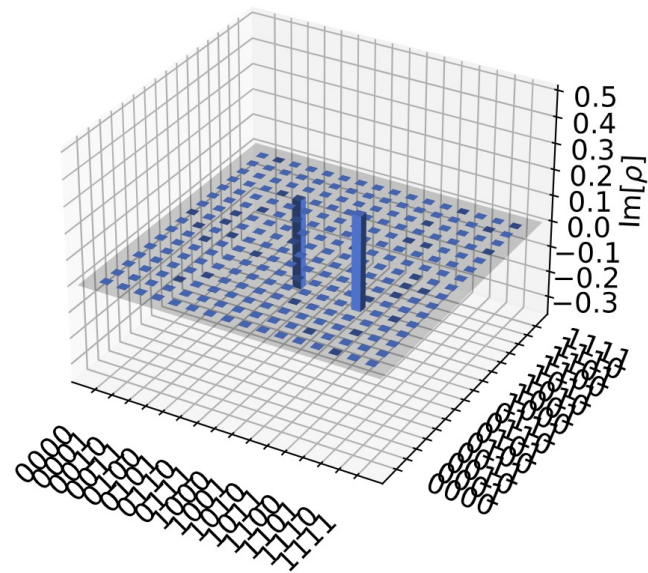
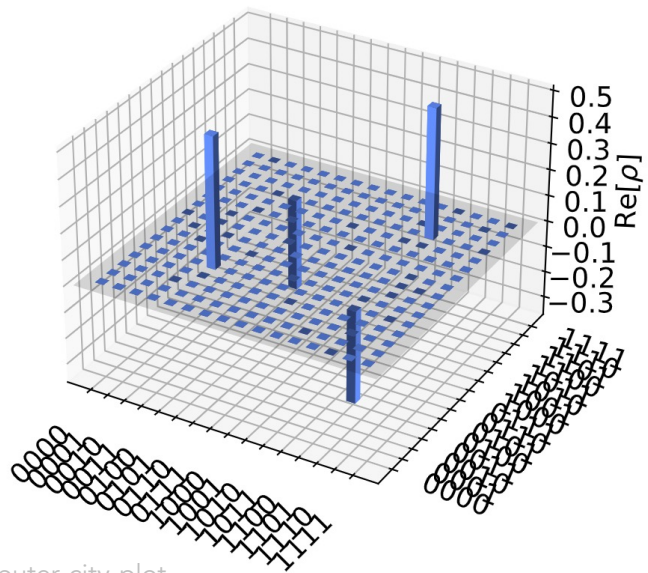



figures-0 : monolithic-quantum-computer-circuit-example



figures-1 : monolithic-quantum-computer-city-plot

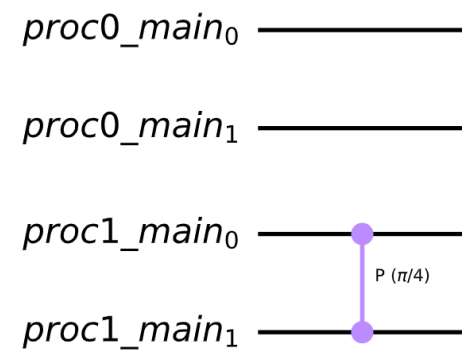
*proc0_main*₀ ———

*proc0_main*₁ ———

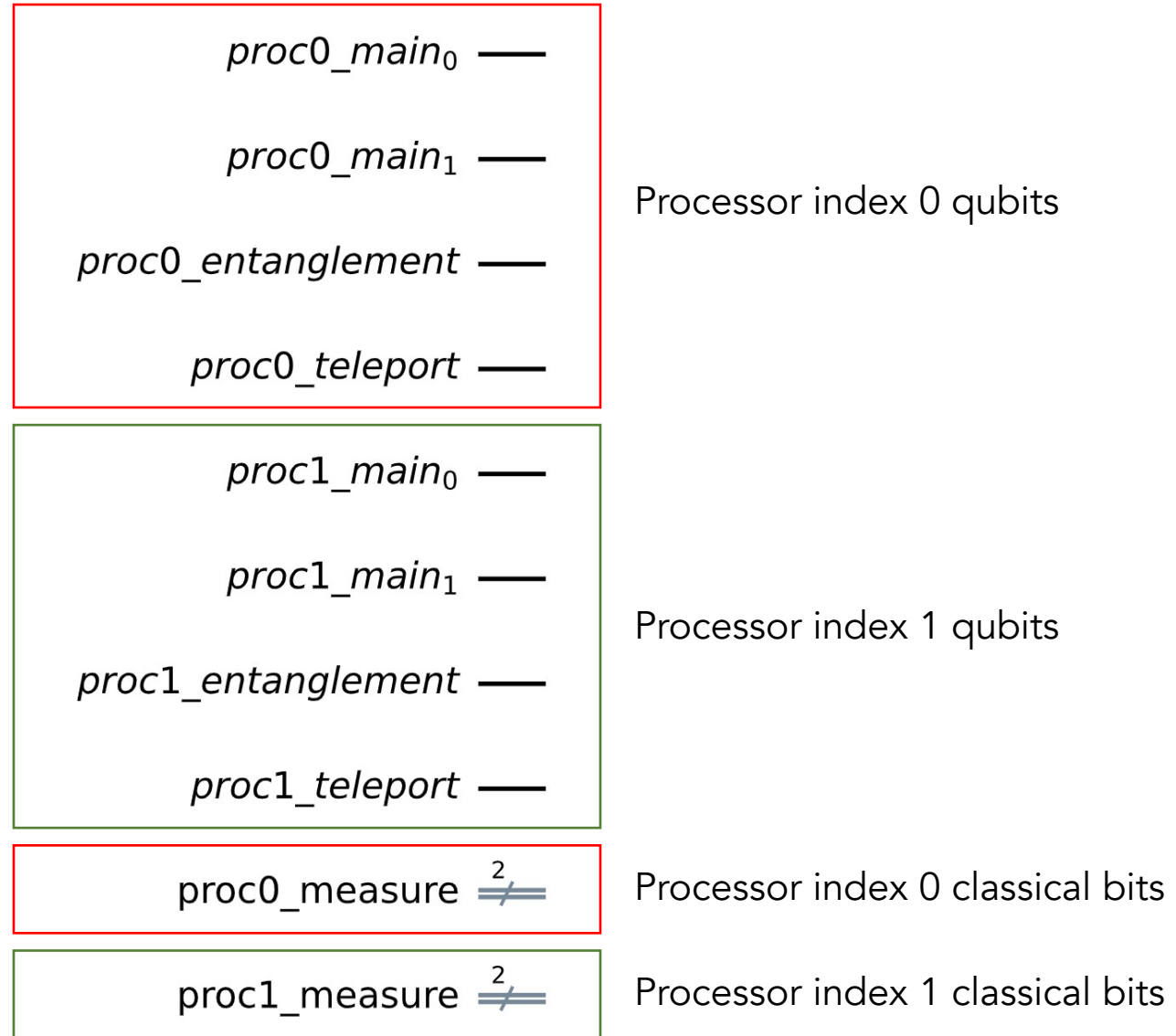
*proc1_main*₀ —  —

*proc1_main*₁ ———

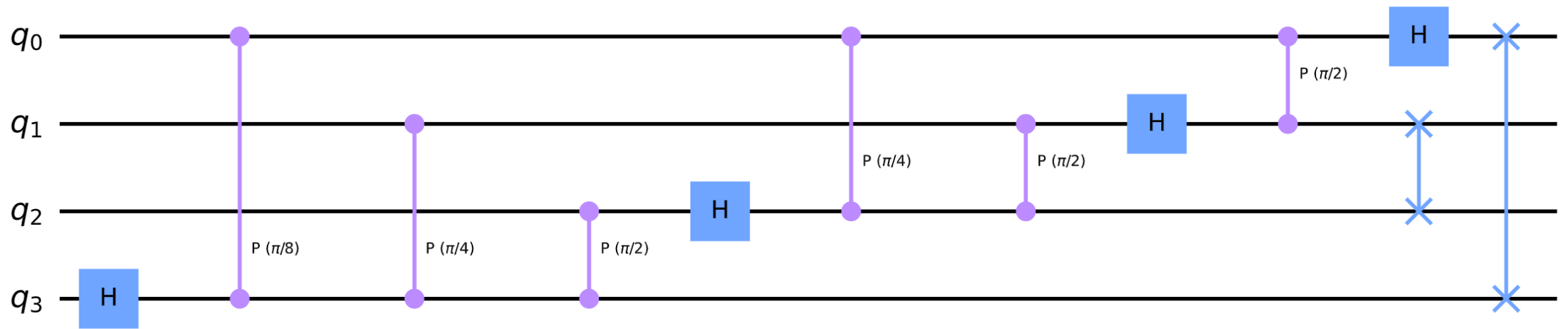
figures-2 : single-qubit-gate-global-to-local-mapping



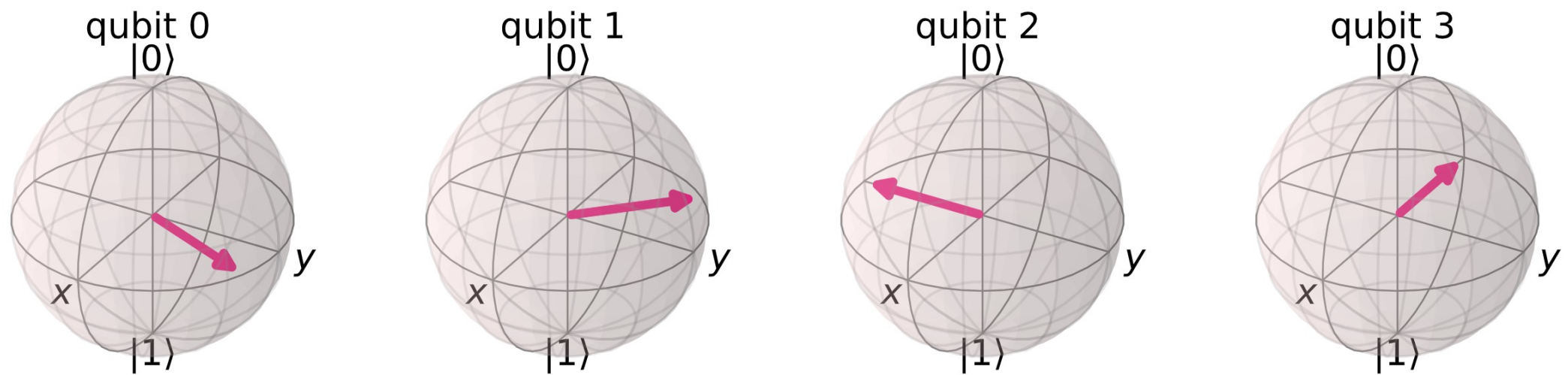
figures-3 : two-qubit-gate-global-to-local-mapping-same-processor



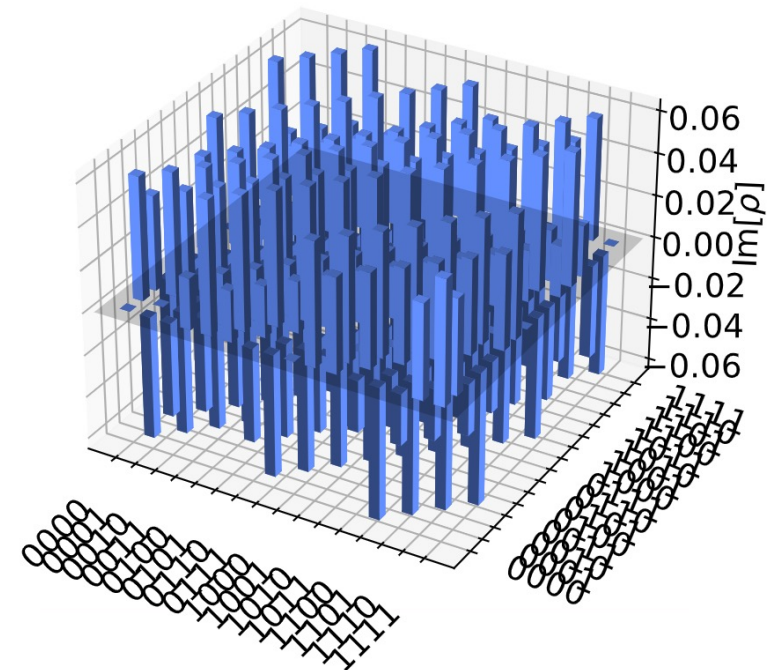
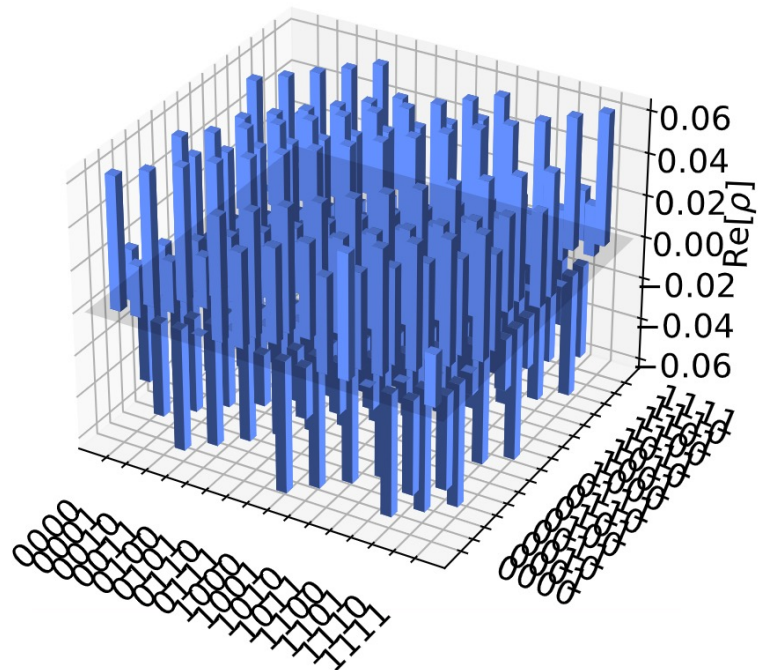
figures-4 : quantum processor registers



figures-5 : 4-bit local quantum Fourier transformation



figures-6 : Bloch multi-vector for 4-bit QFT with input 3



figures-7 : Density matrix city plot for 4-bit QFT with input 3