A Scalable Generator of Non-Hermitian Test Matrices computed from Given Spectra for Large-scale Systems

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3rd Workshop on Parallel Programming Models - Productivity and Applications

March 15, 2018

Aachen, Germany









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Eigenvalues and eigenvalue problems

Eigenvalues and eigenvectors

For a square matrix A, if there is a vector $u \in \mathbb{C}^n$ such that

$$Au = \lambda u$$

for some scalar λ , then λ is called the eigenvalue of A with corresponding (right) eigenvector u.

Applications of eigenvalue problems:

- numerical simulation
 - \odot the Schrödinger equation [8], molecular simulation [11], geology [7], etc.
 - opreconditioners for solving linear systems, e.g. UCGLE [12].
- machine learning and pattern recognition
 - principal component analysis (PCA) [4]
 - ⊝ Fisher discriminant analysis (FDA) [2]
 - clustering [9], etc.

Requirement of large-scale matrix generator

The backgroud:

- the eigenvalue problem size in both machine learning and numerical simulation is increasing;
- the numerical methods should be ajusted to the coming exascale platforms.

Thus there are three special requirements on the test matrices for the evaluation of numerical algorithms:

- their spectra must be known and can be easily controlled;
- they should be both sparse, non-Hermitian and non-trivial;
- they could have a very high dimension, which includes the non-zero element numbers and/or the matrix dimension to evaluate the algorithms on large-scale systems.

Related works

The related work:

- the Time Davis collection [5];
- the Matrix Market collection [3];
- Bai's collection [1];
- J. Demmel's generation suite in 1989 to benchmark LAPACK [6], etc.

Only the proposed method by J. Demmel generate the test matrices with given spectra, which can transfer the diagonal matrix with given spectra into a dense matrix with same spectra using the orthogonal matrices, and then reduce them to unsymmetric band ones by the Householder transformation. This method requires $\mathcal{O}(n^3)$ time and $\mathcal{O}(n^2)$ storage even for generating a small bandwidth matrix.

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Mathematical notations (H. Galicher et. al)

For all matrices $A \in \mathbb{C}^{n \times n}$, $M \in \mathbb{C}^{n \times n}$, $n \in \mathbb{N}$, a linear operator \widetilde{A}_A of matrix M determined by matrix A can be set up as Formule (1):

$$\begin{cases}
\widetilde{A_A} : \mathbb{C}^{n \times n} \to \mathbb{C}^{n \times n}, \\
M \to AM - MA.
\end{cases}$$
(1)

$$(\widetilde{A_A})^k(M_0) = \sum_{m=0}^k (-1)^m C_k^m A^{k-m} M_0 A^m.$$
 (2)

$$M_{i+1} = M_i + \frac{1}{i!} (\widetilde{A}_A)^i (M_0), i \in (0, +\infty).$$
 (3)

In order to make $(A_A)'$ tends to $\mathbf{0}$ in limited steps, it is necessary that $A=B^{-1}PB$, then we set the matrix P to be nilpotent, and the matrix B to be the identity matrix $I\in\mathbb{N}^{n\times n}$ for simplification based on the preliminary theoretical research [10].

SMG2S Algorithm (H. Galicher et. al)

The SMG2S algorithm is given as:

Algorithm 1 Matrix Generation Method

Input: $Spec_{in} \in \mathbb{C}^n$, h, d **Output:** $M_t \in \mathbb{C}^{n \times n}$

- 1: Insert random elements in h lower diagonals of $M_o \in \mathbb{N}^{n \times n}$
- 2: Insert $Spec_{in}$ on the diagonal of M_0 and $M_0 = (2d-2)!M_0$
- 3: Randomly insert 1 and 0 on sub-diagonal of $A \in \mathbb{N}^{n \times n}$ with the maximum continuous length of 1 to be d
- 4: **for** $i = 0, \dots, 2(d-2) 1$ **do**
- 5: $M_{i+1} = M_i + (\prod_{k=i+1}^{2d-2} k) (\widetilde{A_A})^i (M_0)$
- 6: end for
- 7: $M_t = \frac{1}{(2d-2)!} M_{2d-2}$

Parallel Implementation of CPUs and GPUs (X. Wu and S. Petiton)

We implement SMG2S on homogenous and heterogeneous machines. The former is implemented based on MPI and $PETSc^1$, the latter is based on MPI, CUDA, and PETSc. The kernel of implementation is the SpGEMM.

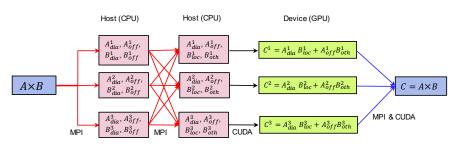


Figure: The structure of a CPU-GPU implementation of SpGEMM, where each GPU is attached to a CPU. The GPU is in charge of the computation, while the CPU handles the MPI communication among processes.

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Experimental hardware environment

We implement SMG2S on the supercomputers *Tianhe-2* and *Romeo*. The node specification for the two platforms is given as following:

Table: Node Specifications of the cluster ROMEO and Tianhe-2

Machine Name	ROMEO	Tiahhe-2	
Nodes Number	BullX R421 × 130	$16000 \times nodes$	
Mother Board	SuperMicro X9DRG-QF	Specific Infiniband	
CPU	2×Intel Ivy Bridge 8 cores 2.6 GHz	2×Intel Ivy Bridge 12 cores 2.2 GHz	
Memory	DDR3 32GB	DDR3 64GB	
Accelerator	NVIDIA GPU Tesla K20X × 2	Intel Knights Corner × 3	





Strong and Weak Scalability Evaluation (X. Wu and S. Petiton)

The strong and weak scaling tests on CPUs are given as:

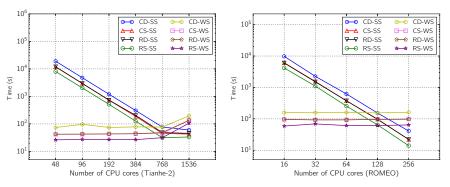


Figure: Strong and weak scalability on *Tianhe-2* and *Romeo*. A base 2 logarithmic scale is used for X-axis, and a base 10 logarithmic scale for Y-axis."CD" is short for "complex double", "CS" for "complex single", "RD" for "real double", "RS" for "real single", "SS" for "strong scalability", and "WS" for "weak scalability". On *Tianhe-2*, the matrix size for strong scalability is 1.6×10^7 , and the matrix sizes for weak scalability range from 1.0×10^6 to 3.2×10^7 . On *Romeo*, the matrix size for strong scalability is 3.2×10^6 , and the matrix sizes for weak scalability range from 4.0×10^5 to 6.4×10^6 . h and d are respectively 8 and 4.

Strong and Weak Scalability Evaluation (X. Wu and S. Petiton)

The strong and weak scaling tests on multi-GPUs are given as:

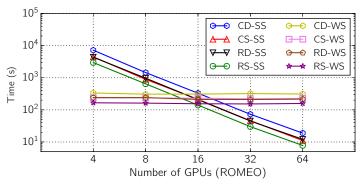


Figure: Strong and weak scalability of GPUs on *Romeo*. A base 2 logarithmic scale is used for X-axis, and a base 10 logarithmic scale for Y-axis."CD" is short for "complex double", "CS" for "complex single", "RD" for "real double", "RS" for "real single", "SS" for "strong scalability", and "WS" for "weak scalability". The matrix size for strong scalability is 8.0×10^5 , and the matrix sizes for weak scalability range from 2.0×10^5 to 3.2×10^6 . h and d are respectively 8 and 4.

Multi-GPU Speedup Evaluation (X. Wu and S. Petiton)

The multi-GPUs speedup over CPUs is given as:

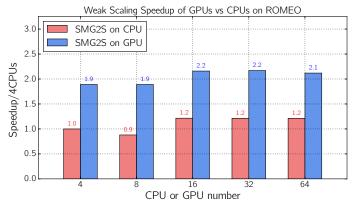


Figure: Weak scaling speedup of GPUs vs CPUs on *Romeo* with real double scalar type. X-axis refers to computing unit number from 4 to 64, and Y-axis refers to the speedup of CPUs or GPUs over time spent by 4 CPUs with matrix size 2.0×10^5 . The matrix sizes for the weak scalability are respectively 2.0×10^5 , 4.0×10^5 , 8.0×10^5 , 1.6×10^6 and 3.2×10^6 . h and d are respectively 8 and 4.

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Verification method (X. Wu and S. Petiton)

We proposed a method to check the ability of SMG2S to keep the given spectra based on the Shifted Inverse Power Method.

Algorithm 2 Shifted Inverse Power Method

Input: Matrix A, initial guess for desired eigenvalue σ , initial vector v_0 **Output:** Approximate eigenpair (θ, v)

- 1: $y = v_0$
- 2: **for** $i = 1, 2, 3 \cdots$ **do**
- 3: $\theta = ||y||_{\infty}$, $v = y/\theta$
- 4: Solve $(A \sigma I)y = v$
- 5: end for

Check error

$$error = \frac{||Av' - \lambda v'||}{||Av'||}$$

Verification results (X. Wu and S. Petiton)

The verification tests have been done with 4 different types of spectra.

Table: Test Spectra information

Spectra Name	spec1	spec2	spec3	spec4
Scalar Type	complex	real	complex	real
Spectra Interval	[10,1000]	[10,1000]	[5,500]	[5,500]

Verification results (X. Wu and S. Petiton)

The accuracy verification results are given as:

Table: Accuracy Verification Results.

Matrix Nº	Size	Spectra	precision	Accuracy	Acceptance (%)	max error
1	100	spec1	double	1×10^{-13}	100	6 × 10 ⁻¹⁴
2	100	spec1	single	$1 imes 10^{-6}$	100	3×10^{-7}
3	100	spec2	double	1×10^{-13}	100	8×10^{-14}
4	100	spec2	single	1×10^{-6}	97	3×10^{-3}
5	100	spec3	double	1×10^{-15}	100	4×10^{-16}
6	100	spec3	single	$1 imes 10^{-6}$	100	6×10^{-7}
7	100	spec4	double	1×10^{-15}	94	4×10^{-4}
8	100	spec4	single	1×10^{-6}	100	9×10^{-7}

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Conclusion and Perspectives

Then

- SMG2S is a method to generate large-scale non-Hermitian matrices with good scalabilities;
- SMG2S has capacility to keep the accuracy of given spectra;
- An open source software should be implemented based on the basic C or C++, CUDA and MPI without PETSc and other large libraries;
- The matrix-matrix multiplication kernel should be optimized and specified for both CPUs and multi-GPUs.

Acknowledgement

- We would like to thank Prof. Yutong LU and their team in the National Supercomputing Center in Guangzhou for providing the use of Tianhe-2.
- This work is partially supported by the ROMEO HPC Center Champagne Ardenne for providing the use of cluster Romeo.
- This work is supported by the German-Japanese-French project MYX partially supported by the French National Research Agency (ANR) under the SPPEXA framework.

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Thank you for your attentions!

Questions?