

## M.ENG PROPOSAL

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### Test/Flight readiness development of ChipSats

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## 1 Background

This project will focus on assisting the SSDS team with two different systems and a milestone test leading up to a spaceflight at some point in the future. The two systems are the alpha ChipSat, and the DeSCENT chip satellites. The Alpha ChipSat is majorly completed – final flight units are expected to be due in the very near future – it is likely no major changes will be made. However, environmental testing still needs to be done on the flight units along with power and some reliability testing. These ChipSattelites will be the main communications interface on the light sail that CubeSat will be deploying.

The DeSCENT mission is slightly different in comparison to the Alpha ChipSat. DeSCENT is the Demonstration of Suborbital Chipsats Ejected from New Shepard Test flight. Where the goal of this mission is to deploy a large number of chip satellites from Blue Origin's New Shepard launch vehicle. The New Shepard typically has an apogee of around 100-106km, just barely at the edge of space by international standards and well into space by U.S. standards. After deploying these craft fall back down to earth, collecting and transmitting data like temperature, pressure, vehicle orientation etc the entire time. Under ideal conditions these chipsats will be communicating their location to us once on the ground, allowing for at least one or two recoveries. However there are a lot of factors at play for that to happen out of one hundred we can likely hope for 3-4 to be recovered.

## 2 Tasks and Requirements

### 2.1 DeSCENT

On the DeSCENT mission there is still a lot of work to be done over the year. There is significant amounts of work to be done on the power and RF side of the V1 chipsat We are just beginning the work of a new version of the DeSCENT chipsat durrently dubbed v2 - but hopefully changing to Tiberius. On this new version some of the major work to be done is:

1. WIO-E5 footprinting
2. WIO-E5 PCB implementation
3. Breadboarding to test WIO-E5 circuitry

4. Testing of PCBs when they arrive
5. Power testing and design
  - (a) Falling weight balance
  - (b) Maple seed design
6. Breadboarding and testing of both v1 and v2

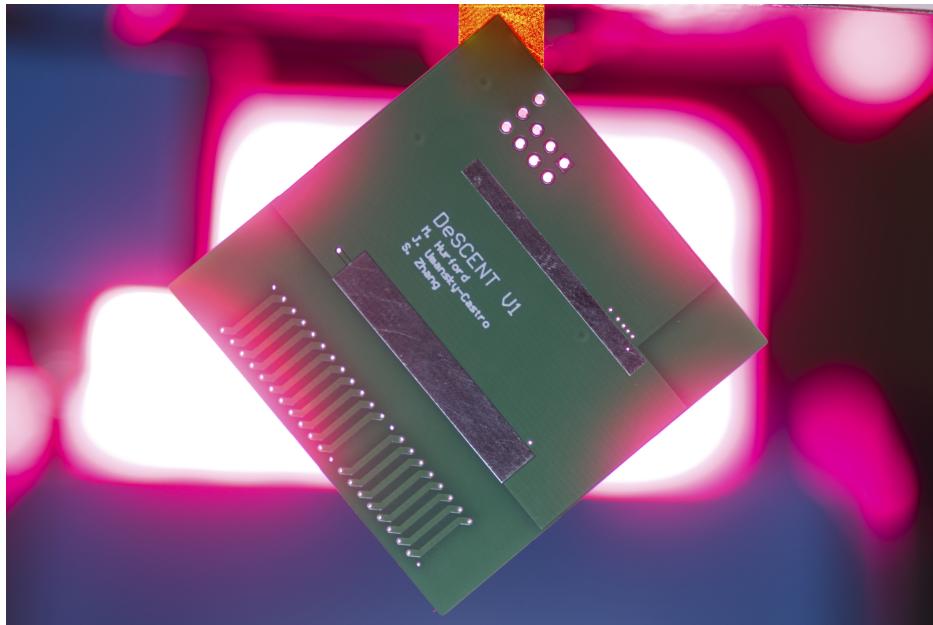


Figure 1: Version one of the DeSCENT ChipSat.

## 2.2 Alpha ChipSat and Picoballoon

As far as the Alpha ChipSat goes there is far less to do. Mainly involving finishing touches and testing the infrastructure that we are planning to use for communications once in space. The infrastructure test will be done using a high altitude balloon. Some goals for this part of the project are:

1. Validate balloon test solar setup
  - (a) Power is on and consistent throughout flight

- (b) Longer duration than the last flight – both signals dropped at 90 minutes.
  - (c) Able to transmit consistently – very power hungry
2. Launch balloon test
  3. circumnavigate the globe with the high altitude balloon
  4. Solve GPS power up issue

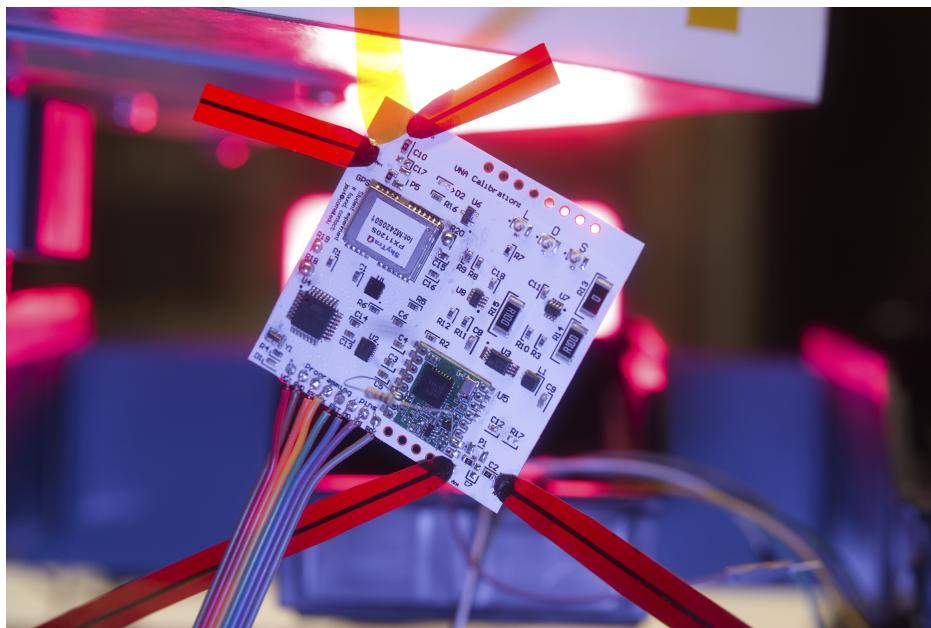


Figure 2: A current Alpha ChipSat.

### 3 Deliverables

Given the short timeframe of a semester, and excluding a lot of work that is going to be done I have honed in on two main deliverables.

1. 1-2 more PicoBalloon launches
2. A fully tested and working V1 DeSCENT

- (a) Or a breadboarded and tested V2 chipsat

While there will be plenty of work outside these deliverables taking place over the course of the semester, these are what I believe are good milestones.

## 4 Timeline

A timeline for this semester can be seen in figure 3 there are a significant amount of tasks that overlap. In some cases it could be seen that there are also very few tasks for november. Both of these observations are due to the fact that the PicoBalloon is highly affected by weather and the fact that future work on the PicoBalloon is based on how the second test goes in october, weather prevailing.

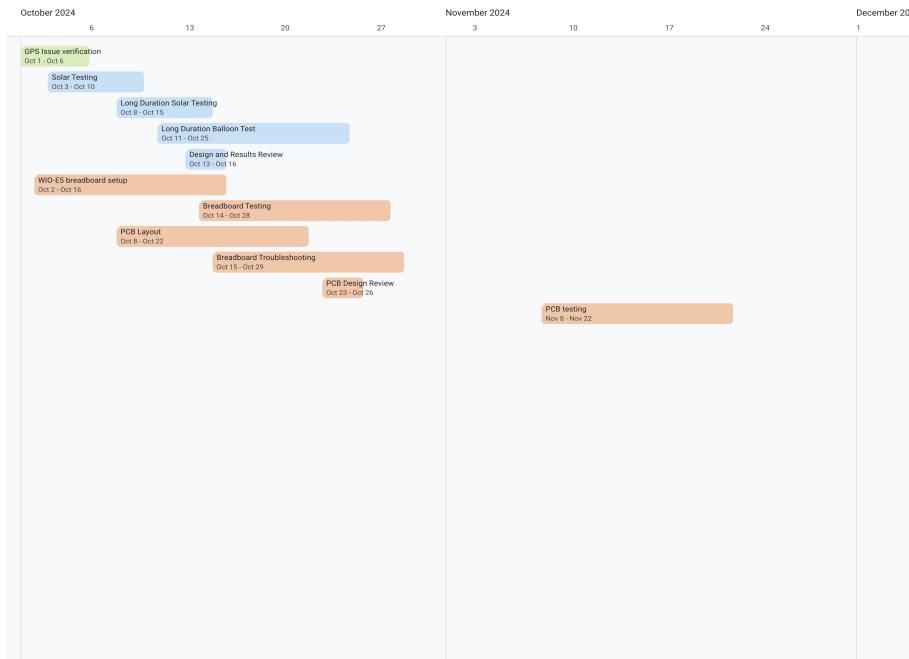


Figure 3: A (mostly) semester long timeline for SSDS work

The DeSCENT mission tasks are all in orange, these tasks are on a rolling timeline essentially as some of them are sequential, and V2 is not the main priority. The launch window could open as soon as november, so working out any issues with the V1 is crucial before then. If the focus this semester becomes solely V1 then V2 will be the focus of next semester. Figure 3 is difficult to read so here is the data that went into the chart Moving into

Task	Duration(days)
GPS Issue verification	5
Solar Testing	7
Long Duration Solar Testing	7
Long Duration Balloon Test	14
Design and Results Review	3
WIO-E5 breadboard setup	21
Breadboard Testing	21
PCB Layout	14
Breadboard Troubleshooting	21
PCB Design Review	3
PCB testing	14

the next semester will consist majorly of testing the V2 chipsat, which is beginning to be ordered as of now – Nov 2024. Given that this project is research based I originally left out next semester from the timeline because it would be heavily based on what occurred this semester. This semester has been filled with more issues than wins on the picoballoon chip sats and the v2 mcu, less progress than initially intended has been achieved. This actually works out well on a project basis as it leaves a significant amount of work for the V2 chipsat in semester 2.

## 5 Conclusion

Though time is extremely limited there is significant work and progress that can be made on both described systems, including the PicoBalloon project. With launches slated for next semester a majority of work must be completed this semester. Projects will continue into V2 or even new project opportunities next semester. With that being said this is a perfect opportunity for me to learn what goes into space systems design and learn the specific techniques used in this design space, all while providing my own expertise to the group.