Bryan S. Kim

Assistant Professor Syracuse University 4-181 CST, 111 College Pl, Syracuse, NY 13244 bkim 01@syr.edu sites.google.com/view/bryansjkim 1.315.443.1249

Research Interests

- Flash and non-volatile memory-based systems
- Data storage systems
- File systems and key-value store

Professional Experience

• Assistant Professor

Department of Electrical Engineering & Computer Science

Aug. 2019 – present

Syracuse University

Postdoctoral Researcher Mar. 2018 – June 2019 Institute of Computer Technology Seoul National University

Education

Seoul National University

Ph.D. in Computer Science & Engineering Feb. 2018

- Advisor: the late Prof. Sang Lyul Min

- Thesis: An Autonomic SSD Architecture

Seoul National University

M.S. in Electrical Engineering & Computer Science

Aug. 2009

- Advisor: the late Prof. Sang Lyul Min

- Thesis: Efficient Flash Memory Read Request Handling Based on Split Transactions

University of California, Berkeley

B.S. in Electrical Engineering & Computer Science

May 2006

Grants & Awards

Bryan S. Kim. CNS CORE: SMALL: CPR FOR FLASH-BASED STORAGE

NSF \$488,277

• Systems. Funded by National Science Foundation, 2020

Publications

Youil Han, Bryan S. Kim, Jeseong Yeon, Sungjin Lee, and Eunji Lee.

TEKSDB: WEAVING DATA STRUCTURES FOR A HIGH-PERFORMANCE
KEY-VALUE STORE. In International Conference on Measurement and
Modeling of Computer Systems, 2019

SIGMETRICS'19
acceptance rate:
17.1%

Bryan S. Kim, Eunji Lee, Sungjin Lee, and Sang Lyul Min. CPR FOR SSDs.

HotOS'19

• In ACM SIGOPS Workshop on Hot Topics in Operating Systems, 2019

acceptance rate: 24.0%

Youil Han, Bryan S. Kim, Jeseong Yeon, Sungjin Lee, and Eunji Lee. TEKSDB: WEAVING DATA STRUCTURES FOR A HIGH-PERFORMANCE

KEY-VALUE STORE. In Proceedings of the ACM on Measurement and Analysis of Computing Systems, 3(1): 8:1–8:23, 2019

POMACS'19

•	Bryan S. Kim, Jongmoo Choi, and Sang Lyul Min. Design Tradeoffs for SSD Reliability. In <i>USENIX Conference on File and Storage Technologies</i> , 2019: 281–294	FAST'19 acceptance rate: 17.9%
•	Bryan S. Kim. The Human Manual. In ACM Crossroads Student Magazine, 25(1): 34–37, 2018	XRDS'18
•	Geonhee Lee, Hyeon Gyu Lee, Juwon Lee, Bryan S. Kim* and Sang Lyul Min. An Empirical Study on NVM-based Block I/O Caches. In ACM SIGOPS Asia-Pacific Workshop on Systems, 2018	APSys'18 acceptance rate: 36.0%
•	Bryan S. Kim, Hyun Suk Yang, and Sang Lyul Min. AutoSSD: AN Autonomic SSD Architecture. In <i>USENIX Annual Technical Conference</i> , 2018: 677–689	ATC'18 acceptance rate: 20.1%
•	Bryan S. Kim. Utilitarian Performance Isolation in Shared SSDs. In USENIX Workshop on Hot Topics in Storage and File Systems, 2018	HotStorage'18 acceptance rate: 36.7%
•	Bryan S. Kim, Yonggun Lee, and Sang Lyul Min. Framework for Efficient and Flexible Scheduling of Flash Memory Operations. In <i>IEEE Non-Volatile Memory Systems and Applications</i> , 2017: 1–5	NVMSA'17 acceptance rate: 33.3%
•	Bryan S. Kim and Sang Lyul Min. QoS-aware Flash Memory Controller. In <i>IEEE Real-Time and Embedded Technology and Applications Symposium</i> , 2017: 51–62	RTAS'17 acceptance rate: 23.7%
•	Eyee Hyun Nam, Bryan S. Kim, Hyeonsang Eom, and Sang Lyul Min. OZONE (O3): AN OUT-OF-ORDER FLASH MEMORY CONTROLLER ARCHITECTURE. In <i>IEEE Transactions on Computers</i> , 60(5): 653–666, 2011	TC'11
•	Bryan S. Kim, Eyee Hyun Nam, Yoon Jae Seong, Hang Jun Min, and Sang Lyul Min. Efficient Flash Memory Read Request Handling Based on Split Transactions. In <i>International Workshop on Software Support for Portable Storage</i> , 2009	IWSSPS'09
•	Joon Ho Um, Bryan S. Kim, Sung Gab Lee, Eyee Hyun Nam, and Sang Lyul Min. Flash Memory-Based Development Platform for Homecare Devices. In <i>IEEE International Conference on Systems, Man, and Cybernetics</i> , 2008: 2259–2263	SMC'08
•	Jin Hyuk Yoon, Eyee Hyun Nam, Yoon Jae Seong, Hongseok Kim, Bryan S. Kim, Sang Lyul Min, and Yookun Cho. Chameleon: A High Performance Flash/FRAM Hybrid Solid State Disk Architecture. In <i>IEEE Computer Architecture Letters</i> , 7(1): 17–20, 2008	CAL'08
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Patents

Bryan S. Kim and Sang Lyul Min. CONTROL DEVICE FOR DYNAMICALLY
ALLOCATING STORAGE SPACE AND DATA STORAGE DEVICE INCLUDING
THE CONTROL DEVICE. Korea Patent Application 10-2018-0116646, filed
Sep. 2018; U.S. Patent Application 16/284924, filed Feb. 2019

Korea: filed
U.S.: filed

101727983, filed Oct. 2009, and issued June 2016

Bryan S. Kim and Sang Lyul Min. SEMICONDUCTOR DEVICE FOR SCHEDULING TASKS FOR MEMORY DEVICE AND SYSTEM INCLUDING THE Korea: filed • Same. Korea Patent Application 10-2017-0153547, filed Nov. 2017; U.S. U.S.: granted Patent 10,635,351, filed Mar. 2018, and issued Apr. 2020; China Patent China: filed Application 2018-1-0298334.X, filed Apr. 2018 Bryan S. Kim and Eyee Hyun Nam. MEMORY APPARATUS AND CONTROL • METHOD THEREOF. Korea Patent 10-1564574, filed Nov. 2013, and issued Korea: granted Oct. 2015 Hongseok Kim, Bryan S. Kim, and Eyee Hyun Nam. MEMORY APPARATUS AND CONTROL METHOD THEREOF. Korea Patent 10-1531965, filed Nov. Korea: granted 2013, and issued June 2015 Sang Lyul Min, Bryan S. Kim, Jinhyuk Kim, Donggi Lee, Taesung Jung, Byeongse So, Duckhyun Chang. MEMORY DEVICE AND PROGRAM METHOD U.S.: granted THEREOF. Korea Patent 10-1544607, filed Oct. 2008, and issued Aug. 2015; Korea: granted U.S. Patent 8,493,782, filed Oct. 2009, and issued July 2013; China Patent China: granted

Industry Experience

• SK Telecom Manager at Storage Tech. Lab	Seongnam, South Korea Apr. 2013 – Sep. 2015
• Oracle Corporation Research intern at Solaris kernel team	Santa Clara, USA June 2011 – Sep. 2011
• Samsung Advanced Institute of Technology Research intern at Semiconductor lab	Yongin, South Korea July 2010 – Sep. 2010
n&k Technology Inc. Application engineer	San Jose, USA July 2006 – July 2007

Teaching

•	CIS700: Storage Systems for Big Data Instructor	Syracuse University Spring 2020
•	CIS341: Computer Organization & Programming Systems Instructor	Syracuse University Spring 2020
•	CIS486: Design of Operating Systems Guest lecturer	Syracuse University Oct. 2019
•	${\sf ECS}$ 101: Introduction to Engineering and Computer Science $Guest\ lecturer$	Syracuse University Oct. 2019
•	ECS 691: Fundamentals of Research Guest lecturer	Syracuse University Oct. 2019
•	CSE791: Storage for Big Data & Cloud Computing Instructor	Syracuse University Fall 2019
•	035.001: Introduction to Computer Science Instructor (rating: 4.68/5.00)	Seoul National University Spring 2019

• 035.001: Introduction to Computer Science
Instructor (rating: 4.62/5.00)

• CSE140: Digital Systems Design
Teaching assistant (rating: 4.75/5.00)

• CSE240A: Advanced Computer Architecture
Teaching assistant (rating: 4.51/5.00)

• CSE240A: Advanced Computer Architecture
Teaching assistant (rating: 4.51/5.00)

• Fall 2011

Talks

• CPR for SSDs ACM Workshop on Hot Topics in Operating Systems	May 2019
Towards Performant and Reliable Flash-Based Storages *Technische Universität Dresden	May 2019
Taming Performance Variability in SSDs Soongsil University	Apr. 2019
Design Tradeoffs for SSD Reliability $USENIX\ Conference\ on\ File\ and\ Storage\ Technologies$	Feb. 2019
$ {\color{red} \textbf{SSD Reliability Management for Unreliable Flash Memory} } \\ {\color{red} \textbf{Korean Conference on Semiconductors} } $	Feb. 2019
Performance Predictability for Flash-Based Storages Syracuse University, University of Wisconsin–Madison	Feb. 2019
Performance Implications for Flash Memory Error Handling $SK\ Hynix$	Dec. 2018
$ {\color{red} \bullet } \begin{array}{l} \text{AutoSSD: an Autonomic SSD Architecture} \\ \textbf{\textit{USENIX Annual Technical Conference}} \end{array} $	July 2018
$ \begin{array}{c} \textbf{Utilitarian Performance Isolation in Shared SSDs} \\ \textbf{\textit{USENIX HotStorage}} \end{array} $	July 2018
• The Balancing Act in SSDs $DGIST$	June 2018
Evaluating the Performance and Reliability of Flash Storages $SK\ Hynix$	June 2018
• An Autonomic SSD KIISE SIG on File and Storage Technology	May 2018
• NVM-based Storage Systems for HPC I/O Nodes **KIISE SIG on Heterogenous Computing and Storage*	Jan. 2018
	Nov. 2017
$ \begin{array}{c} \textbf{Efficient and Flexible Flash Memory Operation Scheduling} \\ \textbf{\textit{IEEE Non-Volatile Memory Systems and Applications} \end{array} $	Aug. 2017
QoS-aware Flash Memory Controller <i>IEEE Real-Time and Embedded Technology and Applications Symposium</i>	Apr. 2017

Institutional Services

- Undergraduate academic advising
- CIS doctoral program qualifying exam committee

Academic Services

IEEE Transactions on Computer-Aided Design (TCAD) Reviewer 2019, 2020

Design Automation Conference (DAC)

**Technical Program Committee*

2019, 2020

Student Mentoring

• Xiangqun Zhang (Ph.D., Syracuse University)	$Aug.\ 2020-present$
• Omkar Desai (Ph.D., Syracuse University)	$Aug.\ 2020-present$
• Ziyang Jiao (Ph.D., Syracuse University)	$Aug.\ 2020-present$
• Minwook Kim (Ph.D., Seoul National University)	June 2018 – present
• Hyeongyu Lee (Ph.D., Seoul National University)	Jan. 2018 – present
• Juwon Lee (M.S., Seoul National University)	Jan. 2018 - Mar. 2020
• Seunggeun Chi (B.S., Seoul National University)	Jan. 2018 – Dec. 2018
• Geonhee Lee (M.S., Seoul National University)	Jan. 2018 – July 2018
• Yonggun Lee (M.S., Seoul National University)	Jan. 2017 – Aug. 2017