Bryan S. Kim

Assistant Professor Syracuse University 4-181 CST, 111 College Pl, Syracuse, NY 13244  $bkim 01 @ syr.edu\\ {\tt sites.google.com/view/bryansjkim}\\ 1.315.443.1249$ 

Aug. 2009

HotOS'19

#### Research Interests

- Flash and non-volatile memory-based systems
- Data storage systems
- File systems and key-value store

## Professional Experience

• Assistant Professor

Department of Electrical Engineering & Computer Science

Aug. 2019 – present
Syracuse University

Postdoctoral Researcher Mar. 2018 – June 2019 Institute of Computer Technology Seoul National University

### Education

Seoul National University

Ph.D. in Computer Science & Engineering Feb. 2018

- Advisor: the late Prof. Sang Lyul Min

- Thesis: An Autonomic SSD Architecture

Seoul National University

M.S. in Electrical Engineering & Computer Science

- Advisor: the late Prof. Sang Lyul Min

- Thesis: Efficient Flash Memory Read Request Handling Based on Split Transactions

University of California, Berkeley

B.S. in Electrical Engineering & Computer Science May 2006

### Funding & Grants

Bryan S. Kim. CNS CORE: SMALL: CPR FOR FLASH-BASED STORAGE

NSF
Systems. Funded by National Science Foundation, 2020

\$488,277

### **Publications**

Jeseong Yeon, Leeju Kim, Youil Han, Hyeon Gyu Lee, Eunji Lee, and Bryan S. Kim. Jellyfish: A Fast Skip List with MVCC. To appear in ACM/IFIP International Middleware Conference, 2020

Middleware '20 acceptance rate: 25.2%

Youil Han, Bryan S. Kim, Jeseong Yeon, Sungjin Lee, and Eunji Lee.

TEKSDB: WEAVING DATA STRUCTURES FOR A HIGH-PERFORMANCE
KEY-VALUE STORE. In ACM International Conference on Measurement and
Modeling of Computer Systems, 2019

SIGMETRICS'19
acceptance rate:
17.1%

Bryan S. Kim, Eunji Lee, Sungjin Lee, and Sang Lyul Min. CPR FOR SSDs.

 $\bullet$  In ACM SIGOPS Workshop on Hot Topics in Operating Systems, 2019 acceptance rate: 24.0%

| • | Youil Han, Bryan S. Kim, Jeseong Yeon, Sungjin Lee, and Eunji Lee. TEKSDB: WEAVING DATA STRUCTURES FOR A HIGH-PERFORMANCE KEY-VALUE STORE. In <i>Proceedings of the ACM on Measurement and Analysis of Computing Systems</i> , 3(1): 8:1–8:23, 2019      | POMACS'19                             |
|---|--|---------------------------------------|
| • | Bryan S. Kim, Jongmoo Choi, and Sang Lyul Min. Design Tradeoffs for SSD Reliability. In <i>USENIX Conference on File and Storage Technologies</i> , 2019: 281–294  | FAST'19<br>acceptance rate:<br>17.9%  |
| • | Bryan S. Kim. The Human Manual. In $ACM$ Crossroads Student Magazine, $25(1)$ : $34-37$ , $2018$   | XRDS'18                               |
| • | Geonhee Lee, Hyeon Gyu Lee, Juwon Lee, Bryan S. Kim* and Sang Lyul Min. An Empirical Study on NVM-based Block I/O Caches. In ACM SIGOPS Asia-Pacific Workshop on Systems, 2018   | APSys'18 acceptance rate: 36.0%       |
| • | Bryan S. Kim, Hyun Suk Yang, and Sang Lyul Min. AutoSSD: AN AUTONOMIC SSD Architecture. In <i>USENIX Annual Technical Conference</i> , 2018: 677–689   | ATC'18 acceptance rate: 20.1%         |
| • | Bryan S. Kim. Utilitarian Performance Isolation in Shared SSDs. In USENIX Workshop on Hot Topics in Storage and File Systems, 2018   | HotStorage'18 acceptance rate: 36.7%  |
| • | Bryan S. Kim, Yonggun Lee, and Sang Lyul Min. Framework for Efficient and Flexible Scheduling of Flash Memory Operations. In <i>IEEE Non-Volatile Memory Systems and Applications</i> , 2017: 1–5  | NVMSA'17<br>acceptance rate:<br>33.3% |
| • | Bryan S. Kim and Sang Lyul Min. QoS-AWARE FLASH MEMORY CONTROLLER. In <i>IEEE Real-Time and Embedded Technology and Applications Symposium</i> , 2017: 51–62   | RTAS'17 acceptance rate: 23.7%        |
| • | Eyee Hyun Nam, Bryan S. Kim, Hyeonsang Eom, and Sang Lyul Min. Ozone (O3): An Out-of-Order Flash Memory Controller Architecture. In <i>IEEE Transactions on Computers</i> , 60(5): 653–666, 2011   | TC'11                                 |
| • | Bryan S. Kim, Eyee Hyun Nam, Yoon Jae Seong, Hang Jun Min, and Sang Lyul Min. Efficient Flash Memory Read Request Handling Based on Split Transactions. In <i>International Workshop on Software Support for Portable Storage</i> , 2009                 | IWSSPS'09                             |
| • | Joon Ho Um, Bryan S. Kim, Sung Gab Lee, Eyee Hyun Nam, and Sang Lyul Min. Flash Memory-Based Development Platform for Homecare Devices. In <i>IEEE International Conference on Systems, Man, and Cybernetics</i> , 2008: 2259–2263                       | SMC'08                                |
| • | Jin Hyuk Yoon, Eyee Hyun Nam, Yoon Jae Seong, Hongseok Kim, Bryan S. Kim, Sang Lyul Min, and Yookun Cho. Chameleon: A High Performance Flash/FRAM Hybrid Solid State Disk Architecture. In <i>IEEE Computer Architecture Letters</i> , 7(1): 17–20, 2008 | CAL'08                                |

### **Patents**

Bryan S. Kim and Sang Lyul Min. Control Device for Dynamically ALLOCATING STORAGE SPACE AND DATA STORAGE DEVICE INCLUDING Korea: filed THE CONTROL DEVICE. Korea Patent Application 10-2018-0116646: filed Sep. 2018; U.S. Patent Application 16/284924: filed Feb. 2019

Bryan S. Kim and Sang Lyul Min. Semiconductor Device for SCHEDULING TASKS FOR MEMORY DEVICE AND SYSTEM INCLUDING THE

SAME. Korea Patent Application 10-2017-0153547: filed Nov. 2017; U.S. Patent 10,635,351: filed Mar. 2018 and issued Apr. 2020; China Patent Application 2018-1-0298334.X: filed Apr. 2018

Bryan S. Kim and Eyee Hyun Nam. Memory Apparatus and Control

METHOD THEREOF. Korea Patent 10-1564574: filed Nov. 2013 and issued Oct. 2015

Hongseok Kim, Bryan S. Kim, and Eyee Hyun Nam. Memory Apparatus

AND CONTROL METHOD THEREOF. Korea Patent 10-1531965: filed Nov. 2013 and issued June 2015

Sang Lyul Min, Bryan S. Kim, Jinhyuk Kim, Donggi Lee, Taesung Jung, Byeongse So, Duckhyun Chang. MEMORY DEVICE AND PROGRAM METHOD

Thereof. Korea Patent 10-1544607: filed Oct. 2008 and issued Aug. 2015; U.S. Patent 8,493,782: filed Oct. 2009 and issued July 2013; China Patent 101727983: filed Oct. 2009 and issued June 2016

U.S.: filed

China: filed

U.S.: granted

Korea: filed

Korea: granted

Korea: granted

U.S.: granted Korea: granted China: granted

**Industry Experience** 

SK Telecom Seongnam, South Korea Manager at Storage Tech. Lab Apr. 2013 - Sep. 2015 Santa Clara, USA Oracle Corporation

Research intern at Solaris kernel team June 2011 - Sep. 2011

Samsung Advanced Institute of Technology Yongin, South Korea Research intern at Semiconductor lab July 2010 - Sep. 2010

n&k Technology Inc. San Jose, USA Application engineer July 2006 - July 2007

# Teaching

CIS700: Storage Systems for Big Data Syracuse University Instructor*Spring 2020* 

Syracuse University CIS341: Computer Organization & Programming Systems Instructor*Spring 2020* 

CIS486: Design of Operating Systems Syracuse University

Guest lecturer Oct. 2019

ECS 101: Introduction to Engineering and Computer Science Syracuse University Guest lecturer Oct. 2019

ECS 691: Fundamentals of Research Syracuse University Guest lecturer Oct. 2019

| • CSE791: Storage for Big Data & Cloud Computing Instructor   | Syracuse University Fall 2019                      |
|---|--|
| • 035.001: Introduction to Computer Science<br>Instructor (rating: 4.68/5.00)   | Seoul National University<br>Spring 2019           |
| • 035.001: Introduction to Computer Science Instructor (rating: $4.62/5.00$ )   | Seoul National University<br>Spring 2018           |
| • CSE140: Digital Systems Design • Teaching assistant (rating: 4.75/5.00)   | University of California, San Diego $Winter\ 2012$ |
| • CSE240A: Advanced Computer Architecture Teaching assistant (rating: 4.51/5.00)  | University of California, San Diego $Fall\ 2011$   |
| Talks   |  |
| • CPR for SSDs  **ACM Workshop on Hot Topics in Operating Systems*  | May 2019   |
| Towards Performant and Reliable Flash-Based Storages  *Technische Universität Dresden   | May 2019   |
| Taming Performance Variability in SSDs Soongsil University  | Apr. 2019  |
| $ \begin{array}{c} \textbf{Design Tradeoffs for SSD Reliability} \\ \textbf{\textit{USENIX Conference on File and Storage Technologies} \end{array} $             | Feb. 2019  |
| $ {\color{red} \bullet } {\color{blue} {\rm SSD~Reliability~Management~for~Unreliable~Flash~Memor} \\ {\color{blue} {\it Korean~Conference~on~Semiconductors} } $ | Feb. 2019  |
| • Performance Predictability for Flash-Based Storages Syracuse University, University of Wisconsin–Madison  | Feb. 2019  |
| Performance Implications for Flash Memory Error Handle $SK\ Hynix$  | ing  Dec. 2018                                     |
| • AutoSSD: an Autonomic SSD Architecture<br>USENIX Annual Technical Conference  | July 2018  |
| Utilitarian Performance Isolation in Shared SSDs $USENIX\ HotStorage$   | July 2018  |
| • The Balancing Act in SSDs $DGIST$   | June 2018  |
| Evaluating the Performance and Reliability of Flash Store $SK\ Hynix$   | rages  June 2018                                   |
| • An Autonomic SSD • KIISE SIG on File and Storage Technology   | May 2018   |
| NVM-based Storage Systems for HPC I/O Nodes KIISE SIG on Heterogenous Computing and Storage   | Jan. 2018  |
| DRAM-less Flash Memory Storage Device   | Nov. 2017  |

Nov. 2017

SK Hynix

| Efficient and Flexible Flash Memory Operation Scheduling <i>IEEE Non-Volatile Memory Systems and Applications</i> | Aug.~~2017            |
|---|-----------------------|
| QoS-aware Flash Memory Controller <i>IEEE Real-Time and Embedded Technology and Applications Symposium</i>        | n Apr. 2017           |
| Institutional Services  |                       |
| • Undergraduate academic advising   |                       |
| • CISE doctoral program qualifying exam committee   | 2020                  |
| • Faculty reviewer for SOURCE (undergrad research proposal)   | 2020                  |
| Academic Services   |                       |
| • IEEE Transactions on Computer-Aided Design (TCAD)   |                       |
| - Reviewer  | 2019, 2020            |
| • Design Automation Conference (DAC)  |                       |
| <ul><li>Technical Program Committee</li><li>Session co-chair</li></ul>  | $2019,\ 2020 \ 2020$  |
| • European Conference on Computer Systems (EuroSys)   |                       |
| - Shadow Program Committee  | 2019                  |
| • International Symposium on Computer Architecture (ISCA)   |                       |
| - Student Volunteer   | 2016                  |
| Student Mentoring   |                       |
| • Xiangqun Zhang (Ph.D., Syracuse University)   | Aug. 2020 – present   |
| • Omkar Desai (Ph.D., Syracuse University)  | $Aug.\ 2020-present$  |
| • Ziyang Jiao (Ph.D., Syracuse University)  | $Aug.\ 2020-present$  |
| • Minwook Kim (Ph.D., Seoul National University)  | June 2018 - Mar. 2020 |
| • Hyeongyu Lee (Ph.D., Seoul National University)   | Jan. 2018 - Mar. 2020 |
| • Juwon Lee (M.S., Seoul National University)   | Jan. 2018 - Mar. 2020 |
| • Seunggeun Chi (B.S., Seoul National University)   | Jan. 2018 – Dec. 2018 |
| • Geonhee Lee (M.S., Seoul National University)   | Jan. 2018 - July 2018 |
| • Yonggun Lee (M.S., Seoul National University)   | Jan. 2017 – Aug. 2017 |