1 Orange/W A+ SIN+ RX+ STEP+	CTAADL 4.3	45 12 CTX CRX - 24V	CTAIDL 4.3	45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+
2 Orange A- SIN- RX- STEP-	STMBL 4.3	36 RST DIO - CK 5V	STMBL 4.3	36 RST DIO - CK 5V	2 Orange A- SIN- RX- STEP-
3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR-	1f3debee	-PTC+ I -PTC+	1f3debee	-PTC+ -PTC+	3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR-
5 Blue/W Z+ REF+ TX+ ERR+	github.com/rene-dev/stmbl	-PTC+ -PTC+ + BRK + BRK servoterm	github.com/rene-dev/stmbl	+ BRK + BRK servoterm	5 Blue/W Z+ REF+ TX+ ERR+
6 Green B- COS- CK- DIR-	gicilab.com/rene-dev/scilibi	W V U L	gichub.com/rene-dev/scilibi	W V U L	6 Green B- COS- CK- DIR-
7 Brown/W VCC PTC+ VCC EN+	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 _24V	OUT2 OUT1 OUT0 24V	7 Brown/W VCC PTC+ VCC EN+
8 Brown GND PTC- GND EN-	+ AIN0 - + AIN1 - CMD	2 + AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	8 Brown GND PTC- GND EN-
2201/ 1// 1// 1/	-74110 -74111 A	B 7 A B	A	В В	2201/ 11/ 1/ 1/
320V W V U	₊ 320V ₋ CMD	FB0 FB1	₊ 320V ₋ CMD	FB0 FB1	_320V_ W V U
1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP-	STMBL 4.3	45 12 CTX CRX - 24V		45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP-
3 Green/W B+ COS+ CK+ DIR+	JIMDL 4.5	36 RST DIO - CK 5V	3 MDL 7.5	36 RST DIO - CK 5V	2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+
4 Blue Z- REF- TX- ERR-	1f3debee	-PTC+ -PTC+	1f3debee	-PTC+ I -PTC+	4 Blue Z- REF- TX- ERR-
5 Blue/W Z+ REF+ TX+ ERR+	github.com/rene-dev/stmbl	+ BRK + BRK servoterm	github.com/rene-dev/stmbl		5 Blue/W Z+ REF+ TX+ ERR+
6 Green B- COS- CK- DIR-		W V U		W V U	6 Green B- COS- CK- DIR-
7 Brown/W VCC PTC+ VCC EN+	OUT2 OUT1 OUT0 +24V	/_ OUT2 OUT1 OUT0 _ 24V_	OUT2 OUT1 OUT0 _24V	OUT2 OUT1 OUT0 24V	7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
8 Brown GND PTC- GND EN-	+ AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	8 Brown GND PTC- GND EN-
320V W V U	320V CMD	EDO ED1	320V CMD		320V W V U
1 Orange/W A+ SIN+ RX+ STEP+	+350A* CWD	FB0 FB1	+ -1 CITIE	FB0 FB1	1 Orange/W A+ SIN+ RX+ STEP+
2 Orange A- SIN- RX- STEP-	STMBL 4.3	45 12 CTX CRX - 24V 36 RST DIO - CK 5V		45 12 CTX CRX - 24V 36 RST DIO - CK 5V	2 Orange A- SIN- RX- STEP-
3 Green/W B+ COS+ CK+ DIR+		⊨ ĽQ	ا مقت ا	36 RST DIO - CK 5V	3 Green/W B+ COS+ CK+ DIR+
4 Blue Z- REF- TX- ERR-	1f3debee	-PTC+ -PTC+	1f3debee	-PTC+ -PTC+	4 Blue Z- REF- TX- ERR-
5 Blue/W Z+ REF+ TX+ ERR+	github.com/rene-dev/stmbl	W V U servoterm	github.com/rene-dev/stmbl	W V U servoterm	5 Blue/W Z+ REF+ TX+ ERR+
6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+	OUT2 OUT1 OUT0 24\		OUT2 OUT1 OUT0 24V		6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+
8 Brown GND PTC- GND EN-	+ - + - + - + - + - + - + - + - + - + -	/- + - + - + - + ²⁴	+ - + - + - + - + - + - + - + - + - + -	- + - + - + - + ²	8 Brown GND PTC- GND EN-
	+ AIN0 - + AIN1 - ACME	B + AIN0 - + AIN1 - CMD B	+ AIN0 - + AIN1 - ACMD	+ AIN0 - + AIN1 - ACMD	
320V W V U	_320V_ CMD	FB0 FB1	_320V_ CMD	FB0 FB1	320V W V U
1 Orange/W A+ SIN+ RX+ STEP+		45 12 CTX CRX - 24V	1 1	45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+
2 Orange A- SIN- RX- STEP-	STMBL 4.3	36 RST DIO - CK 5V	CTABLES I		2 Orange A- SIN- RX- STEP-
3 Green/W B+ COS+ CK+ DIR+	1f3debee	PTC+ L-PTC+	1f3debee	36 RST DIO - CK 5V	3 Green/W B+ COS+ CK+ DIR+
4 Blue Z- REF- TX- ERR-		-PTC+ -PTC+ + BRK + BRK servoterm	#4	-PTC+ -PTC+ + BRK + BRK servoterm	4 Blue Z- REF- TX- ERR-
5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR-	github.com/rene-dev/stmbl	W V U	github.com/rene-dev/stmbl	W V U	5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR-
7 Brown/W VCC PTC+ VCC EN+	OUT2 OUT1 OUT0 .24V	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 24V	7 Brown/W VCC PTC+ VCC EN+
8 Brown GND PTC- GND EN-	+ - + - + - + - + - +		+ - + - + - + *	- + - + - + - + ^{2¬}	8 Brown GND PTC- GND EN-
	+ AIN0 - + AIN1 - ACML) _B + AIN0 - + AIN1 - ACMD	+ AIN0 - + AIN1 - ACMD	+ AIN0 - + AIN1 - ACMUB	
₊ 320V ₋ W V U	₊ 320V ₋ CMD	FB0 FB1	₊ 320V ₋ CMD	FB0 FB1	₊ 320V ₋ W V U
1 Orange/W A+ SIN+ RX+ STEP+	c=1451 45	45 12 CTX CRX - 24V	C=1451 45	45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+
2 Orange A- SIN- RX- STEP-	STMBL 4.3	36 RST DIO - CK 5V	STMBL 4.3	36 RST DIO - CK 5V	2 Orange A- SIN- RX- STEP-
3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR-	1f3debee	-PTC+ I -PTC+	1f3debee	-PTC+ I -PTC+	3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR-
4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+	l	-PTC+ -PTC+ + BRK + BRK servoterm	#4	+ BRK + BRK servoterm	4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+
6 Green B- COS- CK- DIR-	github.com/rene-dev/stmbl	W V U L	github.com/rene-dev/stmbl	w v u L	6 Green B- COS- CK- DIR-
7 Brown/W VCC PTC+ VCC EN+	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 24V	OUT2 OUT1 OUT0 24V	7 Brown/W VCC PTC+ VCC EN+
8 Brown GND PTC- GND EN-	+ AIN0 - + AIN1 - CME	B + AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	+ AIN0 - + AIN1 - CMD	8 Brown GND PTC- GND EN-
2201/	PARTO PARTO	BI 7 THE B	2201/ L	B T AINO - TAINT - A B	2201/
[.320V W V U	[_320V_ CMD	FB0 FB1 I			
	+3231 CIVID	FDU FDI	₊ 320V ₋ CMD	FB0 FB1	_320V_ W V U
1 Orange/W A+ SIN+ RX+ STEP+	1 -1	45 12 CTX CRX - 24V	1 1	45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+
1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP-	STMBL 4.3	45 12 CTX CRX - 24V	CTARL 43	45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP-
1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+	1 -1	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ I -PTC+	1 1	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ L-PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+
1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+	STMBL 4.3	45 12 CTX CRX - 24V 36 RST DIO - CK 5V 	STMBL 4.3	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK servoterm	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP-
1+	STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ +BRK + BRK W V U	STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR-
1+	STMBL 4.3	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U	STMBL 4.3	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 +24V	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 +24V	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U Servoterm OUT2 OUT1 OUT0 + 24V	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _24V + AIN0 - + AIN1 - ACME	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U / OUT2 - OUT1 OUT0 24V OB BRITH 24V OB BRITH	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _24V + AIN0 - + AIN1 - ACMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1 Orange/W	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _24V +AIN0 - +AIN1ACME _320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U /- OUT2 OUT1 OUT0 + H - + - + - + - + - + - + - + CMD B + AINO - + AIN1 - ACMD FBO FB1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACMD 4320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1 Orange/W	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V - + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 +	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ +BRK +BRK W V U OUT2 OUT1 OUT0 24V 3 + AIN0 - +AIN1 - ACMD B FB0 FB1 45 12 CTX CRX - 24V	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN+ RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1 Orange/W	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _24V + AIN0 - + AIN1ACME _320V	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ +BRK +BRK W V U / OUT2	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V - + 1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ +BRK +BRK W V U OUT2 OUT1 OUT0 24V - + - +	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 4 Blue Z- REF- TX- ERR-
1 Orange/W	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _24V + AIN0 - + AIN1ACME _320V	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ +BRK +BRK W V U / OUT2 OUT1 OUT0 24V - + - + - + - + - + - + - + CMD B + AINO - + AIN1 - ACMD FB0 FB1 45 12 CTX CRX - 24V 36 RST DIO - CK 5V	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 5 Blue/W Z+ REF+ TX+ ERR+
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V - + - + - + - + - + - + CMD B + AINO - + AIN1 - CMD FB0 FB1 45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ W V U servoterm	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A+ SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK+ DIR-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V -	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U /- OUT2 OUT1 OUT0 24V - + - + - + - + - + - + - + - + - + - +	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A+ SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK+ DIR-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V - + - + - + - + - + - + - + + - + - + - +	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK SV PTC+ -PTC+ + BRK + BRK W V U OUT2 OUT1 OUT0 24V - + - + - + - + - + - + - + - + - + - +	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 24V + AIN0 - + AIN1 - ACME 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V -	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACMD ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACMD 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR- 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 3 20V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR- 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + 24V + AIN0 - + AIN1 - ACMD - 24V + AIN0 - + AIN1 - ACMD - 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -BRK + BRK	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 3 QOV W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK- DIR- 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3	45 12 CTX CRX - 24V 36 RST DIO - CK 5V -PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V -	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACMD - AC	45 12 CTX CRX - 24V 36 RST DIO - CK SV PTC+ -PTC+ -PTC+ -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A+ SIN+ RX+ STEP+ 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN+ RX+ STEP+ 3 GREEN/W STEP- GND EN-
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1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 4 AIN0 - + AIN1 - ACME 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V -	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK SV PTC+ +BRK +BRK W V U OUT2 OUT1 OUT0 24V 36 RST DIO - CK SV PTC+ +BRK +BRK W V U OUT2 OUT1 OUT0 24V 36 RST DIO - CK SV 37 RST DIO - CK SV 38 RST DIO - CK SV 38 RST DIO - CK SV 39 RST DIO - CK SV 30 RST DIO - CK SV 30 RST DIO - CK SV 31 AINO - AIN1 - CMD 32 RST DIO - CK SV 36 RST DIO - CK SV 36 RST DIO - CK SV	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 5 Blue/W Z+ REF+ TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 3 Green/W B+ COS+ CK+ DIR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W CC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _ 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _ 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _ 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 _ 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -BRK +BRK W V U	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK- DIR- 5 Blue/W Z+ REF- TX- ERR- 6 Green B- COS- CK- DIR- 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CF- CRN- 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 5 Blue/W Z+ REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W CC- PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX- STEP- 5 Blue/W Z+ REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 24V + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U / OUT2 OUT1 OUT0 24V -	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -BRK +BRK W V U	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W A+ SIN+ RX+ STEP- 3 Green/W B+ COS+ CK+ DIR+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 5 Blue/W Z+ REF- TX- ERR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
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1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 4 AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 4 AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 4 AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 4 AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 4 AIN1 - ACME 320V CMD STMBL 4.3 1f3debee	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee GITMBL 4.3 1f3debee GITMBL 4.3 1f3debee GITMBL 4.3 1f3debee	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -PTC+ -PTC	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W CVC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX- STEP+ 2 Orange A- SIN- RX- STEP+ 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z- REF- TX- ERR-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -PTC+ -PTC	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1 Orange/W	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+ + BRK + BRK W V U 0 UT2 OUT1 OUT0 24V 6 RST DIO - CK 5V 7 OUT2 OUT1 OUT0 24V 6 RST DIO - CK 5V 7 OUT2 OUT1 OUT0 6 H AINO - H AIN1 - ACMD 7 OUT2 OUT1 OUT0 8 H AINO - H AIN1 - ACMD 8 H AINO - H AIN1 - ACMD 9 H AINO - H AIN1 - ACMD 10 UT2 OUT1 OUT0 12 UT3 OUT3 OUT0 14 UT3 OUT3 OUT0 15 UT3 CRX - 24V 16 UT3 CRX - 24V 17 OUT2 OUT1 OUT0 18 UT3 CRX - 24V 18 UT3 CRX	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACMD 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACMD - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACMD - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACMD - 320V CMD	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -PTC+ -PTC+	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 6 Green B- COS- CK- DIR- 7 Brown/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN-
1	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 - 24V + AIN0 - + AIN1 - ACME - 320V CMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK 5V PTC+ -PTC+	STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 + AIN0 - + AIN1 - ACMD STMBL 4.3 1f3debee github.com/rene-dev/stmbl OUT2 OUT1 OUT0 STMBL 4.3 1f3debee github.com/rene-dev/stmbl	45 12 CTX CRX - 24V 36 RST DIO - CK SV -PTC+ -PTC+ -PTC+ -PTC	1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK+ DIR+ 4 Blue Z- REF- TX- ERR- 5 Blue/W Z+ REF+ TX+ ERR+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 5 Blue/W Z- REF- TX- ERR- 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 6 Green B- COS- CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 2 Orange A- SIN- RX- STEP- 3 Green/W B+ COS+ CK- DIR- 7 Brown/W VCC PTC+ VCC EN+ 8 Brown GND PTC- GND EN- 320V W V U 1 Orange/W A+ SIN+ RX+ STEP+ 5 Blue/W Z+ REF- TX+ ERR- 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR- 5 Blue/W Z+ REF- TX- ERR-