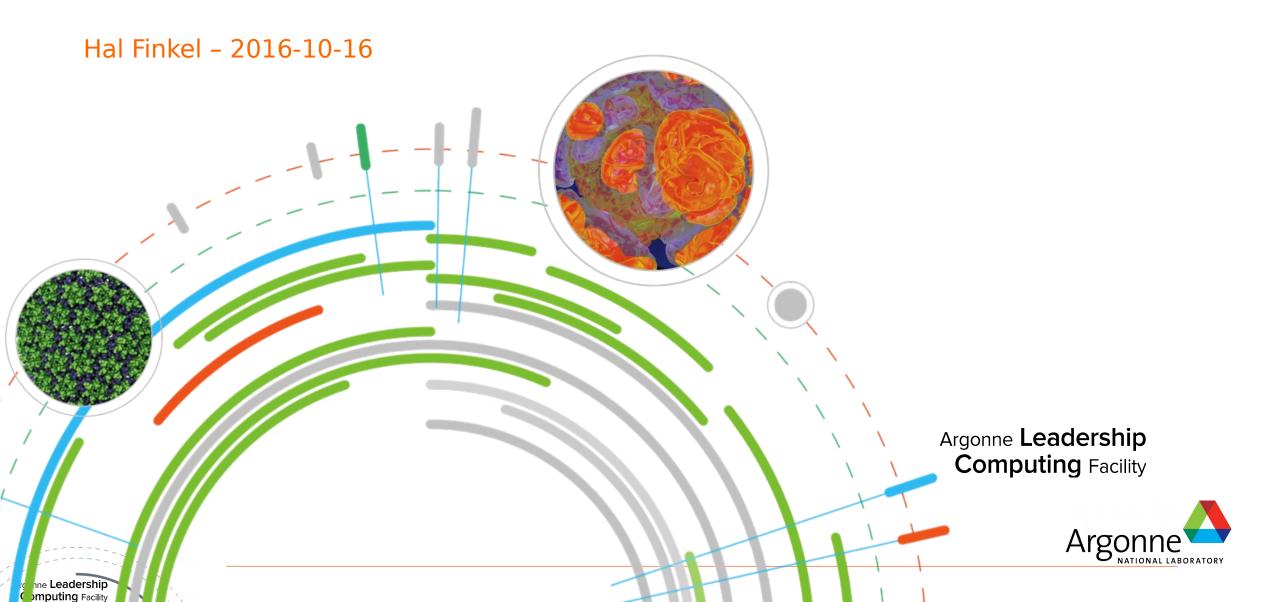
Clang/LLVM: A Modern C++ Compiler with an HPC Twist



The LLVM Compiler Infrastructure

Site Map:

Overview Features Documentation Command Guid FAQ Publications LLVM Projects Open Projects LLV M U sers Bug Database

LLVM Overview

The LLV M Project is a collection of modular and reusable compiler and toolchain technologies. Despite its name. LLV M has little to do with traditional virtual machines, though it does provide helpful libraries that can be used to build them. The name "LLV M" itself is not an acronym; it is the full name of the project.

VI began as a research project at the asity of Illinois, with the goal of ling a modern, SSA-based compilation zy capable of supporting both static vnamic compilation of arbitrary amming languages. Since then, LLV M own to be an umbrell a project sting of a number of subprojects, many ich are being used in production by a variety of commercial and open source its as well as being widely used in micresearch. Code in the LLVM t is licensed under the "UIUC" BSD-

rimary sub-projects of LLV M are:

The LLVM Core libraries provide a modern source- and targetindependent optimizer, along with code generation support for many popular CPUs (as well as some less common ones) These libraries are built around a well specified code representation known as the LLV M intermediate representation ("LLV M IR"). The LLV M C ore libraries are well documented, and it is particularly easy to invent your own language (or port an existing compiler) to use LLVM as an optimizer and code

Clang is an "LLV M native" C/C++/Objective-C compiler, which

Latest LLVM Release!

Jan 6, 2014: LLV M 3.4 is now available for download! LLVM is publicly available under an open source License. Also, you might want to check out the new features in SVN that will appear in the next LLV M release. If you want them early, download LLVM through anonymous

ACM Software System Award!

LLV M has been aw arded the 2012 ACM Software System Award! This award is given by ACM to one software system worldwide every year. LLV Mis in highly distinguished company Click on any of the individual recipients' names on that page for the detailed citation describing the award.

Upcoming Releases

Onward to 3.5!

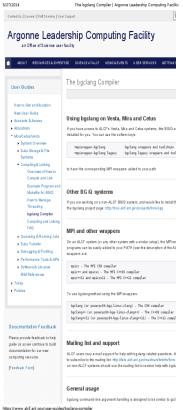
Developer Meetings

Proceedings from past meetings

- April 7-8, 2014
- Nov 6-7, 2013
- April 29-30, 2013
- November 7-8, 201
- April 12, 2012
- N ovember 18, 2011
- September 2011

What is LLVM and Clang?

- MPI-specific warnings and static analysis!
- The thread sanitizer (and other sanitizers)
- Optimization reports



bgclang (LLVM/clang on the BG/Q)

For usage information, and information specific to using bgclang on ALCF's BG/Q machines (Vesta, Mira and Cetus), please visit: http://www.alcf.anl.gov/user-guides/bgclang-compiler

If your system administrators have not been kind enough to install horland on your system, you can either direct them to this page, or install the distribution yourself. RPMs are provided (see below), and these are *relocatable* RPMs, meaning that they can be installed by a non-root user

Please note that, if you wish to use dynamic linking (which you must do when certain features, like address sanitizer, are enabled), you must install boclang in a directory that is mounted from the compute nodes (read-only is sufficient).

If you're using bgclang, please subscribe to the mailing list: http://lists.alcf.anl.gov/mailman/listinfo/llvm-bqq-discuss.

bgclang downloads (for installing on your own)

For those managing their own installs, note that the MPI wrappers are installed in the PREFIX/mpi/{bgclang,bgclang.legacy}/bin directories. The non-MPI compiler wrappers are located in the PREFD(/w bin directory

RPMs, etc.

http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-binutils-r209570-20140527-1-

http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-r209570-20140527-1-

http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-sleef-r209570-20140527-1-1.ppc64.rpm http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-libcxx-r209570-20140527-1

1.ppc64.rpm http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-libomp-r209570-20140527-1-

http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-compiler-rt-r209570-20140527-

http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-stage1-3.4-1.ppc64.rpm

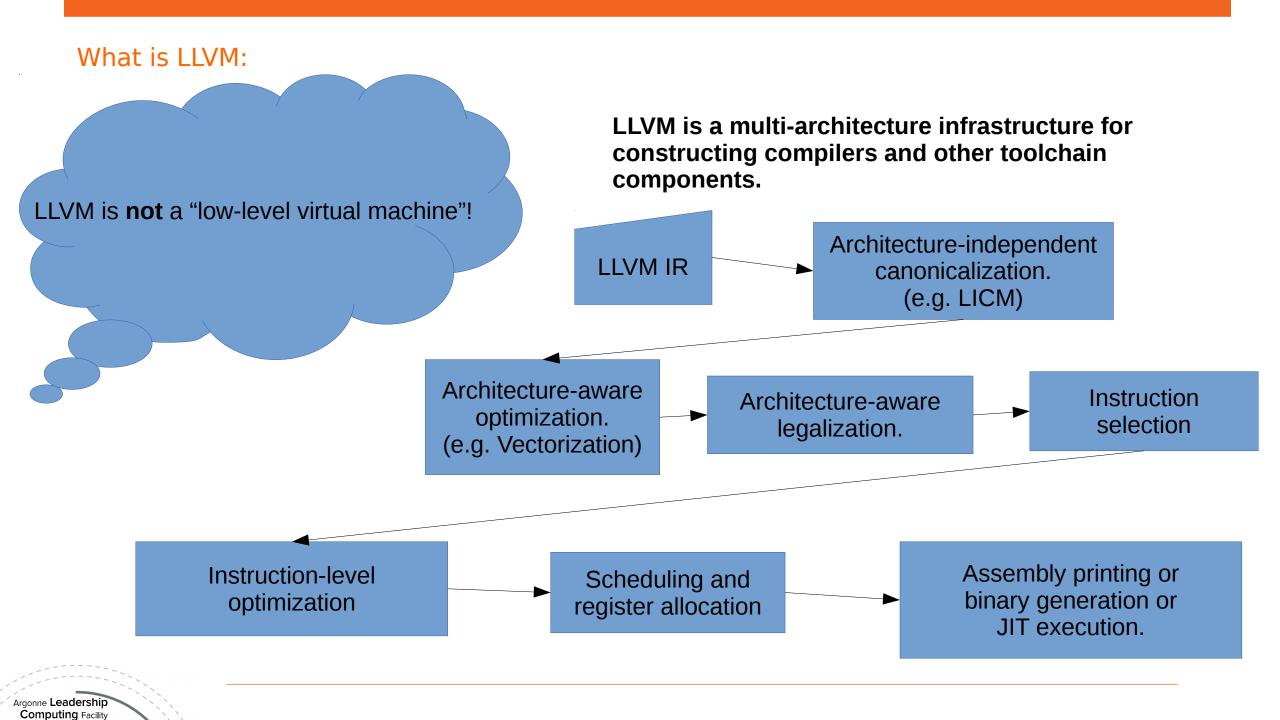
http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/bgclang-stage1-libcxx-3.4-2.ppc64.rpm

http://www.mcs.anl.gov/~hfinkel/bgclang/RPMS/ppc64/vpkg-bin-sh-1-1.ppc64.rpm

A non-root (regular) user can install these RPMs (because they are relocatable), but in addition to specifying the installation prefix (with the --prefix argument), an alternate RPM database

directory needs to be specified (in a directory to which you actually have write permission). For example, to install bgclang into the /tmp/bgclang directory using /tmp/bgclang/rpm as the RPM https://trac.alcf.anl.gov/projects/llvm-bgg/wiki/WikiStart

Argonne Leadership Computing Facility



What is Clang:



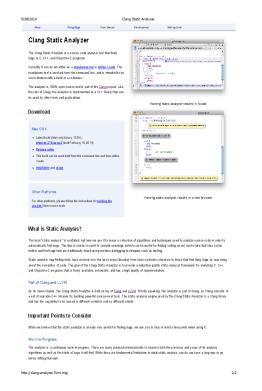
C++ Source (C++14, C11, etc.) Parsing and semantic analysis

Code generation

LLVM IR

Static analysis





MPI-specific warning messages

These are not really MPI specific, but uses the "type safety" attributes inspired by this use case:

See Clang's test/Sema/warn-type-safety-mpi-hdf5.c, test/Sema/warn-type-safety.c and test/Sema/warn-type-safety.cpp for more examples, and: http://clang.llvm.org/docs/AttributeReference.html#type-safety-checking

MPI-specific static analysis

If you don't know how to run the static analyzer, see: http://clang-analyzer.llvm.org/scan-build.html Also, this is useful: http://btorpey.github.io/blog/2015/04/27/static-analysis-with-clang/

Proper tools aside, if you have a recent Clang behind your mpicc, for example, you can run something like this:

mpicc --analyze -Xanalyzer -analyzer-output=html -Xanalyzer -analyzer-checker=optin.mpi.MPI-Checker \
-o /tmp/somedir mpit4.c

and you might see some warnings like this:

and because we asked for HTML output, we'll also get this (in /tmp/somedir)...



MPI-specific static analysis

Currently handles:

- Missing waits
- Unmatched waits
- Double uses of requests

(plus the static analyzer has lots of non-MPI-specific check too)

http://dl.acm.org/citation.cfm?id=2833159

Annotated Source Code

```
#include "mpi.h"
     #include <stdio.h>
     int main() {
     void missingWait2() {
       int rank = 0;
       double buf = 0;
       MPI Comm rank(MPI COMM WORLD, &rank);
       if (rank == 0) {
12

    Assuming 'rank' is not equal to 0 →

            ← Taking false branch →
       } else {
13
         MPI Request sendReql, recvReql;
14
15
         MPI Isend(&buf, 1, MPI DOUBLE, rank + 1, 0, MPI COMM WORLD, &sendReq1);
16
             ← Request is previously used by nonblocking call here. →
         MPI_Irecv(&buf, 1, MPI_DOUBLE, rank - 1, 0, MPI_COMM_WORLD, &recvReq1);
17
             - Request 'sendReq1' has no matching wait.
18
         MPI Wait(&recvReql, MPI STATUS IGNORE);
19
20
```

Thread-safety warnings

```
class attribute ((lockable)) Mutex {
public:
 void Lock() __attribute__((exclusive_lock_function));
 void ReaderLock() __attribute__((shared_lock_function));
 void Unlock() __attribute__ ((unlock_function));
 bool TryLock() __attribute__((exclusive_trylock_function(true)));
 bool ReaderTryLock() __attribute__((shared_trylock_function(true)));
 void LockWhen(const int &cond) attribute ((exclusive lock function));
};
Mutex sls mu;
Mutex sls mu2 attribute ((acquired after(sls mu)));
int sls_guard_var_attribute_((guarded_var)) = 0;
int sls_guardby_var__attribute_ ((guarded_by(sls_mu))) = 0;
```

Compile with -Wthread-safety

For more information, see: http://clang.llvm.org/docs/ThreadSafetyAnalysis.html and Clang's test/SemaCXX/warn-thread-safety-analysis.cpp

Thread-safety warnings

Argonne Leadership

```
A simple example:
void sls_fun_bad_2() {
 sls mu.Lock();
                        /tmp/ts.cpp:19:10: warning: acquiring mutex 'sls_mu' that is already held [-Wthread-safety-analysis]
 sls mu.Lock();
                         sls mu.Lock();
 sls mu.Unlock();
Also integrated with libc++, so you'll automatically get some of the benefit from using std::mutex,
std::lock guard, etc. (you need to pass define LIBCPP ENABLE THREAD SAFETY ANNOTATIONS
in order to enable this feature)
#define LIBCPP ENABLE THREAD SAFETY ANNOTATIONS
#include <thread>
                         /tmp/ts2.cpp:8:31: warning: acquiring mutex 'sls_mu' that is already held [-Wthread-safety-analysis]
                           std::lock guard<std::mutex> x(sls mu);
std::mutex sls mu;
                         /tmp/ts2.cpp:9:10: warning: acquiring mutex 'sls_mu' that is already held [-Wthread-safety-analysis]
                          sls mu.lock();
void sls fun bad 2() {
 sls mu.lock();
 std::lock guard<std::mutex> x(sls mu);
 sls mu.lock();
 sls mu.unlock();
```

Sanitizers

The sanitizers (some now also supported by GCC) – Instrumentation-based debugging

- Checks get compiled in (and optimized along with the rest of the code) Execution speed an order of magnitude or more faster than Valgrind
- You need to choose which checks to run at compile time:
 - Address sanitizer: -fsanitize=address Checks for out-of-bounds memory access, use after free, etc.: http://clang.llvm.org/docs/AddressSanitizer.html
 - Leak sanitizer: Checks for memory leaks; really part of the address sanitizer, but can be enabled in a mode just to detect leaks with -fsanitize=leak: http://clang.llvm.org/docs/LeakSanitizer.html
 - Memory sanitizer: -fsanitize=memory Checks for use of uninitialized memory: http://clang.llvm.org/docs/MemorySanitizer.html
 - Thread sanitizer: -fsanitize=thread Checks for race conditions: http://clang.llvm.org/docs/ThreadSanitizer.html
 - Undefined-behavior sanitizer: -fsanitize=undefined Checks for the execution of undefined behavior: http://clang.llvm.org/docs/UndefinedBehaviorSanitizer.html
 - Efficiency sanitizer [Recent development]: -fsanitize=efficiency-cache-frag, -fsanitize=efficiency-working-set (-fsanitize=efficiency-all to get both)

And there's more, check out http://clang.llvm.org/docs/ and Clang's include/clang/Basic/Sanitizers.def for more information.



Thread Sanitizer

```
#include <thread>
int g_i = 0;
std::mutex g_i_mutex; // protects g_i
void safe_increment()
  // std::lock_guard<std::mutex> lock(g_i_mutex);
  ++g_i;
int main()
  std::thread t1(safe_increment);
  std::thread t2(safe_increment);
  t1.join();
  t2.join();
```

Everything is fine if I uncomment this line...

Thread Sanitizer

\$ clang++ -std=c++11 -stdlib=libc++ -fsanitize=thread -O1 -o /tmp/r1 /tmp/r1.cpp \$ /tmp/r1

```
WARNING: ThreadSanitizer: data race (pid=486)
 Write of size 4 at 0x000001521cb8 by thread T2:
   #0 safe increment() <null> (r1+0x00000049d2ac)
   #1 void* std:: 1:: thread proxy<std:: 1::tuple<std:: 1::unique ptr<std:: 1:: thread struct, std:: 1::default delete<std:: 1:: thread struct
ct> >, void (*)()> >(void*) <null> (r1+0x00000049d455)
  Previous write of size 4 at 0x000001521cb8 by thread T1:
   #0 safe increment() <null> (r1+0x00000049d2ac)
   #1 void* std:: 1:: thread proxy<std:: 1::tuple<std:: 1::unique ptr<std:: 1:: thread struct, std:: 1::default delete<std:: 1:: thread struct
ct> >, void (*)()> >(void*) <null> (r1+0x00000049d455)
 Location is global '<null>' at 0x00000000000 (r1+0x0000001521cb8)
  Thread T2 (tid=489, running) created by main thread at:
    #0 pthread create /home/hfinkel/public/src/llvm/projects/compiler-rt/lib/tsan/rtl/tsan interceptors.cc:902 (r1+0x000000420aa5)
    #1 std:: \overline{1}::thread::thread<void (&)(), , void>(void (&)()) <null> (r1+0x00000049d3b6)
   #2 main <null> (r1+0x00000049d2ea)
 Thread T1 (tid=488, finished) created by main thread at:
    #0 pthread create /home/hfinkel/public/src/llvm/projects/compiler-rt/lib/tsan/rtl/tsan interceptors.cc:902 (r1+0x000000420aa5)
   #1 std:: 1::thread::thread<void (&)(), , void>(void (&)()) <null> (r1+0x00000049d3b6)
   #2 main <null> (r1+0x00000049d2dd)
SUMMARY: ThreadSanitizer: data race (/tmp/rl+0x49d2ac) in safe increment()
ThreadSanitizer: reported 1 warnings
```



Optimization Reporting - Design Goals

To get information from the backend (LLVM) to the frontend (Clang, etc.)

- To enable the backend to generate diagnostics and informational messages for display to users.
- To enable these messages to carry additional "metadata" for use by knowledgeable frontends/tools
- To enable the programmatic use of these messages by tools (auto-tuners, etc.)
- To enable plugins to generate their own unique messages

```
sqlite3.c:60198:7: remark: sqlite3StrICmp inlined into sqlite3Pragma [-Rpass=inline]
   if( sqlite3StrICmp(zLeft, "case_sensitive_like")==0 ){
sqlite3.c:60200:40: remark: getBoolean inlined into sqlite3Pragma [-Rpass=inline]
        sqlite3RegisterLikeFunctions(db, getBoolean(zRight));
sqlite3.c:60213:7: remark: sqlite3StrICmp inlined into sqlite3Pragma [-Rpass=inline]
   if( sqlite3StrICmp(zLeft, "integrity_check")==0
sqlite3.c:60214:7: remark: sqlite3StrICmp inlined into sqlite3Pragma [-Rpass=inline]
   || sqlite3StrICmp(zLeft, "quick_check")==0
sqlite3.c:44776:8: remark: sqlite3VdbeMemFinalize inlined into sqlite3VdbeExec [-Rpass=inline]
   rc = sqlite3VdbeMemFinalize(pMem, p0p->p4.pFunc);
```

See also: http://llvm.org/docs/Vectorizers.html#diagnostics

Optimization Reporting - Design Goals

There are two ways to get the diagnotics out:

- By using some fontend that installs the relevant callbacks and displays the associated messages
- By serializing the messages to a file (YAML is currently used as the format), and then processing them with an external tool

\$ clang -O3 -c -o /tmp/or.o /tmp/or.c -fsave-optimization-record

\$ Ilvm-opt-report /tmp/or.opt.yaml

Vectorized by a factor of 4, interleaved by a factor of 2.

Unrolled by a factor of 16

```
< /tmp/or.c
              void bar();
              void foo() { bar(); }
              void Test(int *res, int *c, int *d, int *p, int n) {
                int i;
              #pragma clang loop vectorize(assume safety)
      V4,2
                for (i = 0; i < 1600; i++) {
                  res[i] = (p[i] == 0) ? res[i] : res[i] + d[i];
10
   U16
                for (i = 0; i < 16; i++) {
13
                  res[i] = (p[i] == 0) ? res[i] : res[i] + d[i];
14
15
16 I
                foo();
17
18
                foo(); bar(); foo();
19
20
```

Optimization Reporting - Design Goals

But how code is optimized often depends on where it is inlined...

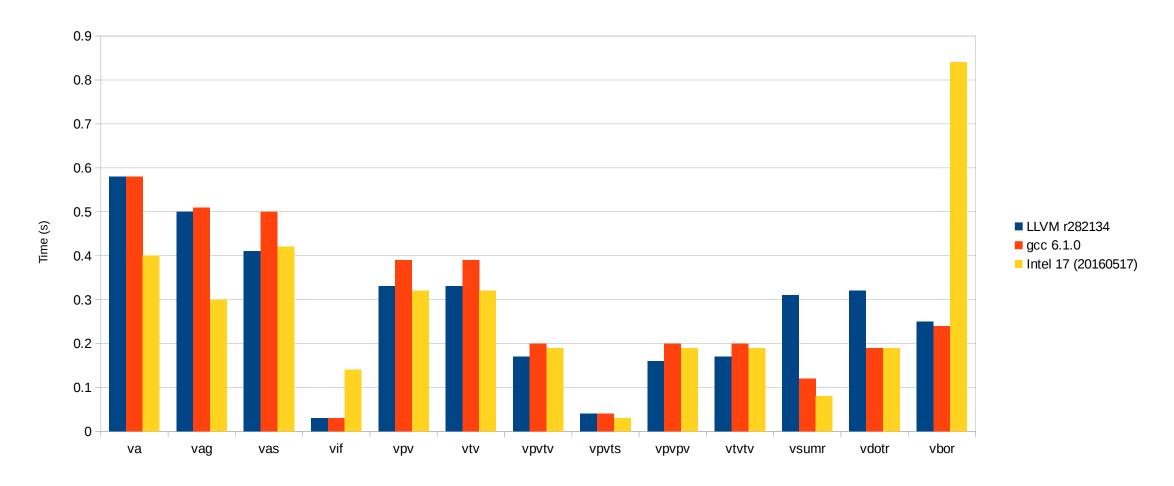
This loop is unrolled when inlined into quack() and quack2(), but not in foo itself.

```
< /tmp/q.cpp
         void bar();
         void foo(int n) {
 > foo(int):
           for (int i = 0; i < n; ++i)
  > quack(), quack2():
           for (int i = 0; i < n; ++i)
             bar();
         void quack() {
 8 I
           foo(4);
10
11
         void quack2() {
12 I
           foo(4);
13
14
```

A viewer that creates HTML reports is also under development (the current prototype, called opt-viewer, is in LLVM's utils directory).

LLVM Supports AVX-512! (KNL Performance)

→ llvm/projects/test-suite/MultiSource/Benchmarks/TSVC/ControlLoops-dbl



Clang Can Compile CUDA!

CUDA is the language used to compile code for NVIDIA GPUs.

\$ clang++ axpy.cu -o axpy --cuda-gpu-arch=<GPU arch>

For example: --cuda-gpu-arch=sm_35

When compiling, you may also need to pass --cuda-path=/path/to/cuda if you didn't install the CUDA SDK into /usr/local/cuda (or a few other "standard" locations).

For more information, see: http://llvm.org/docs/CompileCudaWithLLVM.html

Clang's CUDA aims to provide better support for modern C++ than NVIDIA's nvcc.



Back to vectorization for a moment...

→ OpenMP 4 SIMD directives are supported and often useful (as is putting 'restrict' on function arguments and using -ffast-math)

```
void foo(float * __restrict__ A, float * __restrict__ B, float * __restrict__ C) {
    #pragma omp simd
    for (int I = 0; ...)
        C[i] = A[i] + B[i];
    }
```

→ -fveclib=SVML will generate calls to Intel's vector math-functions library (to autovectorize calls to cos, etc.)



Acknowledgments

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- → ALCF is supported by DOE/SC under contract DE-AC02-06CH11357

