

# §4.3 Scheduling

- block distribution
  - cyclic " }
  - block-cyclic " }
- false sharing issue

→ matrix-vector multiplication

$$m \begin{pmatrix} A \end{pmatrix}^{n \times n} * (x) \Rightarrow (b) \}^m$$

$$b_i = \sum_{j=0}^{n-1} A_{ij} * x_j, \text{ for all } i \in \{0 \dots m-1\}$$

- sequential code

```
void sequential_mult(vector<double>& A,
                    vector<double>& x,
                    vector<double>& b, int m, int n)
{
    for (row = 0 ~ m-1)
    {
        accum = 0;
        for (int col = 0 ~ n-1)
            accum += A[row*n + col] * x[col];
        b[row] = accum;
    }
}
```

as 1-D array like

- int main (---)

```
{
    n = ...;
    m = ...;
    vector<double> A(m*n);
    vector<double> x(n);
    vector<double> b(m);
    // initialize A, x here
    sequential_mult(A, x, b, m, n);
}
```

int n=1 <= 15; + n=1 then left shift 15 bits  
 int m=1 <= 15; ⇒ 2<sup>15</sup> = 32768

alloc. time overhead

2.7 sec  
 vs.  
 1.8 \* 10<sup>-5</sup> sec.

initialize all entries

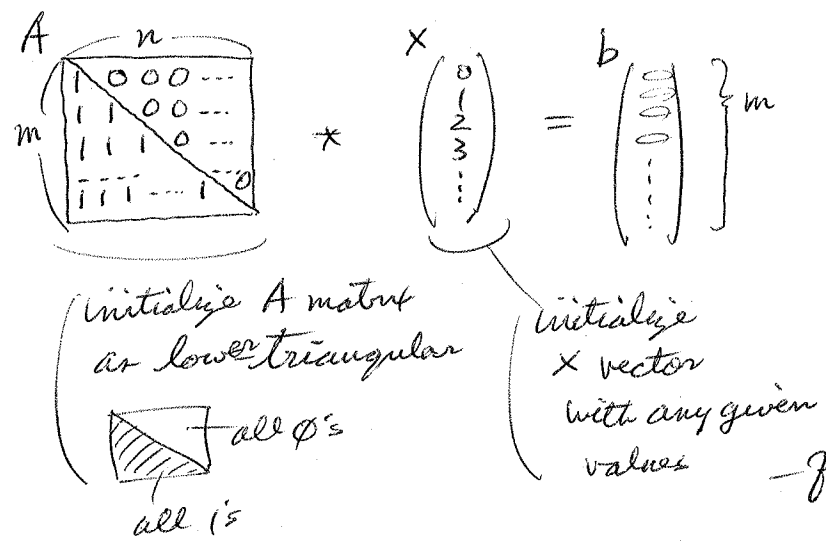
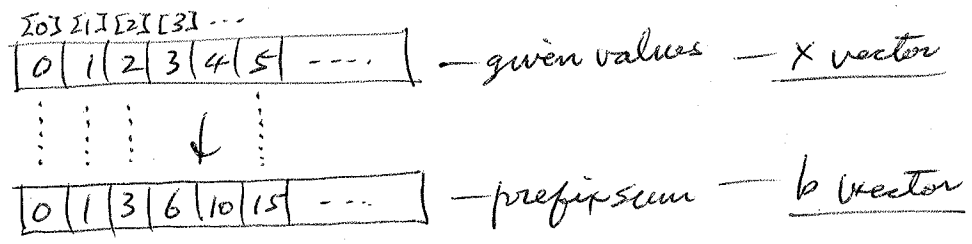
less alloc. time way  
 using dynamic array.

double\* A = new double[m\*n];

or,  
 vector<no-init-t  
 <double>>  
 A(m\*n);

\$> g++ -O2 -std=c++11 DMV.cpp &  
 (dense matrix vector product)

— prefix sum computation with matrix-vector mult.



— for (int row = 0 ~ m-1)  
 for (int col = 0 ~ n-1)  
 $A[\text{row} * n + \text{col}] = \text{row} \geq \text{col} ? 1 : 0;$  // linear storage, row-major layout.

— macro for get time — inline expansion

```
#include <sys/time.h>
...
#define GET_TIME(now) \
{ struct timeval t; gettimeofday(&t, NULL); \
  now = t.tv_sec + t.tv_usec / 1000000.0; \
}

...
double start_time; ...
GET_TIME(start_time);
GET_TIME(end_time); — cout << end_time - start_time;
```

— block distribution — version 1 — main() creates threads.

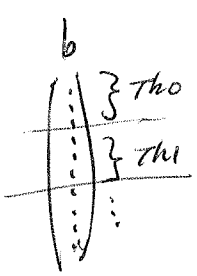
DMV ex).

```

void block_parallel_mult((int id) double A[], double x[], double b[],
                          #include <math> int m, int n, int num_threads)
{
    int chunk = ceil((double)m * num_threads); // - for not evenly divisible case.
    int lower = id * chunk;
    int upper = min(lower + chunk, m);
    for (int row = lower ~ upper - 1)
    {
        double accum = 0.0;
        for (int col = 0 ~ n - 1)
            accum += A[row * n + col] * x[col];
        b[row] = accum;
    }
}

```

$\lceil \frac{m}{p} \rceil$  (assign 1 more to earlier threads)



— int main(---)

```

{
    double* A = new double[m * n];
    double* x = new double[n];
    double* b = new double[m];
    // initialize A and x here
    vector<thread> threads;
    for (int id = 0 ~ num_threads - 1)
        threads.emplace_back(block_parallel_mult, id, A, x, b, m, n, num_threads);
    for (auto& thread : threads)
        thread.join(); // wait for all threads terminate
    for (int i = 0 ~ m - 1)
        cout << b[i] << endl;
    delete[] A; delete[] x; delete[] b;
    return 0;
}

```

— block distribution (BMV) — version 2 — using lambda function

void block\_parallel\_mult(double A[], double X[], double b[],  
int m, int n, int num\_threads)

*lambda func.*

```

{
    auto block = [&](int id) → void
    {
        int chunk = ceil((double)m / num_threads);
        int lower = id * chunk;
        int upper = min(lower + chunk, m);
        for (int row = lower; row < upper; row++)
        {
            double accum = 0.0;
            for (int col = 0; col < n; col++)
                accum += A[row * n + col] * X[col];
            b[row] = accum;
        }
    };

    vector<thread> threads;
    for (int id = 0; id < num_threads; id++)
        threads.emplace_back(block, id);
    for (auto & thread : threads)
        thread.join();
}

```

int main(---)

```

{
    double* A = new double[m * n];
    double* X = ---
    double* b = ---
    // init A and X here.

    block_parallel_mult(A, X, b, m, n, num_threads);

    for (int i = 0; i < m; i++)
        cout << b[i] << endl;

    delete A; delete X; delete b;

    return 0;
}

```

# Lambda function usage

ex)

- auto addOne = [I] (int & v) { return v+1; };

error  $\left( \begin{array}{l} \text{int } w = 1; \\ \text{auto add-w} = [I] (int \& v) \{ \text{return } v + \overset{?}{w} \}; \end{array} \right)$   $\left. \begin{array}{l} \text{compile} \\ \text{time error} \end{array} \right\}$   
w is declared out of  $\lambda$ -func.

-  $\left( \begin{array}{l} \text{int } w = 1; \\ \text{auto add-w} = [w] (int \& v) \{ \text{return } v + w \}; \end{array} \right)$   
capture w by value

-  $\left( \begin{array}{l} \text{int } w = 1; \\ \text{auto add-w} = [\&w] (int \& v) \{ \text{return } v + w \}; \end{array} \right)$   
capture w by reference

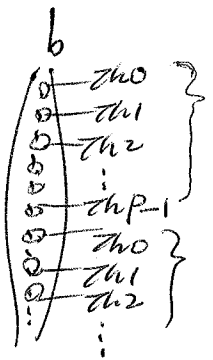
- auto add-w = [ $\&$ ] (int & v) { return v+w; };  
capture everything accessed in  $\lambda$ -func. <sup>by</sup> ~~as~~ reference.

- auto add-w = [=] (int & v) { return v+w; };  
capture everything accessed in  $\lambda$ -func. <sup>by</sup> value

cyclic distribution (PMV)

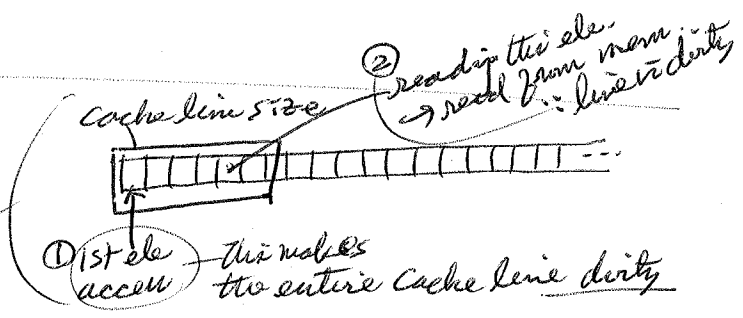
void cyclic\_parallel\_mult (double A[I], double X[I], double b[I],  
int m, int n, int num\_threads)

```
{  
  auto cyclic = [I] (int id) -> void  
  {  
    for (row = id ~ m-1, row += num_threads)  
    {  
      accum = 0.0;  
      for (col = 0 ~ n-1)  
        accum += A[row * n + col] * X[col];  
      b[row] = accum;  
    }  
  }  
  vector<thread> threads;  
  for (id = 0 ~ num_threads - 1)  
    threads.emplace_back(cyclic, id);  
  for (auto& thread : threads)  
    thread.join();  
}
```



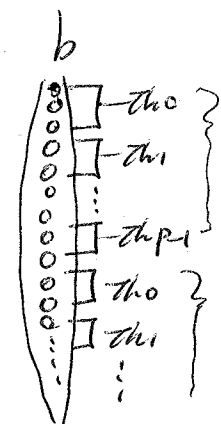
fine-grained cyclic

may cause false sharing issue



→ sol: block-cyclic distribution

in fact,  
(  
  block distr — block size =  $\frac{m}{p}$   
  cyclic distr — block size = 1  
extreme cases of block-cyclic distr.



— block size should be same as cache line size.

# false sharing

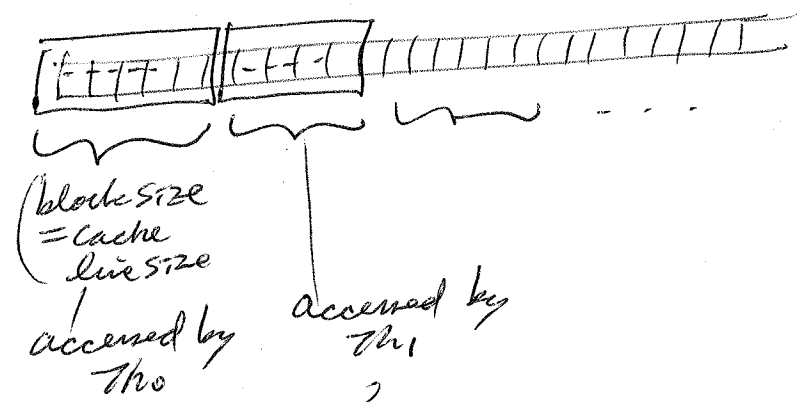
```

24) auto cyclic =  $\sum b[i]$  (int  $i$  id)  $\rightarrow$  void
{
    for (int row=id; row<m; row+= numthreads)
    {
        b[row] = 0;
        for (col=0 ~ n-1)
            b[row] += A[row*n+col] * X[col];
    }
}

```

$\rightarrow$  result is correct, but memory access overheads.  
(due to false sharing).

24)  $b[i]$  is updated by  $th_0$  on core 0; (cache line in core 0 is dirty)  
 $b[i]$  is read by  $th_1$  on core 1;  $\rightarrow$  core 1 accesses mem for same cache line.  
 or updated cache



block size  
 (too) small  
 $\rightarrow$  false sharing  
 $\rightarrow$  load balanced  
 vs.  
 big  
 $\rightarrow$  cache friendly access  
 $\rightarrow$  load imbalance

24) Intel  
 cache line size  
 64 bytes  
 $\Rightarrow$  block size  
 good  $C=16$  for int  
 $C=8$  for 64bit data

no confliction  
 24)  $th_0$  - local cache update  
 and then, read same line  
 $\rightarrow$  no problem  $\rightarrow$  cache up to date.

# block-cyclic distribution (PMU)

void block\_cyclic\_parallel\_mult (double A[], ... X[], ... b[],  
int m, int n, int num\_threads, int chunk-size)

```

{ auto block_cyclic = [&](int id) -> void
{
  int offset = id * chunk_size; // chunk_size = 64 * 8 = 512
  int stride = num_threads * chunk_size; // (cache line size) (double type size)
  // next jump
  for (int lower = offset; lower < m; lower += stride)
  {
    int upper = min(lower + chunk_size, m);
    for (int row = lower; row < upper; row++)
    {
      double accum = 0.0;
      for (int col = 0; col < n; col++)
      {
        accum += A[row * n + col] * X[col];
      }
      b[row] = accum;
    }
  }
} // auto

vector<thread> threads;
for (int id = 0; id < num_threads; id++)
  threads.emplace_back(block_cyclic, id); // call lambda.
for (auto& thread : threads)
  thread.join();
}
  
```

int main(...)

```

{
  ...
  block_cyclic_parallel_mult(A, x, b, m, n, num_threads, 64 / sizeof(double));
  for (i = 0; i < m; i++)
    cout << b[i] << endl;
  ...
}
  
```