

Errors

Warnings

Memory Leaks

All Details

SHOW

HIDE

All Source

SHOW

HIDE

All Blocks

SHOW

HIDE

Show Errors

- | | |
|---|------------------------------|
| <input type="checkbox"/> ABR | <input type="checkbox"/> ABW |
| <input type="checkbox"/> BFM | <input type="checkbox"/> BRP |
| <input type="checkbox"/> CGB | <input type="checkbox"/> DFM |
| <input type="checkbox"/> FMR | <input type="checkbox"/> FMW |
| <input type="checkbox"/> FRP | <input type="checkbox"/> IMR |
| <input checked="" type="checkbox"/> IMW | <input type="checkbox"/> PIR |
| <input type="checkbox"/> SBR | <input type="checkbox"/> SBW |
| <input type="checkbox"/> UAR | <input type="checkbox"/> UAW |
| <input type="checkbox"/> UMR | |

IMW: write to invalid memory address 0x0 (4 bytes)main() + 0x3b (line ~16) in "test2.cc"

```
13:
14:   x = NULL;
15:
16:   x[0] = 10;
17:
18: }
19:
```

_start() + 0x7c

Summary

Errors: 1

Warnings: 0

Leaked: N/A

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