

to Logger/Panicle

$R_1 = 100 \text{ k}\Omega$ pull down (free start up)

$$V_{REF} = 1.21V \rightarrow V_{OUT} (\text{Actual}) = 5.03V$$

Boost calculations

$$C_1 \geq 4.7 \mu F \quad (\text{Specified for stability by datasheet})$$

$$C_1 = 10 \mu F, 16V \text{ XSR/X2R}$$

↑
Low ESR

↙ two in parallel (increases cap, reduces ESR)

$$C_2 = 10 \mu F \times 2, 16V \text{ XSR/X2R}$$

$$10 \mu F \leq C_2 \leq 100 \mu F$$

↑ increase from minimum to middle of range to improve noise & transient performance (cleaner line)

Choose higher than standard value since connecting across cable and unknown source impedance, allows for greater stability margin, noise reduction,

$$L_1 = 4.7 \mu H$$

Recommended Balanced value

$$2.2 \mu H \leq L_1 \leq 10 \mu H$$

$$I_{sat} \geq 0.7A$$

$$DCR \leq 0.55 \Omega$$

$$PN, WORTH = 74405024047$$

$$270 \Omega, 1.9A \text{ sat}$$

$$PN, TDK = VLS252010HBX-4R7M-1$$

$$250 \Omega, 1.6A \text{ sat}$$

Output Ripple Calc

$$\Delta I_L = \frac{V_{in}(V_{out} - V_{in})}{L \cdot f_s \cdot V_{out}} = \frac{3.3(5 - 3.3)}{(4.7 \mu H) \cdot 500 \text{ kHz} \cdot 5V} = 0.477A$$

$$\Delta V_{out} = \Delta V_{cap} + \Delta V_{ESR} = \frac{I_{out, max} \cdot D}{f_s \cdot C_{out}} + ESR_{eff} \left(\frac{I_{out, max}}{1-D} + \frac{\Delta I_L}{2} \right)$$

$$\frac{V_{out}}{V_{in}} = \frac{1}{1-D} \rightarrow 1.515(1-D) = 1$$

$$D = 0.34$$

$$f_s = 500 \text{ kHz (given)}, I_{out, max} = 20 \text{ mA (Rated 13mA max sensor draw)}$$

$$ESR = 3 \text{ m}\Omega @ 500 \text{ kHz for } 10 \mu F \text{ murata XSR capacitor (taken from sim summary)}$$

$$ESR_{eff} = 3 \text{ m}\Omega \parallel 3 \text{ m}\Omega = 1.5 \text{ m}\Omega$$

$$C_{out} = 20 \mu F (10 \mu F \parallel 10 \mu F)$$

$$\Delta V_{out} = \frac{20 \text{ mA} \cdot 0.34}{500 \text{ kHz} \cdot 20 \mu F} + 1.5 \text{ m}\Omega \left(\frac{20 \text{ mA}}{1-0.34} + \frac{0.477}{2} \right) = 6.8 \times 10^{-9} + 4.032 \times 10^{-9} = 1.08 \text{ mV}$$

can be reduced by increasing inductance of L_1 and capacitance of C_2