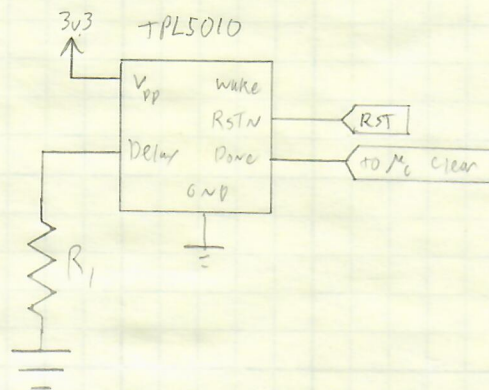




## WD Detail

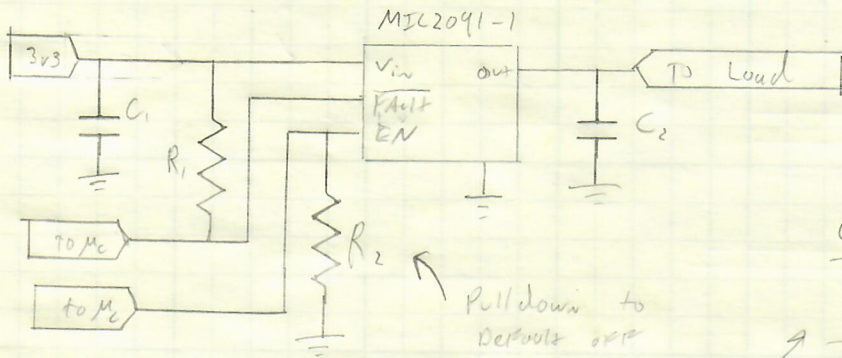


$R_1 = 100k\Omega$  for Reset time  $> 30\mu s$  (low freqs)

$R_1 = 4.3k\Omega$  for Reset time  $= S_{min}$  (mid freqs)

## Power Biasing

switch design

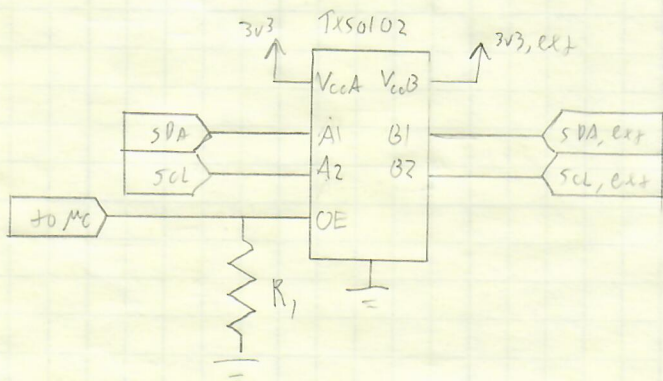


$R_1 = R_2 = 100k\Omega$

$C_1 = 1\mu F, 16V$  XSR to prevent ringing

$C_2 = 10\mu F, 16V$  XSR

since distance to load is large (external to Board) can use  $1\mu F$  for SP switch



$R_1 = 100k\Omega$

$C_1 = C_2 = 0.1\mu F, \geq 10V$

