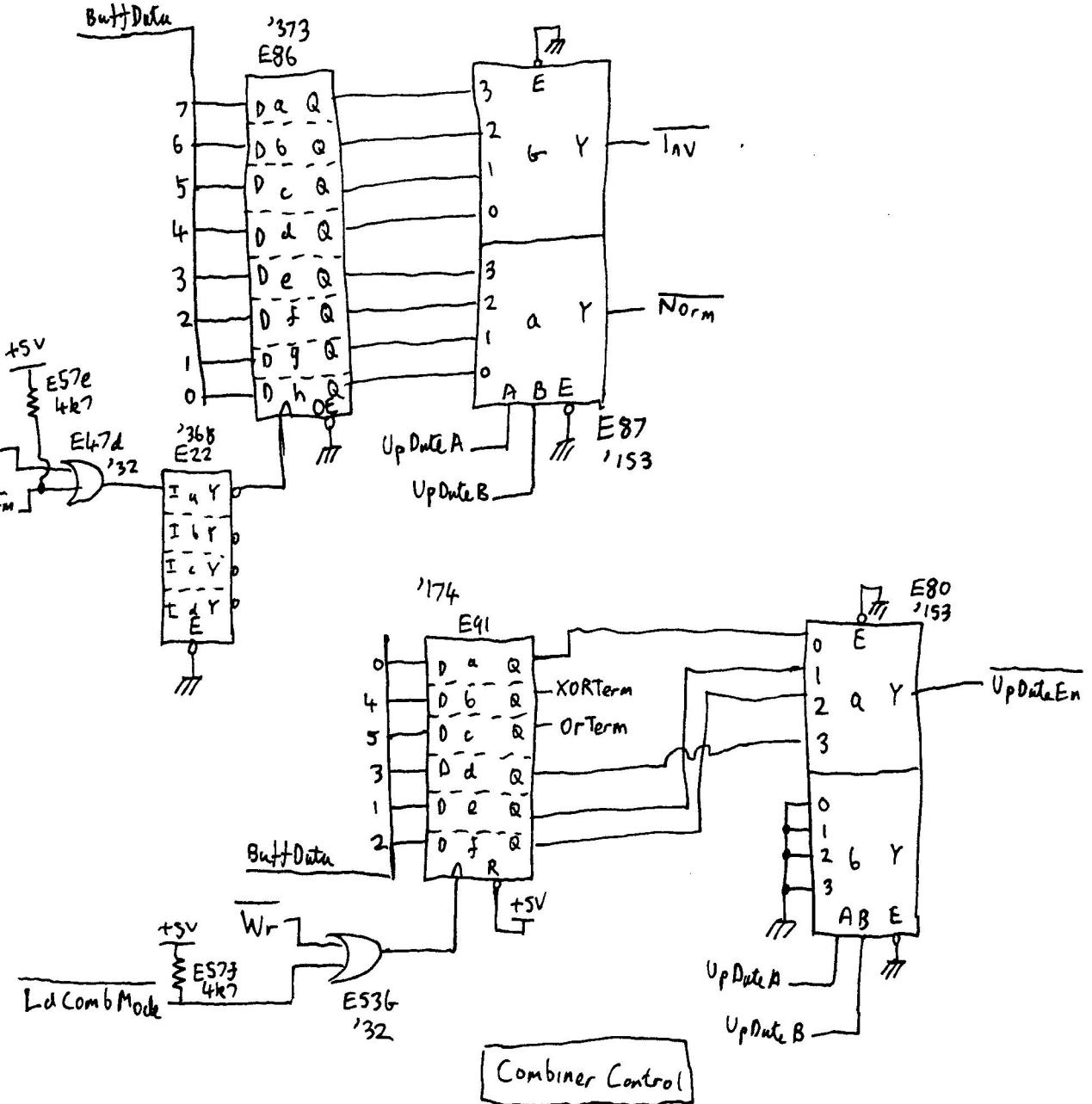
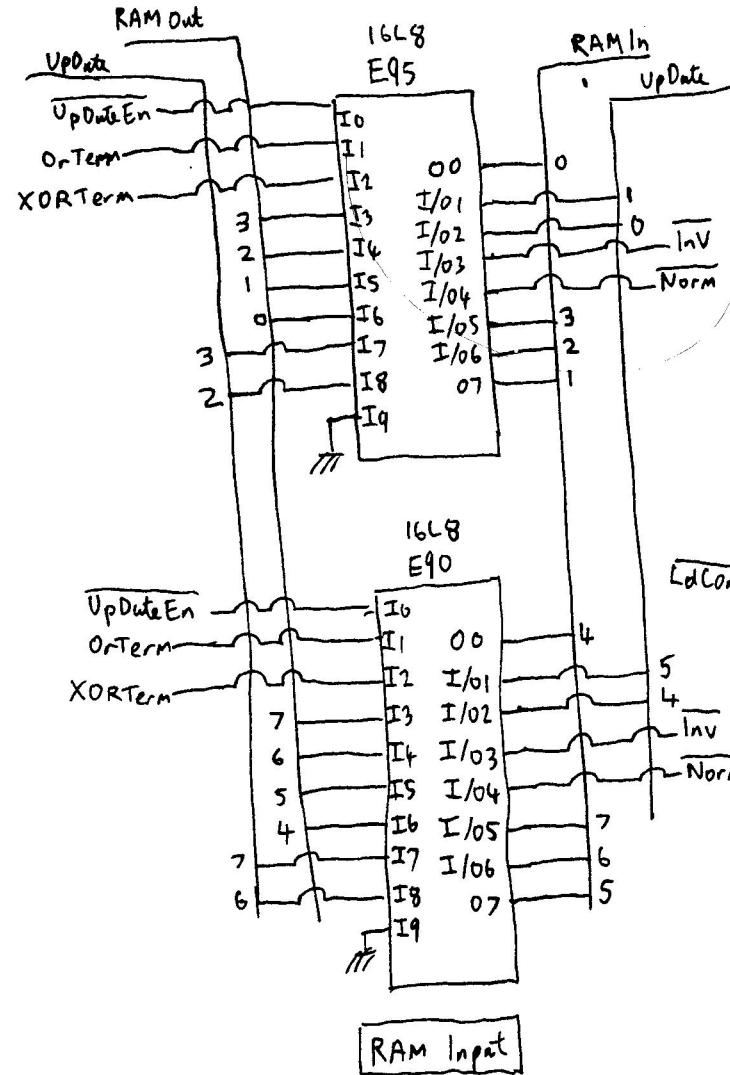
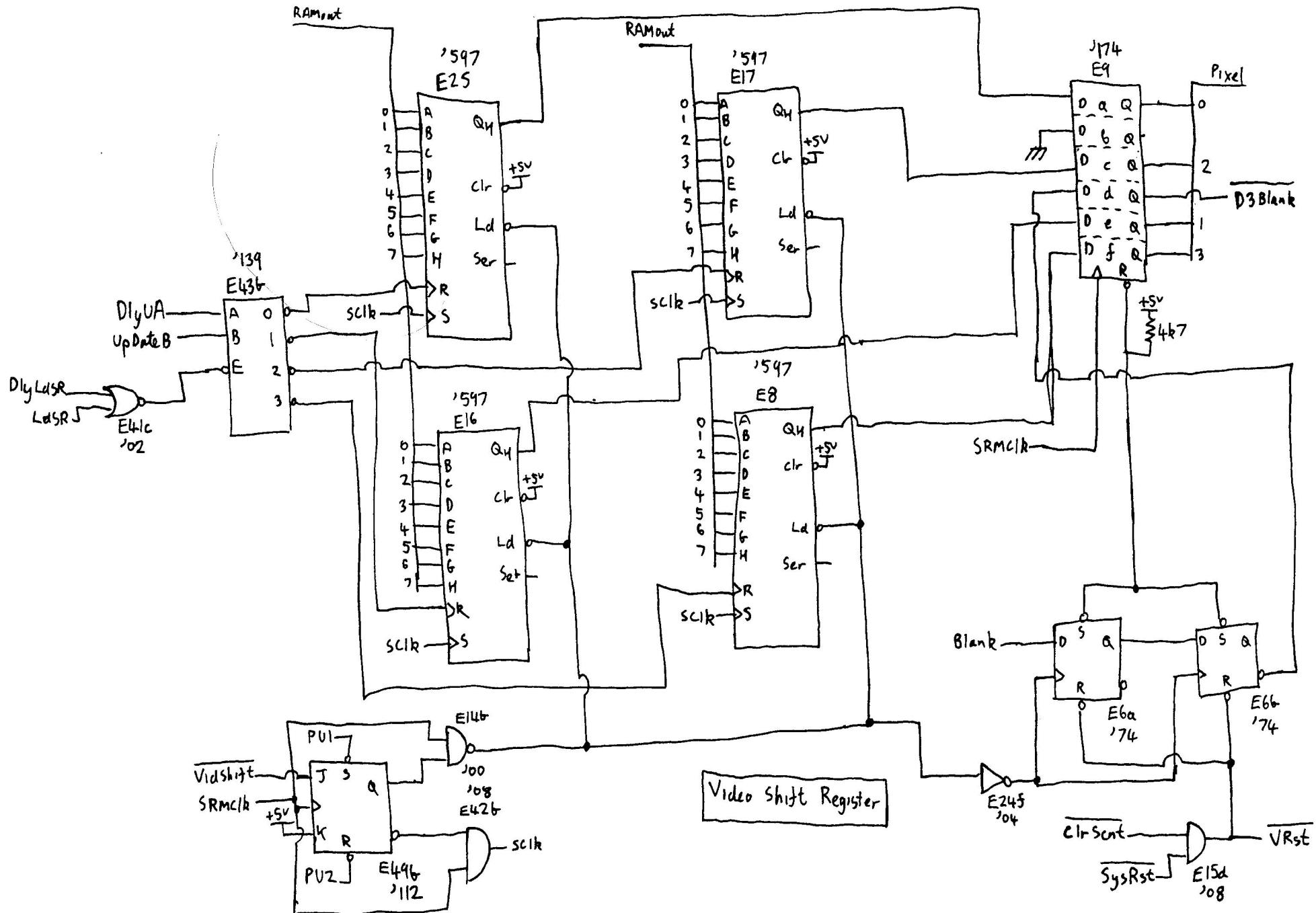
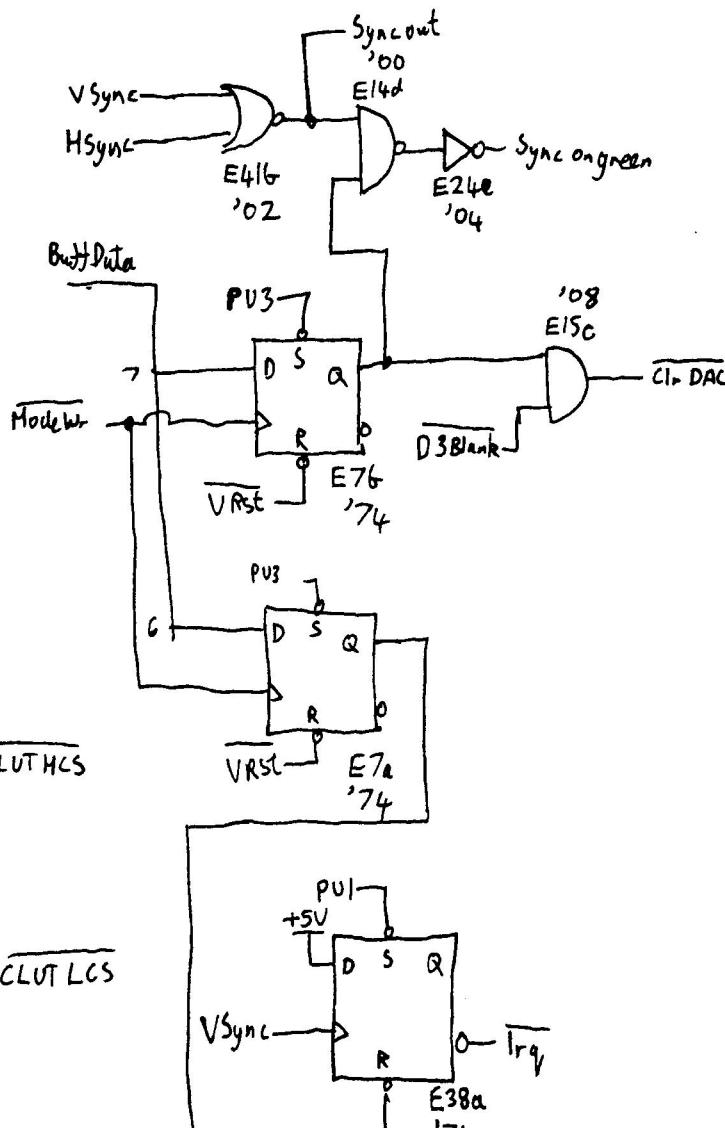
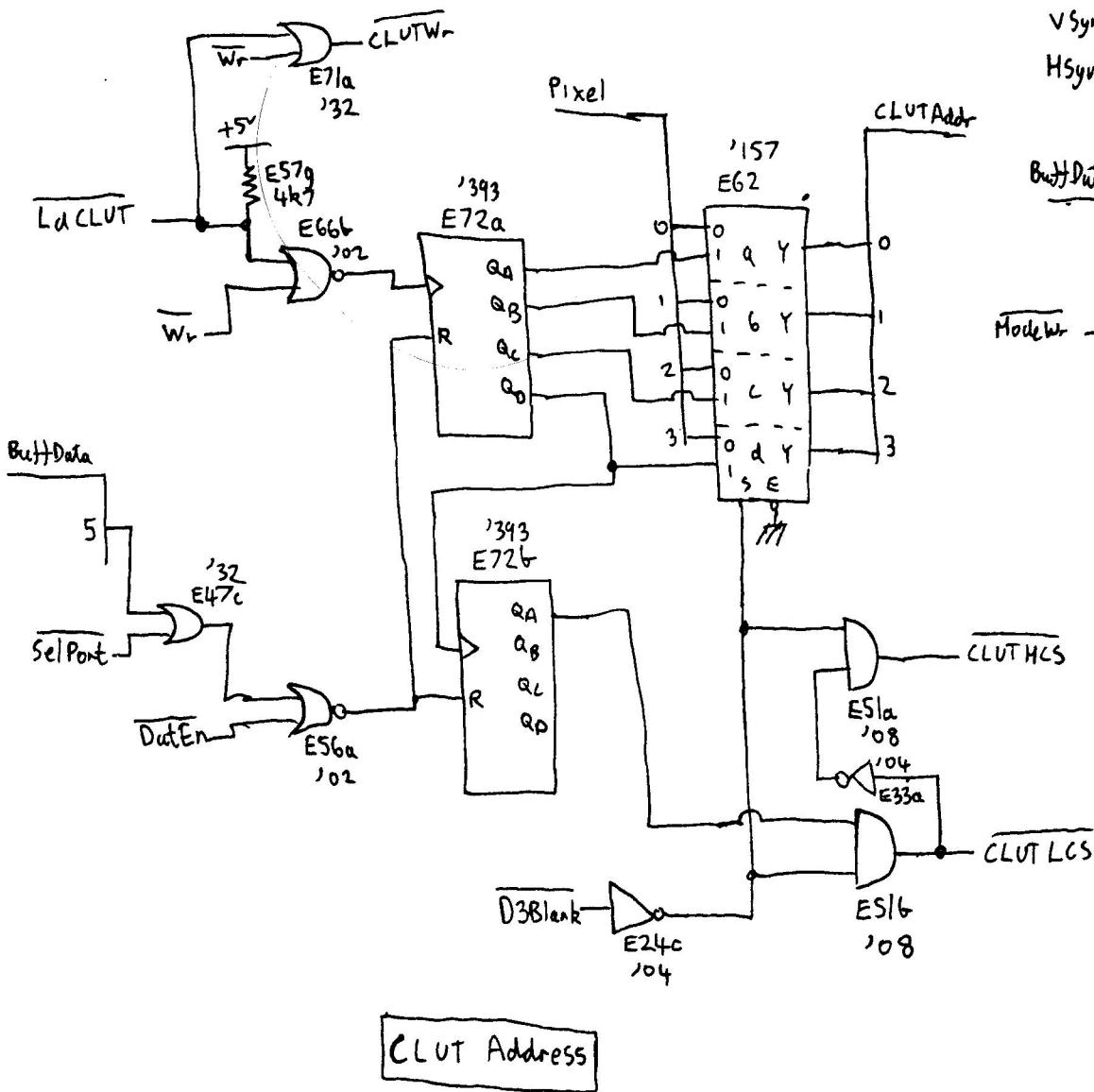
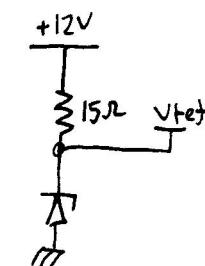
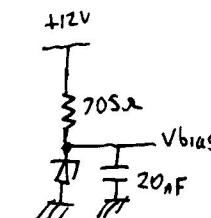
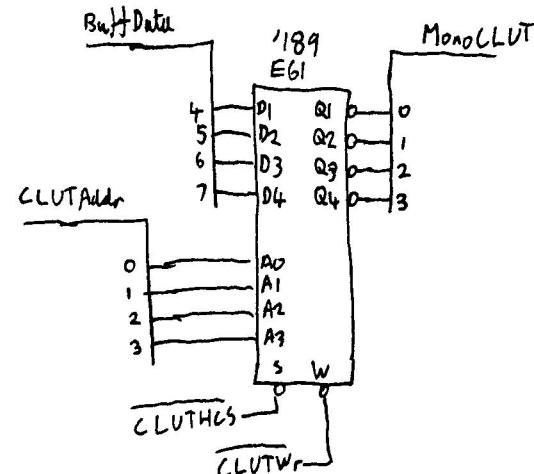
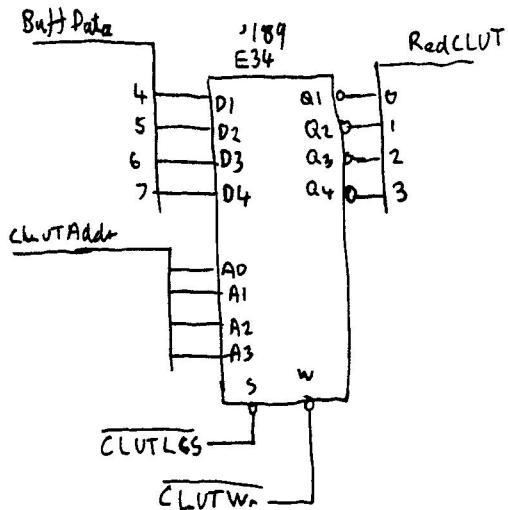
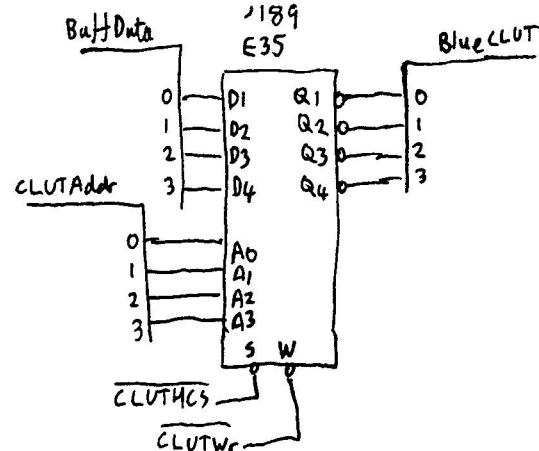
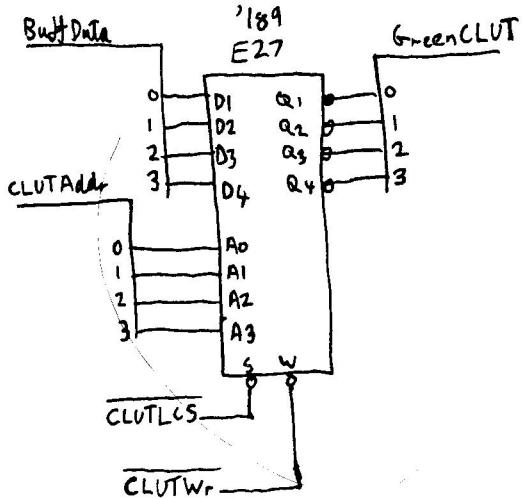


Update Constants

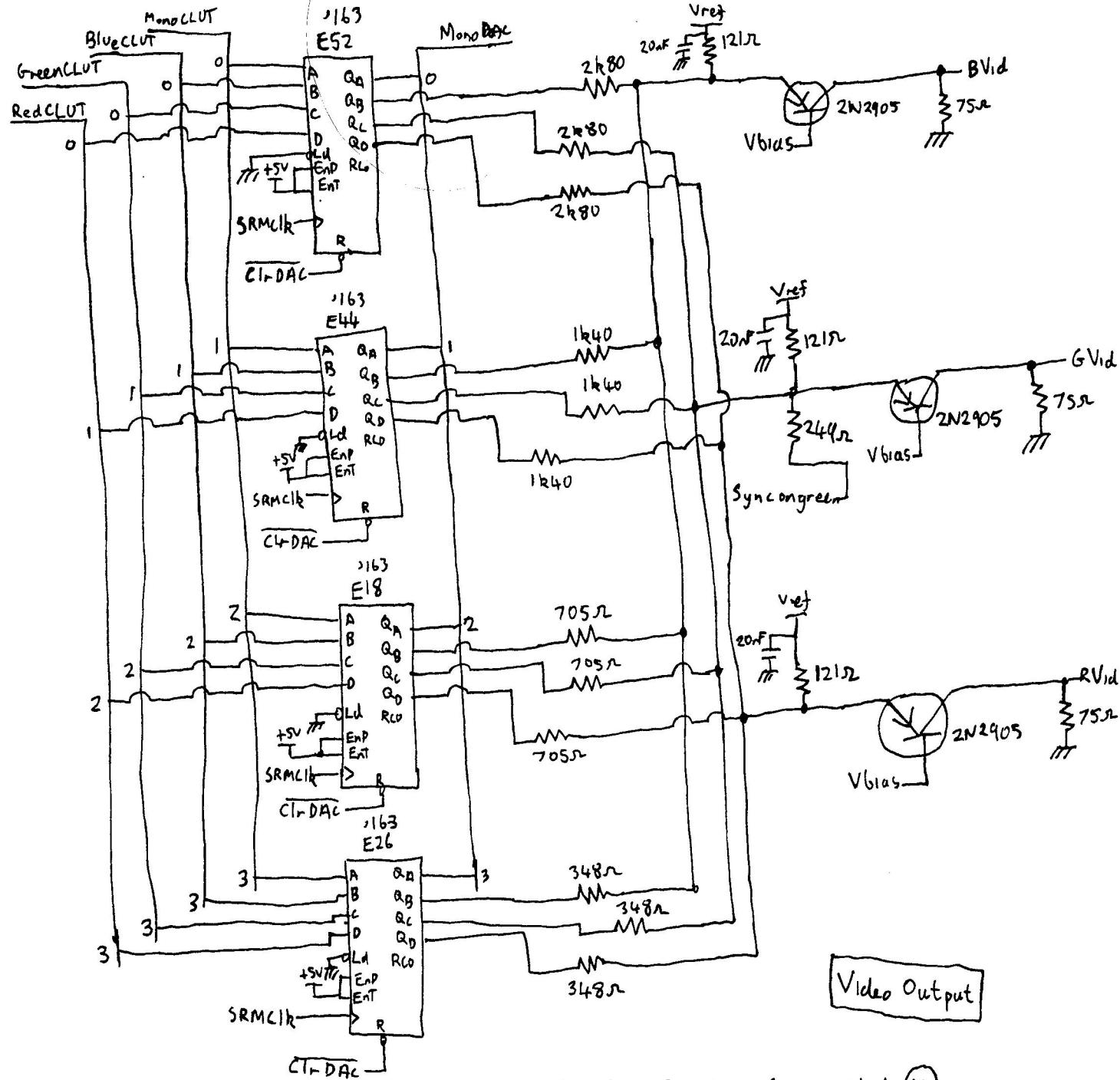








Colour Look-up Table



; JED2EQN -- JEDEC file to Boolean Equations disassembler (version v002)
; Copyright (R) National Semiconductor Corporation 1990,1991
; Disassembled from gphpal.jed. Date: 1-1-80

chip gphpal PALL6L8

pinout
 i1 i2 i3 i4 i5 i6 i7 i8 i9 GND
 nc o12 o13 o14 i15 i16 i17 i18 o19 vcc

equations D_{01} D_{02} D_{03} V_{P2} V_P D_{04}

二

```

o19.oee = vcc

```

```

/o14 = /i1 * /i3 * /i16 * i8
+ /i2 * /i1 * /i3 * /i15 * /i8
+ /i2 * /i1 * i3 * /i4 * /i8
+ /i2 * /i1 * i3 * i4 * i8
+ i2 * /i1 * /i3 * /i4 * /i8
+ i1 * /i4
o14.oe = vcc

```

```

+ /i2 * /i1 * /i3 * /i15 * /i9
+ /i2 * /i1 * i3 * /i15 * /i9
+ /i2 * /i1 * i3 * i5 * i9
+ i2 * /i1 * /i3 * /i15 * /i9
+ i1 * /i15
013.oee = vcc

```

```

/o12 = /i1 * /i3 * i18 * /i16
+ /i2 * /i1 * /i3 * /i18 * /i15
+ /i2 * /i1 * i3 * /i18 * /i16
+ /i2 * /i1 * i3 * i18 * i6
+ i2 * /i1 * /i3 * /i18 * /i16
+ i1 * /i6
o12.oe = vcc

```

卷之三

Update Einheitsmatrix mit					Norm
Ortsteile x ordnen					Inv
1	2	3	15	16	
0	x	0	x	0	
0	0	0	0	x	
0	0	-1	x	x	$\text{DO} := \overline{\text{UP}}$
0	1	0	x	x	$\text{DO} := \overline{\text{UP}} \oplus \text{DT}$
1	x	x	x	x	$\text{DO} := \text{UP}$
			0	0	$\text{DO} := \text{DT}$