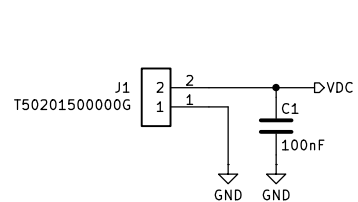
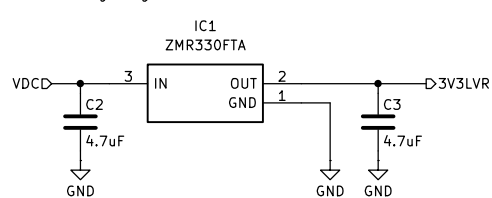


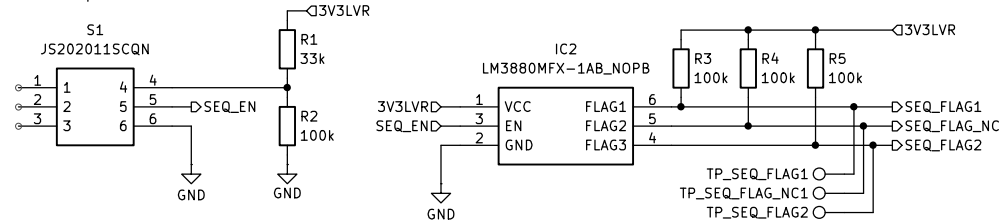
# DC Power Supply



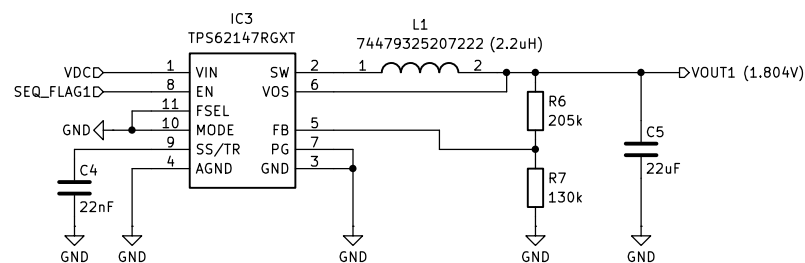
# Linear Voltage Regulator



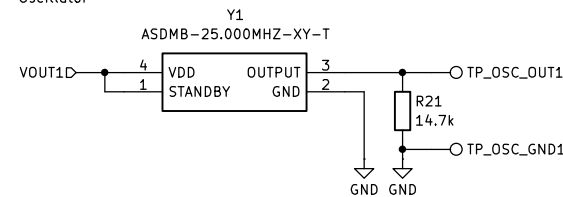
# Power Sequencer



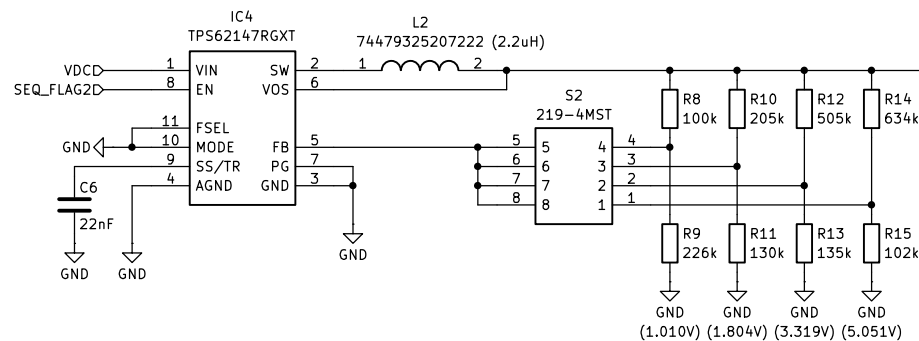
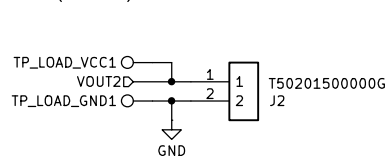
# Buck Converter



# Oscillator



# Load(Resistive)



Author: Park Siyoung  
Github: [https://github.com/bsiyoung/FPGA\\_Pre\\_Test](https://github.com/bsiyoung/FPGA_Pre_Test)

Sheet: /  
File: fpga\_pre\_test.kicad\_sch

**Title: FPGA Pre Test**

Size: A4 Date: 2025-10-09

KiCad E.D.A. 9.0.2

**Rev: v1.0.0**

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