EGA Memory Banks

Bank 0	Bank 1	Bank 2	Bank 3

| 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 1 |

bit 4 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 |
bit 3 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 |
bit 2 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
bit 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 1 |

| 5 | 9 | E | 7 | B | A | 2 | D |

Attribute Controller

| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C | D | E | F |

Pixel

Result on screen