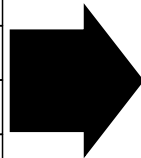


Odd address  
Read word value  
*8281h* with two  
8-bit access



Memory Address	Value
A0000h	80
A0001h	81
A0002h	82
A0003h	83
A0004h	84
A0005h	85



Even address  
Read word value  
*8382h* with single  
16-bit access