Condor FPGA PS-PL

requirements

# Scope

This documents details the requirements for the Condor FPGA SW and FW.

It is based on the requirements detailed in Elbit’s 5495-6000-00PS\_PIDS-PSU\_VER18 (1).docx spec.

Document and breaks down the into FPGA tasks.

Out of the Elbit spec we detail here only the tasks for the SW and FW.

# PS and PL pin list

This section lists each of the operational pins on the FPGA (both PS and PL) and adds a short description of the purpose of the pin. It also links to the section that explains what is the algorithm needed to be done for the pin.

# Tasks

This chapter lists all tasks done by the FPGA. We may reference the Elbit spec for applicable tasks.

# Power On – Off

The following is the sequence requirements:

# Lamp indication

# Uarts

# SPIs