THANH DAT, BUI.

DESIGN VERIFICATION INTERN

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Dynamic and dedicated student with academic experience in Embedded Systems and Electronics. I'm ready to uplevel my expertise in digital logic design, system architecture, and FPGA development to innovate and excel in challenging design and validation environments.

Education Projects

Black-box Adversarial Attack for Unsupervised Deep One-Class Classification in Time-Series Domain *2023 - Now*

- Role: Undergraduate Research Student; Project for future publication in Data Science domain.
  - Instructor: Tung, Kieu (Assistant Professor at Aalborg University, Department of Computer Science, Data Engineering, Science and Systems, tungkvt@cs.aau.dk).
- With Deep One-Class Classification being a superior method that inspired by SVM (Support Vector Machine), but employed a neural network that maps data into a hypersphere of minimal volume, we aim to create an black-box attack that can work across different models.

Computer Embedded Systems Laboratory (CESLAB) - HCMUS, VNUHCM *2022 - Now*

- Role: Undergraduate Research Student.
  - Instructor: Huu Thuan, Huynh (Head of Computer and Embedded Systems Department, hhthuan@hcmus.edu.vn).
- Exprienced with working on IoT and Embedded Boards. (Raspberry Pi, STM32...)
- Great experience with Embedded Systems and Verilog HDL from Practice Labs provided by Intel FPGAcademy on DE10-Standard board.
- Basic understanding on **digital** topics: Computer Architecture; Fundamentals of Sequential Logic Design; Digital Signal Processing...

Ported Adafruit Fingerprint Sensor Library from Arduino to C++ for STM32 Embedded Board. *Mar 2021 - Apr 2021*

- Role: Lead Developer of Final Project for **Microprocessor** course.
  - Instructor: Duc Hung, Le (Head of DESLAB and Electronics Department, ldhung@hcmus.edu.vn).
  - Graded 8.7 for Theory Class, 9.5 for Practice Labs.

Education

University of Science, Vietnam National University HCMc *2021 - Now*

- Majors in Electronics and Telecommunication.
- GPA: 8.44/10

Skills

- English (IELTS Academic 7.0)
- C/C++
- Linux (Bash Shell)
- Verilog HDL (Intel Quartus, ModelSim)
- Matlab, Simulink
- Microsoft Office