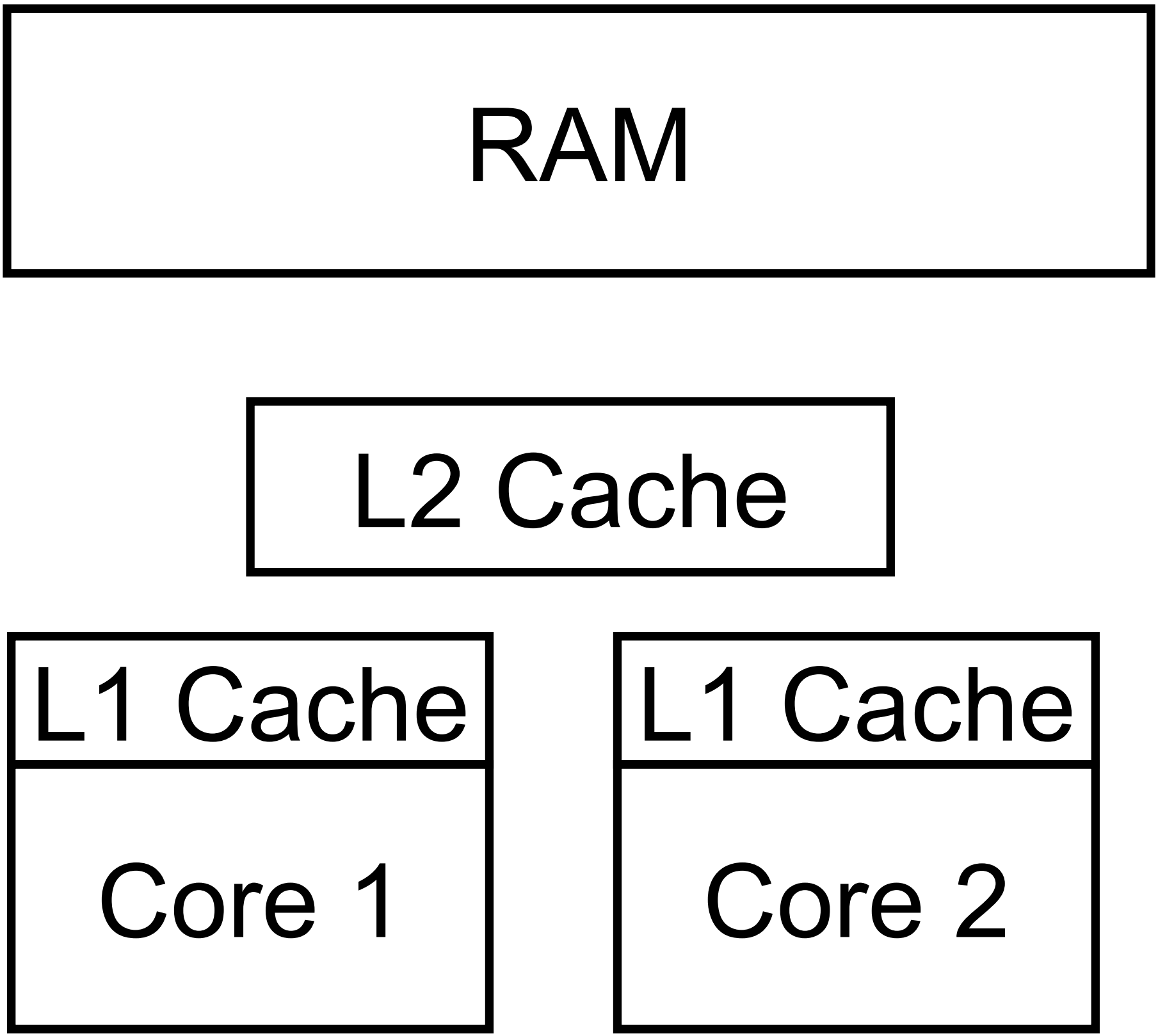
RAM
L2 Cache
L1 Cache
Core 1
L1 Cache
Core 2