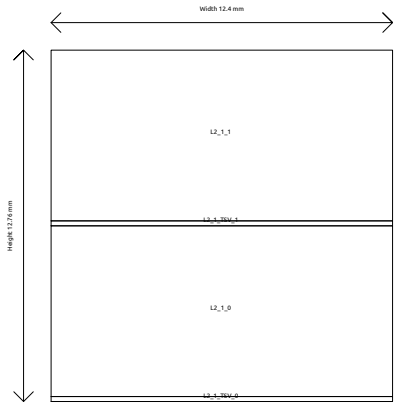
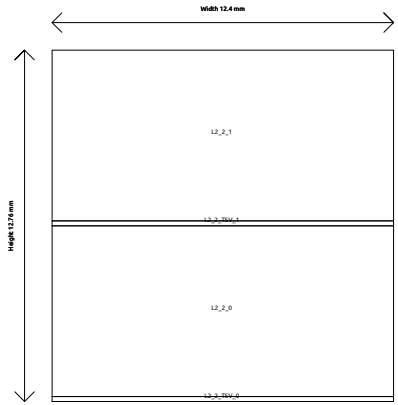
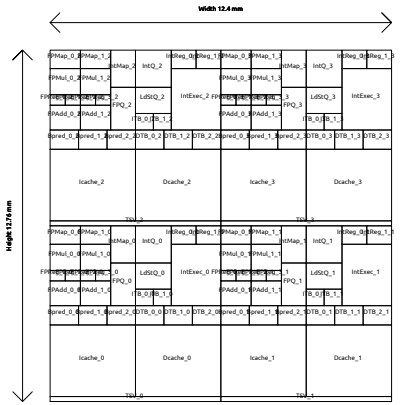

Width 12.4 mm
Height 12.76 mm
L2_1_1
L2_1_TSV_1
L2_1_0
L2_1_TSV_0


Width 12.4 mm
Height 12.76 mm
L2_2_1
L2_2_TSV_1
L2_2_0
L2_2_TSV_0


Width 12.4 mm
Height 12.76 mm
FPMap_0_0
FPMap_1_2
IntMap_2
IntQ_2
IntReg_0_2
IntReg_1_2
FPMul_0_2
FPMul_1_2
FPReg_0_2
FPReg_1_2
FPQ_2
LdStQ_2
IntExec_2
FPAdd_0_2
FPAdd_1_2
ITB_0_2
ITB_1_2
Bpred_0_2
Bpred_1_2
Bpred_2_2
DTB_0_2
DTB_1_2
DTB_2_2
FPMap_0_3
FPMap_1_3
IntMap_3
IntQ_3
IntReg_0_3
IntReg_1_3
FPMul_0_3
FPMul_1_3
FPReg_0_3
FPReg_1_3
FPQ_3
LdStQ_3
IntExec_3
FPAdd_0_3
FPAdd_1_3
ITB_0_3
ITB_1_3
Bpred_0_3
Bpred_1_3
Bpred_2_3
DTB_0_3
DTB_1_3
DTB_2_3
Icache_2
Dcache_2
Icache_3
Dcache_3
TSV_3
FPMap_0_1
FPMap_1_1
IntMap_0
IntQ_0
IntReg_0_0
IntReg_1_0
FPMul_0_1
FPMul_1_1
FPReg_0_1
FPReg_1_1
FPQ_0
LdStQ_0
IntExec_0
FPAdd_0_1
FPAdd_1_1
ITB_0_1
ITB_1_1
Bpred_0_1
Bpred_1_1
Bpred_2_1
DTB_0_1
DTB_1_1
DTB_2_1
FPMap_0_0
FPMap_1_0
IntMap_1
IntQ_1
IntReg_0_1
IntReg_1_1
FPMul_0_0
FPMul_1_0
FPReg_0_0
FPReg_1_0
FPQ_1
LdStQ_1
IntExec_1
FPAdd_0_0
FPAdd_1_0
ITB_0_0
ITB_1_0
Bpred_0_0
Bpred_1_0
Bpred_2_0
DTB_0_0
DTB_1_0
DTB_2_0
Icache_0
Dcache_0
Icache_1
Dcache_1
TSV_0
TSV_1