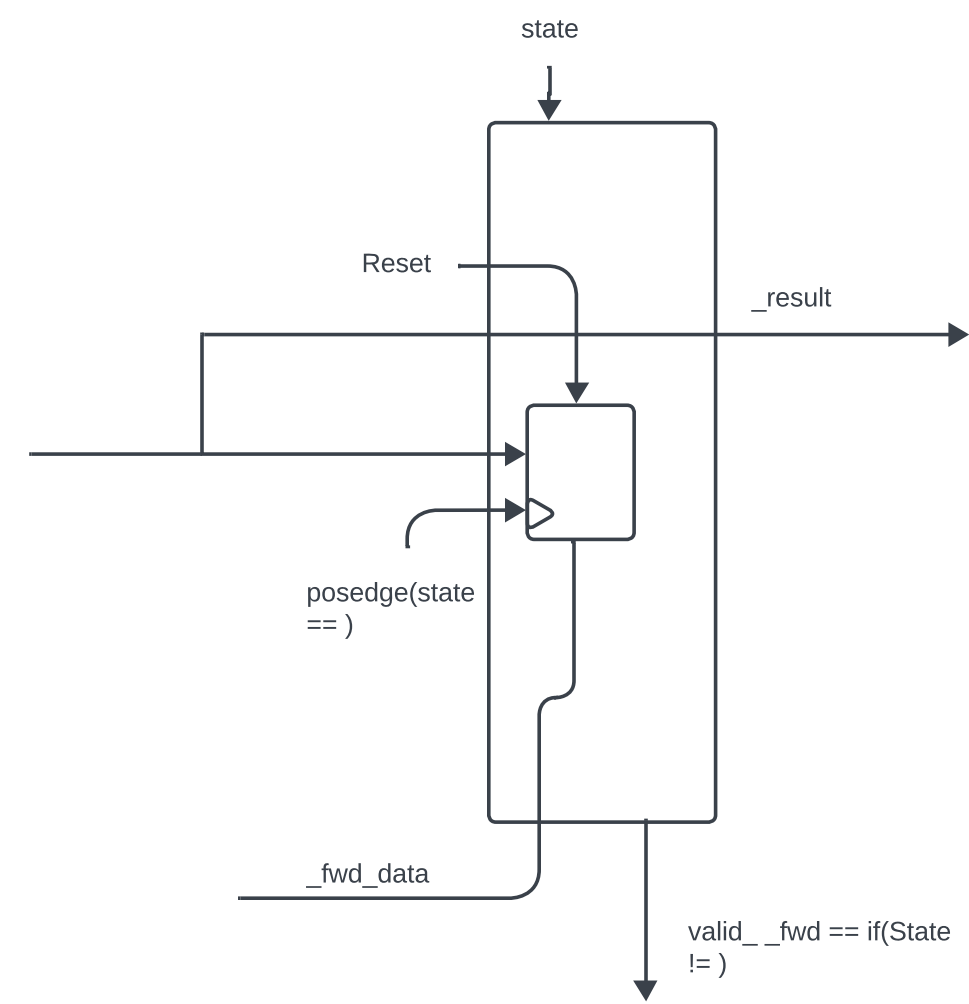


Concat for cpu to DE\_EXE ?? I assume that decode needs to send data to cpu to help it decide control word

How thorough do we need to be? Do we need to show design of entire caches, arbiters, control units...?

Ben's rough idea of enabling fwding



Do we want to separate the DE and IF stages??

What is PC\_x vs PC\_q? I just hooked everything up to PC\_x...

i'm trying to go over what's already here but basically the control signals (green) would be part of the pipeline regs --tongyu

