the received message (for every element). Implementation Rules [IR]: [TRI]: Vector clock vci is incremented between any two successive events in process Pi vcici] = vcici] +d, d>0. [IR2]: It event a is the sending event of meridge & m by process pi then to meridge & msigned a vector timestamp tm=Vcica) On receiving the lame mig m by process pi. VCj is updated as, MK, VCj[K] = max [VCj[K], tm [K]) Kis no. of process. Example: Consider a process (P) with a vector size N for each process: the above set of rules mentioned are to be executed by the vector clock: [2,0,0] [3,0,0] [4,4,1] [5,4,1] P2 [0,1,0] [2,2,0] [2,4,1] [2,5,1]
P3 [0,0,1] [0,0,2] . = EVENT EXECUTING RULES TIME 0 = EVENT EXECUTING RULE 2

AIM: To implement vector clock in Distributed System.

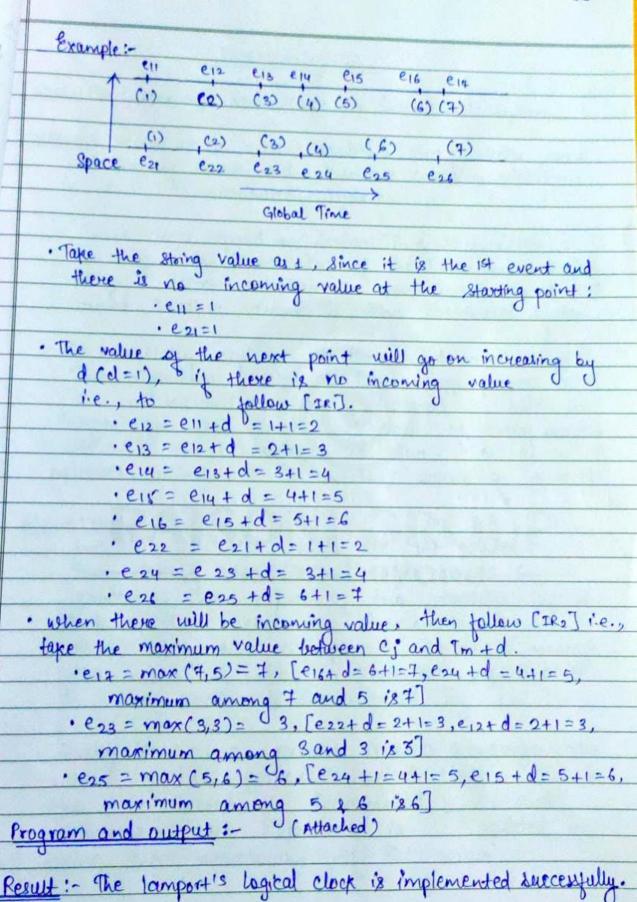
THEORY:- Vector clock is an algorithm that generates partial ordering of events and detects causality violations in a distributed system.

- · These clocks expand an Scalar Time to facilitate a causally consistent view of the distributed system. they detect whether a contributed event has caused another event in the distributed system.
- · It essentially captures all the causal relationships.

 This algorithm helps us label every process with a vector (a list of integers) with an integer for each local clock of every process within the system. So for N given processes, there will be vector/ array of size N.

ALGORITHM:

- · Initially, all the clocks are set to here.
 · Every time, an internal event occurs in a process, the value of the processes's logical clock in the vector is incremented by 1.
 · Also, every time a process sends a message, the value of the processes's logical clock in the vector is incremented by 1.
- · levery time, a process receives a message, the value of the processes's logical clock and in the vector is incremented by 1, and moreover, each element is updated by taking the maximum of the value in



ith element is circi and in this are	
jth element is cirji and contains Pi's for the current time in process pj.	latest value
o tweeter time in process pj.	

· d: drift time, generally dix 1.

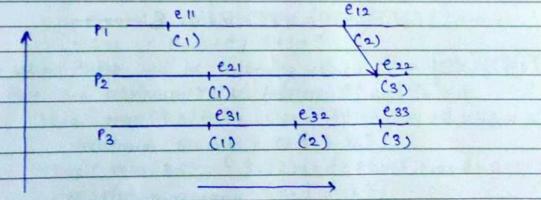
Implementation Rules [IR]:

- · [IRI]: & a > b ['a' happened before 'b' within the same process then, cicb)= cica) + d
- · [IR2]: Ci = max (Cj.tm+d) [sy there's more number of processes, then to = value of cica).

 Cj = max value between Cj and tm+d]

Limitation:

- · In case of [TRI], if a > b, then c(a) < c(b) -> true.
- be true of [IR2], if a > b. then c(a) < c(b) > May be true.



space time

AIM: To implement hamport's hagical clock.

THEORY: hamport's logical clock was created by heslie hamport. It is a procedure to determine the order of events occurring. It provides a basis for the more advanced vector clock algorithm.

Distributed operating system, hamport logical clock is needed.

ALGORITHM:

- · Happened before relation (->): a -> b, means 'a' happened before 'b'.
- · Logical clock: The criteria for the logical clocks
- · [ct]: Ci(a) < Ci(b), [Ci -> Logical Clock, If 'a'
 happened before 'b', then time of 'a'
 will be less than 'b' in a particular
- · [c2]: Ci(a) < Cj(b), [clock value of ci(a)
 is less than cj(b)].

Reference:

- · Process: Pi
- and j: jth event in the ith process.
- . + m; vector time span for message m.

- This example depicts the vector clocks mechanism in which the vector clocks are updated after execution of internal events, the arrows indicate I how the values of vectors are sent in between the processes (P1, P2, P3).
- · Vector clock algorithms are used in distributed systems to provide a councilly consistent ordering of events but the entire vector is sent to each process for every message sent, in order to keep the vector clock in sync.

PROGRAM & OUTPUT: (Attached)

RESULT:- The vector clock is implemented successfully.

____a vision bevond---

Practical No:1 Aim -> To demonstrate classical cipher. Encrypt the given Text message with the help of following algorithm. algorithm. Agent Toda apphabet with its equivalent 7-bit ASCII Code 2) swap first jour bites with last jour bits for each alphabets.

Write a hexadecimal equivalent for every four Theory -> Cryptography is a memod of Protecting information and communication through the use of codes. So that only those for whom the information is intended can read & process Cryptography rejers to secure information & Communication techniques derived from mathamatical Concepts and a set of rule-based Calcutions called algorithms to transform messages in that they are hard to deapher. Plain Text -> Information that can be directly read by humans or a machine. iper Text -> The encrypted Text. Encryption -> The process of converting the plain Text to Cipher Text. Decryption -> The process of reversing back ciphertext to Plaintext.

e. append(8[i][4:]) print (e) dy bin To Hexa(n):

num = int (n, 2) hex_num = hex (num) return (nex-num) print ("Hexadecimal Equivalent: ")
for i in range (Ien(e)): print (bin To Hexa (c[i]), end =" ") "Reagult" -> The program to demonstrate classical cipher executed successfully.

a) Replacing each alphabet with its equivalent
7 bit ASCII code b) swapping jirst jour bits with last four bits for each alphabets.

2) Obtaining a hexadecimal equivalent for every jour bits is implemented success jump.

```
Program -> num=input ("Enter the &tring")
 ascij values - Lord (character) for character in num)
 print ("Equivalent agai value of string: ", acii-values)
  3H1 = 1; F=1; C=1; SW=1; S=0
    dy decimal To Binary (n):
          return bin(n). replace ("b", "")
  For in num:
        Num: ord(i)
        ch = decimal to Binary ( Num)
        Str1. append (ch)
 print ("Refore swapping: ", str1)
   for jin range (lentarin): #-> for reversing each bing char
    tor I in SHILL
              C.inscrt(0,1)
            f-append(c)
          # print ("rev: , f)
               0 = 0
tori in range (1cn(f1); # swaping first 4 bits with last 4 bits
  of each character for j in f[i]:
                 if lence) < 4;
                    e.ingert (oii)
            else:
                e. in {crt (4, i)
            (e)
      print ("Ayer swapping", sw)
           for in range (ien (2w)):

3. append ("".join (sw[i]))
          # print(s)
               C= [
```

Practical No -> 4.

Aim -> Write a program to implement Ricart's Agarwala Algorithm. Theory -> Richart - Agarwala Algorithm is an algorithm for Mutual exclusion in a distibuted system Proposed by Gleen Ricart and Ashok Agarwala. This algorithm is an extension and Optimization of Lamports Distibuted Mutual Excellision Algorithm. like Lamports algorithm, it also follow permission based approch to ensure Mutical Exelusion.

Algorithms ->

- 1) To enter the Critical Section (CS) :- When a site Si Wants to enter the critical section, it send a times tamped REQUEST section message to au Other sites.

- When a site Sj recives a REQUEST message to site Si if and only if—

 ① site Sy is neither requesting nor Currently executing the critical section

 ② In case site Sj is requesting, the timestamp of site Si's request is smaller than its own request.

Otherwise the request is deferred by

2) To Execute the Critical Section >> Site Si enters the Critical Section if it has recived the REPLIN message from all Other eites.

	Prac	ctical	L NO->4	-		
a	program	to	implement	Ricart	Agarwaia	muha

exclusion algorithm. of all satisfies all the at 11 by Ricart - Agarwala Mutual Exclusion Algorithms

Requesting the CS-> time stamped SI Requesting (mag) & Si

Si Replay msg Sin Sin repeal grant provide page to hover to and

- 1) If sj neither requesting
 2) Nor Executing
- 3 If requesting but long timestamp

Mulant Exclusion

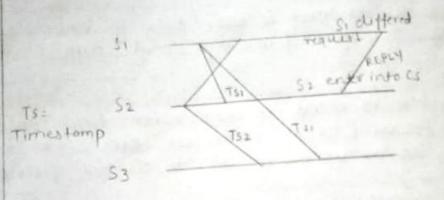
- 2) Executing the CS -> nod a/ Si enter the CS after recived REPLY msg from au sites.
- result The program to implificant 3> Releasing the CS ->

Aim-> Write

> [3] { dejerted msg (Request)

Aim: Write a program to implement Ricard's
Agrawala mulual Exclusion algorithm

PAM: optimization of the lamport algorithm



condition

D Executing the Cs

SI enter the Cs after received

REPLY msg from all sites

3 Releasing the Cs A.
Si REPLY Sj

server by the client's operating system

* The message es passed to the server stub

by the server operating system.

* The panameter are tremoved from the

message by the server stub

* Then the server procedure is called by the

server stub.

Result: Thus a program to implement chent server based program using the RPC is executed successfully Aim: Write a client server program based on RPC

Theory: RPC: Remote Procedure cau is a soft ware communication protocol that one program can use to request a service from a program located in another computer on a network without having to understand the network defails

server client model. The chent server model is distributed application structure that partitions task or workload between the providers of a resource or service called server's and a service requesters called clients

A client has a request message that the RPC translates and sends to the server this request may be a procedure or a function call to a remote server when the server receives the request, it sends the required response back to the client The client is blocked while the server is processing the call and only resumed execution after the server is finished.

The sequence of events in a resum remote procedure call are given below

* The client stub is called by the client

I the client stub makes a system call to
the message to the server and puts the
parameters in the message

* The message is sent from the client to the

the sites in the request set(R) and places the request in the request queue [(ts., 1)] is the times tamp of the request

by when a site si receives the request (tsi, 1) may from site si, 11 return the REPLY may to si and places si's request in request queue ey s

Executing the critical section: Site Si enters the Cs when the two following cond are on hold

[Li] Si has recieved a msg with timestamp lauger than (tsii) from all the sites [Li] Si is request is at to of the queue

Releasing (ritical Section: Site SI, upon exitions the 1's removes request from the top of grequest queue and send the timestamp release may to all sites in request when sites a selease may from site SI.

If removes Si's request from its quant

Result: The program to implement lamport's mutual exclusion algorithm is studied and executed successfully

Alm: Write a program to implement Ricard's Agrawala mutual exclusion algorithm

Theory. Ricard's Agrawala mutual exclusion algorithm is the optimization of lamport's algorithm that dispress with RELEASE message by clearly menging them with REPLY message in this algorithm for all 1<=1<- N Ri = 351,52, - SN }

Implementation: Requesting the critical section i) when a site si wants to enter the (s, it sends a timestamped REQUEST message to all the in its regliest set of when a site si receives the REQUEST mag from site Silit send a REPLY message to site si if the site si is neither requesting nor executing the cs or if the site sj is requesting and si's own request's timestamp. The request is deferred otherwise Executing the intical section 31 Site Si enters the Cs after it has received REPLY message from all flue sites in its request set releasing the critical section AT When site Sj. exits the Cs. it sends REPLY message to all the deferred requests A sit's REPLY message are blocked only by sites that are requesting the cs with higher priority Theor when a site sends out REPLY message to all the deferred requests The site sends out REPLY message to all the deffered request. The site with the next highest priority request recieves the last needed REPRY message and enters the cs. The execution of cs request Alm : Write a program to implement lamport's mutual exclusion algorithm

Theory : A mutual exclusion is a program object that present simultaneously access 7 the phased resources. Their concept is used In concurrent programming with a critical section a piece of tode in tolich process accessed.

Requirments of mutual exclusion algorithm of freedom from Stauvation at fairness

classification of mutual exclusion algorithm

if Token based algorithm

a) Non-Token based algorithm

critical section : When more than one process access a same code segment that segment is known as critical section

Basic idea of non-token based algorithm The sites have there states of Requesting the critical section of Executing the critical section 3] Releasing the critical section

lamport's mutual Exclusion.

Requesting critical section
of when site sicents to enter the cs it sends

in this algorithm is always in the order of their timestamps Result Thus a program to implement Ricard's Agrawala mutual exclusion algorithm is executed successfully.

Practical No. 7 dim: INIAP to implement Chandy's Missais Haus deadlock detection algarithm. Theosey: Chandy's - Missa - Hous's alistribuled alecallock detection also is an edge chasing also to detect deadlock on distributed system. In edg chasing algo, a special mag called probe is used in dedallook detection. A probe is a triplet (is k) which denotes that process pi hous initiated the deadlock detection a the may u being sent by the home site of process Pj to the home site of process Pk. Algorithm ? Process of sending prope:

Toprocess Pi is locally dependent on itself then declare a devidlock Else for all Pj and Pk check following condition: · a) Process Pi in locally dependent on sprocess · b) Process Pj is waiting on process Pk. of Process Pj and process Pk are on different stes. probe(i, j, k) to the home site of process Pk On the receipt of prob (i,j, k) at home sike of Process Pre in blocked allowing conditions: 1. · a) dependent is [i] is false . 6) Process Pk has not replied to all requests of · c) process Pj. I all by the above conditions are found to be true then : -

Set dependent (i) to true. Now, If 1 == i then, declare the Pi is deadlock. Elge for all for 9 po check gallerenting conditions. Proces Pk is locally dependent on process Pm and · a) Process Pm is waiting lipen process Pn and Process pro and process pro aire on different sites. Send probe (i, m, n) to the home site of process 4. Po 14 above conditions satisfy. Thus, the probe may travels along the edges of transaction would fat (TWF) grouph and when the probe my leturn to its initialing process then il is said that depollock how been detected. Petfarmance: algorithm teguire at most exchange of m(n-1)/2 my to detect devallark there, mis no of processes and n is the number of sites. The delay in detecting the deadlock is O(n). Advantages : othere is no need for special data structure of probe meg, which is very small & involves only B integers a a two demensional boolean altay dependent is used in the deadlock defection process. Al each site, only a wittle computation is required a overhead is also low.

Disadvantages: Olu main disadvantage of a distributed defection algo is that all sites thay not aware of the processes invalved in the Odeadlock this makes resolution difficult. Also proof of correction of the algo is difficult. Result: Hence, we have successfully implemented Chamby Misrois - Haas's deadlock detection algo. RAISONI GROUP -- a vision beyondAin: WAP to implement sliding window protocal.

Theory: Klindow Sliding Technique: INST is a computational technique which cims to reduce the use of nested loser and explace it with a single loop, thereby reducing the time complexity.

What is sliding Window? Consider a long chain connected together. Suppose you want to apply all in the complete chair with your hands, without pouring the ail from above.

One way to do so it to is to:

pick some ail

apply anto a section of chain,

then again pick some out.

then apply it to the next section where call is not applied yet.

and so on till the complete chain is oiled.

dhe second way is known as the slicing window technique and the portion which if slided from one end to end, is known as sliding Window.

Presiqueite to use sliding window technique. the use of slicting window technique can be done in a very specific scenatio, where the size of window for computation is fixed throughout the complete nested loop only then the Time complexity can be reduced.



deplying Slicking window technique:

We compute the sum of first k elements

and of n terms using a linear loop a store

the sum in variable window sum.

Then we will graze linearly over the

array till it treaches the end a simultaneously

keep track of maximum sum.

To get the current sum of block of k elements

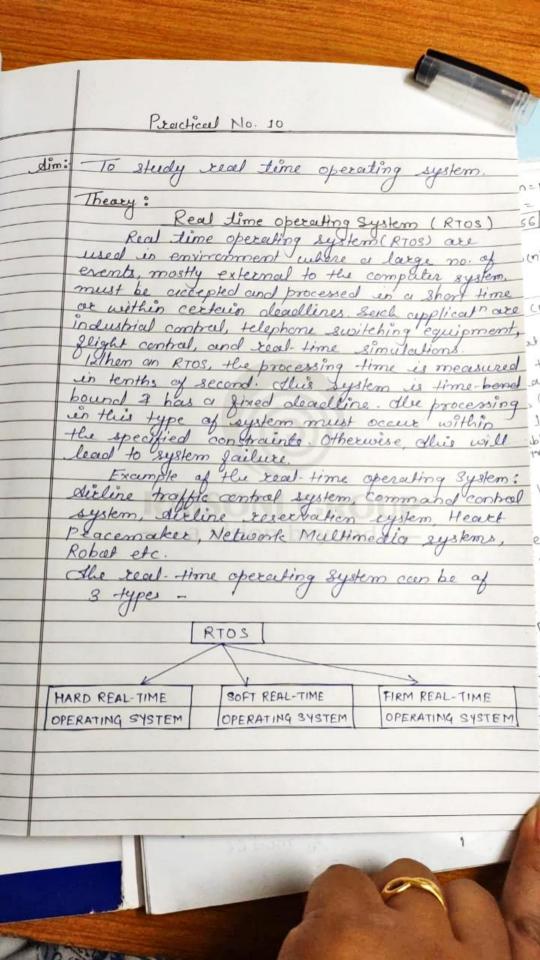
just submack the first element grom the

previous block a add the last element of

the current block. 2. the current block. Result: Hence we have successfully impleme-nted slicting window technique. RAISONI GROUP -a vision beyond-

Advantages of plots: Energy efficiency: - pBFT can achieve distributed consensus without carrying and complex mathematical computation. Zilliga employs pBFT in combination with Pow-like complex computation round for every 100th block. Transaction finality: The transaction do not require finalized and agreed upon. Low reward variance: Freezy mode in the network takes part in responding to the request by
the client g hence every mode can be incentiving leading to low variance in rewarding the nodes that help in decision making. Result: Hence, we have successfully learned about solution of Byzantine agreement problem.

Practical No. 9 dim: INIAP for solution of Byzantine agreement problem Theaty:
Byzantine Agreement problem: In To the Byzantine Agreement problem, a single value Julich it to be agreed on is initialized by an arbitrary processar and all nonfaulty processoes have to degree on that value. Solution of Byzantine agreement problem: In general, a soln to an agreement problem must part three tests: tecmination, agreement, and validity. As application to the Byzantine General's problem, these three tests are : a solution her to guarantee that all correct processes eventually reach a decision regarding the value of the order they have been given. all correct processes have to decide on the same value of the order they have been given. If the source process Is a correct proces, all processes have to decide on the relue that was axiginal given by the source process. Type of Byzantine failures: can considered. One is fail - stop other is arbitrary - node failure. Some of the arbitrare node gailures are given below: Respond with an incarrect result. Respond with a deliberately misleading result Respond with a different result to different parts of the system



HARD Real-Time operating system : system quatantee that critical task be completed within a large of time. for ex, a topot is hired to weld a car body. If the respect welds too early or too late, the car cannot be sald, so it is a hard read time? system that requires complete care welding by xabet hardly on the time. Soft real time operating system: this operating w system provides some relaxation in the time limit Forex, - Multimedia systems, digital audio system; etc. Explicit, programmer defined a controlled processes are encountered in real time system A separate process is changed with handling a single external event. Also process is activated upon occurrence of the related event signalledy an interrupt. Multitasking operation is accomplished by scheduling processes for execution independ-Tently of each other. Each process is assigned a certain level of priority that corresponds to the relative importance of the event that it services. Other processor is allocated to the highest priority processes. This type of schedule called priority-based preemptive scheduling is used by xoul-time system.

Firm Real-time Operating System: type have to follow deadline as well. In spite have unintended consequences, including a eduction in the quality of the product. Ex. Multimedia applications. -Advantages: af real-time operating systems are as fallows: Maximum Consumption. Maximum utilization of devices and systems. Alus more output from Task Shifting Time assigned for shifting tasks in these system is very less for ex, in order older systems, it takes about 10 microsecond. Shifting one task to another & in the latest systems it takes 3 microseconds. Focus On Application focus on running application and less impostence to applications that are in the Real-Time Operating System In Embedded System-Since the size of program is small, RTDS can also be embedded systems like in transport and others.

These type of systems are error - free. 6. Memory allocation - Mem in these types of systems. Disadvantages:

Operating systems are as fallows

e=1 1. Limited Tasks and their concentration is very less on few applies, * outions to avail extors. Use Heavy System Resource - Sometime the system resources are not so good and they are expensive as well. Complex Algorithms difficult for the designer to write on. Device Driver And Interrupt Signals - It needs 4. specific device drivers and interrupts signals to respond earliest to interrupts. Thread Priority
It is not good to set thread

priority as these systems are very less prone

to switching teusks AND THE PARTY OF T

6. Minimum Switching - RTOS performs minimal task switching. Result: Hence we have successfully loarned about real-time operating system. RAISONI GROUP — a vision beyond—