

Static analysis

Software Analysis Topic 5

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Today's menu

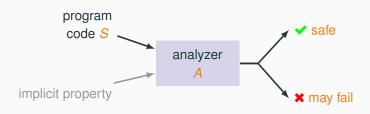
Data-flow analysis

Abstract interpretation

Type systems

Static analysis in practice

Static analysis: the very idea



Static analysis:

- analyzes real program code
- each analyzer targets a fixed set of hard-coded properties (compromise on flexibility)
- the output reports safe/unsafe for each program location individually
- is completely automatic
- · is sound but incomplete

Static analysis: checked properties

The properties that are checked by static analysis are often general safety properties – stating the absence of errors of a certain kind:

- · integer variables do not overflow
- there are no type errors
- there are no null-pointer dereferencing
- there are no out-of-bound array accesses
- there are no race conditions

Static analysis: this lecture

Static analysis is a vast field that has developed many techniques. Every software analysis technique that is static can be seen as a form of static analysis – although it may not be called that way.

Other names for the whole field are program analysis (which is often implicitly static by default) or software analysis.

Static analysis: this lecture

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In this lecture we have a look at three classic static analysis techniques:

data-flow analysis approximates the behavior of programs on their <u>control-flow</u> graph

abstract interpretation is a general framework to define and check the correctness of static analyses

type systems are a widely used form of static analysis to reason about the <u>values</u> expressions may have at runtime

Using static analysis

Static analysis has numerous applications:

avoiding bugs/verification: checking the absence of erroneous

behavior such as overflows, division by zero, and out-of-bound array access

security: checking the enforcement of security

properties such as non-interference

compiler optimization: improving the efficiency of programs at

compile time based on the static information about their behavior

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The most important thing I have done as a programmer in recent years is to aggressively pursue static code analysis.

John Carmack



Static vs. dynamic

Static

- at compile time before execution
- related to a program's code, or to any other (formal) model of the software
- · without executing the software
- on generic inputs

Dynamic:

- at run time during execution
- related to a program's behavior
- while executing the software
- on specific inputs

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"Software analysis" denotes techniques, methods, and tools useful to establish that some software behaves according to some properties.

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"Software analysis" denotes techniques, methods, and tools useful to establish that some software behaves according to some properties.

Therefore, static analysis infers properties of the dynamic behavior of programs without explicitly running them.

Static analysis: precision and expressiveness

Software analyses that target <u>undecidable properties</u> cannot be both sound and complete.

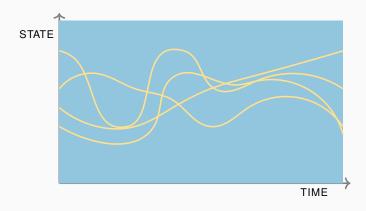
There is also a trade-off between soundness, expressiveness, and automation.

Static analysis:

- achieves soundness but gives up <u>completeness</u> that is static analysis is <u>imprecise</u>
- targets fixed properties of certain kinds (such as control-flow properties) – thus giving up <u>expressiveness</u> while keeping automation

Program behavior

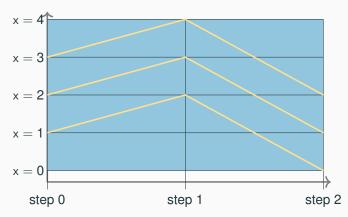
The (generic) behavior of a program consists of all its possible executions as sequences of states:



Each line is a different execution.

Program behavior: example

```
assume 1 ≤ x ≤ 3
// step 0
x := x + 1
// step 1
x := x - 2
// step 2
```



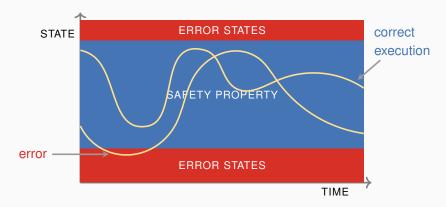
Safety properties and error states

A safety property is a set of program states that characterize correct executions. Its complement is the set of error states.



Safety properties and error states

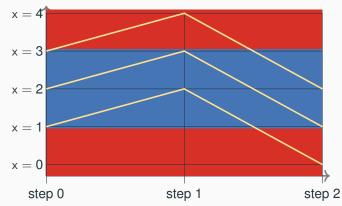
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An execution is correct (safe) iff it never enters an error state.

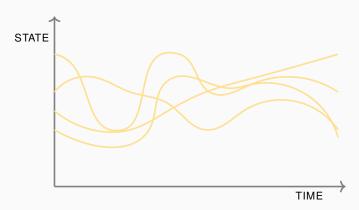
Safety properties and error states: example

```
assume 1 \leq x \leq 3 
// step 0 
x := x + 1 
// step 1 
Safety property: 1 \leq x \leq 3 
Error states: x < 1 \vee x > 3 
x := x - 2 
// step 2
```



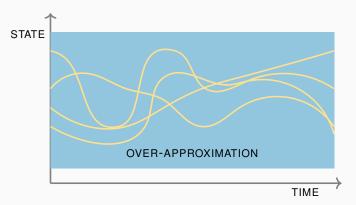
Approximations

An abstraction is an approximation of the behavior – typically in the form of a set of reachable states – which is easier to analyze than the concrete behavior.



Approximations

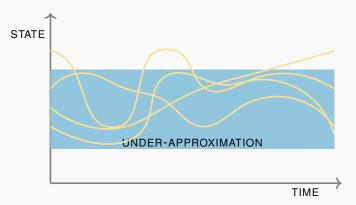
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An over-approximation is a superset of all possible executions: it includes <u>all</u> concrete executions but may <u>also</u> include executions that are not feasible.

Approximations

An abstraction is an approximation of the behavior – typically in the form of a set of reachable states – which is easier to analyze than the concrete behavior.



An under-approximation is a subset of all possible executions: it includes <u>no</u> executions that are unfeasible, but may not include all concrete executions.

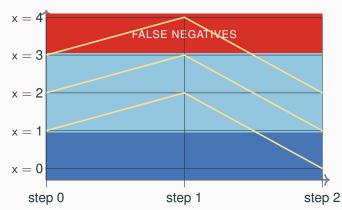
Under-approximation: example

```
assume 1 ≤ x ≤ 3
// step 0
x := x + 1
// step 1
x := x - 2
// step 2
```

Safety property: $x \le 3$

Error states: x > 3

Under-approximation: $1 \le x \le 3$



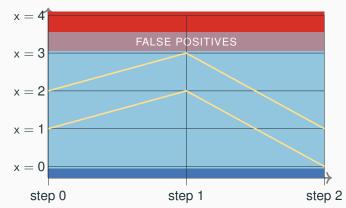
Over-approximation: example

```
assume 1 ≤ x ≤ 2
// step 0
x := x + 1
// step 1
x := x - 2
// step 2
```

Safety property: $x \leq 3$

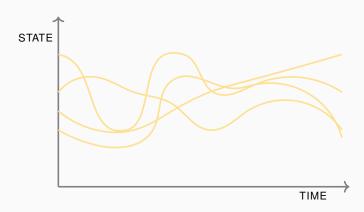
Error states: x > 3

Over-approximation: $0 \le x \le 3.5$



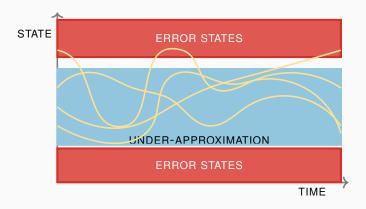
Soundness and precision

Static analysis is based on over-approximations to be sound – possibly sacrificing precision (completeness).



Soundness and precision

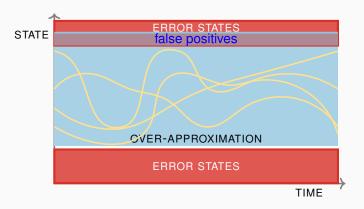
Static analysis is based on over-approximations to be sound – possibly sacrificing precision (completeness).



An analysis based on <u>under-approximations</u> is <u>unsound</u>: it may miss <u>errors</u> (generate false negatives).

Soundness and precision

Static analysis is based on over-approximations to be sound – possibly sacrificing precision (completeness).



An analysis based on over-approximations is imprecise: it may report spurious errors (generate false positives).

Precision vs. efficiency

When designing a static analysis, precision is often traded-off against efficiency:

- · perfect precision is often impossible due to undecidability
- even for decidable problems, high precision may still be too computationally expensive
- low precision leads to many false positives, which users have to identified as such manually

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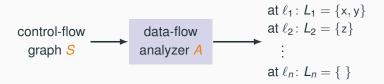
Designing a static analysis requires to balance precision and efficiency in a way that is practical.

Data-flow analysis

What is a data-flow analysis

A data-flow analysis is a kind of static analysis that:

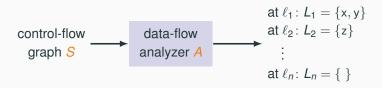
- works on the control-flow graph of the input program
- derives information about the data flow: what values are read (used) and written (defined) at specific program points



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A data-flow analysis is a kind of static analysis that:

- works on the control-flow graph of the input program
- derives information about the data flow: what values are read (used) and written (defined) at specific program points



The property under analysis is derivable from the analysis's output. Example: live variables analysis.

output: for each program point which variables are live – will be read before being overwritten

property: is variable v live at ℓ_k ? Check if $v \in L_k$

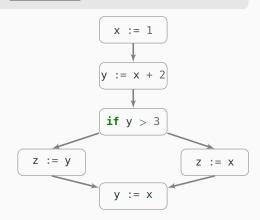
Data-flow analysis

Control-flow graphs

Control-flow graphs

The control-flow graph (CFG) of a program is a directed graph representing possible execution paths:

- · each statement corresponds to a node in the graph
- · edges connect nodes of consecutive statements



We define the control-flow graphs of Helium programs – ignoring declarations since they do not affect the program state which is what static analysis targets.

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Each atomic statement corresponds to a single CFG node.

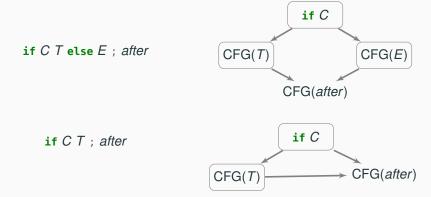
skip

$$v_1, ..., v_n := E_1, ..., E_n$$

$$\left(v_1,\ldots,v_n:=E_1,\ldots,E_n\right)$$

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Each conditional statement introduces a branch.



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Each loop statement introduces a branch and a loop.

while CB; after

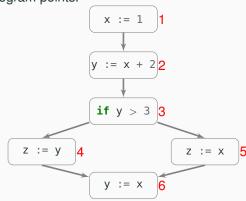


Labels

We label statements (and expressions of conditionals and loops) to be able to refer to specific program points.

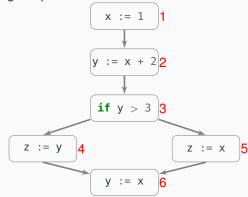
{
$$x := 1 }^{1}$$

{ $y := x + 2 }^{2}$
if $(y > 3)^{3}$
{ $z := y }^{4}$
else
{ $z := x }^{5}$
{ $y := x }^{6}$



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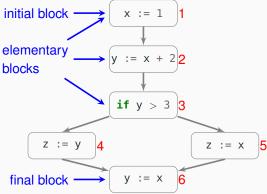


elementary block: a labeled node in the CFG (also: program point)
initial block: block where execution begins

final block: block after which execution terminates

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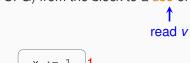
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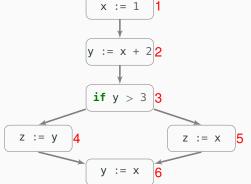
Data-flow analysis

Live variables analysis

A variable v is live at the <u>exit</u> from a block if there is some path (on the CFG) from the block to a <u>use</u> of v that does not redefine v.



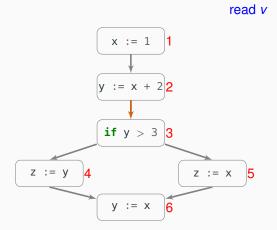




Examples:

- y at 2:
- z at 4:

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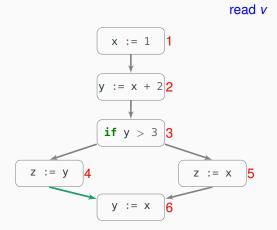


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write v

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Examples:

- y at 2: live
- z at 4: not live

write v

Live variables analysis

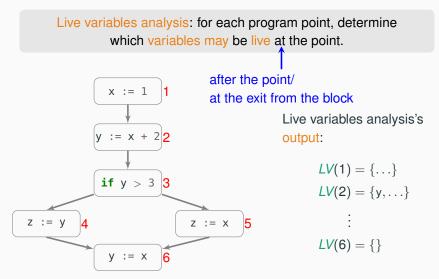
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Live variables analysis: for each program point, determine which variables may be live at the point.

after the point/ at the exit from the block

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Live variables analysis: for each program point, determine which variables may be live at the point.

over-approximation after the point/ at the exit from the block

A may analysis is an over-approximation: LV(k) is a superset of the live variables at k.

- if x ∈ LV(k) x may or may not be <u>live</u> at k (for example because it is live along certain paths but not live along others)
- if $y \notin LV(k)$ y is definitely not live at k

The analysis has to be <u>sound</u>, and then as <u>precise</u> as possible given the information available in the CFG.

Live variables analysis: applications

A variable v is live at the <u>exit</u> from a block if there is some path (on the CFG) from the block to a <u>use</u> of v that does not redefine v.

If a variable v is not live after it is defined in an assignment, the assignment is useless and can be removed without changing program behavior.

Dead assignment elimination: any block *k* such that:

- 1. k is an assignment to variable v
- 2. v is not live at k that is, $k \notin LV(k)$

can be eliminated without affecting program behavior.

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 ${ x := 7 }^{2}$
if z > y
y := x

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```
\{x := 4\}^1
\{x := 7\}^2

if z > y
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x \text{ not live here:}
the assignment is useless

x \text{ may be live here:}
the assignment is possibly useful
```

Record the possibly live variables at the entry and exit of every elementary block.



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$$\begin{array}{c}
\downarrow LV_{\text{IN}}(3) \\
y := x \\
\downarrow LV_{\text{OUT}}(3)
\end{array}$$

For each block, relate LV_{IN} to LV_{OUT} .

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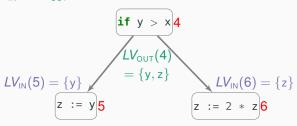
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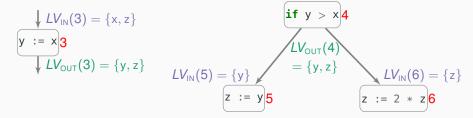
$$\bigvee_{\mathbf{y} := \mathbf{x}} \frac{LV_{\text{IN}}(3)}{3}$$

$$\bigvee_{\mathbf{y} \in LV_{\text{OUT}}(3)} \frac{LV_{\text{OUT}}(3)}{3}$$

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Work backward from the exit block to the entry block.

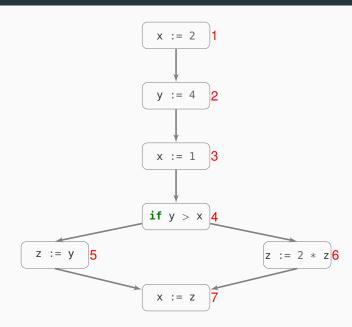
$$LV_{ extsf{IN}}(k) = (LV_{ extsf{OUT}}(k) \setminus ext{``assigned at k''}) \cup ext{``used at k''}$$
 $LV_{ extsf{OUT}}(k) = \bigcup_{\substack{h ext{ direct successor of k}}} LV_{ extsf{IN}}(h)$

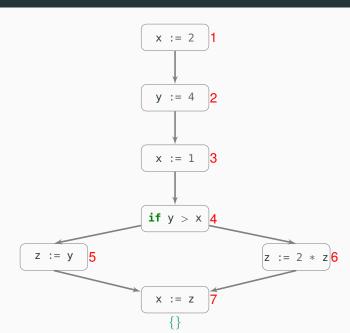
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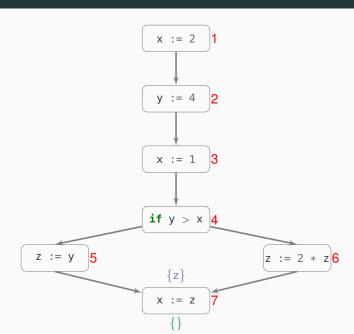
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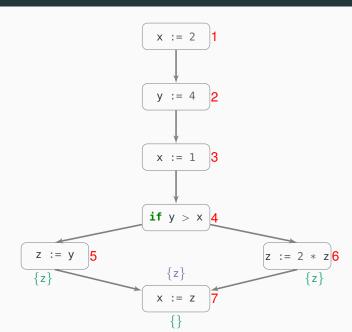
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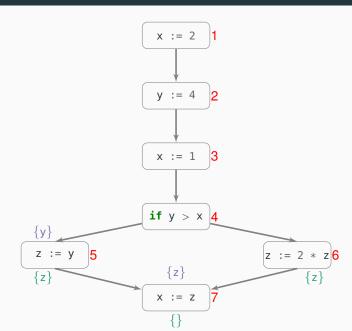
The analysis's final output is: $LV(1) = LV_{OUT}(1), \dots, LV(n) = LV_{OUT}(n)$

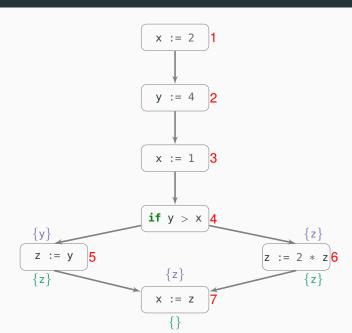


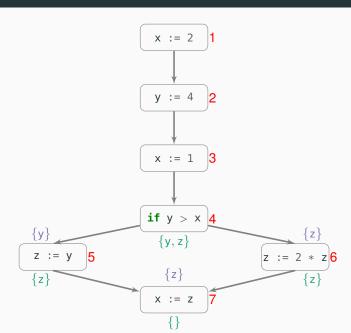


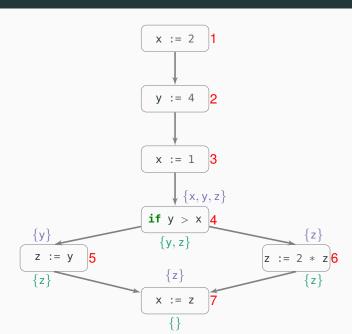


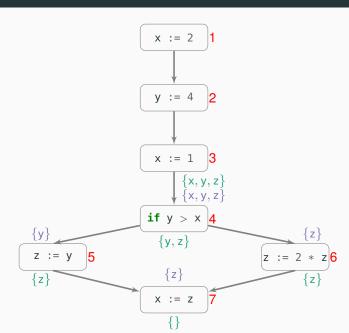


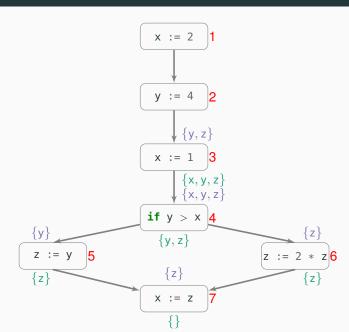


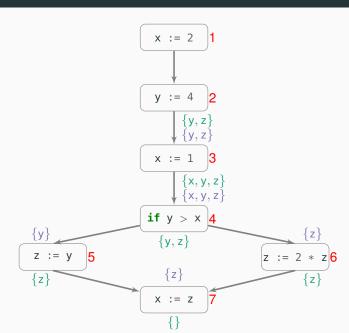


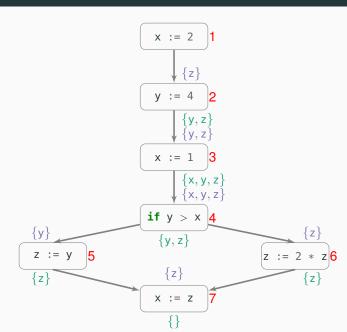


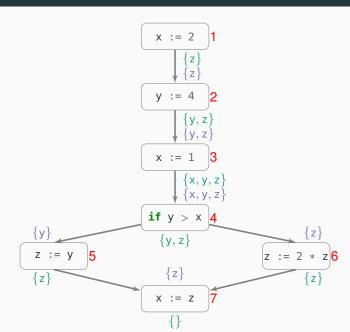


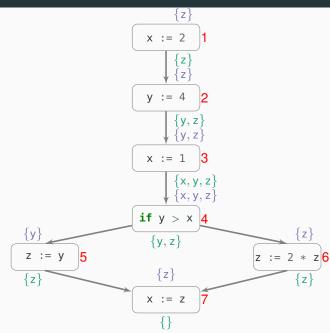


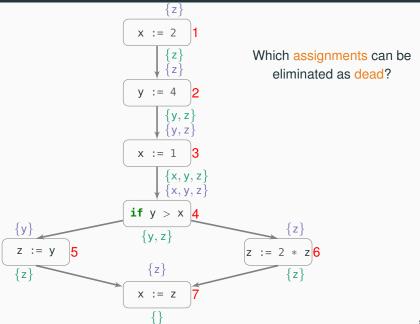


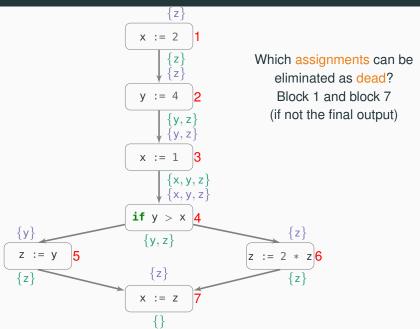












Formalizing data-flow analyses

We formalize the idea of live variables analysis as an equation system:

- $LV_{IN}(k)$ and $LV_{OUT}(k)$ are variables
- the equations formalize the relations:

$$LV_{\mathsf{IN}}(k) = (LV_{\mathsf{OUT}}(k) \setminus \text{``assigned at } k\text{''}) \cup \text{``used at } k\text{''}$$
 $LV_{\mathsf{OUT}}(k) = \bigcup_{\substack{h \text{ direct successor of } k}} LV_{\mathsf{IN}}(h)$

for every possible block type (assignment or branch condition)

The analysis result is the solution of the equation system, which can be computed using standard algorithms.

Data-flow equations

for every node h that follows k in the CFG (i.e., h is a direct successor of k)

For every block k:

$$LV_{\text{OUT}}(k) = \bigcup_{(k \to h) \in \text{CFG}} LV_{\text{IN}}(h)$$

$$LV_{\text{IN}}(k) = \left(LV_{\text{OUT}}(k) \setminus \text{kill}_{LV}(k)\right) \cup \text{gen}_{LV}(k)$$

variables assigned at k

variables used at k

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variables assigned at k

variables used at k

If f is a final node, it has no successors, and hence $LV_{OUT}(f) = \{\}$.

Data-flow equations

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For every block *k*:

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 $LV_{\text{IN}}(k) = (LV_{\text{OUT}}(k)) \text{ kill}_{LV}(k)) \cup \text{gen}_{LV}(k)$

variables assigned at k

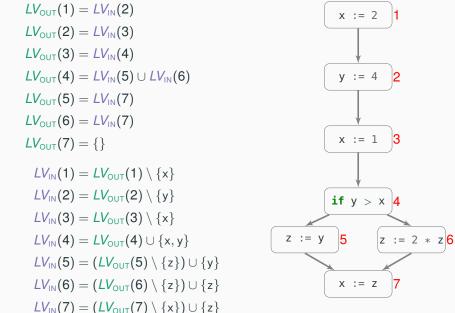
variables used at k

If f is a final node, it has no successors, and hence $LV_{OUT}(f) = \{\}$.

We define $kill_{LV}$ and gen_{LV} for every block type:

$$\begin{aligned} & \text{kill}_{LV}(\textbf{skip}) = \{\} \\ & \text{kill}_{LV}(\textbf{v} := E) = \{\textbf{v}\} \end{aligned} \qquad \begin{aligned} & \text{gen}_{LV}(\textbf{skip}) = \{\} \\ & \text{gen}_{LV}(\textbf{v} := E) = \{\textbf{x} \mid \textbf{x} \text{ is a (free) variable in } E\} \\ & \text{kill}_{LV}(\textbf{if/while } C) = \{\} \end{aligned} \qquad \begin{aligned} & \text{gen}_{LV}(\textbf{if/while } C) = \{\textbf{x} \mid \textbf{x} \text{ is a (free) variable in } C\} \end{aligned}$$

Equation system for live variables analysis: example



Data-flow analysis

Equation solving

$$\begin{split} LV_{\text{OUT}}(1) &= LV_{\text{IN}}(2) \\ LV_{\text{OUT}}(2) &= LV_{\text{IN}}(3) \\ LV_{\text{OUT}}(3) &= LV_{\text{IN}}(4) \\ LV_{\text{OUT}}(4) &= LV_{\text{IN}}(5) \cup LV_{\text{IN}}(6) \\ LV_{\text{OUT}}(5) &= LV_{\text{IN}}(7) \\ LV_{\text{OUT}}(6) &= LV_{\text{IN}}(7) \\ LV_{\text{OUT}}(7) &= \{\} \\ LV_{\text{IN}}(1) &= LV_{\text{OUT}}(1) \setminus \{x\} \\ LV_{\text{IN}}(2) &= LV_{\text{OUT}}(2) \setminus \{y\} \\ LV_{\text{IN}}(3) &= LV_{\text{OUT}}(3) \setminus \{x\} \\ LV_{\text{IN}}(4) &= LV_{\text{OUT}}(4) \cup \{x,y\} \\ LV_{\text{IN}}(5) &= (LV_{\text{OUT}}(5) \setminus \{z\}) \cup \{y\} \\ LV_{\text{IN}}(6) &= (LV_{\text{OUT}}(6) \setminus \{z\}) \cup \{z\} \\ LV_{\text{IN}}(7) &= (LV_{\text{OUT}}(7) \setminus \{x\}) \cup \{z\} \end{split}$$

 $LV_{OUT}(1) = LV_{IN}(2)$

$$LV_{\text{OUT}}(2) = LV_{\text{IN}}(3)$$
 $LV_{\text{OUT}}(3) = LV_{\text{IN}}(4)$
 $LV_{\text{OUT}}(4) = LV_{\text{IN}}(5) \cup LV_{\text{IN}}(6)$
 $LV_{\text{OUT}}(5) = LV_{\text{IN}}(7)$
 $LV_{\text{OUT}}(6) = LV_{\text{IN}}(7)$
 $LV_{\text{OUT}}(7) = \{\}$
 $LV_{\text{IN}}(1) = LV_{\text{OUT}}(1) \setminus \{x\}$
 $LV_{\text{IN}}(2) = LV_{\text{OUT}}(2) \setminus \{y\}$
 $LV_{\text{IN}}(3) = LV_{\text{OUT}}(3) \setminus \{x\}$
 $LV_{\text{IN}}(4) = LV_{\text{OUT}}(4) \cup \{x, y\}$

 $LV_{IN}(5) = (LV_{OUT}(5) \setminus \{z\}) \cup \{y\}$ $LV_{IN}(6) = (LV_{OUT}(6) \setminus \{z\}) \cup \{z\}$ $LV_{IN}(7) = (LV_{OUT}(7) \setminus \{x\}) \cup \{z\}$ The equations over variables $LV_{\text{OUT}}(1), LV_{\text{OUT}}(2), \dots, LV_{\text{IN}}(7)$ are formally equivalent to equations over set variables X_1, \dots, X_{14} .

$$LV_{\text{OUT}}(1) = LV_{\text{IN}}(2) \qquad \qquad X_1 = LV_{\text{IN}}(2) \\ LV_{\text{OUT}}(2) = LV_{\text{IN}}(3) \qquad \qquad X_2 = LV_{\text{IN}}(3) \\ LV_{\text{OUT}}(3) = LV_{\text{IN}}(4) \qquad \qquad X_3 = LV_{\text{IN}}(4) \\ LV_{\text{OUT}}(4) = LV_{\text{IN}}(5) \cup LV_{\text{IN}}(6) \qquad \qquad X_4 = LV_{\text{IN}}(5) \cup LV_{\text{IN}}(6) \\ LV_{\text{OUT}}(5) = LV_{\text{IN}}(7) \qquad \qquad X_5 = LV_{\text{IN}}(7) \\ LV_{\text{OUT}}(6) = LV_{\text{IN}}(7) \qquad \qquad X_6 = LV_{\text{IN}}(7) \\ LV_{\text{OUT}}(7) = \{\} \qquad \qquad X_7 = \{\} \\ LV_{\text{IN}}(1) = LV_{\text{OUT}}(1) \setminus \{x\} \qquad \qquad X_8 = LV_{\text{OUT}}(1) \setminus \{x\} \\ LV_{\text{IN}}(2) = LV_{\text{OUT}}(2) \setminus \{y\} \qquad \qquad X_9 = LV_{\text{OUT}}(2) \setminus \{y\} \\ LV_{\text{IN}}(3) = LV_{\text{OUT}}(3) \setminus \{x\} \qquad \qquad X_{10} = LV_{\text{OUT}}(3) \setminus \{x\} \\ LV_{\text{IN}}(4) = LV_{\text{OUT}}(4) \cup \{x,y\} \qquad \qquad X_{11} = LV_{\text{OUT}}(4) \cup \{x,y\} \\ LV_{\text{IN}}(5) = (LV_{\text{OUT}}(5) \setminus \{z\}) \cup \{y\} \qquad \qquad X_{12} = (LV_{\text{OUT}}(5) \setminus \{z\}) \cup \{y\} \\ LV_{\text{IN}}(6) = (LV_{\text{OUT}}(6) \setminus \{z\}) \cup \{z\} \qquad \qquad X_{13} = (LV_{\text{OUT}}(6) \setminus \{z\}) \cup \{z\} \\ LV_{\text{IN}}(7) = (LV_{\text{OUT}}(7) \setminus \{x\}) \cup \{z\} \qquad \qquad X_{14} = (LV_{\text{OUT}}(7) \setminus \{x\}) \cup \{z\}$$

 $LV_{IN}(7) = (LV_{OUT}(7) \setminus \{x\}) \cup \{z\}$

 $X_{14} = (X_7 \setminus \{x\}) \cup \{z\}$

 $LV_{OUT}(1) = LV_{IN}(2)$

 $LV_{IN}(6) = (LV_{OUT}(6) \setminus \{z\}) \cup \{z\}$

 $LV_{IN}(7) = (LV_{OUT}(7) \setminus \{x\}) \cup \{z\}$

$$LV_{\text{OUT}}(2) = LV_{\text{IN}}(3) \qquad \qquad X_2 = F_2(X_1, \dots, X_{14})$$

$$LV_{\text{OUT}}(3) = LV_{\text{IN}}(4) \qquad \qquad X_3 = F_3(X_1, \dots, X_{14})$$

$$LV_{\text{OUT}}(4) = LV_{\text{IN}}(5) \cup LV_{\text{IN}}(6) \qquad \qquad X_4 = F_4(X_1, \dots, X_{14})$$

$$LV_{\text{OUT}}(5) = LV_{\text{IN}}(7) \qquad \qquad X_5 = F_5(X_1, \dots, X_{14})$$

$$LV_{\text{OUT}}(6) = LV_{\text{IN}}(7) \qquad \qquad X_6 = F_6(X_1, \dots, X_{14})$$

$$LV_{\text{OUT}}(7) = \{\} \qquad \qquad X_7 = F_7(X_1, \dots, X_{14})$$

$$LV_{\text{IN}}(1) = LV_{\text{OUT}}(1) \setminus \{x\} \qquad \qquad X_8 = F_8(X_1, \dots, X_{14})$$

$$LV_{\text{IN}}(2) = LV_{\text{OUT}}(2) \setminus \{y\} \qquad \qquad X_9 = F_9(X_1, \dots, X_{14})$$

$$LV_{\text{IN}}(3) = LV_{\text{OUT}}(3) \setminus \{x\} \qquad \qquad X_{10} = F_{10}(X_1, \dots, X_{14})$$

$$LV_{\text{IN}}(4) = LV_{\text{OUT}}(4) \cup \{x, y\} \qquad \qquad X_{11} = F_{11}(X_1, \dots, X_{14})$$

$$LV_{\text{IN}}(5) = (LV_{\text{OUT}}(5) \setminus \{z\}) \cup \{y\} \qquad \qquad X_{12} = F_{12}(X_1, \dots, X_{14})$$

 $X_1 = F_1(X_1, \ldots, X_{14})$

 $X_{13} = F_{13}(X_1, \dots, X_{14})$

 $X_{14} = F_{14}(X_1, \ldots, X_{14})$

$$LV_{\text{OUT}}(1) = LV_{\text{IN}}(2)$$

$$LV_{\text{OUT}}(2) = LV_{\text{IN}}(3)$$

$$LV_{\text{OUT}}(3) = LV_{\text{IN}}(4)$$

$$LV_{\text{OUT}}(4) = LV_{\text{IN}}(5) \cup LV_{\text{IN}}(6)$$

$$LV_{\text{OUT}}(5) = LV_{\text{IN}}(7)$$

$$LV_{\text{OUT}}(6) = LV_{\text{IN}}(7)$$

$$LV_{\text{OUT}}(7) = \{\}$$

$$LV_{\text{IN}}(1) = LV_{\text{OUT}}(1) \setminus \{x\}$$

$$LV_{\text{IN}}(2) = LV_{\text{OUT}}(2) \setminus \{y\}$$

$$LV_{\text{IN}}(3) = LV_{\text{OUT}}(3) \setminus \{x\}$$

$$LV_{\text{IN}}(4) = LV_{\text{OUT}}(4) \cup \{x, y\}$$

$$LV_{\text{IN}}(5) = (LV_{\text{OUT}}(5) \setminus \{z\}) \cup \{y\}$$

$$LV_{\text{IN}}(6) = (LV_{\text{OUT}}(6) \setminus \{z\}) \cup \{z\}$$

$$LV_{\text{IN}}(7) = (LV_{\text{OUT}}(7) \setminus \{x\}) \cup \{z\}$$

$$\vec{X} = F(\vec{X})$$

- $\vec{X} = X_1, \dots, X_{14}$ is a vector of variables
- *F* is a vector function whose components are F_1, \ldots, F_{14}

Least solutions

$$X_{1} = X_{9}$$

$$X_{2} = X_{10}$$

$$X_{3} = X_{11}$$

$$X_{4} = X_{12} \cup X_{13}$$

$$X_{5} = X_{14}$$

$$X_{6} = X_{14}$$

$$X_{7} = \{\}$$

$$X_{8} = X_{1} \setminus \{x\}$$

$$X_{9} = X_{2} \setminus \{y\}$$

$$X_{10} = X_{3} \setminus \{x\}$$

$$X_{11} = X_{4} \cup \{x, y\}$$

$$X_{12} = (X_{5} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (X_{6} \setminus \{z\}) \cup \{z\}$$

 $X_{14} = (X_7 \setminus \{x\}) \cup \{z\}$

We already know a solution to this set of equations:

equations:
$$X_7 = \{\}$$

$$X_8, X_1, X_9, X_{13}, X_5, X_6, X_{14} = \{z\}$$

$$X_{12} = \{y\}$$

$$X_2, X_{10}, X_4 = \{y, z\}$$

$$X_3, X_{11} = \{x, y, z\}$$

Least solutions

 $X_1 = X_9$

$$X_2 = X_{10}$$
 $X_3 = X_{11}$
 $X_4 = X_{12} \cup X_{13}$
 $X_5 = X_{14}$
 $X_6 = X_{14}$
 $X_7 = \{\}$
 $X_8 = X_1 \setminus \{x\}$
 $X_9 = X_2 \setminus \{y\}$
 $X_{10} = X_3 \setminus \{x\}$

 $X_{11} = X_4 \cup \{x, y\}$

 $X_{12} = (X_5 \setminus \{z\}) \cup \{y\}$

 $X_{13} = (X_6 \setminus \{z\}) \cup \{z\}$

 $X_{14} = (X_7 \setminus \{x\}) \cup \{z\}$

We already know a solution to this set of equations:

$$X_7 = \{\}$$

$$X_8, X_1, X_9, X_{13}, X_5, X_6, X_{14} = \{z\}$$

$$X_{12} = \{y\}$$

$$X_2, X_{10}, X_4 = \{y, z\}$$

$$X_3, X_{11} = \{x, y, z\}$$

In this particular case, the equation system admits only this solution.

However, in more general cases, there may be more than one solution.

Intuitively, we want the least solution — that is the solution with the smallest sets — because it corresponds to a more precise (less conservative) analysis.

Partially ordered sets

By introducing an ordering of sets we can order solutions to data-flow equations from small to large – that is from more to less precise.

A partial ordering is a relation \sqsubseteq that is:

reflexive: $\forall d \bullet (d \sqsubseteq d)$

transitive: $\forall c, d, e \bullet (c \sqsubseteq d \land d \sqsubseteq e \Longrightarrow c \sqsubseteq e)$

anti-symmetric: $\forall c, d \bullet (c \sqsubseteq d \land d \sqsubseteq c \Longrightarrow c = d)$

A partially ordered set (poset) $\langle D, \sqsubseteq \rangle$ is a set D whose elements are partially ordered according to \sqsubseteq .

Some familiar examples of posets:

 $\langle \mathbb{R}, \leq \rangle$ the real numbers with the usual order

 $\langle \mathbb{N}, \leq \rangle \;$ the natural numbers (nonnegative integers) with the usual order

 $\langle \wp(S), \subseteq \rangle$ the power set $\wp(S)$ of S with the subset order

$$X_1 =$$

$$X_2 =$$

$$X_4 =$$

$$X_5 =$$

$$X_6 =$$

$$X_7 =$$

$$X_0 =$$

$$X_{10} =$$

$$X_{11} =$$

$$X_{12} =$$

$$X_{13} =$$

$$X_{14} =$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k :

$$\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$$

3. stop when $\vec{X}^{k+1} = \vec{X}^k$

\vec{X}^0

$$X_1 = \{\}$$

$$X_2 = \{\}$$

$$X_3 = \{\}$$

$$X_4 = \{\}$$

$$X_5 = \{\}$$

$$X_6 = \{\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{\}$$

$$X_{10} = \{\}$$

$$X_{11} = \{\}$$

$$X_{12} = \{\}$$

$$X_{13} = \{\}$$

$$X_{14} = \{\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset:
 - $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^0)$$

$$X_1 = \{\}$$
$$X_2 = \{\}$$

$$X_3 = \{\}$$

$$X_4 = \{\} \cup \{\}$$

$$X_5 = \{\}$$

$$X_6 = \{\}$$

$$X_7 = \{\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_9 = \{\} \setminus \{y\}$$

$$X_{10} = \{\} \setminus \{x\}$$

$$X_{11} = \{\} \cup \{x, y\}$$

$$X_{12} = (\{\} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (\{\} \setminus \{z\}) \cup \{z\}$$

$$X_{14} = (\{\} \setminus \{x\}) \cup \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \Box \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k :

$$\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$$

3. stop when $\vec{X}^{k+1} = \vec{X}^k$

\vec{X}^1

$$X_1 = \{\}$$

$$X_2 = \{\}$$

$$X_3 = \{\}$$

$$X_4 = \{\}$$

$$X_5 = \{\}$$

$$X_6 = \{\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{\}$$

$$X_{10} = \{\}$$

$$X_{11} = \{x, y\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k :
 - $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^1)$$

$$X_1 = \{\}$$
$$X_2 = \{\}$$

$$X_3 = \{x, y\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_6 - \{2\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_0 = \{\} \setminus \{y\}$$

$$X_{10} = \{\} \setminus \{x\}$$

$$X_{11} = \{\} \cup \{x, y\}$$

$$X_{12} = (\{\} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (\{\} \setminus \{z\}) \cup \{z\}$$

$$X_{14} = (\{\} \setminus \{x\}) \cup \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \Box \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset:
- $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

\vec{X}^2

$$X_1 = \{\}$$

$$X_2 = \{\}$$

$$X_3 = \{x, y\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{\}$$

$$X_{10} = \{\}$$

$$X_{11} = \{x, y\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k :

$$\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$$

3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^2)$$

$$X_1 = \{\}$$

$$X_2 = \{\}$$

$$X_3 = \{x, y\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_9 = \{\} \setminus \{y\}$$

$$X_{10} = \{x, y\} \setminus \{x\}$$

$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\{z\} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (\lbrace z \rbrace \setminus \lbrace z \rbrace) \cup \lbrace z \rbrace$$

$$X_{14} = (\{\} \setminus \{x\}) \cup \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \square \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

\vec{X}^3

$$X_1 = \{\}$$

$$X_2 = \{\}$$

$$X_3 = \{x, y\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_0 = \{\}$$

$$X_{10} = \{y\}$$

$$X_{11} = \{x, y, z\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{V}^0 = 0$
 - $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^3)$$

$$X_1 = \{\}$$
$$X_2 = \{y\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_9 = \{\} \setminus \{y\}$$

$$X_{10} = \{x, y\} \setminus \{x\}$$

$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\lbrace z \rbrace \setminus \lbrace z \rbrace) \cup \lbrace y \rbrace$$

$$X_{13} = (\lbrace z \rbrace \setminus \lbrace z \rbrace) \cup \lbrace z \rbrace$$

$$X_{14} = \quad \big(\big\{\big\} \setminus \big\{x\big\}\big) \cup \big\{z\big\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \square \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$\vec{X}^4$$

$$X_1 = \{\}$$
$$X_2 = \{y\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{\}$$

$$X_{10} = \{y\}$$

$$X_{11} = \{x, y, z\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

1. start with the least element of the poset:

$$\vec{X}^0 = \{\} \times \cdots \times \{\}$$

2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$

3. stop when
$$\vec{X}^{k+1} = \vec{X}^k$$

$$F(\vec{X}^4)$$

$$X_1 = \{\}$$
$$X_2 = \{y\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_9 = \{y\} \setminus \{y\}$$

$$X_{10} = \{x, y, z\} \setminus \{x\}$$

$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\{z\} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (\lbrace z \rbrace \setminus \lbrace z \rbrace) \cup \lbrace z \rbrace$$

$$X_{14} = (\{\} \setminus \{x\}) \cup \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \Box \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$\vec{X}^5$$

$$X_1 = \{\}$$

$$X_2 = \{y\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{\}$$

$$X_{10} = \{y, z\}$$

$$X_{11} = \{x, y, z\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
 iff $\forall k(X_k \subseteq Y_k)$

We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{V}^0 = 0$
 - $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^5)$$

$$X_1 = \{\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_9 = \{y\} \setminus \{y\}$$

$$X_{10} = \{x, y, z\} \setminus \{x\}$$

$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\{z\} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (\{z\} \setminus \{z\}) \cup \{z\}$$

$$X_{14} = (\{\} \setminus \{x\}) \cup \{z\}$$

Each variable $X_1, ..., X_{13}$ ranges over the poset $\langle \wp(\{x, y, z\}), \subseteq \rangle$.

Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \Box \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
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We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$\vec{X}^6$$

$$X_1 = \{\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{\}$$

$$X_{10} = \{y, z\}$$

$$X_{11} = \{x, y, z\}$$

 $X_{12} = \{y\}$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

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Vector variable \vec{X} also ranges over the poset $\langle \wp(\{x,y,z\})^{14}, \sqsubseteq \rangle$, where:

$$\vec{X} \sqsubseteq \vec{Y}$$
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- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
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$$F(\vec{X}^6)$$

$$X_1 = \{\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{ \} \setminus \{ x \}$$

$$X_9 = \{y, z\} \setminus \{y\}$$

$$X_{10} = \{x, y, z\} \setminus \{x\}$$

$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\{z\} \setminus \{z\}) \cup \{y\}$$

$$X_{13} = (\{z\} \setminus \{z\}) \cup \{z\}$$

$$X_{14} = (\{\} \setminus \{x\}) \cup \{z\}$$

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\vec{X}^7

$$X_1 = \{\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_9 = \{z\}$$

$$X_{10} = \{y, z\}$$

$$X_{11} = \{x, y, z\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

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- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^7)$$

$$X_1 = \{z\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\} \setminus \{x\}$$

$$X_{0} = \{ \} \setminus \{x \}$$

$$X_{0} = \{ y, z \} \setminus \{y \}$$

$$X_{10} = \{x, y, z\} \setminus \{x\}$$

$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\lbrace z \rbrace \setminus \lbrace z \rbrace) \cup \lbrace y \rbrace$$

$$X_{13} = (\{z\} \setminus \{z\}) \cup \{z\}$$

$$X_{13} = (\{z\} \setminus \{z\}) \cup \{z\}$$
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$$\vec{X}^8$$

$$X_1 = \{z\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{\}$$

$$X_0 = \{z\}$$

$$X_{10} = \{y, z\}$$

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$$X_{12} = \{y\}$$

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- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

$$F(\vec{X}^8)$$

$$X_1 = \{z\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{z\} \setminus \{x\}$$

$$X_{0} = \{z\} \setminus \{x\}$$

$$X_{0} = \{y, z\} \setminus \{y\}$$

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$$X_{11} = \{y, z\} \cup \{x, y\}$$

$$X_{12} = (\lbrace z \rbrace \setminus \lbrace z \rbrace) \cup \lbrace y \rbrace$$

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$$\vec{X}^9$$

$$X_1 = \{z\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y, z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

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$$X_{10} = \{y, z\}$$

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$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

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$$F(\vec{X}^9)$$

$$X_1 = \{z\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

$$X_4 = \{y\} \cup \{z\}$$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{z\} \setminus \{x\}$$

$$X_9 = \{y, z\} \setminus \{y\}$$

$$X_{10} = \{x, y, z\} \setminus \{x\}$$

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Fixed points

$$\vec{X}^{10}$$

$$X_1 = \{z\}$$

$$X_2 = \{y, z\}$$

$$X_2 = \{y, z\}$$

$$X_3 = \{x, y, z\}$$

 $X_4 = \{y, z\}$

$$X_5 = \{z\}$$

$$X_6 = \{z\}$$

$$X_7 = \{\}$$

$$X_8 = \{z\}$$

$$X_0 = \{z\}$$

$$X_{10} = \{y, z\}$$

$$X_{11} = \{x, y, z\}$$

$$X_{12} = \{y\}$$

$$X_{13} = \{z\}$$

$$X_{14} = \{z\}$$

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We can find a solution as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
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- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

A value \vec{X}^k such that $F(\vec{X}^k) = \vec{X}^k$ is called a fixed point of F.

Least fixed points and monotonicity

We can find a solution of the data-flow equations as follows:

- 1. start with the least element of the poset: $\vec{X}^0 = \{\} \times \cdots \times \{\}$
- 2. apply F to the current vector \vec{X}^k : $\vec{X}^{k+1} = F(\vec{X}^k) = F^{k+1}(\vec{X}^0)$
- 3. stop when $\vec{X}^{k+1} = \vec{X}^k$

We are interested in the least fixed point of F – that is the smallest according to \sqsubseteq .

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- Does the algorithm above always terminate?
- Does it find a least fixed point?

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- Does the algorithm above always terminate?
- Does it find a least fixed point?

F is a monotonic function: $\vec{X} \sqsubseteq \vec{Y}$ implies $F(\vec{X}) \sqsubseteq F(\vec{Y})$

Therefore, by induction on k: $F^k(\vec{X}^0) \sqsubseteq F^{k+1}(\vec{X}^0)$.

Since the poset $\wp(\{x,y,z\})^{14}$ is finite, F must have a fixed point: it cannot keep on generating new values (finite domain), and it cannot "jump up and down" (monotonicity).

Finally, the fixed point computed from the least element \vec{X}^0 has to be the least fixed point (again thanks to monotonicity).

Naive fixed point algorithm

The algorithm that iterates *F* until it finds a fixed point is guaranteed to terminate but may be inefficient, as it propagates only a few updates in each iteration.

```
// naive fixed point algorithm \vec{X} := \{\} \times \cdots \times \{\} while F(\vec{X}) \neq \vec{X} \vec{X} := F(\vec{X})
```

In our running example, the naive fixed point algorithm takes 10 iterations, which correspond to evaluating $140 = 10 \cdot 14$ equations.

Chaotic iteration

More efficient algorithms avoid recomputing all flow equations, while only propagating those that change. For example the chaotic iteration algorithm propagates one random component that changes in each iteration.

// chaotic iteration algorithm
$$X_1, \ldots, X_n := \{\}, \ldots, \{\}$$
 while $F_k(X_k) \neq X_k$ for any k $X_k := F_k(X_k)$

In our running example, the chaotic algorithm takes 15–19 iterations, each evaluating one equation; the exact number depends on the random order in which elements are computed.

Worklist algorithm

A more efficient algorithm uses a worklist: a stack of edges in the CFG that should be processed.

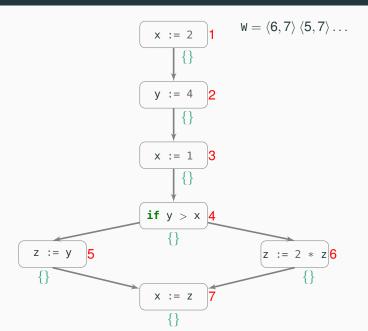
- For each edge \(\(\text{from}, \text{to} \) in the worklist, compute the data-flow equation for \(LV_{IN}(\text{to}) \). If it is not a fixed point:
 - Update $LV_{IN}(to) = LV_{OUT}(from)$ to the new value
 - Add all edges that lead to from to the top of the worklist, so that predecessors of from will be processed next

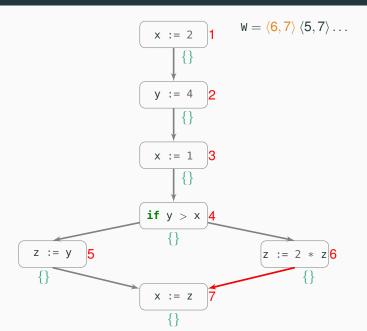
If an edge $\langle \ell, \ell' \rangle$ is in the worklist, it means that the result at block ℓ' has changed and must be propagated backward to its predecessors by computing the data-flow equation for block ℓ' .

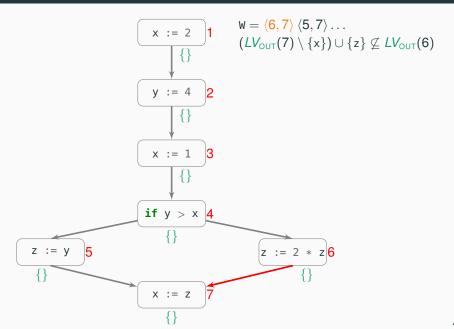
Worklist algorithm

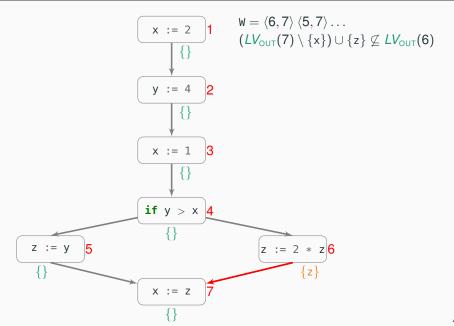
```
// worklist algorithm
LV_{OUT}(1), \ldots, LV_{OUT}(n) := \{\}, \ldots, \{\}
// the worklist initially includes all edges in the CFG
W := edges(CFG)
while W.length > 0
   ⟨from, to⟩ := W.pop() // remove top edge in worklist
                update equation for LV<sub>IN</sub>(to)
  if (LV_{OUT}(to) \setminus kill_{LV}(to)) \cup gen_{LV}(to) \not\subset LV_{OUT}(from)
     // update OUT of 'from'
      LV_{\text{OUT}}(\text{from}) := LV_{\text{OUT}}(\text{from}) \cup (LV_{\text{OUT}}(\text{to}) \setminus \text{kill}_{IV}(\text{to})) \cup \text{gen}_{IV}(\text{to})
      LV_{IN}(to) := LV_{OUT}(from) // IN of successor
     // add predecessors of 'from' to worklist
      for \langle before, from \rangle \in edges(CFG) W.push(\langle before, from \rangle)
// finally, update the IN of initial nodes
for i \in initial(CFG) LV_{IN}(i) := (LV_{OUT}(i) \setminus kill_{LV}(i)) \cup gen_{IV}(i)
```

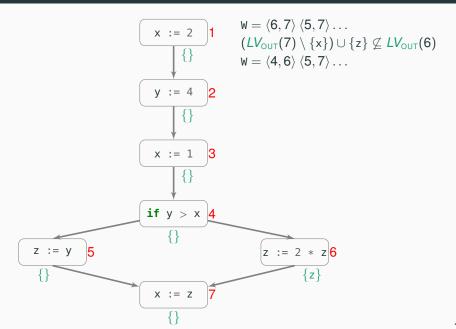
In our running example, the worklist algorithm takes 15 iterations, each evaluating one equation.

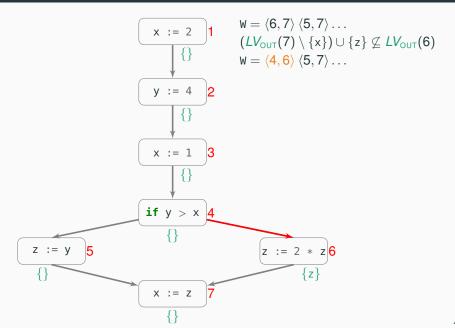


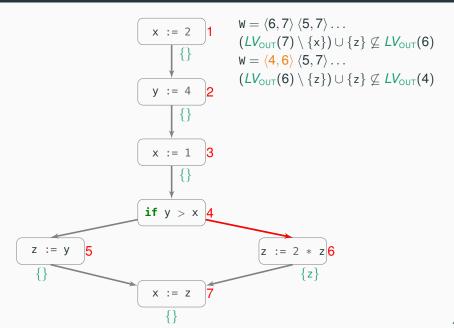


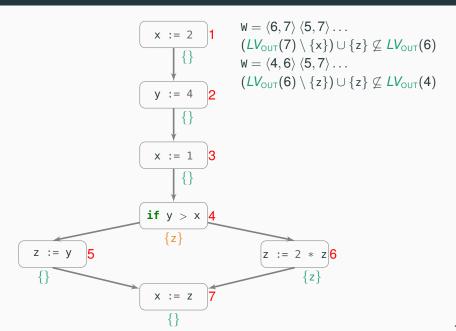


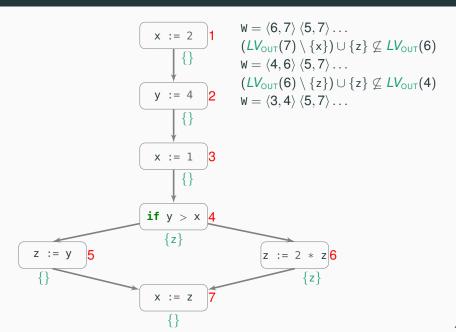


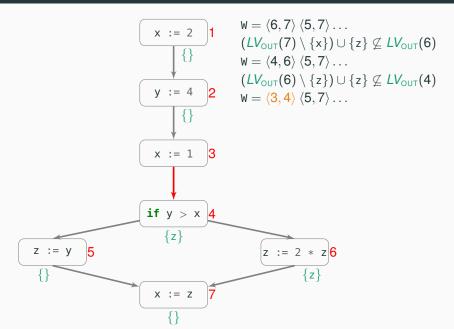


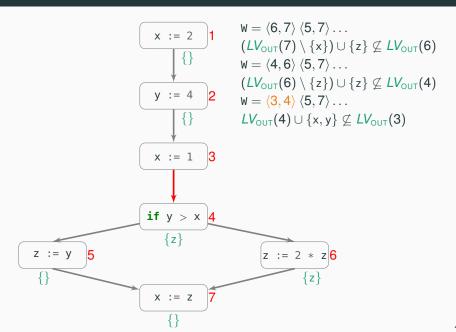


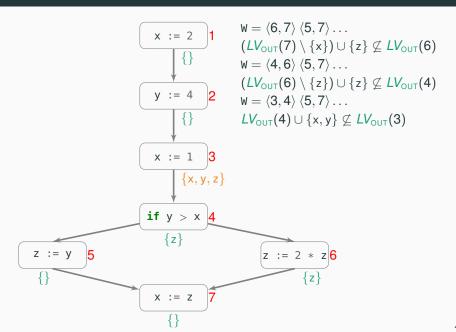












Data-flow analysis

Existence of solutions

Existence of solutions

Computing a data-flow analysis boils down to finding a least (smallest) fixed point of the vector equation:

$$\vec{X} = F(\vec{X})$$

- $\vec{X} = X_1, \dots, X_n$ is a vector of variables, each over domain $D = \wp(\mathcal{V})$, where \mathcal{V} is the set of program variables
- F is a vector function whose components are F_1, \ldots, F_n

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- F is a vector function whose components are F_1, \ldots, F_n

What properties of F and D guarantee that the data-flow equations have a least fixed point?

Complete lattices

A complete lattice is a poset $\langle D, \sqsubseteq \rangle$ such that every subset $S \sqsubseteq D$ of D has:

- a least upper bound (also: lub, join, or supremum) ⊔S
- a greatest lower bound (also: glb, meet, or infimum) $\sqcap S$

Complete lattices

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- a least upper bound (also: lub, join, or supremum) ⊔S
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- Element $u \in D$ is an upper bound of set $S \subseteq D$ if $s \sqsubseteq u$ for all $s \in S$
- The least upper bound

 S of a set S ⊆ D is
 the smallest of its upper bounds
- Element $d \in D$ is an lower bound of set $S \subseteq D$ if $d \sqsubseteq s$ for all $s \in S$
- The greatest upper bound □S of a set S ⊆ D is the largest of its bounds

Complete lattices

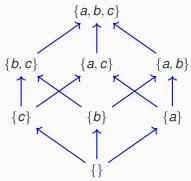
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- a greatest lower bound (also: glb, meet, or infimum) $\sqcap S$
- Element $u \in D$ is an upper bound of set $S \subseteq D$ if $s \sqsubseteq u$ for all $s \in S$
- The least upper bound $\sqcup S$ of a set $S \subseteq D$ is the smallest of its upper bounds
- Element $d \in D$ is an lower bound of set $S \subseteq D$ if $d \sqsubseteq s$ for all $s \in S$
- The greatest upper bound □S of a set S ⊆ D is the largest of its bounds

Every complete lattice is not empty, and has a least element \bot (bottom) and a greatest element \top (top).

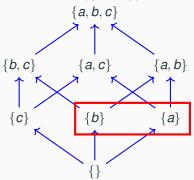
The powerset ordered with respect to the subset \subseteq relation is a complete lattice.

For example $\wp(\{a,b,c\})$:



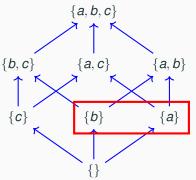
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The upper bounds of

$$S = \{\{a\}, \{b\}\} \subseteq D = \wp(\{a, b, c\}) \text{ are } \{a, b\} \text{ and } \{a, b, c\}.$$

The only lower bound of

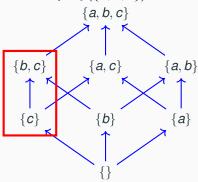
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The powerset ordered with respect to the subset \subseteq relation is a complete lattice.

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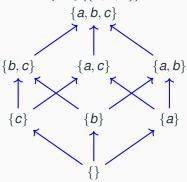
$$S = \{\{c\}, \{b, c\}\} \subseteq D = \wp(\{a, b, c\})$$
 is $\{a, b, c\}$.

The lower bounds of

$$S = \{\{c\}, \{b, c\}\} \subseteq D = \wp(\{a, b, c\})$$
 are $\{c\}$ and $\{\}$.

The powerset ordered with respect to the subset \subseteq relation is a complete lattice.

For example $\wp(\{a,b,c\})$:



The bottom (least element) is {}.

The top (greatest element) is $\{a, b, c\}$.

A function
$$F: D \to D$$
 is monotonic over poset $\langle D, \sqsubseteq \rangle$ if, for all $x, y \in D$, $x \sqsubseteq y$ implies $F(x) \sqsubseteq F(y)$

Intuitively: monotonic means that it respects the order relation.

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Tarski's fixed point theorem: let $F \colon D \to D$ be a monotonic function over complete lattice $\langle D, \sqsubseteq \rangle$. The set of all fixed points of F is also a complete lattice with respect to \sqsubseteq .

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Implications:

- F has at least one fixed point (because complete lattices cannot be empty)
- F has least and greatest fixed points (because its fixed points are a complete lattice)

Tarski's fixed point theorem

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It is Tarski who stated the result in its most general form, [but] some time earlier, Knaster and Tarski established the result for [a] special case.

Knaster-Tarski theorem on Wikipedia



Alfred Tarski

Applying Tarski's fixed point theorem

To apply Tarski's theorem to a data-flow analysis – guaranteeing the existence of a fixed point, which can then be found by iteration – we need to show:

monotonicity: the data-flow vector equation F is monotonic **complete lattice:** the analysis domain D is a complete lattice

Applying Tarski's fixed point theorem

To prove that F is monotonic, we just prove that each component function F_k is monotonic.

Equations of this form:

$$LV_{\text{OUT}}(k) = \bigcup_{(k \to h) \in \text{CFG}} LV_{\text{IN}}(h)$$

 $LV_{\text{IN}}(k) = (LV_{\text{OUT}}(k) \setminus \text{kill}_{LV}(k)) \cup \text{gen}_{LV}(k)$

are monotonic because \setminus and \cup are themselves monotonic.

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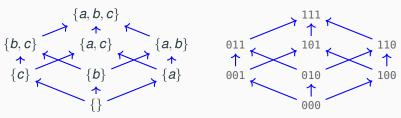
Whenever each variable's domain D_k is a powerset – typically the powerset $\wp(\mathcal{V})$ of the program variables – it is a complete lattice with respect to the subset relation \subseteq .

Then, the overall domain $D = D_1 \times \cdots \times D_n$ is also a complete lattice with respect to \sqsubseteq defined as $X \sqsubseteq Y$ iff $\forall k(X_k \subseteq Y_k)$.

Bit vectors

Elements of powerset $\wp(S)$ of a finite set S can be efficiently represented using bit vectors:

- the length of the bit vector is |S| = n
- an element $s \in \wp(S)$ is uniquely represented by the bit string b_1, \ldots, b_n where $b_k = 1$ iff the kth element of S belongs to s



Join and meet operations are then bitwise logic operations:

Data-flow analysis

Reaching definitions analysis

A definition (v, k) is an assignment to variable v at block k.

A definition (v, k) reaches block r if there is some path (on the CFG) from k to r that does not redefine v

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```
{ x := 5}<sup>1</sup> Examples: which definitions reach (the entry of) block 5?

while (x > 1)^3

{ y := x * y}<sup>4</sup>

{ x := x - 1}<sup>5</sup>
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```

Examples: which definitions reach (the entry of) block 5?

- in the first loop iteration: (x, 1) and (y, 4)
- in the following iterations: (x,5) and (y,4)

Reaching definitions analysis

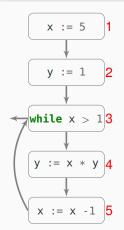
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Reaching definitions analysis's output:

$$\vdots \\ RD_{\,\text{IN}}(5) = RD_{\,\text{OUT}}(4) = \{(x,1),(x,5),(y,4)\}$$

Reaching definitions analysis

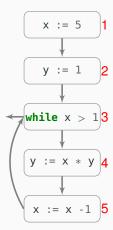
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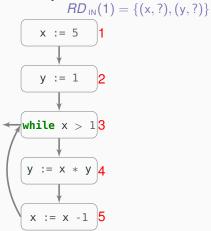
A may analysis is an over-approximation: RD(k) is a superset of the reaching definitions at k.

- if (x, ℓ) ∈ RD_{IN}(k), the definition of x at ℓ may or may not reach k
 (for example because it may be overwritten along certain paths
 but not along others)
- if $(x, \ell) \notin RD_{IN}(k)$, the definition of x at ℓ has definitely not reached k

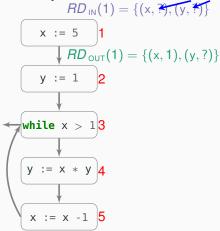
Working forward, record the reaching definitions at the entry and exit of every elementary block.



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$$RD_{IN}(1) = \{(x, 2), (y, 3)\}$$
 $x := 5$
 $RD_{OUT}(1) = \{(x, 1), (y, 2)\}$
 $y := 1$
 $RD_{OUT}(2) = \{(x, 1), (y, 2)\}$

while $x > 1$
 $y := x * y$
 $x := x - 1$
 $y := x * y$

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$$RD_{OUT}(2) = \{(x, 1), (y, 2)\}$$

$$RD_{OUT}(3) = \{(x, 1), (y, 2), (y, 4), (x, 5)\}$$

$$y := x * y$$

$$x := x - 1$$

$$RD_{IN}(1) = \{(x, ?), (y, ?)\}$$

$$x := 5$$

$$PD_{OUT}(1) = \{(x, 1), (y, ?)\}$$

$$y := 1$$

$$RD_{OUT}(2) = \{(x, 1), (y, 2)\}$$

$$PD_{OUT}(3) = \{(x, 1), (y, 2), (y, 4), (x, 5)\}$$

$$y := x * y$$

$$PD_{OUT}(4) = \{(x, 1), (y, 4), (x, 5)\}$$

$$x := x - 1$$

$$RD_{IN}(1) = \{(x, 7), (y, 7)\}$$

$$x := 5 \quad 1$$

$$\downarrow RD_{OUT}(1) = \{(x, 1), (y, ?)\}$$

$$y := 1 \quad 2$$

$$\downarrow RD_{OUT}(2) = \{(x, 1), (y, 2)\}$$

$$while x > 1 \quad 3$$

$$\downarrow RD_{OUT}(3) = \{(x, 1), (y, 2), (y, 4), (x, 5)\}$$

$$y := x * y \quad 4$$

$$\downarrow RD_{OUT}(4) = \{(x, 1), (y, 4), (x, 5)\}$$

$$x := x - 1 \quad 5$$

$$RD_{OUT}(5) = \{(y, 4), (x, 5)\}$$

We formalize the reaching definitions analysis similarly to the live variables analysis but working forward.

For every block k:

$$RD_{\text{IN}}(k) = \bigcup_{(h \to k) \in \text{CFG}} RD_{\text{OUT}}(h)$$

 $RD_{\text{OUT}}(k) = (RD_{\text{IN}}(k) \setminus \text{kill}_{RD}(k)) \cup \text{gen}_{RD}(k)$

We formalize the reaching definitions analysis similarly to the live variables analysis but working forward.

For every block k:

for every node h that precedes k in the CFG

(i.e., h is a direct predecessor of k)

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$$RD_{\text{OUT}}(k) = \left(RD_{\text{IN}}(k)\right) \ker \left(k\right) \cup \operatorname{gen}_{RD}(k)$$

other definitions of the same variables redefined at *k*

variables defined at k

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We define $kill_{RD}$ and gen_{RD} for every block type:

$$\begin{aligned} & \text{kill}_{RD}(\textbf{skip}) = \{\} & \text{gen}_{RD}(\textbf{skip}) = \{\} \\ & \text{kill}_{RD}(\textbf{v} := E) = \{(\textbf{v}, p) \mid \text{for all } p\} & \text{gen}_{RD}(\{\textbf{v} := E\}^k) = \{(\textbf{v}, k)\} \\ & \text{kill}_{RD}(\textbf{if/while } C) = \{\} & \text{gen}_{RD}(\textbf{if/while } C) = \{\} \\ & \text{program points or ?} \end{aligned}$$

The information about which statements <u>produce</u> values and which <u>use</u> them is useful for many program optimizations. A reaching definitions analysis has this information, which can be displayed directly as <u>links</u> between statements.

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{ x := 0 }<sup>1</sup>
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if (z = x)<sup>3</sup>
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Use-definition chains (UD chains): link from each use of a variable to all assignments that may reach it.

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UD chains: definition

location of implicit initialization

Use-definition chains (UD chains): link from each use of a variable to all assignments that may reach it.

$$UD(v,k) = \{q \mid \{v := E\}^q \text{ and } clear(v,q,k)\} \cup \{? \mid clear(v,?,k)\}$$

UD chains: all $p \to q$ such that p uses some x and $q \in UD(x, p)$

Predicate clear(x, p, q) holds iff there is a definition-clear path from p to q: a path such that no block strictly between p and q redefines x.

UD chains: definition

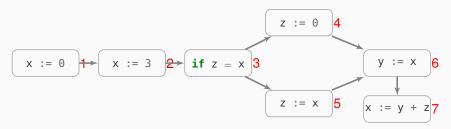
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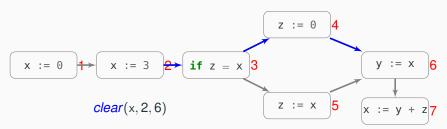
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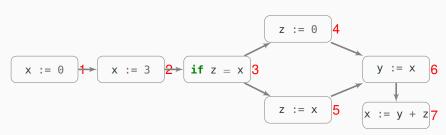


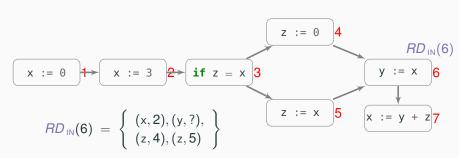
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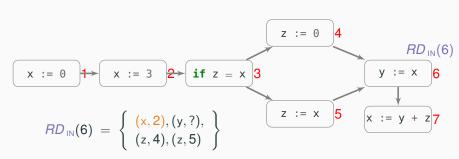
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$$UD(v, k) = \begin{cases} \{q \mid (v, q) \in RD_{IN}(k)\} & \text{if v is used in block } k \\ \{\} & \text{otherwise} \end{cases}$$





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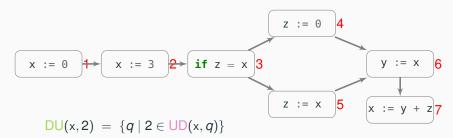
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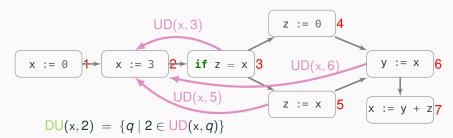


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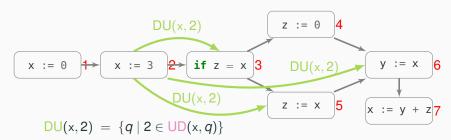


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Data-flow analysis

Slicing

What statements potentially affect the value of sum printed at line 8?

```
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2 \text{ prod} := 1
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7 \quad k := k + 1
8 print(sum)
9 print(prod)
```

What statements potentially affect the value of sum printed at line 8?

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2 \text{ prod} := 1
3 k := 0
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                                      4 \text{ while } k < y
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Program slicing

The program slice of program P according to slicing criterion ℓ (where ℓ is a location in P) is a subset of all statements in P that may affect the values of variables at ℓ

If we <u>only observe</u> variables at location $\underline{\ell}$, we <u>cannot distinguish</u> a run of P from a run of its slice according to ℓ .

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Applications of program slicing

Several program analyses and optimizations are based on slicing:

debugging: by using the location of failure as slicing criterion,

programmers can winnow statements where the

error originates from the others

testing: slicing a failing test is a way of shrinking its size

without losing its failure-triggering capability

parallelization: statements in separate slices can be executed in

parallel without running into race conditions

Different approaches to slicing

Slicing can be done statically or dynamically.

Static slicing is based on general dependencies between statements, and hence it does not depend on particular inputs.

Dynamic slicing is based on the dependencies between statements that occur with specific inputs, and hence it is in general more precise (smaller slices).

Slicing can work forward or backward.

Backward slicing: given a statement ℓ , find which other (previous) statements affect ℓ .

Forward slicing: given a statement ℓ , find which other (following) statements are affected by ℓ .

Program slicing: rigorous definition

The backward program slice of program P according to slicing criterion ℓ is a program S with the following properties:

- S is obtained by deleting zero or more statements from P
- if P halts on some input X, then the values of variables at ℓ are the same in P(X) and in S(X) every time execution reaches ℓ

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To construct S we analyze any possible dependencies between ℓ and other statements:

data dependencies: corresponding to reaching definitions of variables used at ℓ

control-flow dependencies: corresponding to branching statements that may determine if execution reaches ℓ

Data dependence graph

The data dependence graph captures definition-usage dependencies between any pairs of nodes *a* and *b* in the CFG:

$$a \longrightarrow b$$
 iff $b \in DU(v, a)$

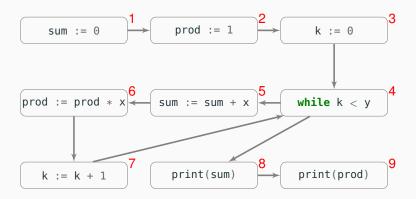
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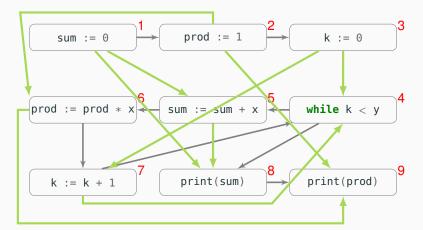


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Control dependence graph

The control dependence graph captures dependencies between branching statements and statements that may or may not execute according to which branch was taken.

```
block a is a branch
a·····▶ b iff and
branch a's outcome determines whether b executes
```

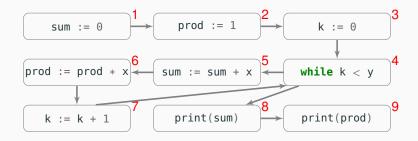
(We also add a special **ENTRY** node on which all statements not within a control structure depend.)

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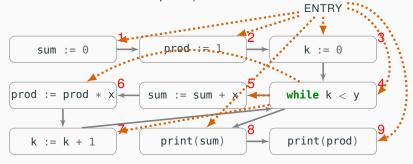
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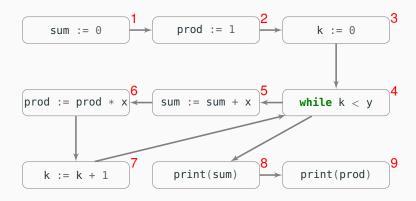


Program dependence graph

The program dependence graph (PDG) combines the data dependence and control dependence graphs.

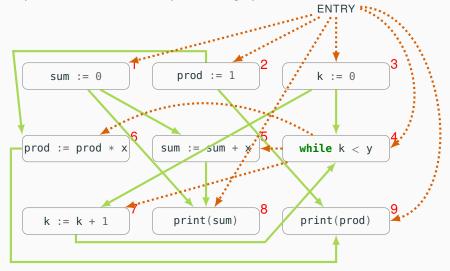
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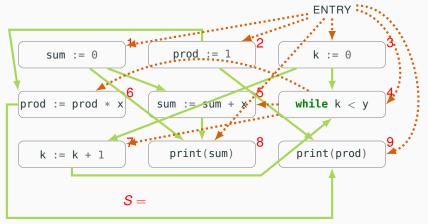


To build a backward slice *S* using the PDG:

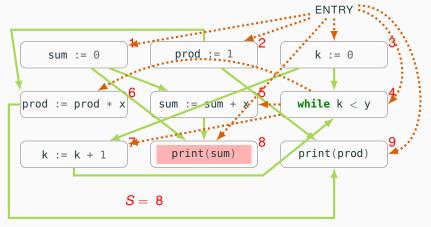
- 1. initially: $S = \{\ell\}$, where ℓ is the slicing criterion
- 2. add to *S* all nodes on which nodes in *S* transitively depend (data or control dependencies)

This corresponds to all nodes s such that $\ell \leftarrow^+ s$, where \leftarrow^+ is the transitive closure of the inverse edge relation \leftarrow in the PDG.

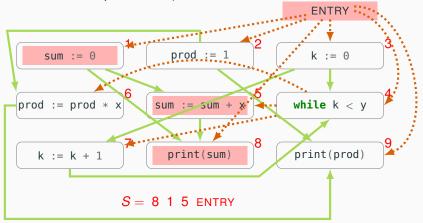
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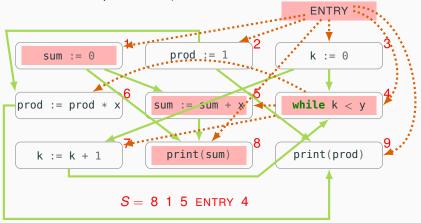
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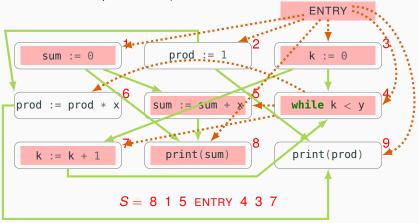
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Data-flow analysis

Static analysis tools example: Frama-C

A mini demo of Frama-C

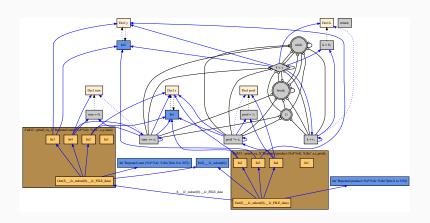
Let's perform some static analyses of the following example (already used to demonstrate slicing):

```
// print x*v and x^v
void sum_prod(int x, int y)
  int sum, prod, k:
  sum = 0;
 prod = 1:
  k = 0:
  while (k < y) {
         sum = sum + x;
         prod = prod * x:
         k = k + 1;
  printf("Repeated sum (%d*%d): %d\n", x, y, sum);
  printf("Repeated product (%d^%d): %d\n", x, y, prod);
```

The simplest way to use Frama-C is through its GUI: frama-c-gui (open a new project, and load the source file).

Frama-C: Program dependence graph

- > frama-c -pdg -pdg-dot="pdg" example.c
- # generate PDG and store it as DOT file 'pdg.sum_prod.dot'



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```
> frama-c -pdg -pdg-dot="pdg" example.c
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```

In the program dependence graph generated by Frama-C:

- · Blue arrows go from a variable's usage to its reaching definitions
- Edges with an empty circle as arrowhead go from a statement to its control dependences
- Nodes while and break denote the loop's entry and exit points

Frama-C: Slicing

Slice using, as slicing criterion, variable sum at the exit of sum_prod:

```
> frama-c -main="sum_prod" -lib-entry -slice-value="sum" example.c \
             -then-on 'Slicing export' -print -ocode sum_exit_slice.c
              void sum_prod(int x, int y)
                int sum;
                int k;
                sum = 0;
                k = 0:
                while (k < y) {
                  sum += x:
                  k ++;
                return;
```

Frama-C: Slicing

Slice using, as slicing criterion, variable sum at the exit of sum_prod:

Another way of specifying the slicing criterion is adding:

```
/* slice pragma stmt; */
```

before the statement representing the slicing criterion. Then, call the analysis with <code>-slice-pragma="sum_prod"</code> instead of <code>-slice-value</code>.

Frama-C: Value analysis

An analysis of the range of values that variables may take, and the possible overflows that may result:

```
> frama-c -eva example.c
[eva:alarm] example.c:11: Warning:
  signed overflow. assert sum + x \le 2147483647;
[eva:alarm] example.c:12: Warning:
  signed overflow. assert prod * x \le 2147483647;
[eval ===== VALUES COMPUTED ======
[eva:final-states] Values at end of function sum_prod:
  sum in [0..2147483646]
  prod in [1..2147483647]
  k in [0..2147483647]
```

Data-flow analysis

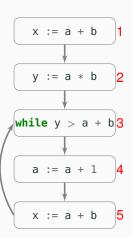
Available expressions analysis

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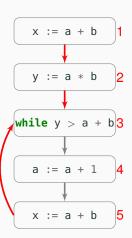


Expressions: a + b, a * b, a + 1.

Which of these expressions are available at (the entry of) 3?

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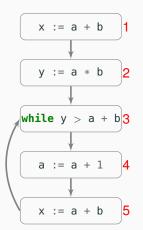
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before the point/ at the entry of the block

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Available expressions analysis's output:

$$AE(1) = \{\}$$
 $AE(2) = \{a + b, a, b\}$
 $AE(3) = \{a + b, a, b\}$
 $AE(4) = \{a + b, a, b\}$
 $AE(5) = \{b\}$

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Available expressions analysis: for each program point, determine which expressions must be available at the point.

under-approximation

before the point/ at the entry of the block

A must analysis is an under-approximation: AE(k) is a subset of the available expressions at k.

- if $E \in AE(k)$, E is definitely available at k
- if E ∉ AE(k), E may or may not be <u>available</u> at k (for example because it is available along certain paths but not along others)

The analysis has to be <u>sound</u>, and then as <u>precise</u> as possible given the information available in the CFG.

Available expressions analysis: applications

An expression E is available at block k if E was evaluated and not later modified on all paths that reach k.

If an expression *E* is available it needs not be recomputed; thus, we can save the value in its first computation in each path, and then read the saved value instead if computing it again. This improves performance the more computing *E* is expensive.

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```
1  x := f(a, b)
2  y := a * b
3  while y > f(a, b)
4  a := a + 1
5  x := f(a, b)
```

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Expression f(a, b) is available at 3. Hence, we can save its value in a fresh variable fab and read it at 3. Assuming the computation of f is expensive, this avoids repeating it when not necessary. Typically f has to be side-effect free for this optimization to be safe.

Formalizing available expressions analysis

We formalize the idea of available expressions analysis as an equation system:

- $AE_{IN}(k)$ and $AE_{OUT}(k)$ are variables over domain $\wp(\mathcal{E})$, where \mathcal{E} is the set of all program expressions
- the equations formalize the relations:

$$AE_{\,\,\text{IN}}(k) = \bigcap_{h \,\, \text{direct predecessor of} \,\, k} AE_{\,\, \text{OUT}}(h)$$
 $AE_{\,\, \text{OUT}}(k) = (AE_{\,\, \text{IN}}(k) \setminus \text{"changed at } k") \cup \text{"not changed at } k"$

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must analysis: available along all paths

The <u>analysis</u> result is the <u>greatest solution</u> of the equation system – greatest so that the under-approximation is as <u>precise</u> as possible.

Data-flow equations

for every node h that precedes k in the CFG (i.e., h is a direct predecessor of k)

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$$AE_{\text{IN}}(k) = \bigcap_{(h \to k) \in \text{CFG}} AE_{\text{OUT}}(h)$$

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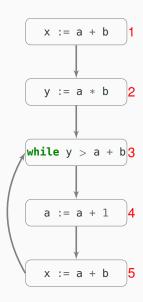
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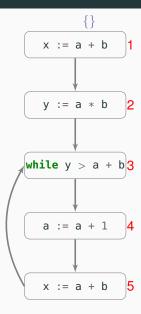
all expressions containing v

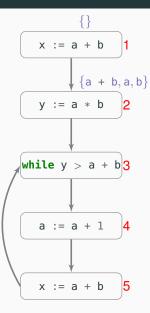
We define $kill_{AE}$ and gen_{AE} for every block type: all subexpressions of E not containing v

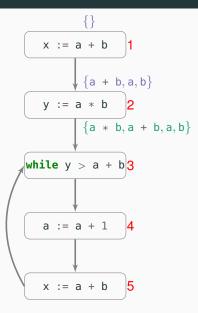
$$\begin{aligned} & \text{kill}_{AE}(\mathbf{skip}) = \{\} \\ & \text{kill}_{AE}(\mathbf{v} := E) = \{e \mid \mathbf{v} \in e\} \end{aligned} & \text{gen}_{AE}(\mathbf{skip}) = \{\} \\ & \text{gen}_{AE}(\mathbf{v} := E) = \{e \mid e \in E \text{ and } \mathbf{v} \not\in e\} \\ & \text{kill}_{AE}(\mathbf{if/while} \ C) = \{\} & \text{gen}_{AE}(\mathbf{if/while} \ C) = \{e \mid e \in C\} \end{aligned}$$

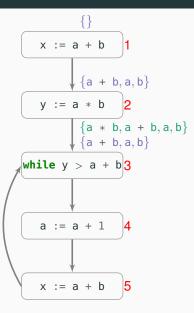
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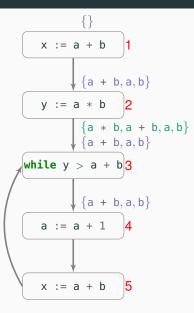


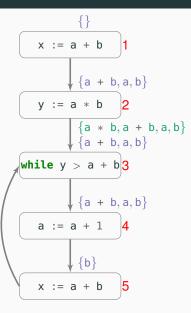


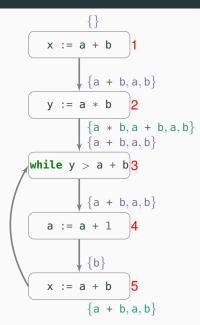












May vs. must analyses

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Accordingly, the notions of soundness and precision are formulated in a way that matches the way the analysis's results are used.

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MAY ANALYSIS

MUST ANALYSIS

analysis approximates property P

analysis output MAY

example: P = live variables

property P is an error property

if v is not live, then I can eliminate an assignment

over-approximation: $P \subseteq MAY$

sound: $x \notin MAY \Longrightarrow x \notin P$

imprecise: $x \in MAY \implies x \in P$

most precise: least fixed point

analysis ouput: *MUST*

example P = available expressions property P is a correctness property

if *E* is available, then I can eliminate an evaluation

under-approximation: $MUST \subseteq P$

sound: $x \in MUST \Longrightarrow x \in P$

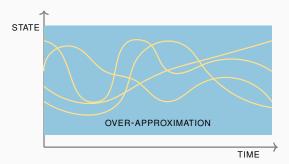
imprecise: $x \notin MUST \implies x \notin P$

most precise: greatest fixed point

Abstract interpretation

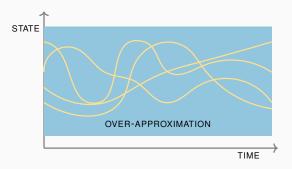
One framework to rule them all

The basic idea behind the data-flow analyses we have seen – as well as many other kinds of static analysis – is to abstract computations by keeping track of partial, simpler information – such as the variables that may be live.



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A crucial concern is correctness: how to ensure that a particular analysis is sound.

Abstract interpretation provides a general framework to construct program analyses and to establish their correctness.

Cousot & Cousot

Abstract interpretation was invented by Patrick and Radhia Cousot in a seminal POPL paper published in 1977.

ABSTRACT INTERPRETATION: A UNIFIED LATTICE MODEL FOR STATIC ANALYSIS
OF PROGRAMS BY CONSTRUCTION OR APPROXIMATION OF FIXPOINTS

Patrick Cousot * and Radhia Cousot **

Laboratoire d'Informatique, U.S.M.G., BP. 53 38041 Grenoble cedex, France





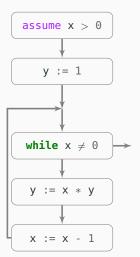
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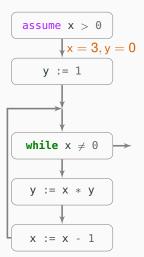
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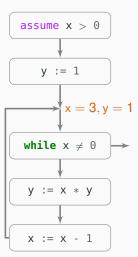


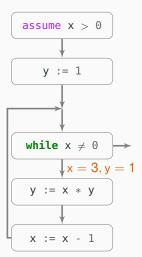
Abstract interpretation

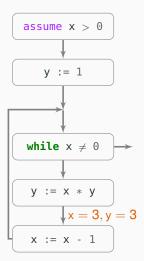
Concrete and abstract computations

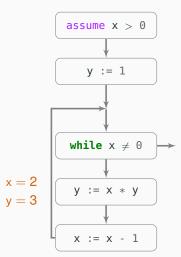


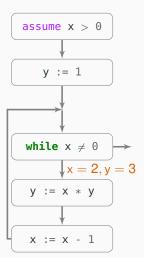


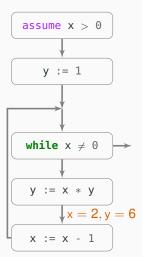


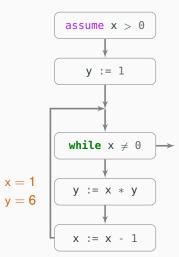


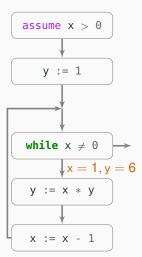


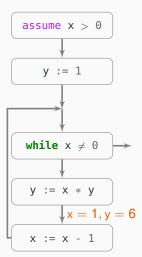


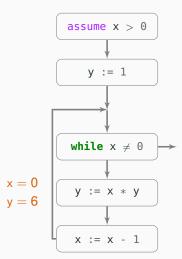


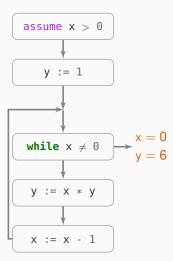


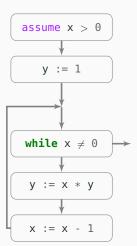


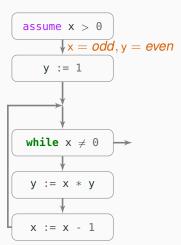


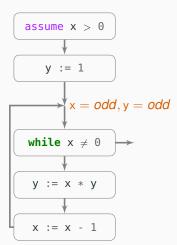


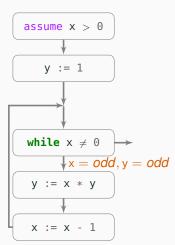


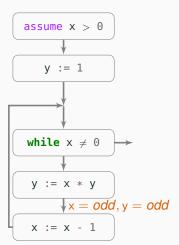




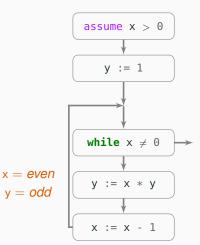




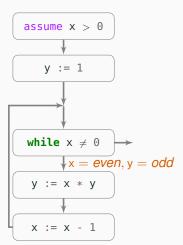


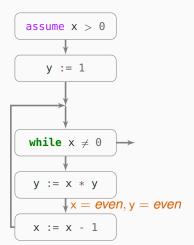


A static analysis defines a form of abstract semantics where computations are sequences of states over an abstract domain that keeps track of partial information about properties of a program's concrete state – for example, whether a variable is even or odd.

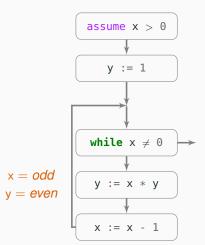


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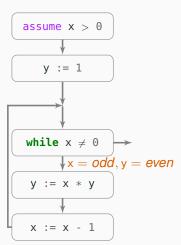


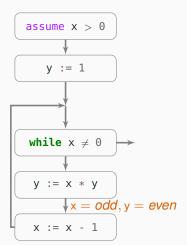


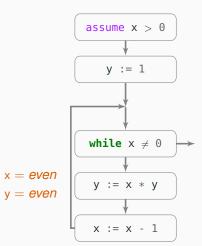
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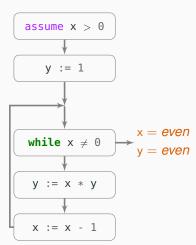


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Abstract interpretation is a framework for constructing abstract semantics and proving that they are sound with respect to the concrete semantics.

Contrast this to the a posteriori approach of data-flow analysis: first define an analysis, then prove that it is correct.

As in the data-flow analyses, computations are captured by the possible values of variables at each program point.

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Now we label edges of the CFG (instead of nodes) because we want to express the possible values of variables before or after executing a statement.

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Concrete state domain:

State: Vars $\to \mathbb{Z}$

Concrete semantics:

set of possible concrete states at every program point

 $C: Labels \rightarrow \wp(State)$



Abstract state domain:

AbstractState: $Vars \rightarrow \left\{ egin{array}{l} odd, \\ even \end{array} \right\}$

Abstract semantics:

set of possible abstract states at every program point

A: Labels $\rightarrow \wp(AbstractState)$

The collecting semantics *C* is a concrete semantics in data-flow fashion, giving the set of possible concrete states at every edge <u>label</u>:

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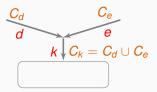
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$$\begin{array}{c|c}
\hline
p & C_p \\
\hline
 & \text{if/while } B \\
\hline
 & \text{false} \\
\hline
 & \text{true} & C_{\text{true}} = \{s \mid s \in C_p \text{ and } \neg \llbracket B \rrbracket_s\} \\
\hline
\end{array}$$

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Collecting semantics: example

initially, the state can be anything assume x > 0y := 1 while $x \neq 0$ y := x * y6

x := x - 1

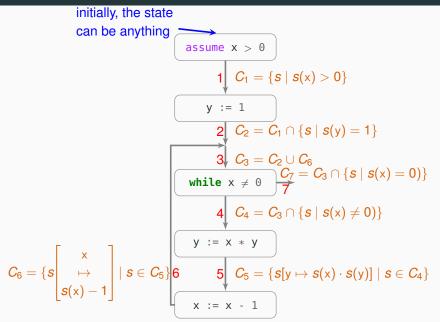
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initially, the state can be anything assume x > 01 $C_1 = \{s \mid s(x) > 0\}$ 2 $C_2 = C_1 \cap \{s \mid s(y) = 1\}$ 4 $C_4 = C_3 \cap \{s \mid s(x) \neq 0\}$ y := x * y $5 \quad C_5 = \{s[y \mapsto s(x) \cdot s(y)] \mid s \in C_4\}$



Collecting semantics: equation solving

The collecting semantics gives a set of equations that look a lot like data-flow equations – except for minor details such as that we have labels on edges instead of entry and exit of blocks.

$$C_{1} = \{s \mid s(x) > 0\}$$

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These equations satisfy the conditions of Tarski's fixed point theorem:

monotonicity: every function C_k is monotonic

lattice: the analysis domain is $\wp(State)^7$, a complete lattice

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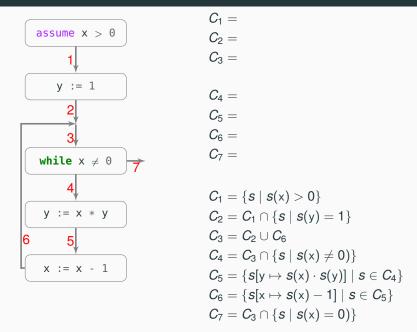
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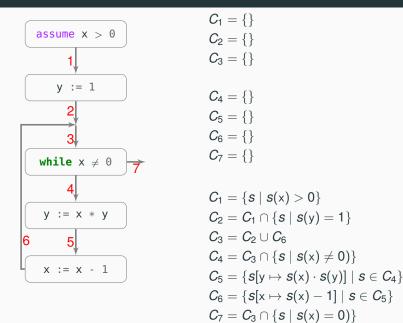
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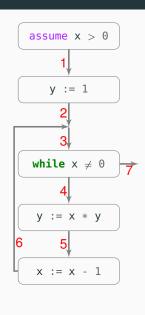
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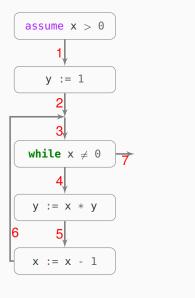
We can compute the concrete semantics by evaluating the equations starting from $\{\} \times \cdots \times \{\}$ until we reach a fixed point.







```
C_1 = \{x = m, y = n \mid m > 0\}
C_2 = \{\}
C_3 = \{\}
C_4 = \{\}
C_5 = \{\}
C_6 = \{\}
C_7 = \{\}
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$$C_{2} = \{x = m, y = 1 \mid m > 0\}$$

$$C_{3} = \{\}$$

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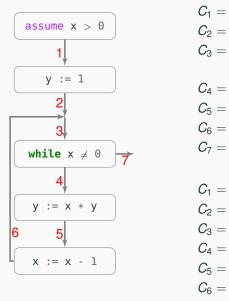
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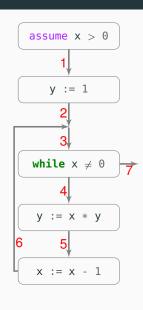
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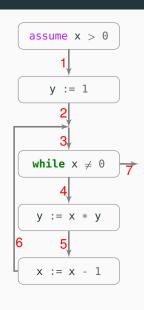
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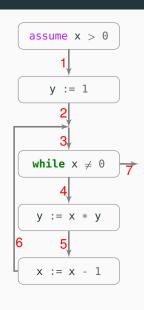
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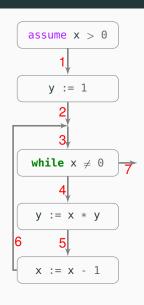
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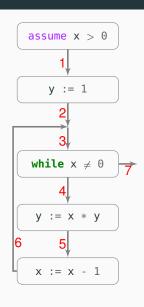
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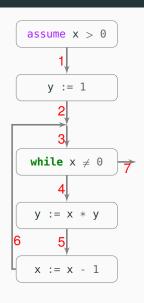
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$$\begin{array}{lll} C_1 = & \{\mathsf{x} = m, \mathsf{y} = n \mid m > 0\} \\ C_2 = & \{\mathsf{x} = m, \mathsf{y} = 1 \mid m > 0\} \\ C_3 = & \{\mathsf{x} = m, \mathsf{y} = 1 \mid m > 0\} \\ & \cup \{\mathsf{x} = m - 1, \mathsf{y} = m \mid m > 0\} \\ C_4 = & \{\mathsf{x} = m, \mathsf{y} = 1 \mid m > 0\} \\ C_5 = & \{\mathsf{x} = m, \mathsf{y} = m \mid m > 0\} \\ C_6 = & \{\mathsf{x} = m - 1, \mathsf{y} = m \mid m > 0\} \\ C_7 = & \{\mathsf{x} = 0, \mathsf{y} = 1 \mid m > 0\} \\ & \text{and so on...} \\ C_1 = & \{\mathsf{s} \mid \mathsf{s}(\mathsf{x}) > 0\} \\ & C_2 = & C_1 \cap \{\mathsf{s} \mid \mathsf{s}(\mathsf{y}) = 1\} \\ & C_3 = & C_2 \cup & C_6 \\ & C_4 = & C_3 \cap \{\mathsf{s} \mid \mathsf{s}(\mathsf{x}) \neq 0\} \} \\ & C_5 = & \{\mathsf{s}[\mathsf{y} \mapsto \mathsf{s}(\mathsf{x}) \cdot \mathsf{s}(\mathsf{y})] \mid \mathsf{s} \in & C_4\} \\ & C_6 = & \{\mathsf{s}[\mathsf{x} \mapsto \mathsf{s}(\mathsf{x}) - 1] \mid \mathsf{s} \in & C_5\} \\ & C_7 = & C_3 \cap \{\mathsf{s} \mid \mathsf{s}(\mathsf{x}) = 0\} \} \end{array}$$

Sign semantics

The sign semantics *A* is an abstract semantics that only keeps track of the sign of integer variables, giving the set of <u>possible abstract</u> states at every edge <u>label</u>:

A: Labels
$$\rightarrow \wp(\underbrace{\textit{Vars} \rightarrow \textit{Sign}}_{\textit{AbstractState}})$$

Domain $Sign = \{\top, +, 0, -, \bot\}$ is a finite set ordered according to \le :



- ⊤ represents all integers
- + represents all positive integers
 - 0 represents the singleton {0}
- represents all negative integers
- ⊤ represents the empty set

The poset $\langle Sign, \leq \rangle$ is a

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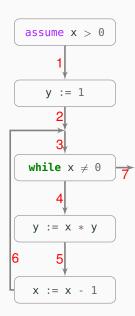
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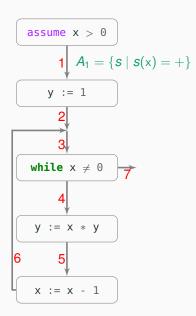
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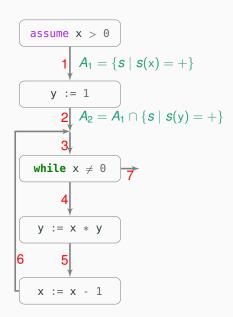


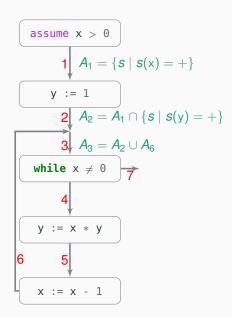
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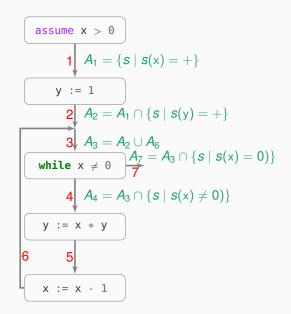
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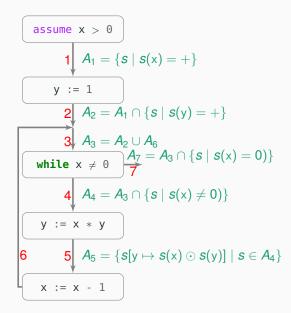


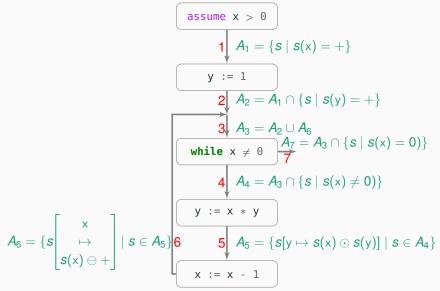












Sign semantics: equation solving

The sign semantics gives a set of equations that are structurally identical to those of the collecting semantics – except that variables range over Sign in the sign semantics, and hence we need to express arithmetic operations \cdot and - as operations \odot and \ominus over the abstract domain.

$$A_{1} = \{s \mid s(x) = +\}$$

$$A_{2} = A_{1} \cap \{s \mid s(y) = +\}$$

$$A_{3} = A_{2} \cup A_{6}$$

$$A_{4} = A_{3} \cap \{s \mid s(x) \neq 0\}\}$$

$$A_{5} = \{s[y \mapsto s(x) \odot s(y)] \mid s \in A_{4}\}$$

$$A_{6} = \{s[x \mapsto s(x) \ominus +] \mid s \in A_{5}\}$$

$$A_{7} = A_{3} \cap \{s \mid s(x) = 0\}\}$$

Sign semantics: equation solving

The sign semantics gives a set of equations that are structurally identical to those of the collecting semantics – except that variables range over Sign in the sign semantics, and hence we need to express arithmetic operations \cdot and - as operations \odot and \ominus over the abstract domain.

$$A_{1} = \{s \mid s(x) = +\}$$

$$A_{2} = A_{1} \cap \{s \mid s(y) = +\}$$

$$A_{3} = A_{2} \cup A_{6}$$

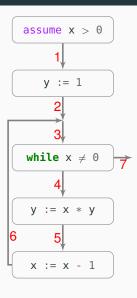
$$A_{4} = A_{3} \cap \{s \mid s(x) \neq 0\}\}$$

$$A_{5} = \{s[y \mapsto s(x) \odot s(y)] \mid s \in A_{4}\}$$

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Since these equations still satisfy the conditions of Tarski's fixed point theorem, we can compute the abstract semantics by evaluating the equations starting from $\{\} \times \cdots \times \{\}$ until we reach a fixed point.



$$A_1 = A_2 = A_3 = A_4 = A_5 = A_6 = A_7 =$$

$$A_{1} = \{ s \mid s(x) = + \}$$

$$A_{2} = A_{1} \cap \{ s \mid s(y) = + \}$$

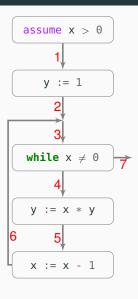
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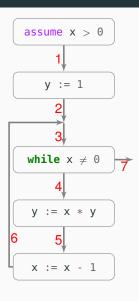
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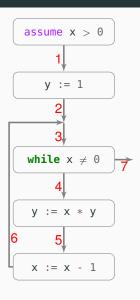
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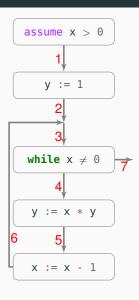
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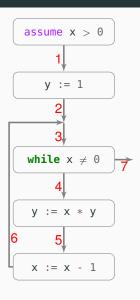
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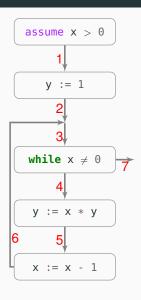
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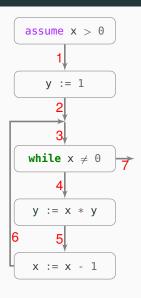
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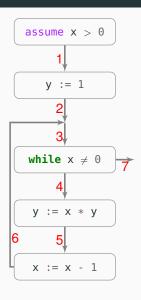
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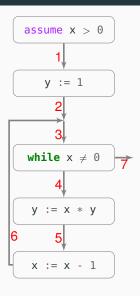
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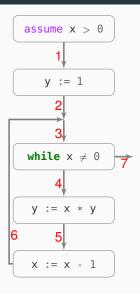
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Abstract interpretation

Correctness

To illustrate in detail how abstract interpretation supports reasoning about the correctness (soundness) of abstract computations, let us focus on a simple example: integer expressions.

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Syntax of very simple integer expressions VE:

$$egin{array}{lll} \emph{VE} \ni & \emph{n} & & \textit{for } \emph{n} \in \mathbb{Z} \\ \emph{VE} \ni & \emph{e}_1 \times \emph{e}_2 & & \textit{for } \emph{e}_1, \emph{e}_2 \in \emph{VE} \\ \end{array}$$

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Let us define a concrete semantics (to evaluate the integer value of any expression) and an abstract semantics (to evaluate the sign of any expression).

Expressions: concrete semantics

The concrete semantics *C* assigns integer values to expressions:

 $C: VE \rightarrow State$

where $\mathit{State} = \mathbb{Z}$

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The definition of *C* is straightforward.

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For notational clarity, we will use <u>square brackets</u> to define the evaluation of semantics.

The abstract semantics *A* assigns to expressions values in the sign domain:

A: $VE \rightarrow AbstractState$ where AbstractState = Sign

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The definition of A:

$$A[n] = sign(n) \qquad \text{where } sign(n) = \begin{cases} + & n > 0 \\ 0 & n = 0 \\ - & n < 0 \end{cases}$$

$$A[e_1 \times e_2] = A[e_1] \otimes A[e_2]$$

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We want to check that the abstract semantics is sound – that is, it correctly represents the sign of the concrete semantics.

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For very simple integer expressions β is sign:

$$\beta(n) = \begin{cases} + & n > 0 \\ 0 & n = 0 \\ - & n < 0 \end{cases}$$

Concretization function

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$$\gamma$$
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For very simple integer expressions γ identifies subsets of \mathbb{Z} :

$$\gamma(s) = \begin{cases} \{n \in \mathbb{Z} \mid n > 0\} & s = +\\ \{0\} & s = 0\\ \{n \in \mathbb{Z} \mid n < 0\} & s = -\end{cases}$$

We have two semantics and the concretization function:

 $C: VE \rightarrow State$ $C: VE \rightarrow \mathbb{Z}$

A: $VE \rightarrow AbstractState$ *A*: $VE \rightarrow Sign$

 γ : AbstractState $\to \wp(State)$ γ : Sign $\to \wp(\mathbb{Z})$

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Soundness requires that the concrete semantics is compatible with the abstract semantics. Formally, C[e] should give one of the possible concretizations of A[e] – as in an over-approximation.

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For example:

$$A[-3 \times 2 \times -5] = +$$
 $C[-3 \times 2 \times -5] = 30$

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Simple integer expressions

To see a more interesting example, let's add the <u>sum</u> as possible operation between integers:

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Syntax of simple integer expressions SE:

$$SE \ni n$$
 for $n \in \mathbb{Z}$ $SE \ni e_1 \times e_2 \;,\; e_1 + e_2 \;,\; -e_1$ for $e_1, e_2 \in VE$ unary minus

Once again, let us define a concrete semantics and an abstract semantics.

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$$\begin{array}{c|c}
0 & + & & \\
\hline
0 & - & & \\
0 & 0 & & 0
\end{array}$$

The abstract semantics A assigns to expressions values in the sign domain:

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The abstract domain $\{+,0,0\}$ is not closed under the interpretation of addition \oplus .

To ensure that the abstract domain is closed under \oplus we include value \top (top), corresponding to "any value". When the abstract value is \top it means that we have no information about the sign.

\oplus	_	0	+
_	_	_	Т
0	_	0	+
+	Т	+	+

To ensure that the abstract domain is closed under \oplus we include value \top (top), corresponding to "any value". When the abstract value is \top it means that we have no information about the sign.

To have a complete lattice, let's also add to the abstract domain value \perp (bottom), corresponding to "no value" (the empty set).

A: $SE \rightarrow AbstractState$ where $AbstractState = Sign = \{-, 0, +, \top, \bot\}$

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To ensure that the abstract domain is closed under \oplus we include value \top (top), corresponding to "any value". When the abstract value is \top it means that we have no information about the sign.

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\oplus	-	0	+	Т
_	_	_	\top	Т
0	_	0	+	\top
+	Т	+	+	Τ
T	Т	T	Т	Т

To ensure that the abstract domain is closed under \oplus we include value \top (top), corresponding to "any value". When the abstract value is \top it means that we have no information about the sign.

To have a complete lattice, let's also add to the abstract domain value \perp (bottom), corresponding to "no value" (the empty set).

 $A: SE \rightarrow AbstractState \quad where \ AbstractState = Sign = \{-, 0, +, \top, \bot\}$

The definition of abstract operations \otimes , \ominus , and \oplus applied to \bot does not matter, since this value will never appear in a specific abstract computation.

Concretization function for Sign

We extend the concretization function γ to Sign

$$\gamma$$
: AbstractState $\to \wp(State)$ that is: $Sign \to \wp(\mathbb{Z})$

For simple integer expressions γ identifies subsets of \mathbb{Z} :

$$\gamma(s) = egin{cases} \{n \in \mathbb{Z} \mid n > 0\} & s = + \ \{0\} & s = 0 \ \{n \in \mathbb{Z} \mid n < 0\} & s = - \ \mathbb{Z} & s = \top \ \{\} & s = oxed{1}$$

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We can see that $\langle Sign, \leq \rangle$ is a partial order induced by

$$a \le b$$
 iff $\gamma(a) \subseteq \gamma(b)$
$$- \bigvee_{\perp}^{\top} \bigvee_{\perp}^{\top} +$$

Abstract interpretation: the framework

To define a static analysis in the framework of abstract interpretation, we start from the concrete domain *C*:

- Define an abstract domain A as a poset ⟨A, ⊑⟩ that must be a complete lattice
- 2. Define a representation function $\beta \colon C \to A$ that maps each concrete value to its "best" abstract value
- 3. The concretization function $\gamma: A \to \wp(C)$ can then be defined as

$$\gamma(a) = \{c \in C \mid \beta(c) \sqsubseteq a\}$$

4. The abstraction function $\alpha \colon \wp(C) \to A$ can then be defined as

$$\alpha(C) = \bigsqcup \{ \beta(c) \mid c \in C \}$$

Abstract interpretation of simple integer expressions

Concrete domain	Abstract domain	Representation function	
${\color{red} C}=\mathbb{Z}$	$\langle A,\sqsubseteq angle = \langle \mathit{Sign}, \leq angle$	eta=sign	

Abstract interpretation of simple integer expressions

Concrete domain Abstract domain Representation function $C = \mathbb{Z}$ $\langle A, \sqsubseteq \rangle = \langle Sign, \leq \rangle$ $\beta = \text{sign}$

Concretization function:

$$\gamma(a) = \{c \in \mathbb{Z} \mid \operatorname{sign}(c) \le a\} = \begin{cases} \{c \in \mathbb{Z} \mid c > 0\} & a = +\\ \{0\} & a = 0\\ \{c \in \mathbb{Z} \mid c < 0\} & a = -\\ \mathbb{Z} & a = \top\\ \{\} & a = \bot \end{cases}$$

Abstract interpretation of simple integer expressions

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Abstraction function ($C \subseteq C$):

$$lpha(C) = \bigsqcup \{eta(c) \mid c \in C\} = egin{cases} + & C = \{1,2,3\} \\ \top & C = \{0,1,2\} \\ \top & C = \{-1,1\} \\ \dots \end{cases}$$

The concretization and abstraction functions have the following properties by construction:

monotonicity α and γ are monotonic functions

Galois connection: α and γ satisfy

$$C \subseteq \gamma(\alpha(C))$$
 for all $C \in \wp(C)$
 $a \supseteq \alpha(\gamma(a))$ for all $a \in A$

Under these conditions, α and γ over their respective domains are said to form a Galois connection.

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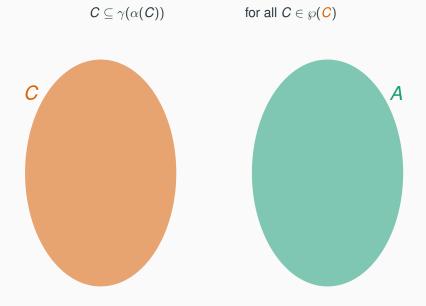
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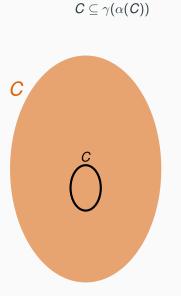
Galois connection: γ and α map between posets C and A in a way that γ and α are "almost inverses" of each other.

Galois connections capture the notion of correctness: the abstraction $\alpha(C)$ is a superset (over-approximation) of the concrete semantics.

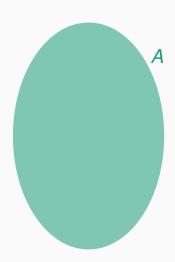
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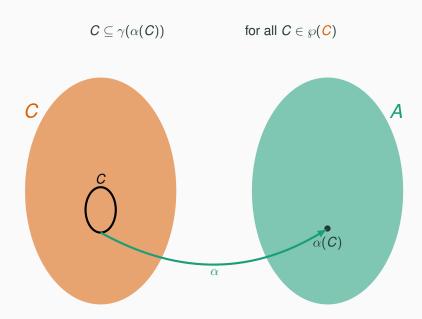
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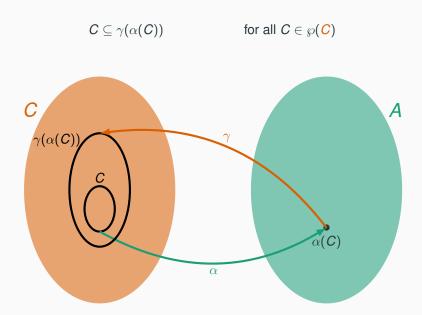


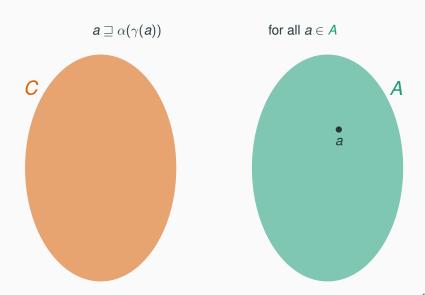


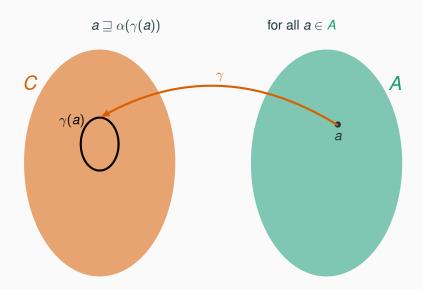
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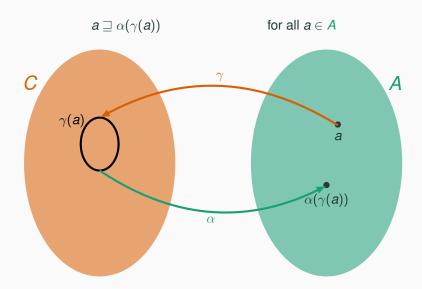


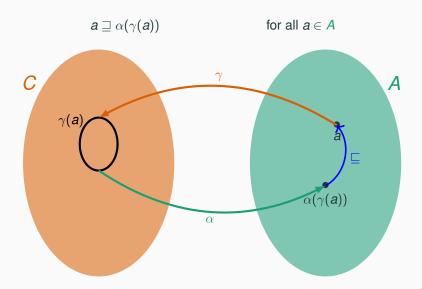




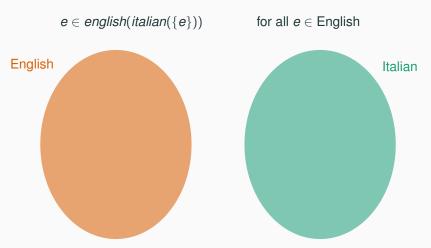




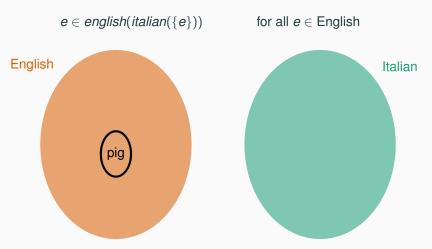




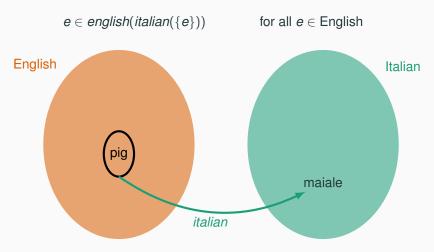
Bilingual dictionaries behave somewhat like Galois connections:



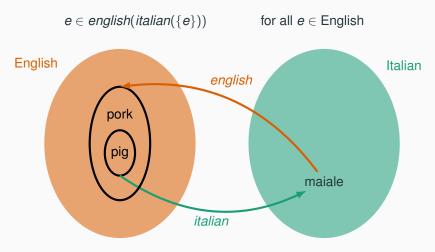
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Galois insertions

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Galois connection: α and γ form a Galois connection

$$C \subseteq \gamma(\alpha(C))$$
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When the following stronger property holds:

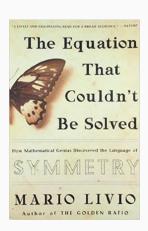
$$a = \alpha(\gamma(a))$$
 for all $a \in A$

we have a Galois insertion: the abstraction is defined in a way that there is no "redundancy" in A to describe C.

Évariste Galois



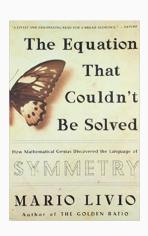
Évariste Galois



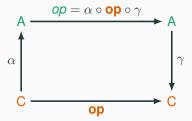
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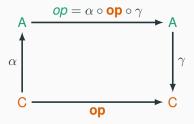
Évariste Galois (1811–1832)



Once we have α and γ that form a Galois connection, we can induce abstract operations from concrete operations.

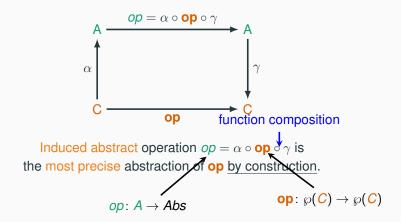


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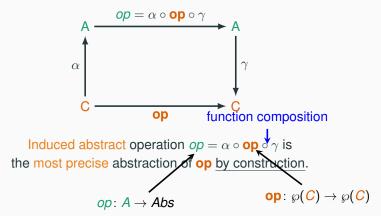


Induced abstract operation $op = \alpha \circ op \circ \gamma$ is the most precise abstraction of op by construction.

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If the induced op is not computable, we can use any approximation op^{\sharp} such that $op(a) \sqsubseteq op^{\sharp}(a)$ for all $a \in A$.

Induced operations: example

In our running example of simple expressions, we can induce \oplus from + and γ , α – which in turn have been built from β .

First we express the concrete operation + as an operation on sets of integers:

$$+ \colon \wp(\mathbb{Z}) \to \wp(\mathbb{Z}) \to \wp(\mathbb{Z})$$
$$+ (N_1, N_2) = \{ n_1 + n_2 \mid n_1 \in N_1, n_2 \in N_2 \}$$

Then we induce the abstract operation:

$$\oplus \colon \textit{Sign} \to \textit{Sign} \to \textit{Sign}$$

$$\oplus (\textit{s}_1, \textit{s}_2) = \alpha(+(\gamma(\textit{s}_1), \gamma(\textit{s}_2)))$$

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For example:

$$+ \oplus - = \alpha(\gamma(+) + \gamma(-)) = \alpha(\{n > 0\} + \{n < 0\}) = \alpha(\mathbb{Z}) = \top$$
$$- \oplus 0 = \alpha(\gamma(-) + \gamma(0)) = \alpha(\{n < 0\} + \{0\}) = \alpha(\{n < 0\}) = -$$

Abstract interpretation

Widening

Range analysis

Let us look at a more informative abstract domain for integer variables: the interval domain.

empty interval

Interval =
$$\{[]\} \cup \{[m,n] \mid m,n \in \mathbb{Z} \cup \{+\infty,-\infty\} \text{ and } m \leq n\}$$

Every element of *Interval* identifies a subset of \mathbb{Z} .

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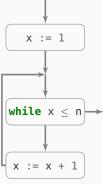
empty interval

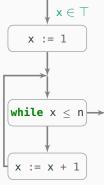
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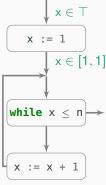
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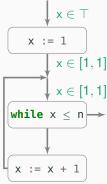
We see that $\langle Interval, \sqsubseteq \rangle$ is a complete lattice:

- \sqsubseteq is the subset relation \subseteq between sets of integers
- \top is $[-\infty, +\infty] = \mathbb{Z}$
- \perp is [] = {}

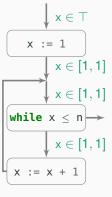




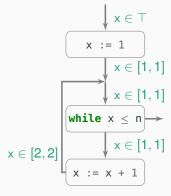




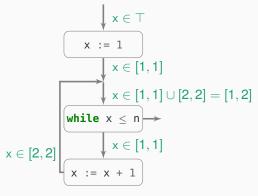
Let us try to do an abstract computation over *Interval* of a simple



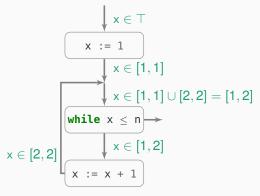
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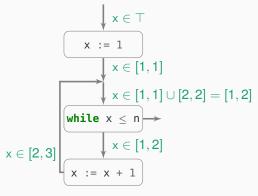
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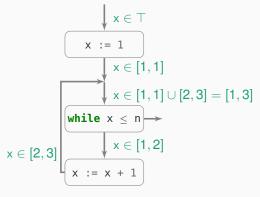
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Let us try to do an abstract computation over *Interval* of a simple



Let us try to do an abstract computation over *Interval* of a simple



Let us try to do an abstract computation over *Interval* of a simple program.

 $x \in T$ x := 1 $x \in [1, 1]$ $x \in [1, 1] \cup [2, 3] = [1, 3]$ $x \in [2, 3]$ $x \in [1, 2]$ $x \in [1, 2]$

Problem: the abstract state of x at loop entry does not converge:

$$[1,1] \sim [1,2] \sim [1,3] \sim \dots$$

The analysis does not terminate – or, if it has access to static information about n, is not faster than executing the concrete computation.

Ascending chain conditions

The interval domain $\langle Interval, \subseteq \rangle$ is a complete lattice, and the data-flow equations are monotonic. Therefore, there exists a fixed point. The problem is that the fixed point is not computable by repeated evaluation from the least element!

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A stronger condition on the abstract domain that guarantees that the fixed point is always computable is the ascending chain condition.

A complete lattice $\langle D, \sqsubseteq \rangle$ satisfies the ascending chain condition if, for every ascending sequence (chain) $a_1 \sqsubseteq a_2 \sqsubseteq a_3 \sqsubseteq \cdots$, there exists n such that $a_n = a_{n+1} = \cdots$.

In other words, the ascending chain condition requires that every sequence of abstract values eventually stabilizes.

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Finite domains obviously satisfy the ascending chain condition.

Forcing termination

The interval domain does not satisfy the ascending chain condition. To terminate, we must avoid getting stuck in the infinite chain:

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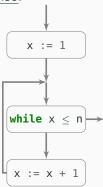
$$[1,1] \sim [1,2] \sim [1,3] \sim \dots$$

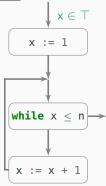
One trick is to forcefully terminate the chain by jumping to a larger value at some point:

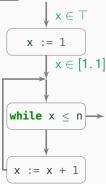
$$[1,1] \sim [1,2] \sim \cdots \sim [1,\infty]$$

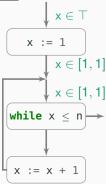
To forcefully terminate the abstract computation we can replace the join operator with a widening operator ∇ :

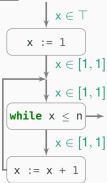
EXACT COMPUTATION	FORCED TERMINATION
$[1,1] \sqcup [2,2] = [1,2]$	$[1,1] \nabla [2,2] = [1,+\infty]$

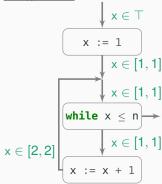


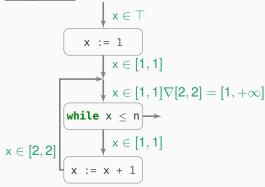


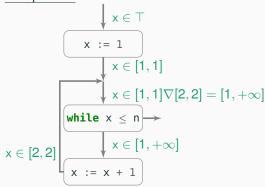


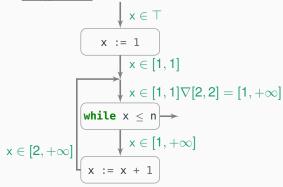


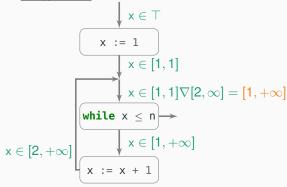












Widening

A widening $\nabla \colon D \times D \to D$ on a poset $\langle D, \sqsubseteq \rangle$ is a function with the properties:

upper bound: for $d_1, d_2 \in D$, $d_1 \sqsubseteq d_1 \nabla d_2$ and $d_2 \sqsubseteq d_1 \nabla d_2$

ascending chain: for all ascending chains $d_1 \sqsubseteq d_2 \sqsubseteq d_3 \sqsubseteq \cdots$, the derived ascending chain $w_1 \sqsubseteq w_2 \sqsubseteq w_3 \sqsubseteq \cdots$

$$w_k = \begin{cases} d_1 & k = 1 \\ w_{k-1} \nabla d_k & k > 1 \end{cases}$$

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Using widening, we ensure that the abstract computation terminates – or we speed up a terminating but slow computation.

Speed is traded-off against precision: using widening we get to a fixed point but it may not be the least fixed point but only an <u>upper</u> bound on the least fixed point.

Abstract interpretation in practice

This was just a brief overview of abstract interpretation.

The abstract interpretation framework includes a vast body of research, and various techniques to support the construction of correct static analyses.

Defining a new analysis is still far from trivial, but the tools of abstract interpretation help us ensuring its correctness a priori – as opposed to defining an analysis first, and then checking its correctness as an afterthought.

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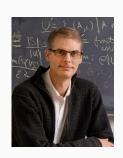
See chapter 12 of Bradley and Manna's "The calculus of computation" for an original presentation of the concepts of abstract interpretation using the notation and terminology of Hoare logic.

Type systems

What are type systems?

A type system is a <u>tractable syntactic</u> <u>method</u> for proving the absence of certain program behaviors by classifying phrases according to the kinds of values they compute.

Benjamin Pierce

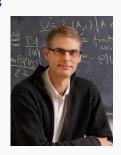


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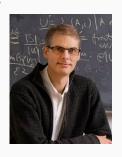


What are type systems?

a static analysis

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Type systems are a form of static analysis for proving the absence of certain errors based on classifying program terms according to the kinds of values they may take.

For example: which expressions are **Boolean** and which are integer.

Well typed programs

A type system consists of rules to check an arbitrary program term.

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Well-typed programs cannot "go wrong" Robin Milner, 1978



In other words, a type system's rules soundly check that each type is used according to the operations and values that it permits.

Type systems

Well typedness

Expression language *E*

To illustrate type systems we initially focus on a very simple language *E* of conditional, relational, and integer arithmetic expressions:

constants conditional expression
$$E ::= \begin{tabular}{ll} E + E \mid E \leq E \mid \mbox{if E then E else E} \\ C ::= \mbox{true} \mid \mbox{false} \mid n \end{tabular} \begin{tabular}{ll} \mbox{for $n \in \mathbb{Z}$} \end{tabular}$$

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$$C ::= \text{ true } | \text{ false } | n \qquad \qquad \text{for } n \in \mathbb{Z}$$

Even though the language is very simple, note that it can express all Boolean combinations of integer comparison expressions:

$$abla A \triangleq \text{ if } A \text{ then false else true}$$

$$A \wedge B \triangleq \text{ if } A \text{ then (if } B \text{ then true else false) else false}$$

$$A = B \triangleq A \leq B \wedge B \leq A$$

$$A < B \triangleq \neg (B \leq A)$$

Expression language E: semantics

The semantics $[\![\,]\!]: E \to \mathbb{Z} \cup \mathbb{B}$ of language E is a set of rules to evaluate expressions.

$$\frac{n \in \mathbb{Z}}{\llbracket n \rrbracket = n} \qquad \qquad \overline{\llbracket \mathsf{true} \rrbracket = \top} \qquad \qquad \overline{\llbracket \mathsf{false} \rrbracket = \bot}$$

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$$\frac{n \in \mathbb{Z}}{[\![n]\!] = n} \qquad \qquad \overline{[\![\mathsf{true}]\!] = \top} \qquad \qquad \overline{[\![\mathsf{false}]\!] = \bot}$$

These rules are partial because they are not applicable to every expression E. For example [true + 4] is undefined: if we apply the rules we get stuck at some point.

Expression language *E*: type system

Types provide a way to check whether an expression can be successfully evaluated without actually evaluating it. To this end, we need to distinguish between two kinds of values – two types integer and Boolean.

 $T ::= Integer \mid Boolean$

For an expression E and a type T, E: T denotes that E has type T: E certainly evaluates to a value of type T.

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A type system is a collection of typing rules to determine the type of an arbitrary expression:

Well typedness

An expression E is well typed (typable) if we can infer that E: T, for some type T, using the type system's rules.

well typed: 3+4+7+0 not well typed: true + 4

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SOUNDNESS

INCOMPLETENESS

if E is well typed, if E is not well typed, the evaluation of E may still be successful of E cannot go wrong example: 3+4+7+0 false positive: if true then 3 else (true + 4)

Well-typed programs don't get stuck

An expression E is well typed (typable) if we can infer that E: T, for some type T, using the type system's rules.

To ensure that a well-typed program does not get stuck, a type system has to satisfy two fundamental properties that, together, ensure safety:

progress: if *E* is well-typed, then either *E* is a value or we can take one step of evaluation

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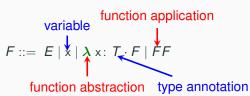
SAFETY = PROGRESS + PRESERVATION

We can prove by structural induction that the simple type system for *E* is safe.

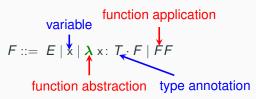
Type systems

Type checking

Let us extend *E* with a syntax for lambda expressions:



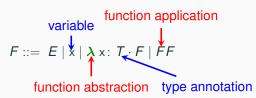
Let us extend *E* with a syntax for lambda expressions:



Function abstraction $\lambda \times T.F$ defines an expression F as an anonymous function of its argument x, which has to be annotated with its type T. In addition to the integer and Boolean types, now we also have a function type $T \to T$ from any type to any type:

$$T ::= \textbf{Integer} \mid \textbf{Boolean} \mid T \rightarrow T$$

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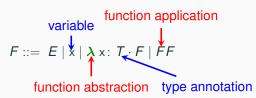
$$T ::=$$
Integer $|$ Boolean $|$ $T o T$

Examples:

$$(\pmb{\lambda} \ x\colon \textbf{Integer} \ . \ (x + x)) \ 4 \qquad \text{evaluates to}$$

$$((\pmb{\lambda} \ f\colon \textbf{Integer} \ \to \textbf{Integer} \ . \ \pmb{\lambda} \ x\colon \textbf{Integer} \ . \ f \ x) \ (\pmb{\lambda} \ y\colon \textbf{Integer} \ . \ (y + 1))) \ 3$$

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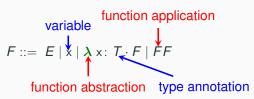
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Lambda language F: semantics

The semantics of language *F* extends *E*'s with a rule to handle lambda expressions:

$$\overline{\llbracket (\boldsymbol{\lambda} \ \boldsymbol{x} \colon T \cdot E_1) \ E_2 \rrbracket = \llbracket E_1 [\boldsymbol{x} \mapsto E_2] \rrbracket}$$

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For example:

```
\begin{split} & \big[ \big[ ((\lambda \text{ f: Integer} \rightarrow \text{Integer }. \ \lambda \ x: \ \text{Integer }. \ (x) \ (\lambda \ y: \ \text{Integer }. \ (y+1)) \ 3 \big] \\ & = \big[ \big[ (\lambda \ x: \ \text{Integer }. \ (y+1)) \ 3 \big] \\ & = \big[ \big[ (\lambda \ y: \ \text{Integer }. \ (y+1)) \ 3 \big] \\ & = \big[ \big[ 3+1 \big] \big] = 4 \end{split}
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For example:

```
 \begin{split} \big[ \big[ \big( (\pmb{\lambda} \text{ f: Integer} \to \text{Integer }. \ \pmb{\lambda} \times : \text{Integer }. \ (\texttt{f} \times) \ (\pmb{\lambda} \ y : \text{Integer }. \ (\texttt{y} + 1))) \ 3 \big] \\ &= \big[ \big[ (\pmb{\lambda} \times : \text{Integer }. \ (\pmb{\lambda} \ y : \text{Integer }. \ (\texttt{y} + 1)) \ X) \ 3 \big] \\ &= \big[ \big[ (\pmb{\lambda} \ y : \text{Integer }. \ (\texttt{y} + 1)) \ 3 \big] \\ &= \big[ (3 + 1) \big] = 4  \end{split}
```

The semantics of *F* is partial, which implies that:

- · we can evaluate abstractions only when they are applied
- we can evaluate variables only when they appear inside lambda expressions

A type system can enforce these rules by means of additional typing rules.

Lambda language *F*: type system

Function abstractions (which can be nested) introduce assumptions about the type of their arguments using type annotations. To keep track of these assumptions, the type system's rules now use an environment Γ , which is a mapping from variables to their types.

 $\Gamma \vdash F : T$ "F has type T under environment Γ "

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$$\Gamma \vdash F : T$$
 "F has type T under environment Γ "

With this new notation in place, the type system for F adds the rules:

$$\frac{\Gamma(\mathsf{x}) = \mathsf{T}}{\Gamma \vdash \mathsf{x} \colon \mathsf{T}} \quad \frac{\Gamma \cup [\mathsf{x} \mapsto \mathsf{T}_1] \vdash \mathsf{F} \colon \mathsf{T}_2}{\Gamma \vdash \lambda \ \mathsf{x} \colon \mathsf{T}_1 \ . \ \mathsf{F} \colon \mathsf{T}_1 \to \mathsf{T}_2} \quad \frac{\Gamma \vdash \mathsf{F}_1 \colon \mathsf{T}_1 \to \mathsf{T}_2}{\Gamma \vdash \mathsf{F}_1 \mathsf{F}_2 \colon \mathsf{T}_2} \quad \frac{\Gamma \vdash \mathsf{F}_1 \colon \mathsf{T}_1 \to \mathsf{T}_2}{\Gamma \vdash \mathsf{F}_1 \mathsf{F}_2 \colon \mathsf{T}_2}$$

For simplicity, we assume that variables are <u>uniquely named</u> throughout a whole expression.

Inverted rules

We can invert every rule of the type system, since they each refer to syntactically distinct terms. Some examples of inverted rules:

RULE Γ⊢ true: **Boolean** $\Gamma \vdash F_1$: Boolean $\Gamma \vdash F_2, F_3$: T $\Gamma \vdash \text{if } F_1 \text{ then } F_2 \text{ else } F_3 : T$ $\Gamma \cup [x \mapsto T_1] \vdash F \colon T_2$ $\Gamma \vdash \lambda \times : T_1 \cdot F : T_1 \rightarrow T_2$ $\Gamma \vdash F_1 \colon T_1 \to T_2 \quad \Gamma \vdash F_2 \colon T_1$ $\Gamma \vdash F_1 \vdash F_2 : T_2$

INVERSION

if
$$\Gamma \vdash \text{true} : T \text{ then } T = \text{Boolean}$$

if $\Gamma \vdash \text{if } F_1 \text{ then } F_2 \text{ else } F_3 : T \text{ then }$
 $\Gamma \vdash F_1 : \text{Boolean } \text{and } \Gamma \vdash F_2, F_3 : T$

$$\begin{array}{c} \text{if } \Gamma \vdash \pmb{\lambda} \ \ \mathsf{x} \colon \ T_1 \ . \ F \colon T \ \text{then} \\ T = T_1 \to T_2 \ \text{for some} \ T_2 \ \text{such that} \\ \Gamma \cup \left[\mathsf{x} \mapsto T_1\right] \vdash F \colon T_2 \end{array}$$

if $\Gamma \vdash F_1 \ F_2 \colon T$ then there is some type T_1 such that $\Gamma \vdash F_1 \colon T_1 \to T$ and $\Gamma \vdash F_2 \colon T_1$

Type checking

Inverted rules lead to a recursive type checking algorithm.

```
typeOf :: Environment -> F -> T
typeOf q fexp = case fexp of
  "true" -> Boolean
  "if" f1 "then" f2 "else" f3 -> if (typeOf g f1) == Boolean
                                    && (typeOf a f2) == (typeOf a f3)
                                 then (typeOf a f2)
                                 else error
  "lambda" x ":" t1 "." f -> let t2 = type0f (g + (x, t1)) f in
                                   t1 -> t2
 f1 f2
                              -> let t1 = type0f q f2
                                     (t1 \rightarrow t2) = type0f q f1 in
                                   †2
  -- more rules...
```

An expression f is well typed iff typeOf [] f returns without errors.

Type checking

Inverted rules lead to a recursive type checking algorithm.

More idiomatically using Haskell's Maybe monad:

```
typeOf :: Environment ->F -> Maybe T
typeOf g fexp = case fexp of
  "true" -> return Boolean
  "if" f1 "then" f2 "else" f3 -> do
             t1 <- typeOf a f1
             t2 <- typeOf a f2
             t3 <- type0f q f3
             if t1 == Boolean && t2 == t3 then return t2 else Nothing
  "lambda" x ":" t1 "." f -> do
             t2 \leftarrow type0f (g + (x, t1)) f
             return (t1 -> t2)
  f1 f2
                              -> do
             t1 <- type0f q f2
             t <- typeOf a fl
             case t of
                (t1 -> t2) -> return t2
                otherwise -> Nothing
  -- more rules...
wellTyped :: F -> Bool
wellTyped f = case typeOf [] f of
  Just _ -> True
  Nothing -> False
```

Type annotations

The type annotations used in lambda abstractions are only used for typing:

- the semantics ignores them
- inconsistent annotations will make typechecking fail (spuriously, if the system is correct)

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 - fails type checking even though it is well typed:

```
(\lambda x: Integer . if x then 0 else 1) true
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· fails type checking and it is not well typed:

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(\lambda x: Integer . if x then 0 else 1) 0
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Annotations are a trade-off between automation and expressiveness (flexibility):

- Users of the type system provide these annotations to support more expressive type checking rules
- The type checker is completely automatic given the annotations

Besides, explicit annotations are also a useful form of documentation.

Type reconstruction

An alternative to typing annotations is type reconstruction: the type checker tries to guess suitable types that make type checking pass.

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An approach to type reconstruction is constraint-based typing:

- typing constraints are equations between type expressions involving type variables
- typing rules generate constraints instead of directly checking them
- an expression is well typed iff the corresponding typing constraints have a solution – providing an instantiation of type variables

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- typing rules generate constraints instead of directly checking them
- an expression is well typed iff the corresponding typing constraints have a solution – providing an instantiation of type variables

 $\Gamma \vdash F : T \mid C$ F has type T under Γ whenever constraints C are satisfied

Here are some examples of constraint-based type rules for F.

We use lowercase letters to denote (fresh) type variables, which we implicitly assume are always fresh to lighten the notation.

Values do not introduce any constraints:

```
\overline{\Gamma \vdash \mathsf{true} \colon \mathsf{Boolean} \mid \{\}} \qquad \overline{\Gamma \vdash n \colon \mathsf{Integer} \mid \{\}}
```

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Conditional expressions introduce constraints about the type t_1 of the condition, and about the two branches' types t_2 , t_3 which have to be equal:

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Function applications introduce constraints about how the types of the applied abstraction, argument, and result are related:

$$\frac{\Gamma \vdash F_1 : t_1 \mid C_1 \quad \Gamma \vdash F_2 : t_2 \mid C_2}{C = C_1 \cup C_2 \cup \{t_1 = t_2 \to t\}}$$
$$\frac{\Gamma \vdash F_1 F_2 : t \mid C}{\Gamma \vdash C_1 \vdash C_2}$$

Here are some examples of constraint-based type rules for F.

We use lowercase letters to denote (fresh) type variables, which we implicitly assume are always fresh to lighten the notation.

Function abstractions, now without type annotations, introduce a fresh type variable t_1 , which will be constrained by function applications:

$$\frac{\Gamma \cup [x \mapsto t_1] \vdash F \colon t_2 \mid \textit{C}}{\Gamma \vdash \lambda \ x \ . \ F \colon t_1 \to t_2 \mid \textit{C}}$$

Type checking the expression λ x . if x then 0 else 1 results in:

Type checking the expression λ \times . if \times then 0 else 1 results in:

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$$C = \{t_x = \text{Boolean}, t_0 = \text{Integer}, t_1 = \text{Integer}, t_0 = t_1\}$$

which is clearly satisfiable.

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$$C = \left\{ egin{aligned} t_{\mathsf{X}} = \mathsf{Boolean}, t_0 = \mathsf{Integer}, t_1 = \mathsf{Integer}, t_0 = t_1, \ t_3 = \mathsf{Integer}, t_{\mathsf{X}}
ightarrow t_0 = t_3
ightarrow t_0 \end{aligned}
ight.$$

which is unsatisfiable.

Unification

The constraints generated by type reconstruction are equations with uninterpreted symbols (the type variables).

Such equations can be solved using the unification algorithm, which is very efficient (runs in linear time).

Type systems

More expressive type systems

Effect systems

The framework of type systems can be extended to accommodate checking more complex properties by extending the language of type annotations.

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An effect system piggybacks a standard type system to keep track of additional information about a program's behavior.

Suppose evaluating an expression may the side effects of modifying the value stored in some global variable. To analyze the side effects of evaluating a given expression:

- add the annotated function type $T_1 \stackrel{\mu}{\to} T_2$: the type of a function from T_1 to T_2 which, when evaluated, may modify variables in μ
- · use type judgments

 $\Gamma \vdash E : T \wr \mu$ E evaluates to a value of type T under Γ; during evaluation, side effects μ may take place

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After building an effect system with suitable rules, type reconstruction algorithms can be modified so that they also compute the side effects of each expression in a program.

Dependent types

Another extension of type systems are dependent types – types whose definition depends on some values.

(regular) type: lists of integers

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(regular) type: lists of integers

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Type checking programs using expressive dependent type annotations (with dependency constraints using an expressive logic) may be very complex or even undecidable – since it is essentially equivalent to deciding the validity of complex logic formulas.

For example, Coq is an interactive theorem prover whose logic is a functional language with very expressive dependent types.

Curry-Howard correspondence

The connection between constructive logic and types is deep as summarized by the Curry-Howard correspondence (also called <u>isomorphism</u>). The intuition is that a constructive proof of a proposition is isomorphic to the typechecking of a term.

LOGIC	TYPES
propositions	types
implication $P \Longrightarrow Q$	function type $P o Q$
conjunction $P \wedge Q$	product type $P \times Q$
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Haskell Brooks Curry, after whom programming languages Haskell, Brook, and Curry are named

Static analysis in practice

Sound vs. soundy

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VIEWPOINT

In Defense of Soundiness: A Manifesto

By Benjamin Livshits, Manu Sridharan, Yannis Smaragdakis, Ondřej Lhoták, J. Nelson Amaral, Bor-Yuh Evan Chang, Samuel Z. Guyer, Uday P. Khedker, Anders Møller, Dimitrios Vardoulakis Communications of the ACM, February 2015, Vol. 58 No. 2, Pages 44-46

We are not aware of a single realistic whole-program analysis tool that does not purposely make unsound choices. The reasons for such choices are engineering compromises [soundness vs. efficiency or precision trade off].

Most common language features are over-approximated. Some specific language features are under-approximated. We introduce the term soundy for such analyses.

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Whole-program analyses

Most static analyses are whole program: they model executions that traverse the overall program state crossing module boundaries.

The main challenge for whole-program analyses is scalability.

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procedure set_zero(x: ref Integer): { [x] := 0 }
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Which variables are modified by the call set_zero(y)?

We need to know all variables y may be aliased to. For this, we need to know all program executions that may reach the call – knowing the <u>callee</u> and the <u>caller</u> in isolation is not enough.

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Alternatives approaches:

annotations: users provide frame specifications of each

procedure - losing automation

coarse approximation: assume that every global variable may be

modified by the call - losing precision

Modular analyses

Some static analysis are naturally modular: they model each procedure or module separately, and have a way of combining each module's analysis results with the others'.

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Is the call b := unknown(c) well typed?
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Type systems are naturally modular because they summarize each program feature through its type.

Challenge: external code

Static whole-program analysis requires access to all source code that is executed. This may not be available:

- if we call a pre-compiled library
- if we use features that wrap native code calls

```
public class HelloJNI {
    // Load native library hello.dll (Windows) or libhello.so (*nix)
    static { System.loadLibrary("hello"); }
    private native void sayHello(); // declare native method
    public static void main(String[] args)
    { new HelloJNI().sayHello(); } // call native method
}
```

Challenge: external code

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How to handle external code:

- unsound approximation: sayHello() does nothing
- imprecise approximation: sayHello() may modify anything
- annotations: users annotate sayHello() with relevant information

Practical solutions typically combine these three approaches.

Challenge: reflection

Reflection provides capabilities to modify a program at runtime. Reflection is particularly powerful in dynamic languages such as Python and Javascript.

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This call executes the source code provided in (string) variable s as if it was declared at the call site.

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The Eval that Men Do

A Large-scale Study of the Use of Eval in JavaScript Applications
Richards, Hammer, Burg, and Vitek: ECOOP 2011

Static analysis and deductive verification

Some of the challenges of static analysis (modularity and tricky language features) are challenges of deductive verification too!

Static analysis and deductive verification tend to target different trade-offs:

- static analyses target scalability and automation (that is, not user annotations)
- deductive verification uses modularity and assumes expert users who can supply complex annotations

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Deductive verification may <u>use some static analysis</u> to lessen the <u>annotation</u> burden (for example, simple loop invariants) or to simplify what is to be <u>proved</u> (for example, assuming programs are well-typed).

Notable static analysis tools

- Astrée checks embedded C programs (no dynamic memory allocation or recursion) for absence of runtime errors such as undefined behavior
 - CCC (Code Contracts Static Checker), formerly known as Clousot, is an abstract interpreter for .NET programs checking the absence of common runtime errors relying on pre-/postconditions to achieve modularity
- **Frama-C** is an extensible analyzer for C programs, which supports a variety of common static analyses (reaching definitions, slicing, ...) as well as <u>deductive</u> verification and <u>dynamic</u> analyses through dedicated plug-ins
 - Infer is static analyzer for C/C++/Objective C and Java code based on separation-logic abstractions of memory usage
 - Scan by Coverity is a multi-language analyzer that can detect memory errors, concurrency issues, and incorrect API usage one of the first static analyzers that was widely applicable with good precision

Type checking tools

the Checker Framework extends Java's type system with custom type annotations that can be used to establish absence of bugs such as null-pointer dereferencing or string safety vulnerabilities

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In addition, every compiler (framework) includes type checkers and modules that perform static analyses enabling compiler optimizations.

Frameworks such as LLVM export APIs to perform and use static analyses in derived applications.

Summary

Static analysis: techniques

Static analysis is a large family of techniques for automatically establishing that a program is free from certain pre-defined erroneous behavior.

Static analysis techniques are normally based on over-approximating behavior at every program point.

soundness/completeness: sound and imprecise (finding a

reasonable trade-off between number of spurious warnings and soundness)

complexity: efficient algorithms that scale up to

large programs

automation: fully automated ("push button")

expressiveness: limited to fixed properties like "absence

of common errors"

Static analysis: tools and practice

Static analysis tools range from the components in a compiler framework that support optimizations, to type checkers, to analyzers that detect possible runtime errors (such as undefined behavior and memory problems). Overall, static analysis is used extensively in software technology.

Besides its usage for compiler construction, case studies of static analysis include the safety verification of large embedded programs – such as Astrée's verification of the absence of runtime errors in Airbus control software (> 130'000 lines of C code).

Main outstanding challenges:

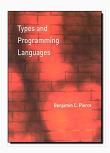
- supporting program features such as <u>reflection</u> and native calls without losing too much <u>soundness</u> or <u>precision</u>
- increasing flexibility and extensibility of frameworks to support checking new properties
- integrating additional information (such as specific assumptions or input constraints) when useful

Credits and further reading

This class's presentations of data-flow analysis and abstract interpretation was based on material by Sebastian Nanz (for the Software Verification course given at ETH Zurich in 2009–2015), which in turn was based on chapters in Principles of Program Analysis.

Principles of Program
Analysis

This class's presentations of type systems was adapted from Types and Programming Languages.



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