EE115 Lab 2

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To detect the envelope of $u(t) = A \ (a_{\rm mod} m_n(t) + 1) \cos(2\pi f_c t)$, we need a diode, a capacitor and a resistor. Assume that the diode has a resistance R_s when the voltage across it is positive (however small) and a resistance $R_o >> R_s$, when the voltage across it is negative. The resistor has the resistance R_l which satisfies $R_o >> R_l >> R_s$, The capacitor has the capacitance C. Answer the following questions and/or perform the following tasks:

1) If $f_c = 10 \text{MHz}$, what is a proper range of $R_s C$?

Below is the inequality the represents the relationship between the frequency of the carrier signal and the R_sC (Resistance of the diode with the capacitor):

$$R_s C \ll \frac{1}{f_c} = R_s C \ll \frac{1}{10 \text{MHz}}$$

Thus, the proper range is: $R_sC << 100 \text{ns}$. Meaning R_sC needs to be much smaller than 100 ns but larger than 0. Effectively that range would be from 0 to 10ps

2) If m(t) has a bandwidth B equal to 10kHz, what is a proper range of R_lC ?

The relationship between the R_lC and the bandwidth (the amount of information transferred per second) of the signal is as follows:

$$\frac{1}{f_c} << R_l C << \frac{1}{B}$$

Thus, the proper range is: $10\mu s << R_l C << 100 \mathrm{Ms}$. Meaning $R_l C$ needs to be much smaller than 100 Ms but larger than 0. Effective that range would be from 0 to 10 hs

3) If $R_s = 10^{-3} \Omega$ and $R_l = 5\Omega$, how do your choose C to meet the above conditions?

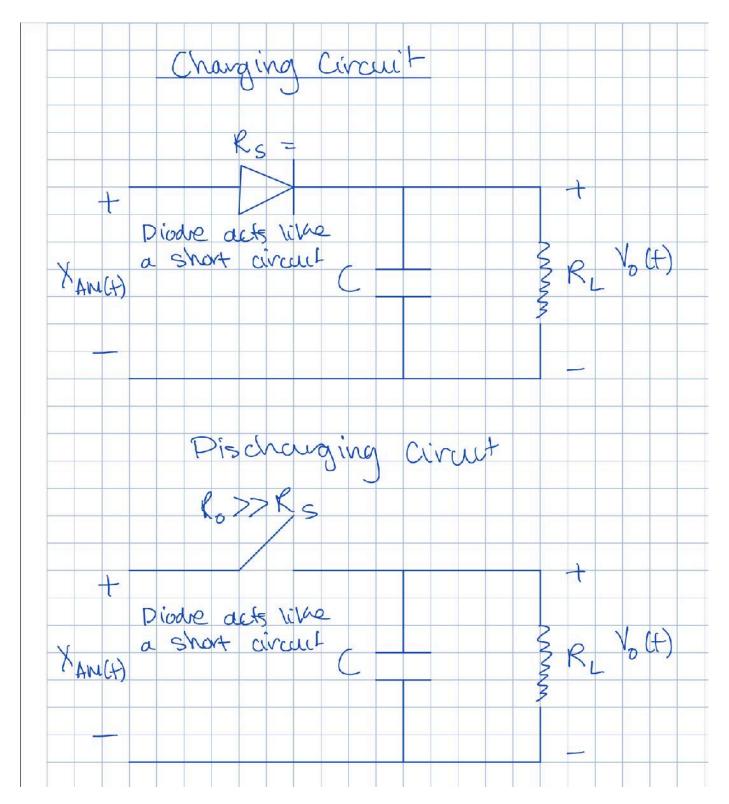
The relationship between time τ and the RC circuit is:

$$\tau = R_{s/l}C = \frac{1}{2\pi f_c} -> C = \frac{1}{2\pi f_c R_{s/l}}$$

The capacitor should be $3.1831 nF \ll C \ll 3 \mu F$

4) Sketch the equivalent circuit (of the envelope detector) when the capacitor is in charging mode, and the equivalent circuit when the capacitor is in discharging mode.

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5) Let the input voltage be $v(t) = \delta(t)$. Determine the corresponding output voltage (impulse response) $h_c(t)$ of the circuit in the charging mode. How does $h_c(t)$ relate to R_sC ? If $v_i(t) = u(t)$ (step function), what would be the output voltage (step response) of the circuit?

Every linear time-invariant signal can be completely characterized by the impulse repsonse. The impulse response can be found by taking the ratio of the output voltage to the input voltage. First step is convert the relationship between parallel components (capacitor and resistor) to the Laplace domain

$$\frac{V_0(s)}{V_i(s)} = H(s) = \frac{\frac{1}{R} + sC}{R_s + \frac{1}{\frac{1}{R} + sC}} = \frac{1}{R_s \left(\frac{1}{R} + sC\right) + 1} = \frac{1}{1 + \frac{R_s}{R} + R_sC}$$

We then assum that the ration between $\frac{R_s}{R} << 1$ thus,

$$V_0(s) = \frac{1}{1 + sR_sC} V_i(s)$$

$$V_0(t) = e^{-\frac{1}{R_s C}t}$$

The relation between $h_c(t)$ and R_sC is that the larger the impediance from the RC circuit the lower the output voltage. Specifically, the larger the value of R_sC the faster the output voltage decays.

When the input voltage is the step response, the output voltage is:

$$V_0(s) = \frac{1}{1 + sR_sC} * \frac{1}{s} = \frac{1}{S} - \frac{R}{1 + sR_sC}$$

Thus the output voltage would be: $V_o(t) = u(t) - e^{-\frac{1}{R_sC}t}u(t)$

6) If the output voltage is initially at $h_d(0) = V$, determine the free response of the output voltage $h_d(t)$ in the discharging mode. How does $h_d(t)$ relate to R_lC ?

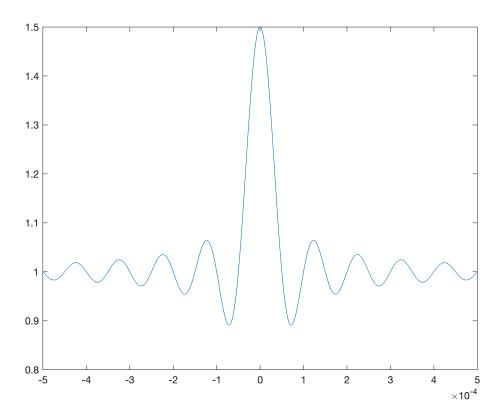
Perform partial traction decomposition

$$I_{O}(s) = 1 - \frac{1}{scr_{L}+1}$$

$$I(t) = S(t) - O(t)t \cdot u(t)$$

7) Assume $A=1, a_{\rm mod}=0.5,$ and $m(t)={\rm sinc}(20\,x\,10^3t)=\frac{{\rm sin}(\pi 20x10^3t)}{\pi 20x10^3t}.$ Plot the envelope of u(t) for $-0.5\,z\,10^{-3} < t < 0.5\,x\,10^{-3}$

```
t = -0.5*10^-3:0.0000001:0.5*10^-3;
y_t = 1+0.5*sinc(20*10^3*t);
figure
plot(t,y_t)
```



8) Now let $f_c = 80 \times 10^3$. Plot u(t) for $-0.5 \times 10^{-3} < t < 0.5 \times 10^{-3}$ for and compaire it with its envelope.

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u_t = (1+0.5*sinc(20*10^3*t)).*cos(2*pi*80*10^3*t);
figure
plot(t,u_t)
```

