

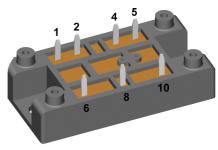
Standard Rectifier Module

3~ Rectifier			
$\boldsymbol{V}_{\text{RRM}}$	=	1800	
I_{DAV}	=	45	
I_{FSM}	=	300	

3~ Rectifier Bridge

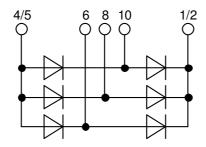
Part number

VUO34-18NO1



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: V1-A-Pack

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Height: 17 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you.

Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

- to perform joint risk and quality assessments; the conclusion of quality agreements;
- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

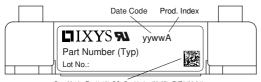
20151102c



Rectifier					Ratings	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			1900	٧
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1800	V
I _R	reverse current	V _R = 1800 V	$T_{VJ} = 25^{\circ}C$			20	μΑ
		$V_R = 1800 \text{ V}$	$T_{VJ} = 150$ °C			1	mΑ
V _F	forward voltage drop	I _F = 15 A	$T_{VJ} = 25^{\circ}C$			1,13	V
		$I_F = 45 \text{ A}$				1,46	٧
		I _F = 15 A	T _{VJ} = 125°C			1,06	٧
		$I_F = 45 A$				1,48	٧
IDAV	bridge output current	T _C = 110°C	$T_{VJ} = 150$ °C			45	Α
		rectangular d = ⅓					
V _{F0}	threshold voltage		T _{VJ} = 150°C			0,81	٧
r _F	slope resistance \(\) for power	loss calculation only				14,9	mΩ
R _{thJC}	thermal resistance junction to ca	ase				1,7	K/W
R _{thCH}	thermal resistance case to heats	sink			0,4		K/W
P _{tot}	total power dissipation		$T_{C} = 25^{\circ}C$			70	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			300	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			325	Α
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			255	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			275	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			450	A ² s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			440	A²s
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			325	A ² s
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			315	A²s
CJ	junction capacitance	$V_R = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		11		pF



Package V1-A-Pack					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
I _{RMS}	RMS current	per terminal				100	Α	
T _{VJ}	virtual junction temperature			-40		150	°C	
T _{op}	operation temperature			-40		125	°C	
T _{stg}	storage temperature			-40		125	°C	
Weight					37		g	
M _D	mounting torque			2		2,5	Nm	
d _{Spp/App}	creepage distance on surface striking distance through air		terminal to terminal	6,0			mm	
$d_{\text{Spb/Apb}}$			terminal to backside	12,0			mm	
V _{ISOL}	Toolation voltage	t = 1 second	50/60 Hz. BMS: Isor ≤ 1 mA	3600			٧	
		t = 1 minute		3000			٧	



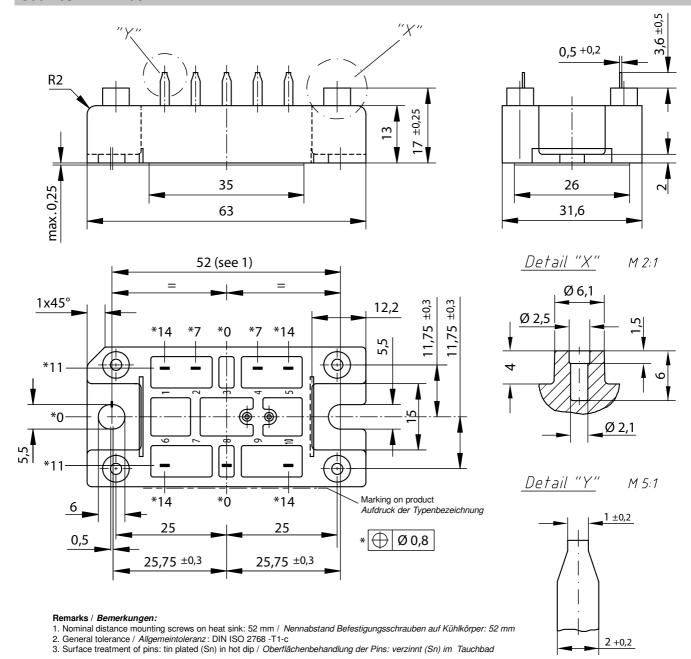
Data Matrix: Typ (1-19), DC+Prod.Index (20-25), FKT# (26-31) leer (33), lfd.# (33-36)

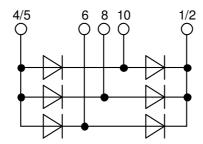
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	VUO34-18NO1	VUO34-18NO1	Blister	24	461156

Equivalent Circuits for Simulation			* on die level	$T_{VJ} = 150 ^{\circ}\text{C}$
$I \rightarrow V_0$	R_0	Rectifier		
V _{0 max}	threshold voltage	0,81		V
$R_{0 \text{ max}}$	slope resistance *	13,7		$m\Omega$



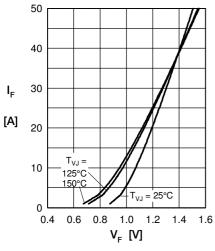
Outlines V1-A-Pack

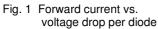






Rectifier





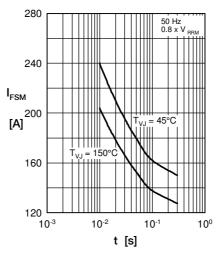


Fig. 2 Surge overload current vs. time per diode

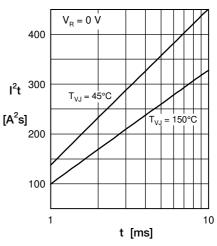


Fig. 3 I²t vs. time per diode

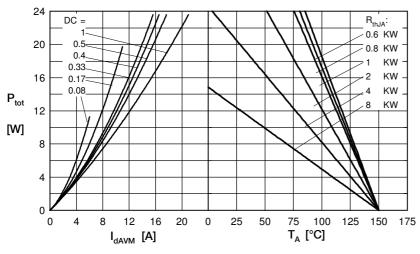


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

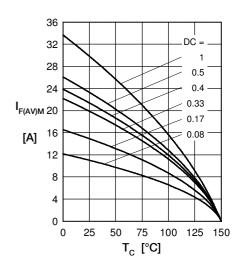


Fig. 5 Max. forward current vs. case temperature per diode

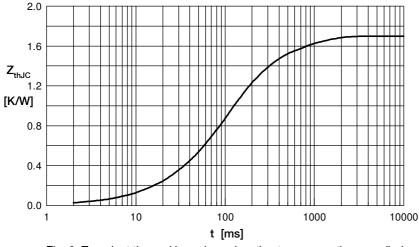


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for Z_{thJC} calculation:

i	$R_{th}^{}$ (K/W)	t _i (s)
1	1.150	0.1015
2	0.150	0.1026
3	0.100	0.4919
4	0.300	0.6200