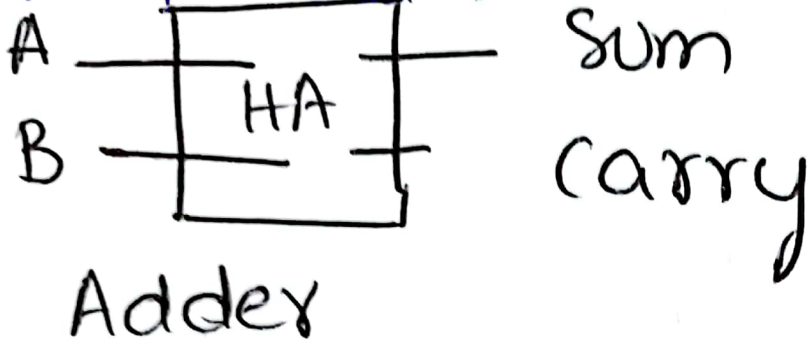


Half Adder



2 bits addition

A	B	Sum	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

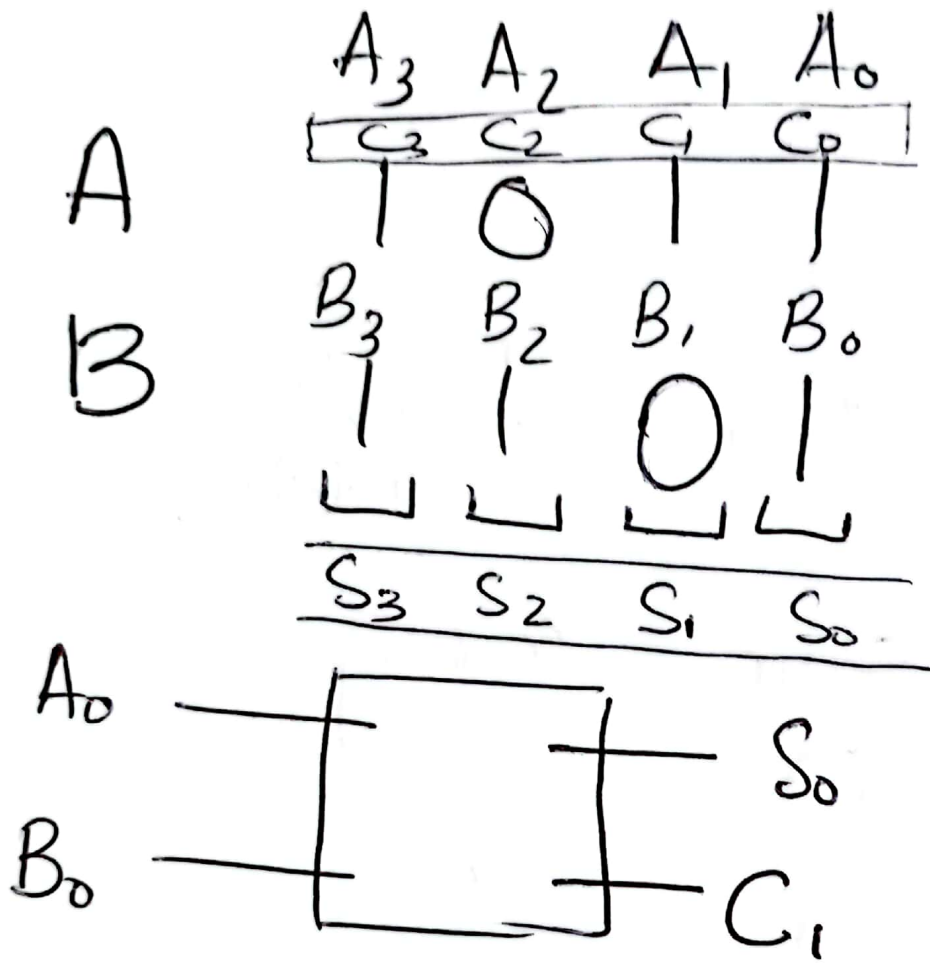
K-Map

	B	0	1
A	0	0	1
1	1	1	0

Sum Expression

$$\begin{aligned} & \bar{A}B \\ & + A\bar{B} \\ & = A \oplus B \\ & \text{3 gate delay} \end{aligned}$$

Carry expression = AB



1 gate delay

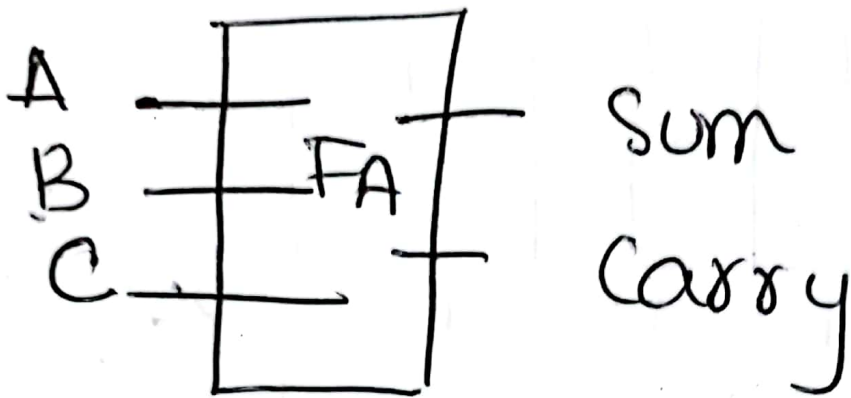
Use 2 Half adders

$$C_1 + A_1 = I_1$$

$$I_1 + B_1 = S_1$$

Full Adder

3 bit add



A	B	C	Sum	Carry
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Y C

BC Sum

(4)

	00	01	11	10
A 0	0	1	0	1
1	1	0	1	0

$$= \bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}\bar{C} + ABC$$

$$= A \oplus B \oplus C$$

→ 2 gate delay

— sum of products

Carry

BC

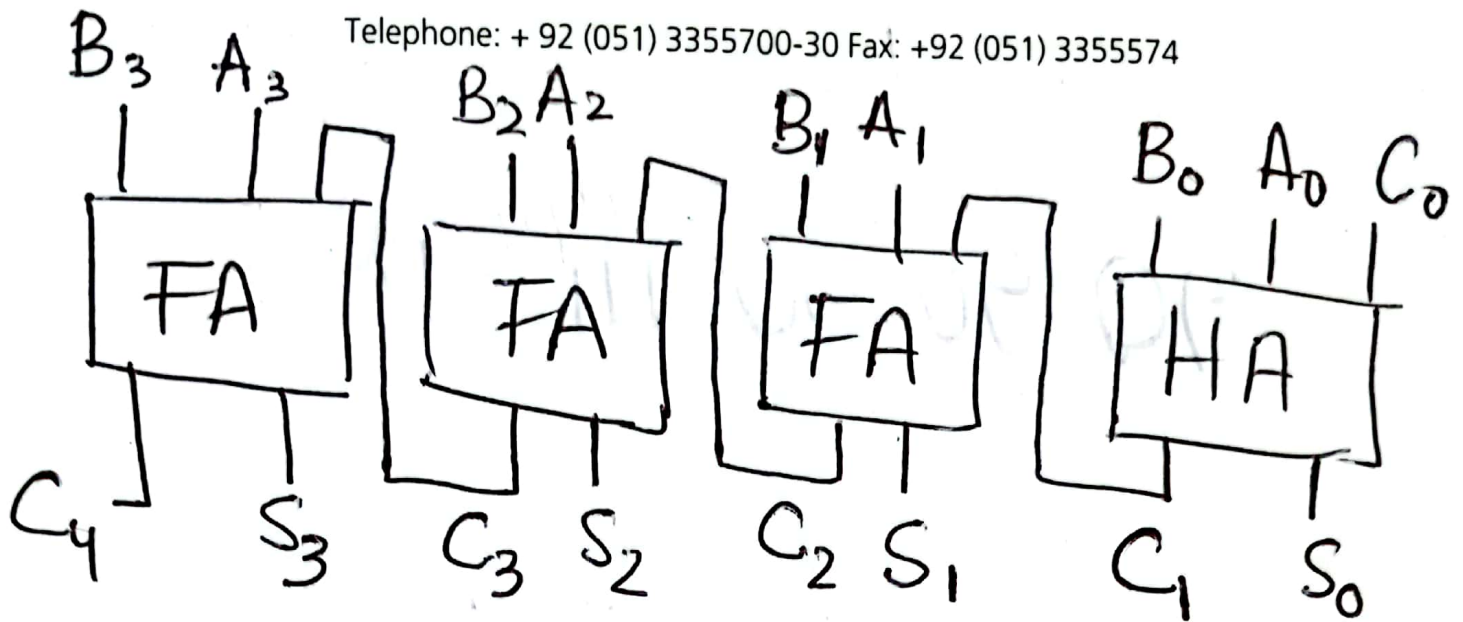
A

	00	01	11	10
0	0	0	1	0
1	0	1	1	1

$$= BC + AC + AB$$

→ 2 gate delay

— put this circuit in the block for carry



- Ripple adder
- Issue: the carry causes delay! Delay!
- The inputs A & B are available instantly
- however C_1 is not available until the addition is performed. (64 bit)

Look - Ahead Carry

(6)

- trade-off complexity
for time efficiency

$$P = A \oplus B, \quad G = A \cdot B$$

$$C_1 = C_0 (A_0 \oplus B_0) + A_0 B_0$$

$$C_1 = C_0 P_0 + G_0 \quad \text{--- (1)}$$

$$C_2 = C_1 P_1 + G_1 \quad \text{--- (2)}$$

$$C_3 = C_2 P_2 + G_2 \quad \text{--- (3)}$$

— Substitute equation (1) in (2)
to get an expression for C_2 that is only a function
of A, B and C_0 (input
that is available immediately)

— Substitute (2) in (3)