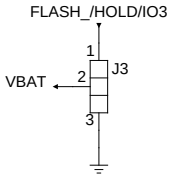
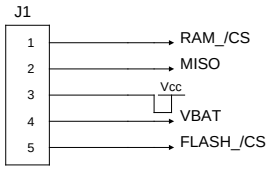
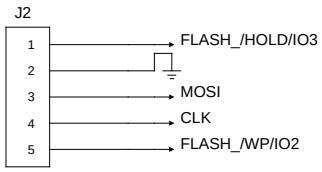
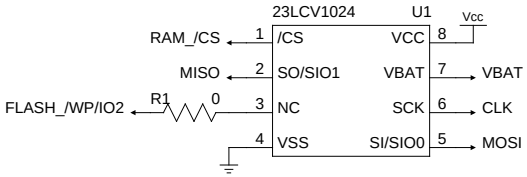
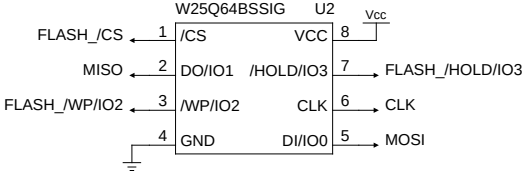




RAM: 2.5 − 5V
Flash: 2.7 − 3.6V







23LCV1024 notes	23LC1024 diffs
when /CS is high, SO is hi−z tie VBAT to VSS if not in use MCU should check the mode first (SPI/SDI)	NC −> SIO2 VBAT −> /HOLD/SIO3 pin 3 should not be left floating in SPI and SDI mode −let MCU pull it high so the bus can recover if SQI is exited by accident

W25Q64BSSIG notes

when /CS is high, DO or SI0−3 are hi−z
when /HOLD is low, DO is hi−z and signals on CLK and DI are ignored

clk, miso, mosi, hold/io3, wp/io2, cs1, cs2, pwr, bat, gnd