CSE 436/536
Digital Integrated Circuits
Assignment 3

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First of all, hello Alp teacher,

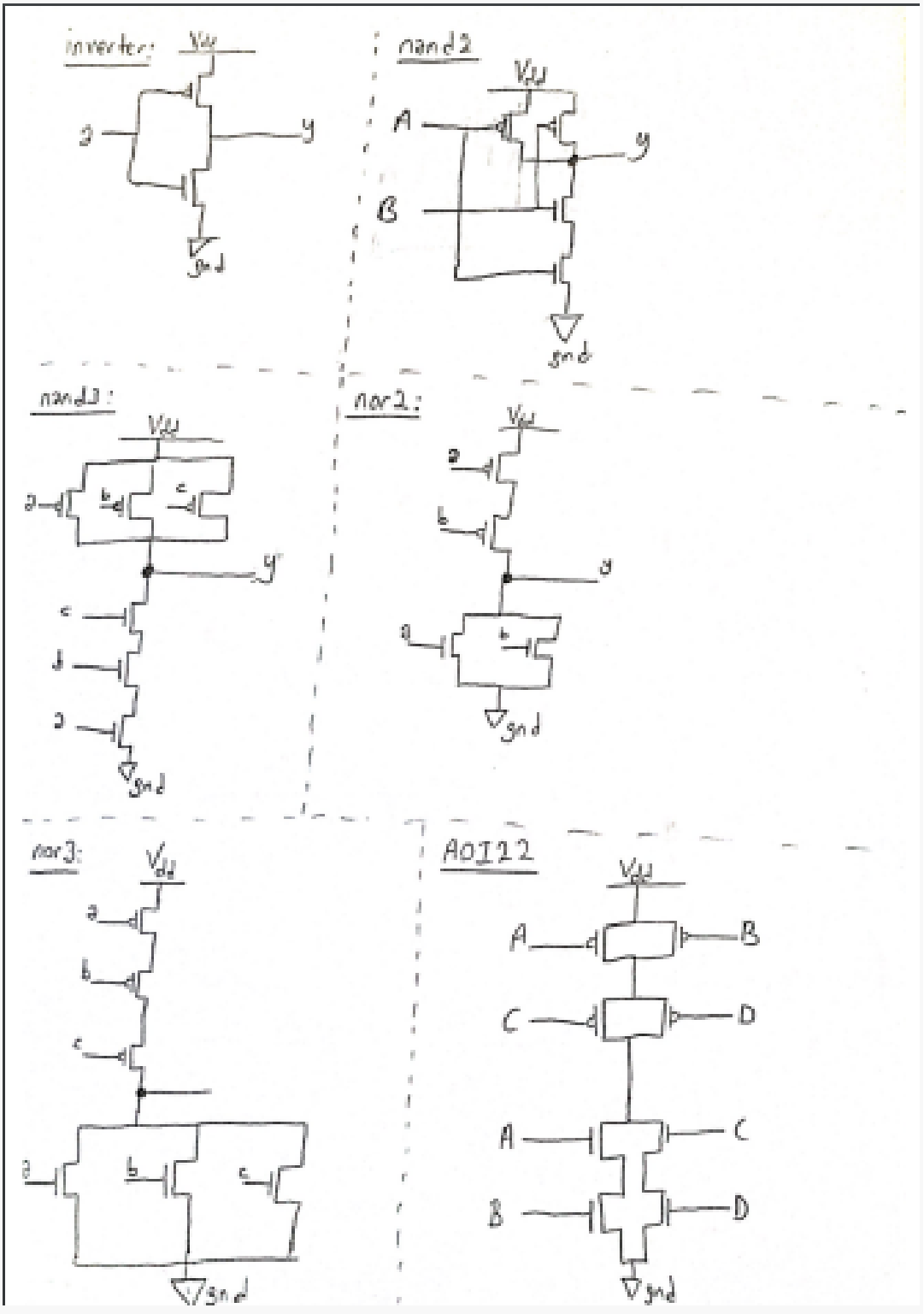
I'm sorry for sending such a half and bad assignment, but I had to return to my family this week because my mental health is really not good these days.

I tried to optimize the components you wanted as much as I could, but because of my mentality, it can probably be done much better, I'm sorry for that.

However, I tried to design and do everything myself in MAGIC, I'm not sure if it's right or wrong, so I couldn't ask anyone. I also made the XOR gate, but I didn't want to put it in this assignment because I did it by watching the tutorial on the internet.

In some examples, I saw that a contact (psubscontact or nsubscontact like that I dont remember exact names right now) was also placed on the metal layers, so I put it in some examples, but not in others.

Thank you.


inverter:
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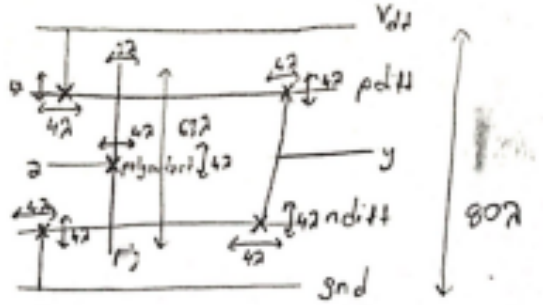
y
gnd
nand2
Vdd
A
B
y
gnd
nand3:
Vdd
a
b
c
y
gnd
nor2:
Vdd
a
b
y
gnd
nor3:
Vdd
a
b
c
gnd
AOI22
Vdd
A
B
C
D
gnd

Inverter



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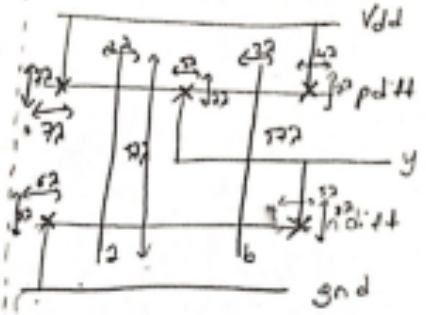

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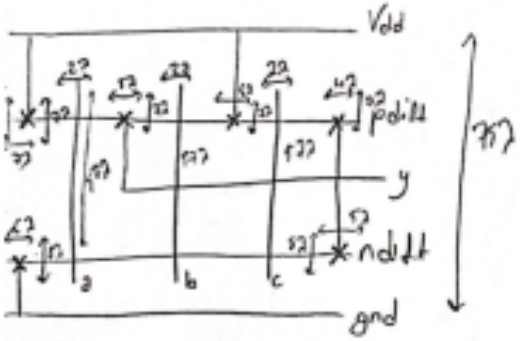
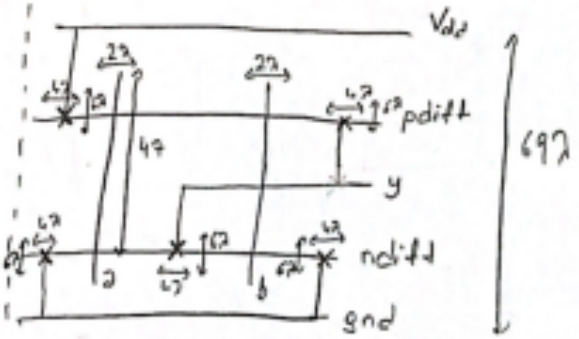


nor2:



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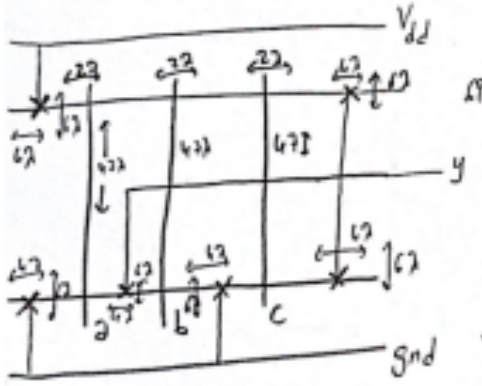

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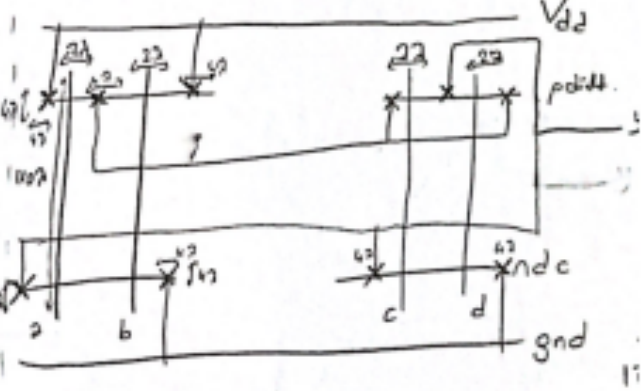
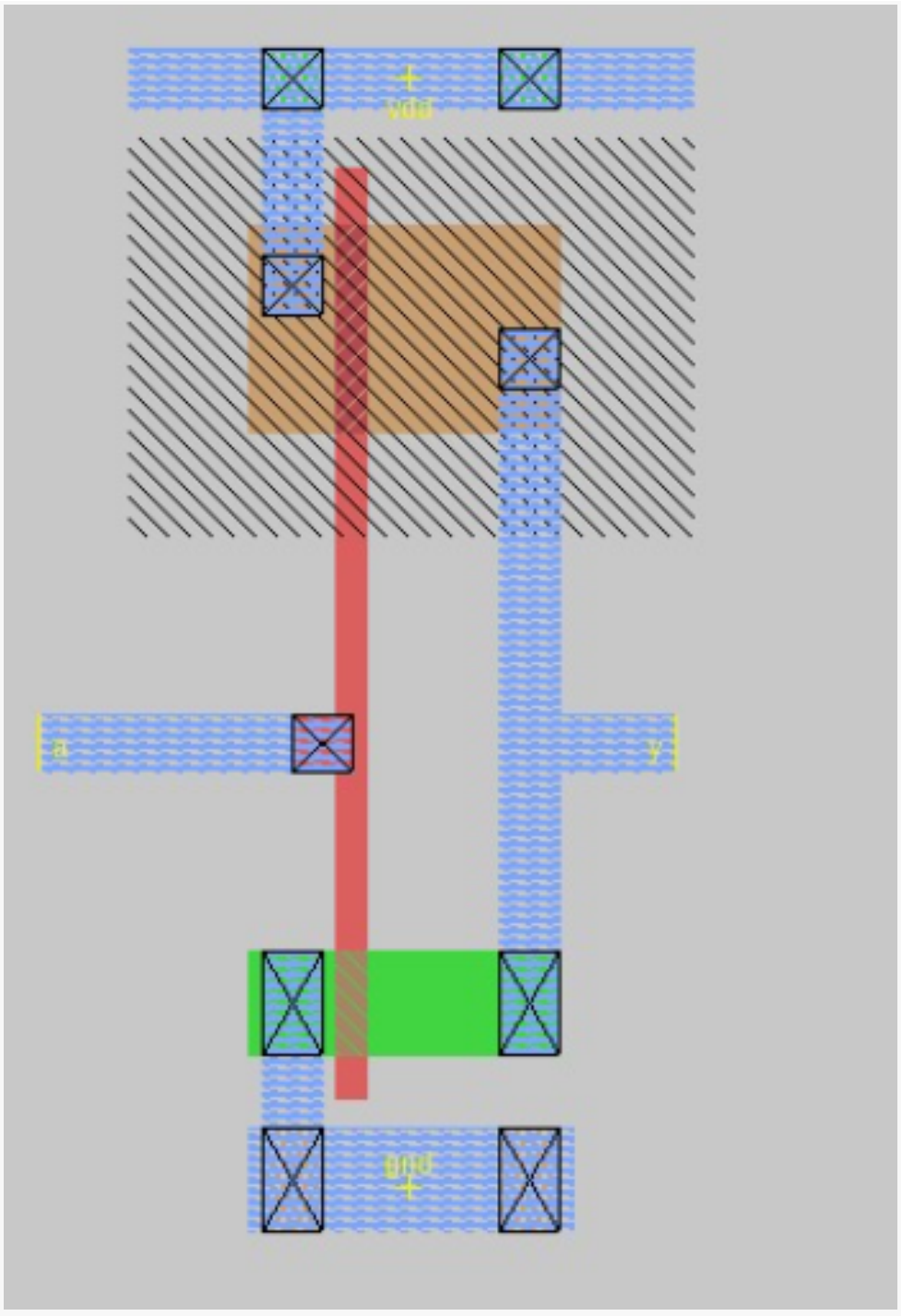


Inverter



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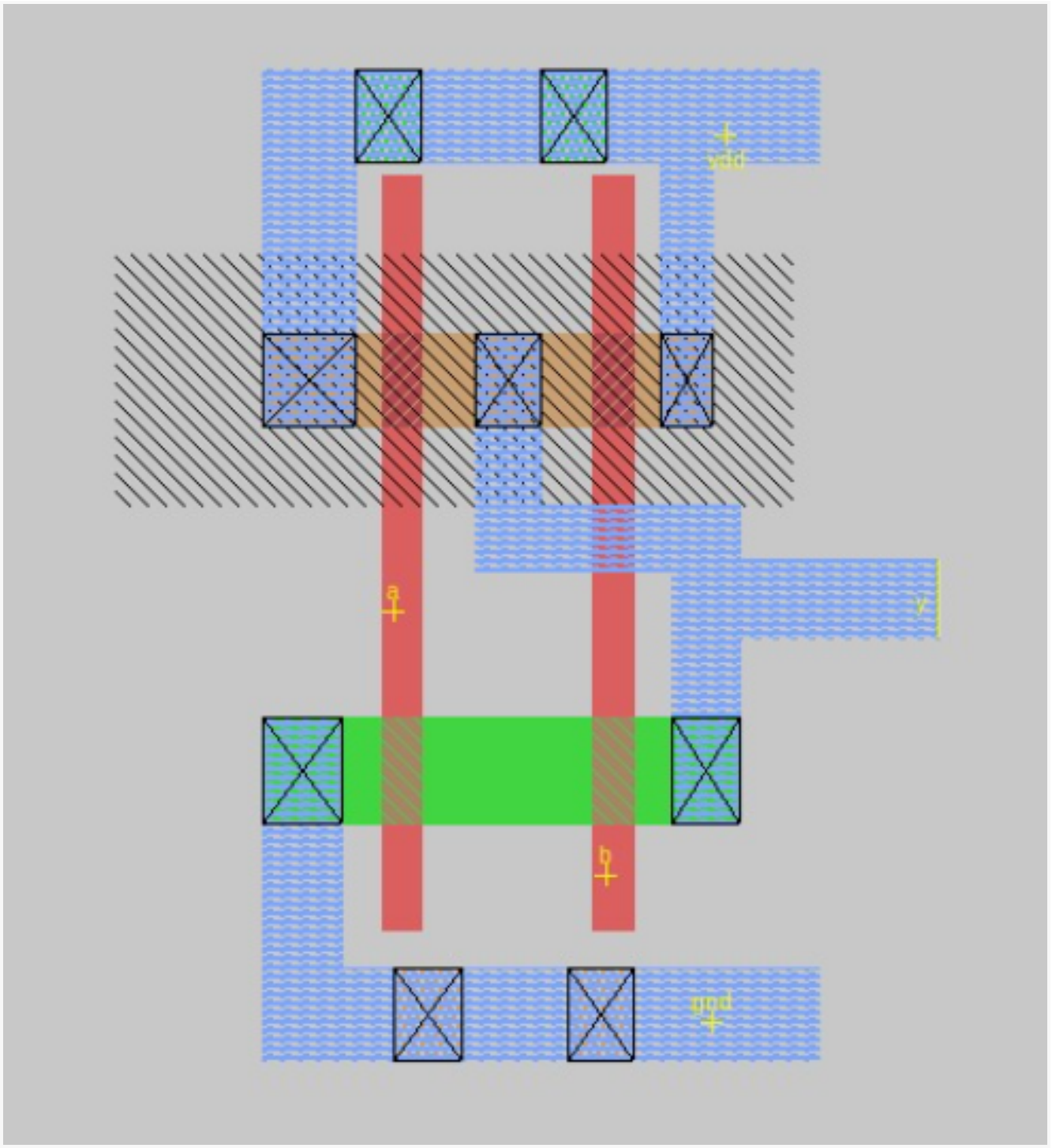

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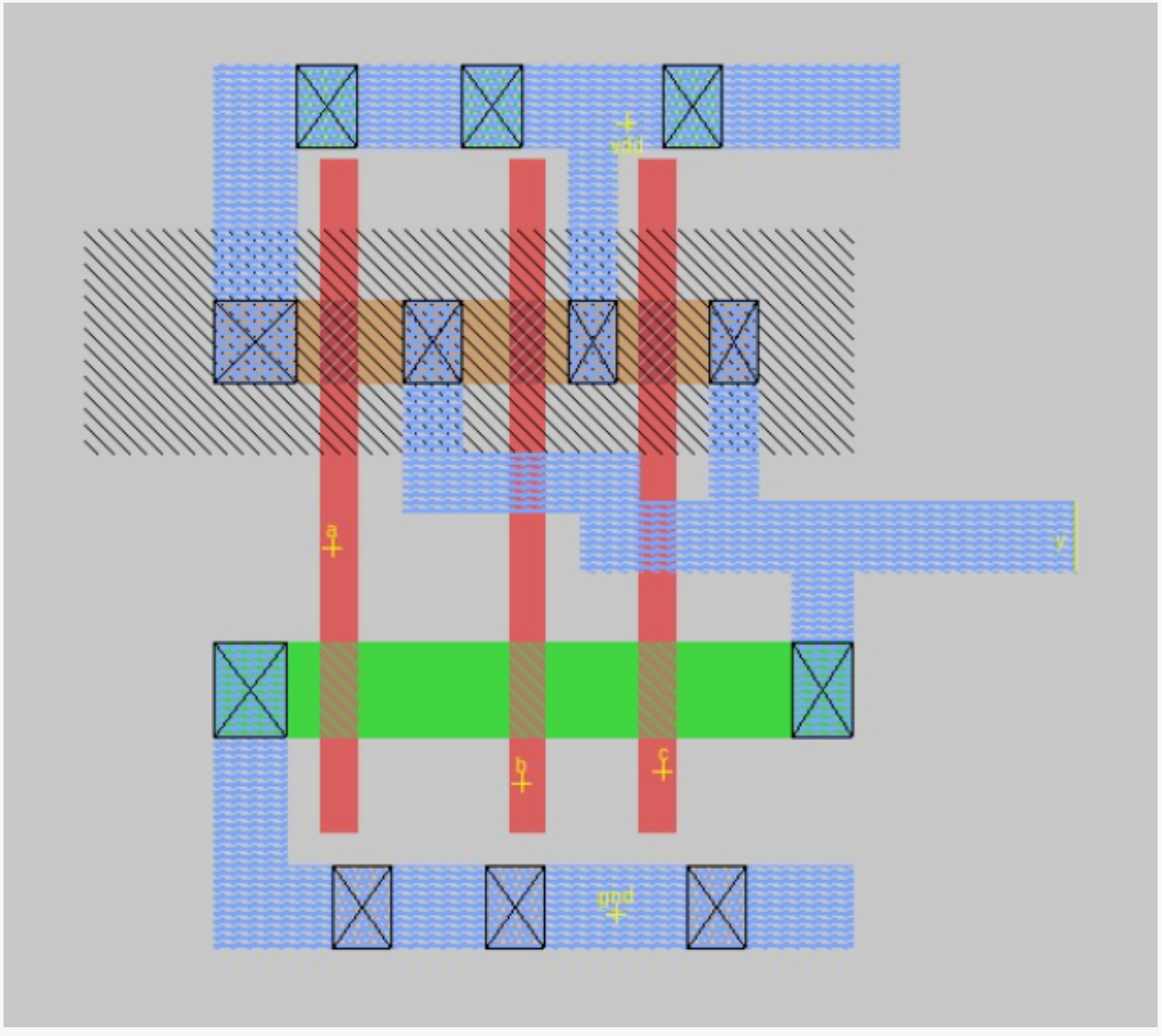
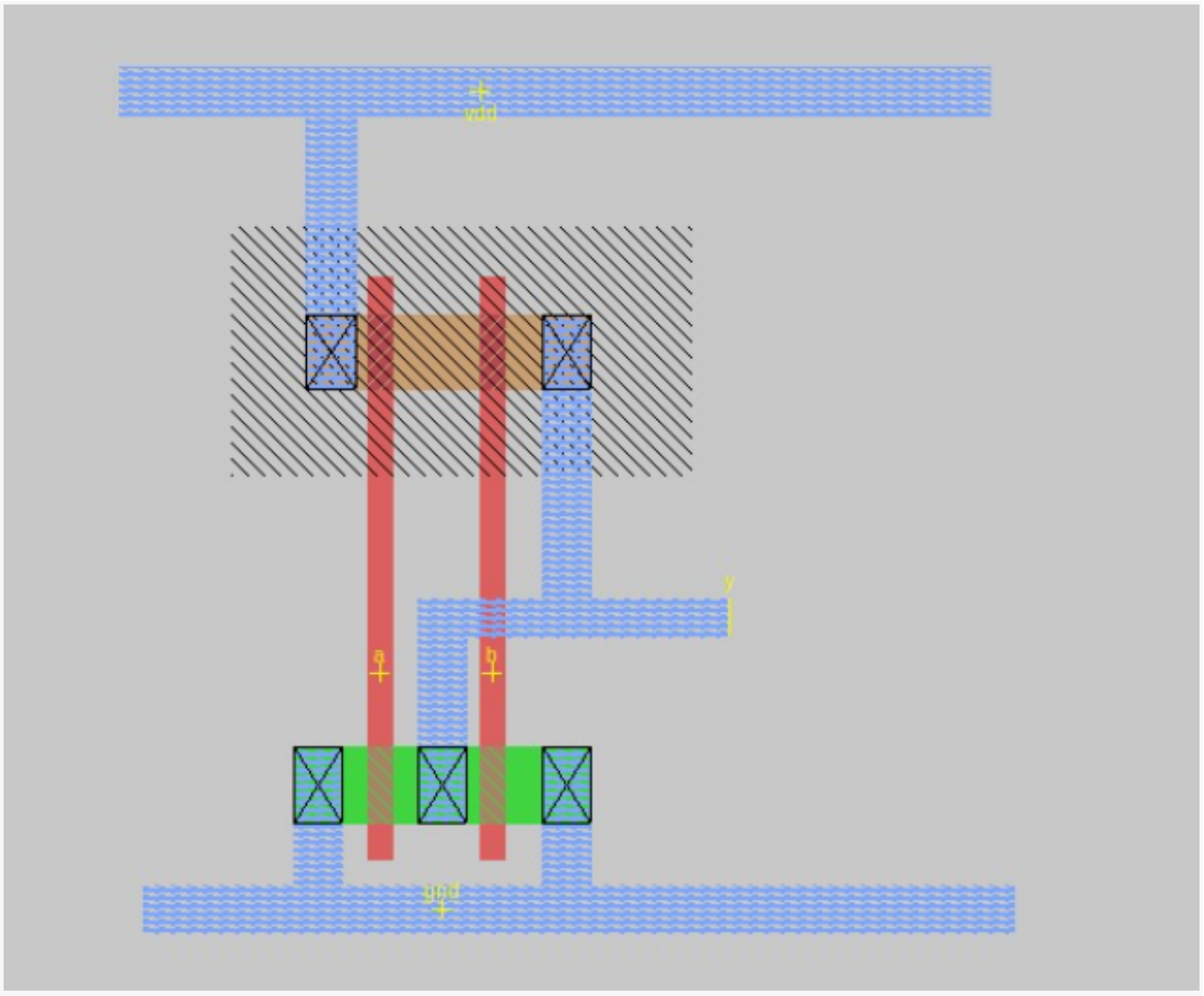
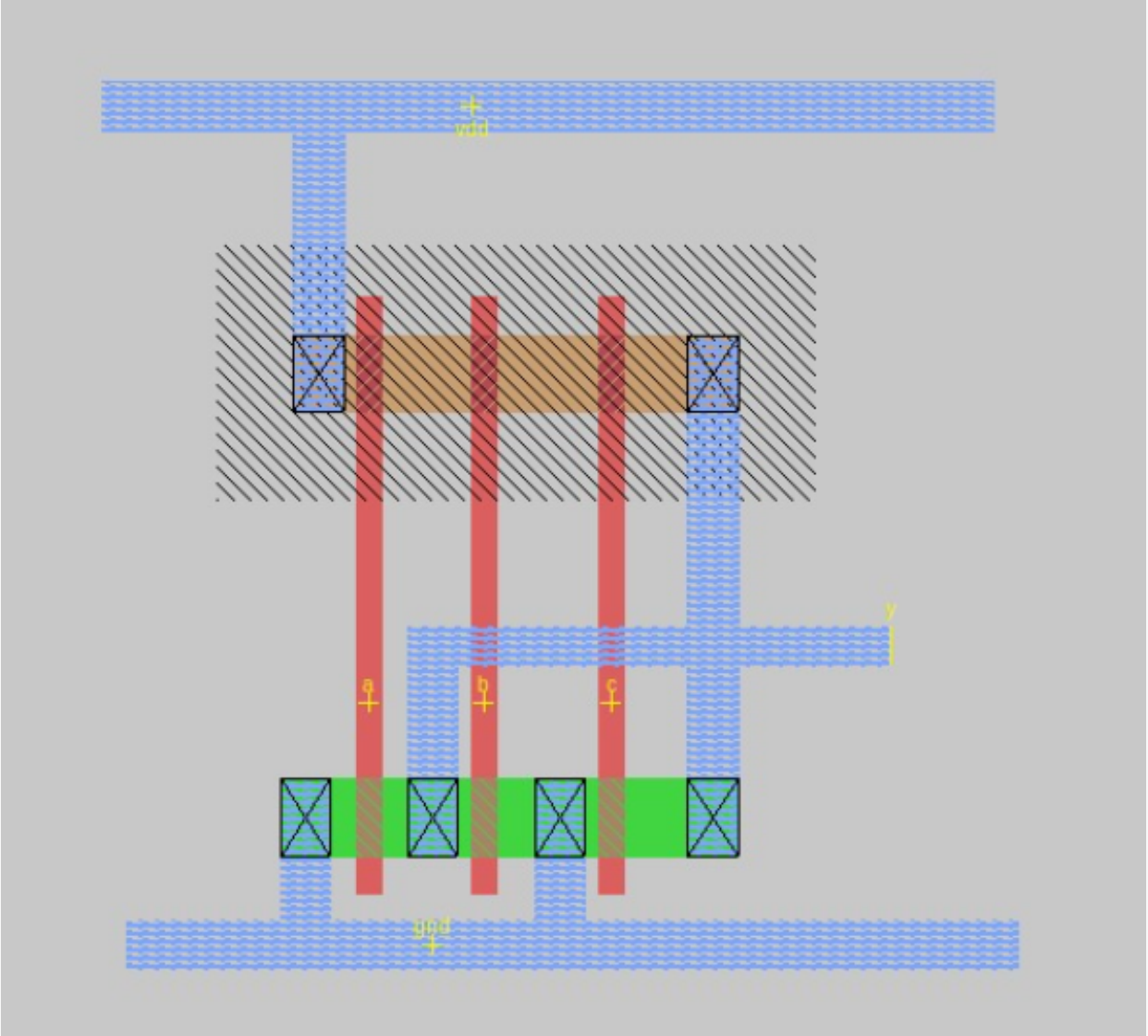
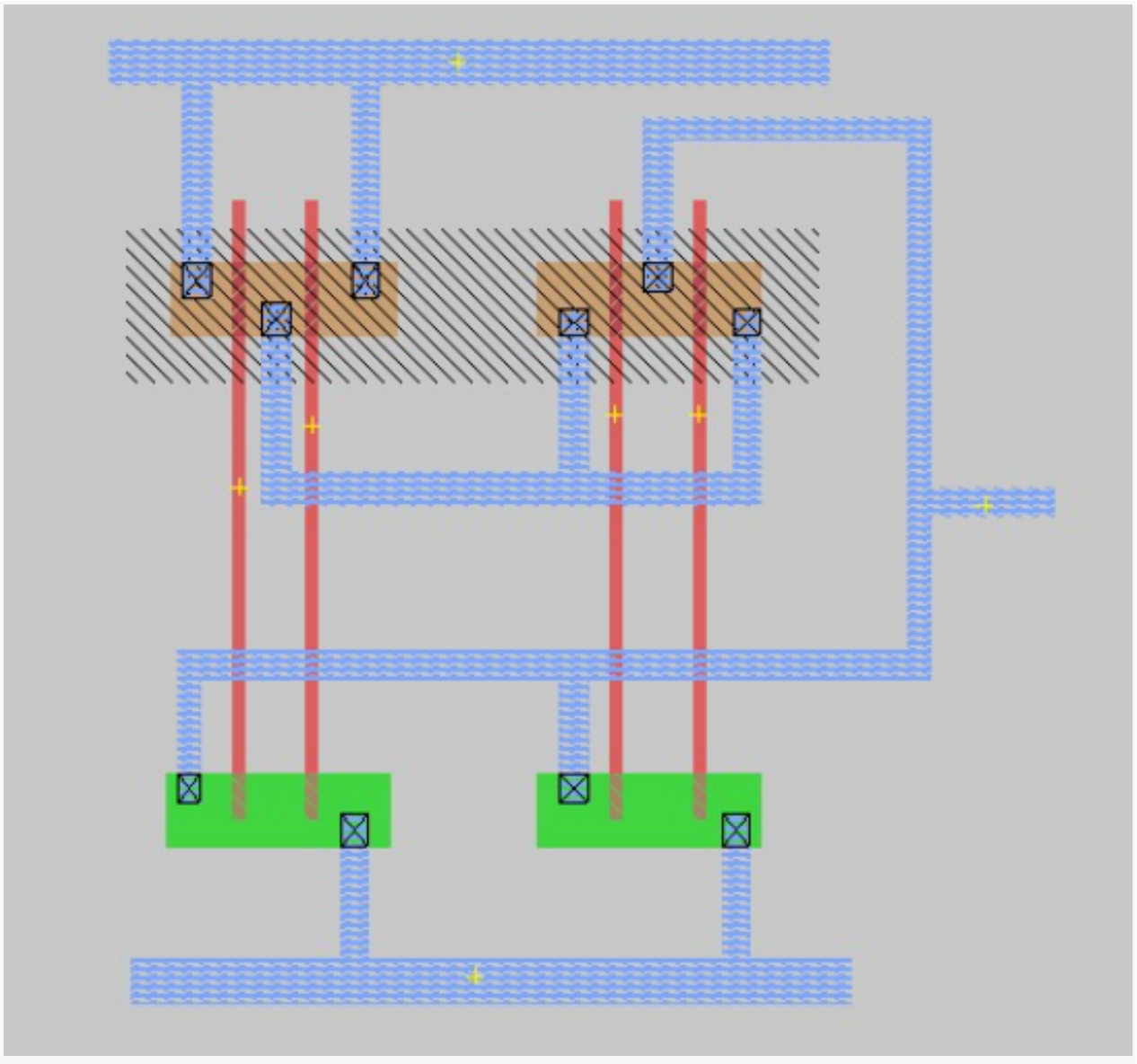


NOR2



NOR3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