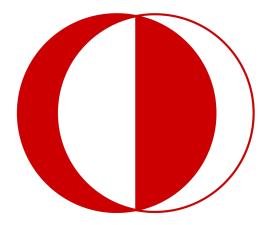
DC/DC Voltage Regulator

Complete Simulation Report

Cemil Ürgüp Burak Kemal Kara

A report presented for the sake of Humanity and Science



Electrical & Electronics Engineering
MIDDLE EAST TECHNICAL UNIVERSITY
TURKEY
23.03.2020

Contents

1	Introduction		
2	Forward Converter topology	2	
3	Design Equations	2	
	3.1 Transformer Considerations	2	
	3.2 MOSFET Considerations	4	
	3.3 Output Inductor Considerations	4	
	3.4 Capacitor Considerations	4	
	3.5 Diode Considerations	4	
4	References	Δ	

Minimum Input Voltage (V)	24
Maximum Input Voltage (V)	48
Output Voltage (V)	15
Output Power (W)	48
Output Volt. Peak-to-Peak Ripple (%)	2
Switching Frequency (fs)	100 kHz

1 Introduction

Switch mode power supplies are commonly used to DC/DC converter applications. Beside of voltage control, it provides galvanic isolations. In this project we used Forward Converter topology which is good choice when output need high current and power range is below 200W. This document aims to discuss component selections according to teoric and computational simulation results.

2 Forward Converter topology

3 Design Equations

The following specifications are design equtions for Forward Converter Design.

3.1 Transformer Considerations

First of all we should calculate turn ratio of transformer. For the sake of simplicity reset winding and primary winding ratio is same.

$$n_1 = n_3 \tag{1}$$

This equation limit max duty ratio to 50%, since core cant demagnetize faster than magnetizing time. Primary and secondary winding ratio can found when critical points are considered. When Input decreased minumum, Duty cycle reached max. ratio. At this point this converter should supply desired V_o .

$$V_o < v_i \frac{n_3}{n_1} D \implies \frac{n_3}{n_1} > \frac{V_o}{V_i \cdot D} = 1.33 \implies 2$$
 (2)

When $V_o = 15V$ as specifications, extra voltage drop on D_2 around 1V.

 $V_{o,max} = 16V.$

Minimum voltage is 24 V.

Maximum D is 50%.

At this point we choose R type material ferrite core (Code:0R45959EC). We selected since,R type core loss is less than P type core. According to this core turning factor can found from Lenz Rule,

$$V = n_1 \frac{d\phi}{dt} \implies V_{i,max} = \frac{n_1 \cdot B_{sat} \cdot A_e}{Dmax \cdot T_s}$$
(3)

$$n_1 > 2.18$$

 $V_{i,max}$ =48 V

 $D_{max}=0.5$

 $B_{sat}=0.3$

 $A_e = 368mm^2$ taken from datasheet [1].

This is minimum turning ratio to avoid core saturation. To calculate max turn number we should calculate wire type to know wire size. At the worst scenario we have $V_{in,min} = 24V$. For primary winding,

$$I_{primary} = \frac{P_o}{V_{in.min}} = 2A \tag{4}$$

AWG	Area mm^2	Max. Current Amperes
AWG#16	1.31	3.7
AWG#18	0.823	2.3

For secondary winding,

$$I_{secondary} = \frac{I_{primary}}{n} = 1A \tag{5}$$

Since this currents ratings are for ideal case, it is safer to choose higher rating wires. From AWG chart, for primary and reset windings can use AWG #16, Secondary AWG #18. Selection criteria is max current. Their properties are shown at table 2. Core window area is calculated from datasheet. Also geometry parameters are taken from datasheet [1].

$$A_w = (E - F) \cdot D = 510mm^2 \tag{6}$$

Since area of wires are known max turn can be calculate by,

$$N_{max} = \frac{k_{fill} \cdot A_w}{A_{pri} + n \cdot A_{sec} + A_{reset}} = 71.73 \tag{7}$$

Increasing turn is good because flux density decreasing and core loss decreasing according to Steinmetz equation, but increased turn also increase resistance of wire and copper loss increased. Core loss equation,

$$P_{fe} = K_{fe}(B_{peak})^{\beta} A_c l_m \tag{8}$$

 $P_{fe}at100kHz,100mTgiven85mW/cm^3 \\ P_{fe}at100kHz,100mTgiven550mW/cm^3$

$$\therefore \beta = 2.7 \quad , K_{fe} = 42$$

Core loss is simply I^2R_{ac} but wires new resistance should be calcualted by skin effect. Since skin depth decrease wire area new resistance calculated by,

$$R_{ac} = \frac{n \cdot MLT \cdot \rho}{Area} \tag{9}$$

To find optimum turn number loss calculated at matlab script which provided at github repo.

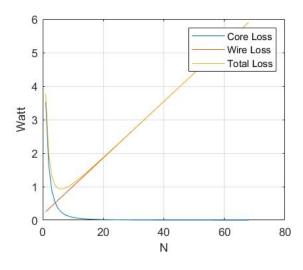


Figure 1: According to number of turn Total loss calculated

It is better to select turn ratio 8 which Total loss is 1 Watt. At this number $R_{pri}=R_{reset}=0.05k\Omega,$ $R_{sec}=0.06k\Omega.$

By using AL number windings inductance found and,

$$X_m = k \cdot \sqrt{X_1 \cdot (X_2 + X_3)} = 637\mu H \tag{10}$$

Since reset and secondary windings direction same they summed. Also from Steinmetz eqn. R_m can found. Where applied voltage is 48 V.

$$P_{fe} = 0.1375Watt \implies R_m = \frac{V^2}{P_{fe}} = 16.7k\Omega \tag{11}$$

- 3.2 MOSFET Considerations
- 3.3 Output Inductor Considerations
- 3.4 Capacitor Considerations
- 3.5 Diode Considerations
- 4 References

References

 $[1] \quad \textit{OR45959EC R Material Core}. \\ \text{https://www.mag-inc.com/Media/Magnetics/Datasheets/OR45959EC.} \\ \text{pdf.}$