**Middle East Technical University**

**Electrical and Electronics Department**

**EE463 - Static Power Conversion I**

**Simulation Project 1:**

**Diode Rectifiers**

**Project Group:** Team 13

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1. **Introduction**

This report includes the Simulink simulation results for the first simulation project of the course: EE463 Static Power Conversion I. The topics of interest in this simulation assignment can be listed as: single & three phase half bridge and three phase full bridge diode rectifiers. The interpretations of the effects of line inductances, line resistances and also non ideal diodes, along with some calculations for a number of parameters such as: the output average voltage and input current THD, can be found in the following sections of this document.

The main purpose of this simulation project is to properly document our knowledge on diode rectifiers, get ourselves accustomed to the newer concepts and to be able to comment on the effects of some of the parameters which were presented in the lectures, with the help of a new simulating tool: Matlab Simulink.

1. **Question 1: Single Phase Half Bridge Rectifiers**
   1. **Plots obtained in Simulink for the Output Voltage with Different Time Steps**

The screenshot of the single phase half bridge rectifier constructed in Simulink can be found in Figure 1, below.

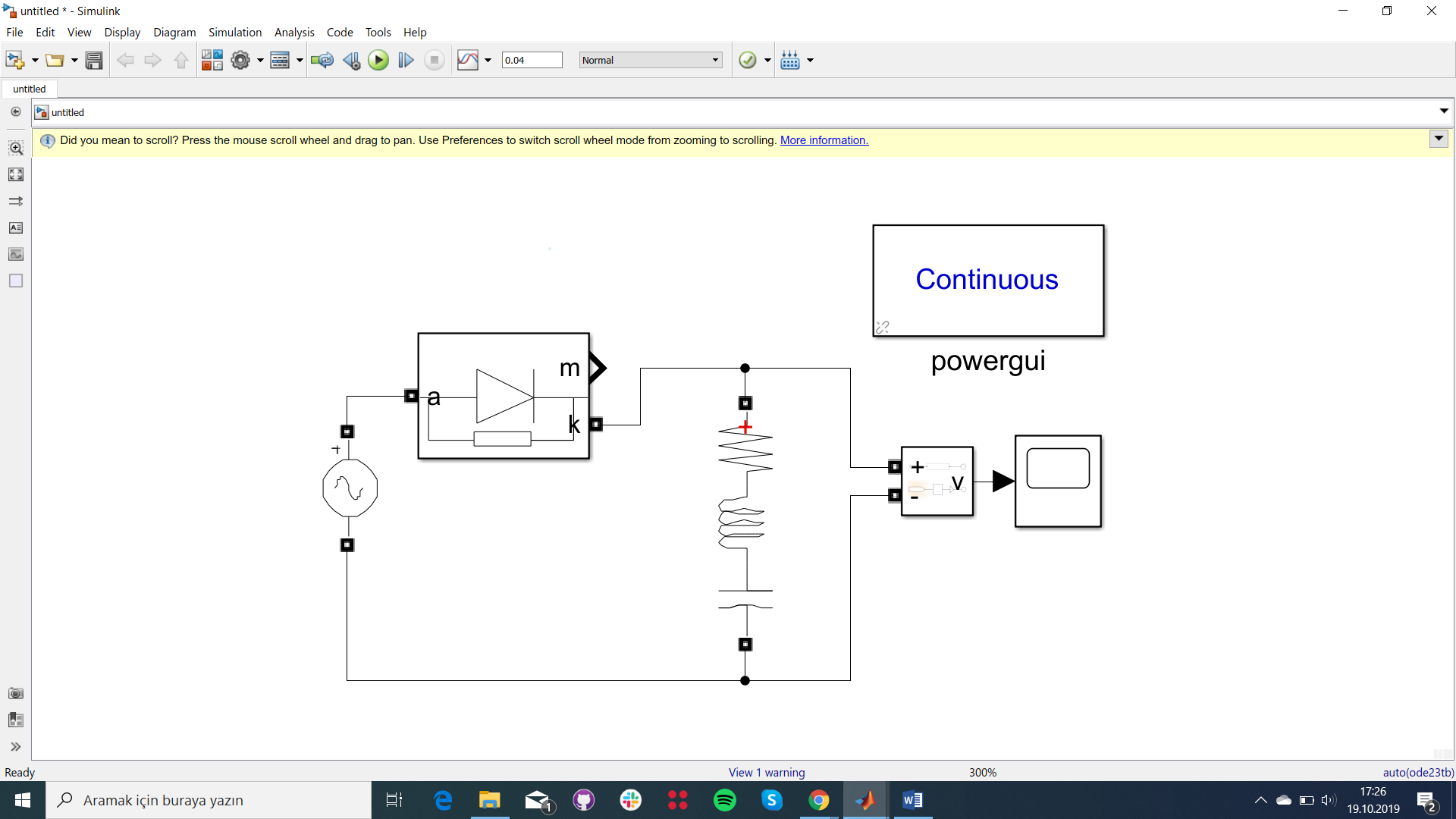


Figure 1. Simulink Schematic for the Single Phase Half Bridge Rectifier

The output voltage waveforms of the single phase bridge rectifier for step times of 1ns, 0.5ms and 5ms can be found in Figures 2,3 and 4, respectively.

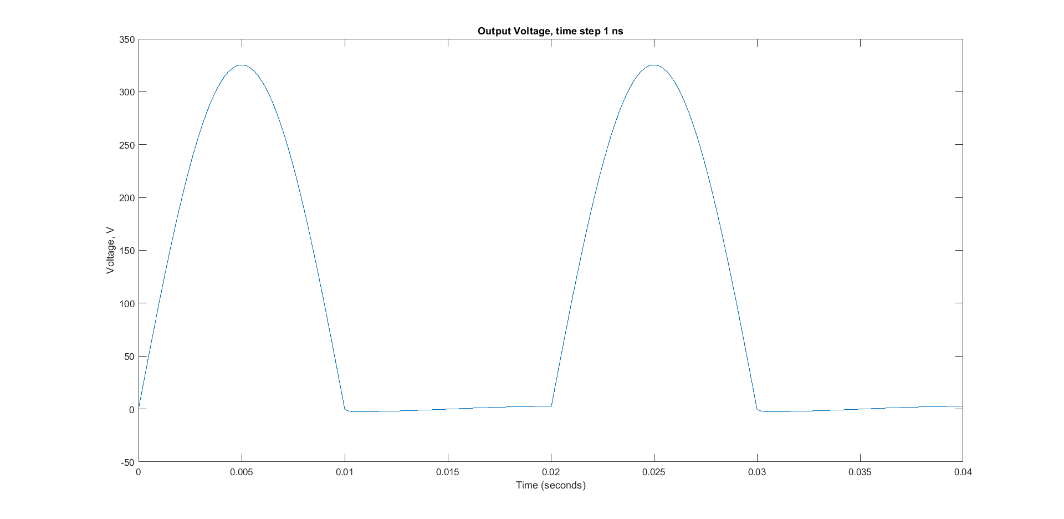


Figure 2. Output waveform of the single phase half bridge rectifier, when the time step = 1ns

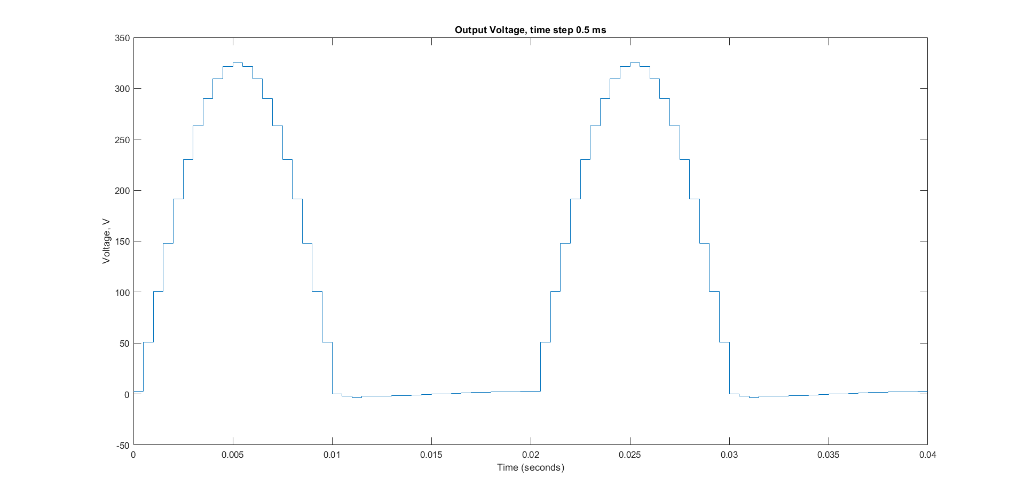


Figure 3. Output waveform of the single phase half bridge rectifier, when the time step = 0.5ms

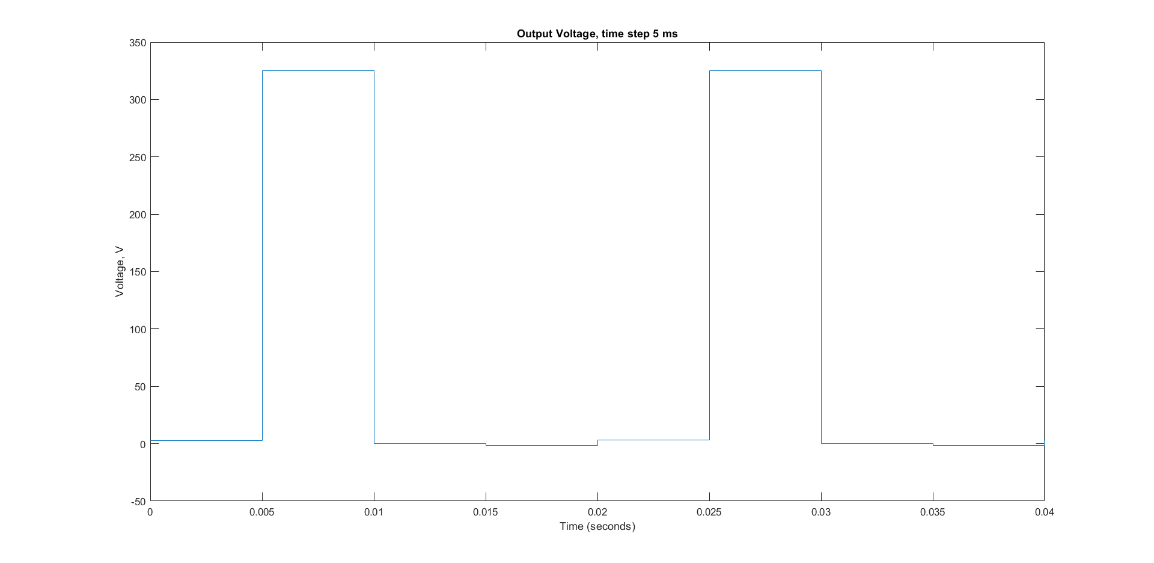


Figure 4. Output waveform of the single phase half bridge rectifier, when the time step = 5ms

* 1. **Significance of Step Time in Simulations**

As one can observe from the waveforms in the figures 2,3 and 4, the step time with which the simulation is executed, has a great effect on the resulting waveform.

The concept of the step time can be interpreted as the sampling rate at which the samples from the simulated signal is taken. Therefore, this parameter is directly linked with the shape of the outcome of the simulated waveform.

The sampling procedure is related with the sampling theorem, in which the minimum required sampling rate (in order to obtain the original signal without any changes in its frequency) is defined as the double of the frequency of the maximum frequency component that exists in the original signal.

If the step time is chosen too small, this corrupts the original curve of the signal, due to the existence of ‘aliasing’, which can be explained in other words as: the appeareance of a higher frequency signal as low frequency due to the lack of sampled points. An example for such a situation can be observed in Figure 4.

If the step time is chosen too large, theis would enable us to have an accurate representation of the original curve (an examplification of this situation can be seen in Figure 2), but unfotunately having too many sampled points would slow down the procedure and it would occupy quite a lot of storage room.

* 1. **Output Average Voltage and Input Current THD Calculations**
  2. **Comparison of Analytical Results with Quantities Obtained Through Simulation Tools**

1. **Question 2: Single Phase Full Bridge Rectifiers**
   1. **Ls and Rs in Practical Applications**
   2. **Finding Minimum Output Filter Capacitance**
   3. **Plots for Output Voltage, Input Current THD and Power Factor**
   4. **Measurments of Output Voltage, Input Current THD and Power Factor**
   5. **Simulation without Ls and Rs**
   6. **Stresses on the Diodes when DiodeXXX Is Used without the effects of Ls and Rs**
   7. **Rectifier Efficiency without the effects of Ls and Rs**
2. **Question 3: Three Phase Full Bridge Diode Rectifiers**
   1. **Plots of Output Voltage and Phase-A Input Current when Ls=0**
   2. **Output Average Voltage Calculations and Comparison with Simulated Results (Ls=0)**
   3. **Harmonic Analysis up to 30th Harmonic**
   4. **Plots of Output Voltage and Phase-A Input Current when Ls=1mH**
   5. **Output Average Voltage Calculations and Comparison with Simulated Results (Ls=1mH)**
   6. **Comparison of the Harmonic Content of the Input Current for Different Values of Ls**
3. **Question 4: Feedback**