

**MIDDLE EAST TECHNICAL UNIVERSITY**

**DEPARTMENT OF ELECTRICAL and ELECTRONICS ENGINEERING**

**EE 463 – STATIC POWER CONVERSION I**

**Term Project Report**

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# Introduction

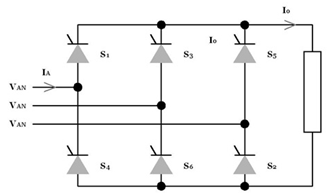
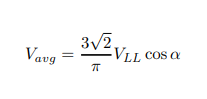
In this report, topologies that can be used in the project were discussed and compared. Then the simulation details and the details of the topology used were explained. Equations used in design, analytical calculations and design decisions were mentioned. The progress steps of the design were explained. Then, the points to be considered while designing PCBs were discussed. Finally, the selected components and their properties were mentioned.

# Topology Selection

In order to control the output current in the project, we need to use controlled rectifier or uncontrolled rectifier + buck converter topologies. Therefore, these topologies have been discussed.

## Three Phase Thyristor

* Thyristor rectifier topologies are generally used in systems with high voltage and current.
* In this topology, the average output voltage of the thyristor rectifier can be controlled by adjusting the firing angle of the thyristors. Thus, output current can be adjusted with the firing angle.



*Figure 1. Three Phase Thyristor Topology and its Output Voltage Equation*

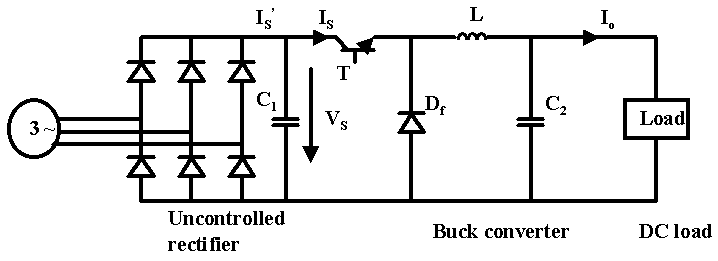
### Advantages

* This topology consists of only 6 thyristors; therefore, the topology structure is not complex.
* In this topology, the input current THD is low because the third harmonics of the input current are not observed.

### Disadvantages

* Although this topology contains only 6 thyristors, thyristors are expensive components compared to diodes.
* To control six thyristors at the same time, six gate drivers and other components are required, which increases the cost considerably.
* Operating and controlling 6 gate drivers synchronously is a very complex process.

## Three Phase Diode Rectifier with Buck Converter

* DC / DC converters are used in many areas such as electrical vehicles, PV - Grid systems.
* In this topology, AC voltage will be converted to DC with an uncontrolled rectifier and then to the desired DC voltage value with a buck converter.
* In this topology, the duty cycle of the buck converter is controlled by sensing the output current.



*Figure 2. Three Phase Rectifier + Buck Converter Topology and its Output Voltage Equation*

### Advantages

* In this topology, the output voltage ripple can be easily reduced by placing a capacitor at the rectifier output and controlling the buck converter output voltage.
* This topology requires only one gate signal. This is a great advantage over controlled rectifiers. Also, the gate signal does not need to be synchronous.
* Although this topology requires more components, its cost is considerably lower than 3 phase-controlled rectifiers.
* In this topology, the input current THD is low because the third harmonics of the input current are not observed.

### Disadvantages

* In this topology, its efficiency will theoretically be less since MOSFET and diode are used in addition to 3 phase-controlled rectifiers.
* Using an LC filter increases the size of the topology.

# Detailed Simulation

## Uncontrolled 3 Phase Rectifier

## Buck Converter

## General Structure and Calculations of Buck Converter

The DC voltage, which is the output of the three phase full wave diode rectifier described in part …, have been connected into an buck converter to step down the voltage value from 250 V (Vmax) to 25 V nominal value for charging the battery. The main structure of the buck converter can be seen from the Figure …. An approximate inductance and capacitance values have been calculated by specifying the on and off cases of switching element of the buck converter, which is MOSFET in this case.

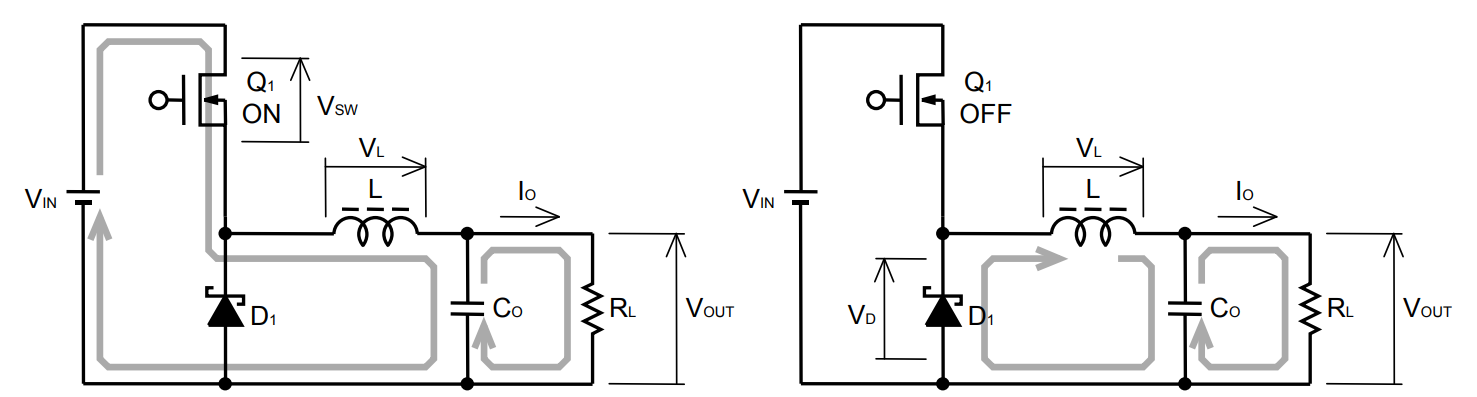


Figure 3: Buck converter ON and OFF cases of the MOSFET

The output current of the buck converter is intended to be 2A with %20 of average current as ripple, while the battery is being charged. Therefore, ON and OFF states of the MOSFET have been considered separately for the same output current level to specify a formula for calculating required inductance value to apply intended output current ripple, Formula 1.

|  |  |  |
| --- | --- | --- |
|  |  | (1) |

: Input Voltage (V) = 250 V

: MOSFET ON state voltage drop (V) = 0.3 V

: Output Voltage (V) =25 V

: Diode forward voltage drop (V) = 0.7 V

: Switching frequency (Hz) = 10kHz

: Output current ripple ratio = 0.2 A/A

: MOSFET ON state voltage drop = 2 A

The values specified above taken into consideration to calculate only an approximate inductance. According to these L have been calculated as 5.76 mH, which is known to be very high value for an inductor. The main reason for such high inductance is to decrease the ripple on the output current. Moreover, since the frequency value is located in the denominator as seen in the Formula (1), the use of low frequency caused the inductor value to increase. Because, the ripple voltage already specified before, frequency is choosed to be the factor that can be changed to adjust the inductance value. Considering the effects of the frequencies in the 10 kHz- 100 kHz range on the conduction losses and switching losses, 50 kHz have been seen as a suitable value to reduce the inductor value by keeping the overall power loss level sufficiently low. Therefore, the inductor value have been achieved to decrease to 1.2 mH, which is 5 times less than the value calculated before, by changing the switching frequency with 50 kHz.

Yunus’un power loss hesaplamaları eklenebilir.

While the inductor is effective in adjusting the output current ripple, capacitor plays an important role in output voltage ripple. Moreover, ESR of the capacitor which stands for ‘Equivalent Series Resistance’ is also an important factor while choosing a capacitor, as can be seen from the Formula (4), where ESL value is assumed to be 0 H. Both ON and OFF states of the MOSFET have been taken into consideration while analyzing the factors affecting the capacitor value.

|  |  |  |
| --- | --- | --- |
|  | [] | (2) |

: Maximum Input Voltage (V)

: Inductor ripple current (A)

: Output capacitor (F)

ESR: Equivalent series resistor of output capacitor (Ω)

ESL: Equivalent series inductor of output capacitor (H)

The finilized approximation used in the capacitor calculations can be seen from Formula (4).

|  |  |  |
| --- | --- | --- |
|  |  | (3) |

|  |  |  |
| --- | --- | --- |
|  |  | (4) |

Hence, approximate capacitor value is calculated as with 0.2 Ω ESR value. Because one of the most important parameters are specified as the ESR in output voltage ripple and capacitor calculations, it was decided to pay attention to the small ESR value, during the selection of capacitor. As the output voltage and current ripples of the converter is already specified before, an upper limit has been determined fort he ESR value.

## Buck Converter Simulation with Estimated Values

An example circuit have been set, using the estimated values, which are calculated at part …. The aim of this circuit is to show that the calculated inductor and capacitor values can reduce the input voltage level to the desired output voltage level with the specified ripple voltage as can be seen from Figure …. Moreover, inductor effect on output current waveform with the addition of a control system can be seen from Figure ….

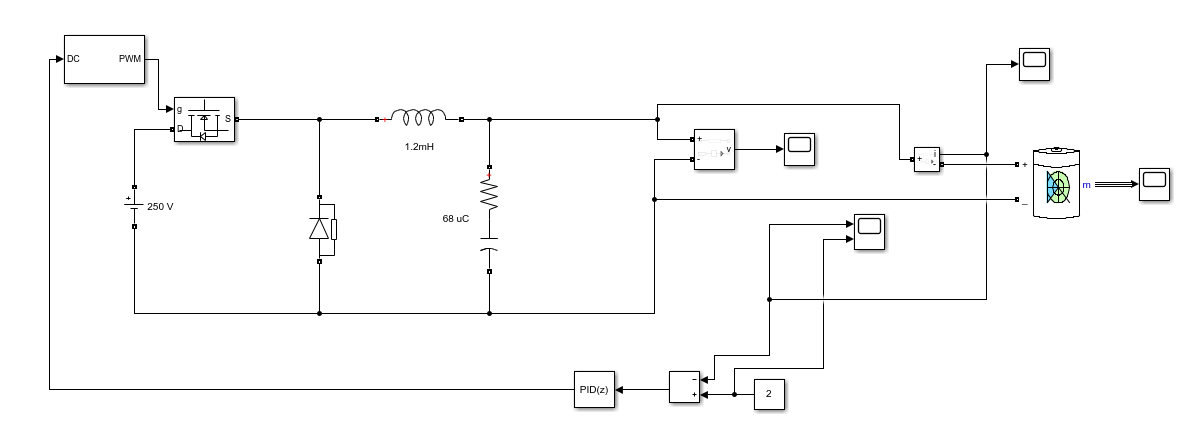


Figure 4: Buck converter Simulink circuit with PI controller

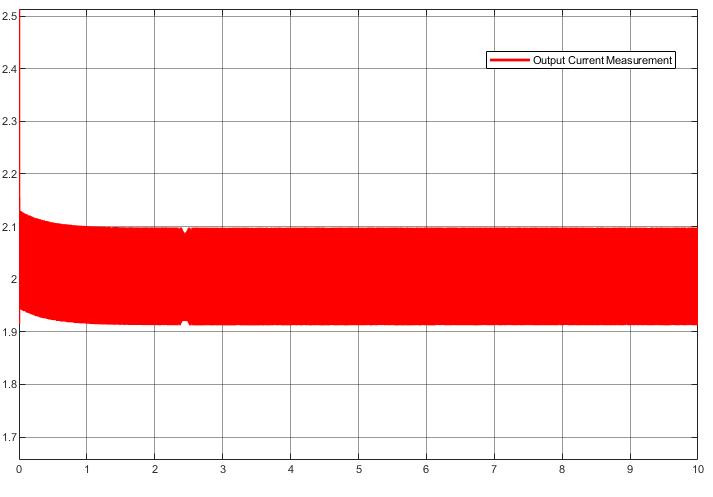
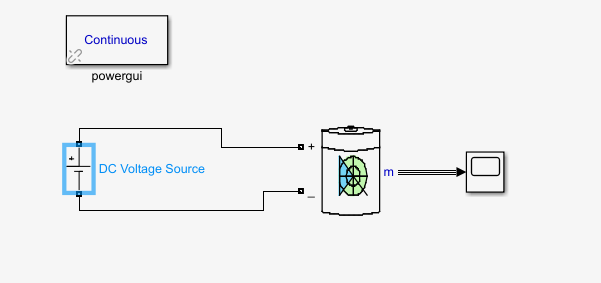
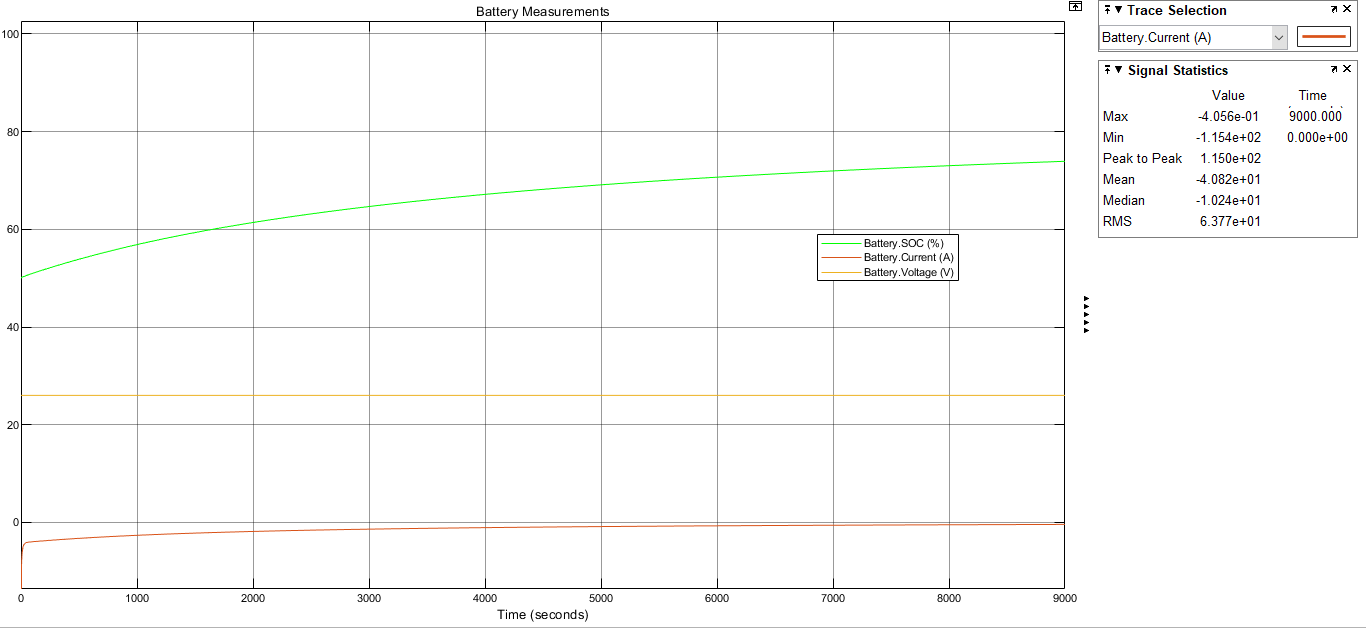


Figure 5: Output current measurement of the buck converter

## Battery

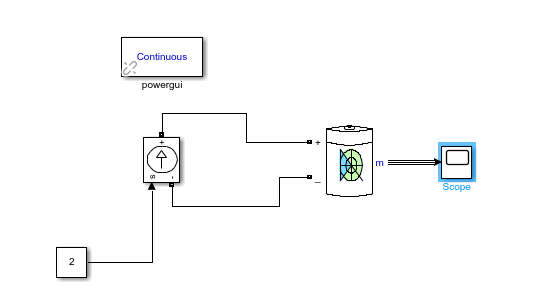
In this part, battery part will be analysed, and some test will be applied.

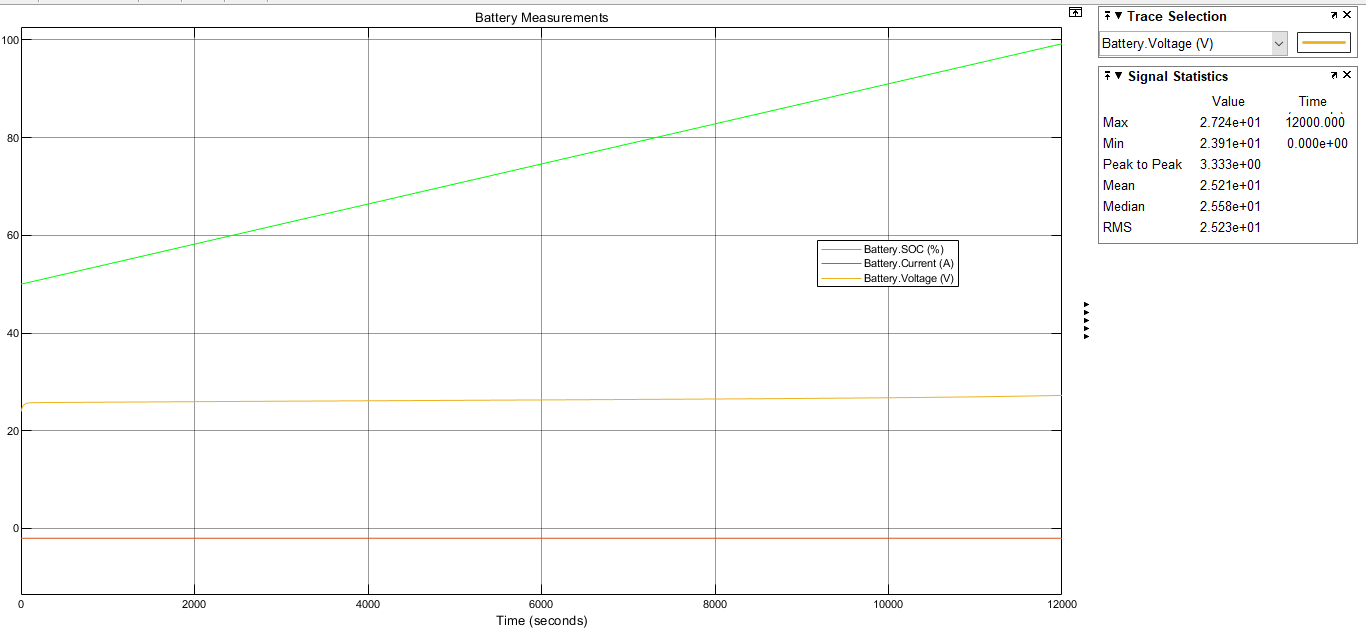




*Figure. Constant Voltage Battery Test*

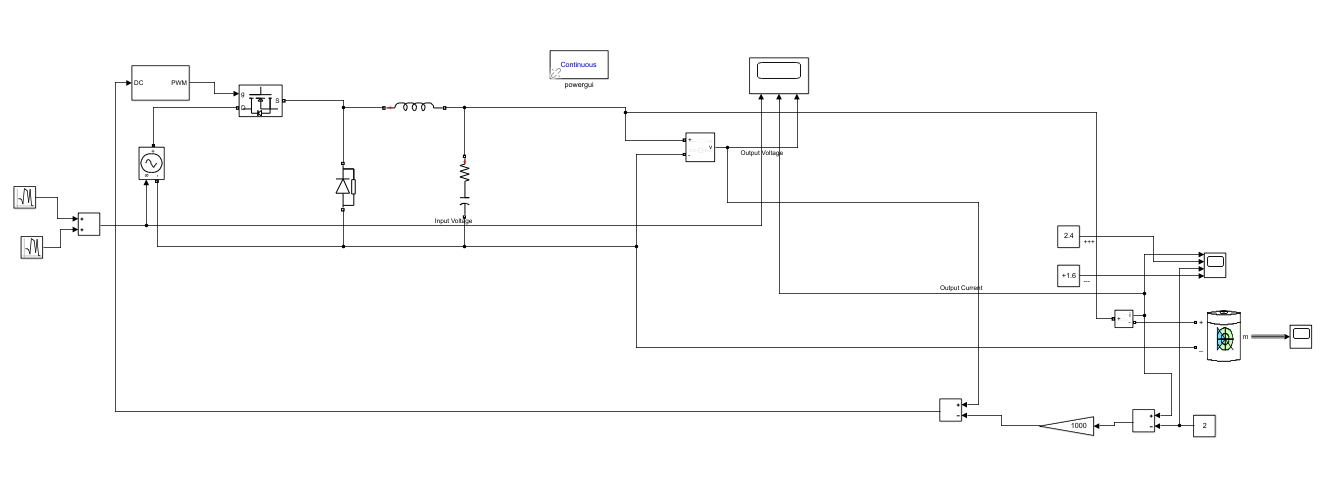
In this test, battery behaviour was observed when a constant voltage of 25 volts was applied to the battery. As can be seen from the graph, it is not enough to give constant input voltage to obtain constant current. Therefore, an increasing voltage must be applied to the battery.

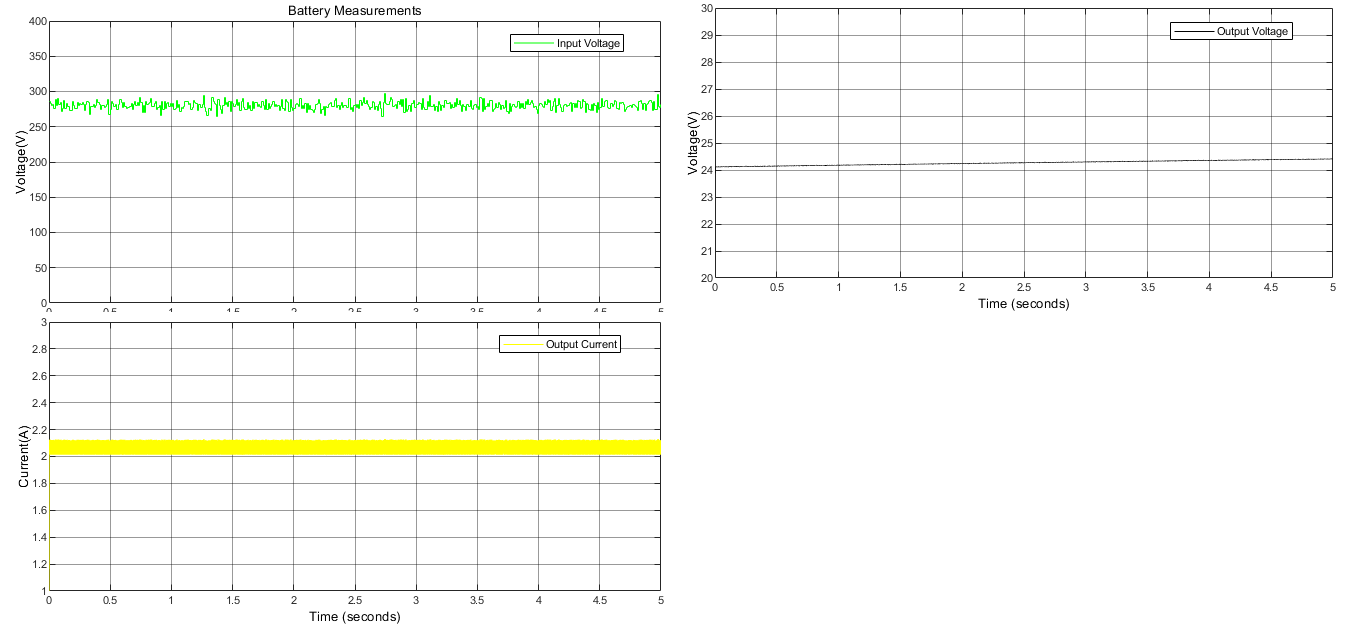




*Figure. Constant Current Battery Test*

In this test, a constant input current of 2 amps was applied to the battery. As can be seen from the graph, an increasing voltage between 24-27 volts should be applied to the battery to charge from %50 to %100.





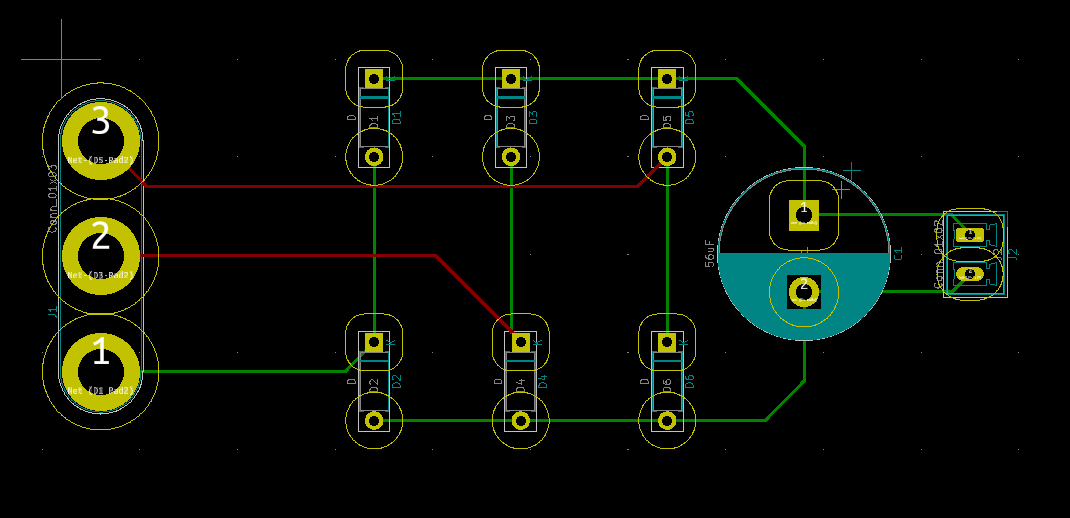
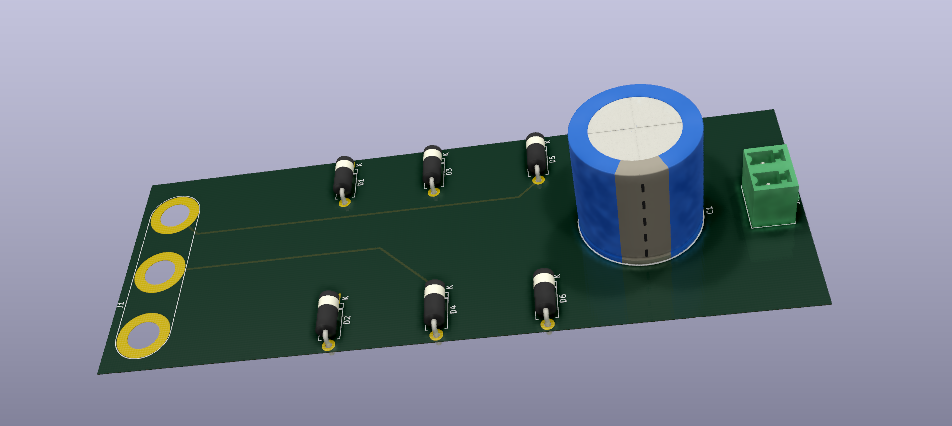
*Figure. Buck Converter and Battery Test with P controller*

In this test, the battery and buck converter part are connected. Buck converter's input is symbolized as noisy input with an average value of 280 V. In the test, an offset was observed in the output, so the P controls part will be converted into PI controls. Also, controller is simplified. In the following sections, all parts were combined with the rectifier and simulations were made in discrete time.

## Controller

## Completed Simulation

# PCB Design



*Figure. PCB Design of Uncontrolled Rectifier*

In this part, the points we pay attention to while designing PCBs will be mentioned. Until now, only the PCB design of the rectifier part has been made for practical purposes. Because as the components used change, it can create big changes in PCB design. For example, thermal conditions have not yet been taken into consideration, but will be available in future designs.

* The footprint of the components will be added to the design by looking at the datasheet. If that footprint is not already available in the program library used, the footprint will be created.
* Considering high voltage conditions, suitable trace spacing will be left according to IPC2221B standards.
* Space will be left for the heatsink, considering the components that consume high power.
* The appropriate track width will be calculated considering the IPC2221 standard. Magnitude of the current should be considered because high current causes heating in traces.
* Silk layer will be used as much as possible to make the design more descriptive.
* PCB thickness will be as recommended in the datasheet of the components used. For example, 1 oz / ft2 is chosen for the rectifier.
* If necessary, control traces will be drawn on the inner layer to avoid noise.
* 90-degree angles will be avoided while drawing traces. Because this reduces the trace thickness, 45-degree angles should be used instead.
* If necessary, nodes will be made at important points.

# Component Selection

## Component Selection for Buck Converter

**Inductor**

The main point while choosing an inductor is its current ratings in addition to inductance. Eventhough, 2A average values expected on top of the inductor, It was observed in … that the current value on the inductor can reach peak values of 2.15 A. Therefore, special attention has been given to the saturation curretns during the selection of the inductor. The inductor models which use ferrite core as magnetic material have been preferred due to their ability to operate at higher frequencies and allowing higher inductor values with smaller size. Two different inductor values have been compared to determine the advantages and disadvatanges each have.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | Inductance (mH) | Current Rating (A) | Saturation Current | Price |
| 1140-272K-RC | 2.7 | 2.2 | 3.9 | 9.56 $ |
| AIRD-03-152K | 1.5 | 2 | 3.4 | 5.52 $ |

Eventhough, high inductance value can decrease the output current ripple even further, considering the price of 1140-272K-RC, choosing AIRD-03-152K evaluated to be more appropriate. When the %10 tolerance of the AIRD-03-152K also included, it can be said that this inductor will not cause any problem in providing the required inductance value fort he converter to supply current within the previously specified ripple current range.

**Capacitor**

One of the most important factors in capacitor selection is the ESR which value. It is already calculated in the part … and an upper bound have been set as 0.25 due to the rating between output voltage and current ripple values. It can be also seen from the Formula … that the required capacitor value increases as the ESR value choosed to be close to 0.25. Therefore, capacitors with around 0.2 Ω ESR values tried to be compared according to their capacitance values and most importantly voltage ratings as capacitor should be able to charge at least until the value of converter output.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | Capacitance (µF) | Voltage Rating (V) | ESR (mΩ) | Price |
| ESC686M035AE3AA | 68 | 35 | 220 | 0.30 $ |

The selected capacitor, ESC686M035AE3AA, can be seen from the Table with its values seen as important.

**Diode**

As the diode will guide the converter during the OFF times of the MOSFET, which will be about %90 dur to the input and output voltage rate of the rectifier, it choosed to be süper fast rectifier diode, which lowers the switching losses due to its fast reverse recovery time. While choosing diode, it should be also considered that the voltage rating accros it will be aapproximately around 300 V coming from the rectifier voltage.

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  | Forward Voltage (V) | Max DC Blocking Voltage | Maximum IF (A) | trr (ns) | Price |
| SF25G | 1.25 | 300 | 2 | 35 | 0.08632 $ |

**MOSFET**

The most important parameter while choosing a mosfet for switching applications is its Ron resistance to decrease loss during the switching operation. Different type of mosfets have been compared and SPN04N60S5 have been found to be more suitable to use in the converter, considering its resistance and price.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | VDS (V) | RDS(on) (Ω) | ID (A) | Price |
| SPN04N60S5 | 600 | 0.95 | 0.8 | 0.986 $ |
| SPN03N60S5 | 600 | 1.4 | 0.7 | 1.54 $ |
| STQ3N45K3-AP | 450 | 4 | 0.6A | 0.76 $ |

# Conclusion

In the ongoing project, inductor design will be made if it needed. Appropriate design will be applied for the selected controls. PCB designs will be made according to the points mentioned. Thermal simulation will be done, heatsink will be placed where necessary and thermal calculations will be made.