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omnetpp-mem32\_L2-4way      Simulation Results

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Number of reference types:

Number of reads	=	2009285423	[20.09%]
Number of writes	=	1237898222	[12.38%]
Number of inst	=	6752816355	[67.53%]
Total	=	10000000000	

Total cycles for all activities:

Cycles for reads	=	25056751184	[42.73%]
Cycles for writes	=	6345933748	[10.82%]
Cycles for inst	=	27243529031	[46.45%]
Total time	=	58646213963	

Average cycles per activity:

Read	=	12.47
Write	=	5.13
Inst	=	8.68

Ideal: Exec. Time = 16752816355; CPI = 2.48

Ideal mis-aligned: Exec. Time = 26618750504; CPI = 3.94

Memory level: L1i

Hits	=	11123145966	[97.05%]
Misses	=	338100123	[2.95%]
Total	=	11461246089	

Kickouts = 338099867, Dirty kickouts = 0, Transfers = 338100123

Memory level: L1d

Hits	=	5669434506	[95.62%]
Misses	=	259457110	[4.38%]
Total	=	5928891616	

Kickouts = 259456854, Dirty kickouts = 105849456, Transfers = 259457110

Memory level: L2

Hits	=	488279575	[69.42%]
Misses	=	215127114	[30.58%]
Total	=	703406689	

Kickouts = 215126602, Dirty kickouts = 54668770, Transfers = 215127114

Cost analysis:

L1i cache cost	=	\$400
L1d cache cost	=	\$400
L2 cache cost	=	\$150
Memory cost	=	\$175
Total cost	=	\$1125