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Parts 1&2:

Code

Loading work.behavioralDecoder

En A0 A1 | O0 O1 O2 O3 | Expected Output

#000|000|All false

#010|000|All false

#001|000|All false

#0 1 1 | 0 0 0 0 | All false

#100|1000|O0Only

#110|0101Only

#101|001|02Only

#111 | 0001 | O3 Only

vsim

Start time: 03:56:52 on Sep 25,2015

Loading work.testFullAdder

Loading work.behavioralFullAdder

A B Ci | COut Sum | Expected Output

#000 | 00 | Both False

#010| 01| Sum True

#100|01|Sum True

#11 0 | 10 | COut True

#001 | 01 | Sum True

#011 | 10 | COut True

#10 1 | 10 | COut True

```
#11 1 | 11 | Both True
```

vsim

Start time: 03:56:55 on Sep 25,2015

Loading work.testMultiplexer

Loading work.behavioralMultiplexer

a1 a0 | in0 in1 in2 in3 | out | Expected Output

#00|0000|0

#00|0001|000

#00|0010|0

#00|0011|00

#00|0100|0

#00|0101|00

#00|0110|00

#00|0111|00

#00|1000|10

#00|1001|1

#00|1010|10

#00|1011|1 0

#00|1100|10

#00|1101|1 0

#00|1110|10

#00|1111| 1 0

#01|0000|0 1

#01|0001|0 1

#01|0010|1 1

```
#01|0011| 1
#01|0100|0
              1
#01|0101|0
              1
#01|0110| 1
              1
#01|0111|1
              1
#01|1000|0
              1
#01|1001|0
              1
#01|1010|1
              1
#01|1011|1
#01|1100|0
              1
# 0 1 | 1 1 0 1 | 0
              1
#01|1110|1
              1
#01|1111|1
              1
#10|0000|0
              2
#10|0001|0
              2
#10|0010|0
              2
#10|0011|0
              2
#10|0100| 1
              2
#10|0101|1
              2
#10|0110| 1
              2
#10|0111| 1
              2
#10|1000|0
              2
#10|1001| 0
              2
```

#10|1010|0

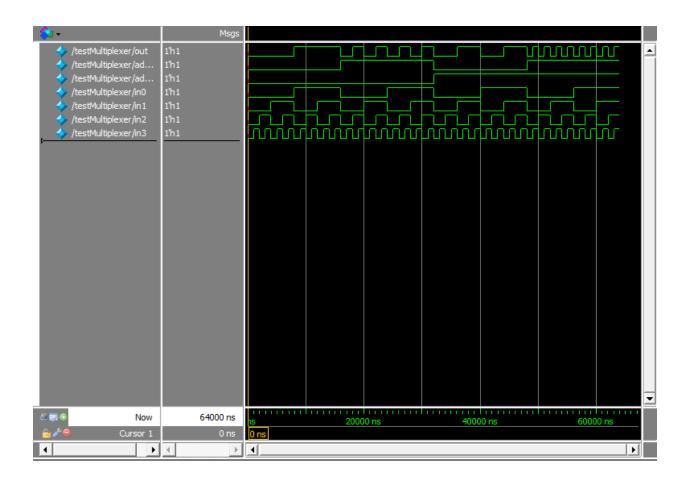
#10|1011| 0

2

2

```
#10|1100|1
#10|1101|1
              2
#10|1110|1
              2
#10|1111|1
              2
#11|0000|0
              3
#11|0001| 1
              3
#11|0010|0
              3
#11|0011|1
              3
#11|0100|0
              3
#11|0101| 1
              3
#11|0110| 0
              3
#11|0111| 1
              3
#11|1000|0
              3
#11|1001| 1
              3
#11|1010| 0
              3
#11|1011| 1
              3
#11|1100|0
              3
#11|1101|1
              3
#11|1110|0
              3
#11|111|1
              3
```

Waveform



Parts 2&3

Code

Loading work.structuralDecoder

En A0 A1 | O0 O1 O2 O3 | Expected Output

#000|000|All false

0 1 0 | 0 0 0 0 | All false

#001|000|All false

0 1 1 | 0 0 0 0 | All false

#100|100Only

```
#110|0101Only
#101|001|02Only
#111 | 0001 | O3 Only
# vsim
# Start time: 03:43:59 on Sep 25,2015
# Loading work.testFullAdder
# Loading work.structuralFullAdder
# A B Ci | COut Sum | Expected Output
#000|0
             0 | Both False
#010|0
             1 | Sum True
#100|0
             1 | Sum True
#110|1
             0 | COut True
#001|0
             1 | Sum True
#011|1
             0 | COut True
#101|1
             0 | COut True
#111|1
            1 | Both True
# vsim
# Start time: 03:44:03 on Sep 25,2015
# Loading work.testMultiplexer
# Loading work.structuralMultiplexer
# a1 a0 | in0 in1 in2 in3 | out | Expected Output
#00|0000|0
```

#00|0001|0

#00|0010|0

#00|0011|0

0

#00	0100	0	0
#00	0 1 0 1	0	0
# 0 0	0110	0	0
# 0 0	0 1 1 1	0	0
#00	1000	0	0
#00	1001	0	0
#00	1010	0	0
#00	1011	0	0
#00	1 1 0 0	0	0
#00	1 1 0 1	0	0
#00	1110	0	0
#00	1111	0	0
# 0 1	0000	0	1
# 0 1	0001	0	1
# 0 1	0010	0	1
# 0 1	0011	0	1
# 0 1	0100	0	1
# 0 1	0 1 0 1	0	1
# 0 1	0 1 1 0	0	1
#01	0 1 1 1	0	1
# 0 1	1000	0	1
#01	1001	0	1
#01	1010	0	1
#01	1011	0	1
# 0 1	1 1 0 0	0	1

#01 1101	0	1	
#01 1110	0	1	
#01 1111	0	1	
#10 0000	0	2	
#10 0001	0	2	
#10 0010	0	2	
#10 0011	0	2	
#10 0100	0	2	
#10 0101	0	2	
#10 0110	0	2	
#10 0111	0	2	
#10 1000	0	2	
#10 1001	0	2	
#10 1010	0	2	
#10 1011	0	2	
#10 1100	0	2	
#10 1101	0	2	
#10 1110	0	2	
#10 1111	0	2	
#11 0000	0	3	
#11 0001	1	3	
#11 0010	0	3	
#11 0011	1	3	
#11 0100	0	3	
#11 0101	1	3	

```
#11|0110| 0
#11|0111|1
                3
# 1 1 | 1 0 0 0 | 0
                 3
#11|1001|1
                 3
#11|1010| 0
                 3
#11|1011|1
#11|1100|0
                 3
#11|1101|1
                 3
#11|1110| 0
#11|111|1
                3
```

Waveform

