```
%3 = alloca i32, align 4
                                             %4 = alloca i32, align 4
                                             %5 = alloca ptr, align 8
                                             %6 = alloca i32, align 4
                                             store i32 0, ptr %3, align 4
                                             store i32 %0, ptr %4, align 4
                                             store ptr %1, ptr %5, align 8
                                             %7 = call noundef nonnull align 8 dereferenceable(8) ptr
                                             ... @ ZStlsISt11char traitsIcEERSt13basic ostreamIcT ES5 PKc(ptr noundef nonnull
                                             ... align 8 dereferenceable(8) @ ZSt4cout, ptr noundef @.str)
                                             store i32 0, ptr %6, align 4
                                             br label %8
                                                                    %9 = load i32, ptr %6, align 4
                                                                     %10 = load i32, ptr %4, align 4
                                                                     %11 = icmp slt i32 %9, %10
                                                                     br i1 %11, label %12, label %17
12:
                                                                                         17:
%13 = call noundef nonnull align 8 dereferenceable(8) ptr
                                                                                         %18 = call noundef nonnull align 8 dereferenceable(8) ptr
... @ ZStlsIStl1char traitsIcEERStl3basic ostreamIcT ES5 PKc(ptr noundef nonnull
                                                                                         ... @ ZStlsIStl1char traitsIcEERStl3basic ostreamIcT ES5 PKc(ptr noundef nonnull
... align 8 dereferenceable(8) @ ZSt4cout, ptr noundef @.str.1)
                                                                                         ... align 8 dereferenceable(8) @ ZSt4cout, ptr noundef @.str.2)
br label %14
                                                                                          ret i32 0
                                              14:
                                              %15 = load i32, ptr %6, align 4
                                               %16 = add nsw i32 %15, 1
                                                store i32 %16, ptr %6, align 4
                                                br label %8, !llvm.loop !6
```

CFG for 'main' function