



COMSM0053 Advanced Computer Architecture

MyArch – A Simulator for a RISC-y Architecture

Student Name **Taharka Okai (zp19374)**
Student ID **1815919**

1 Introduction

2 Architecture Features

2.1 Addressing Modes

This architecture supports the following addressing modes:

- `reg` - Register addressing mode
- `imm` - Immediate addressing mode

3 Instruction Set

3.1 Instruction Structure

3.2 Arithmetic

3.2.1 `add`

3.2.2 `sub`

3.2.3 `mul`

3.2.4 `div`