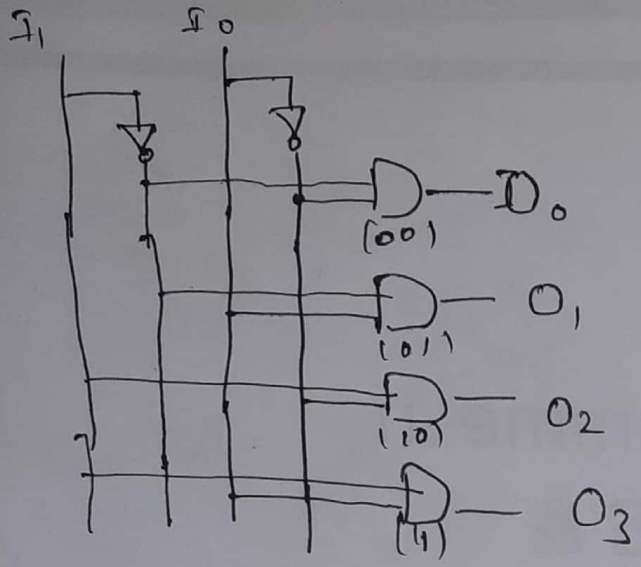
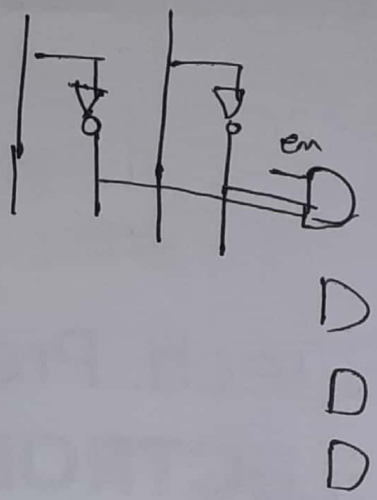
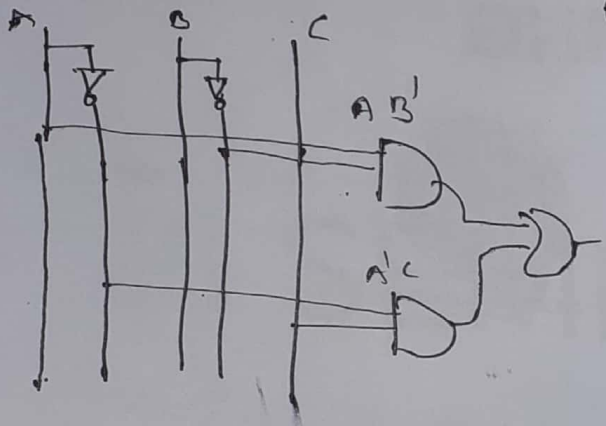
2×4 decoder

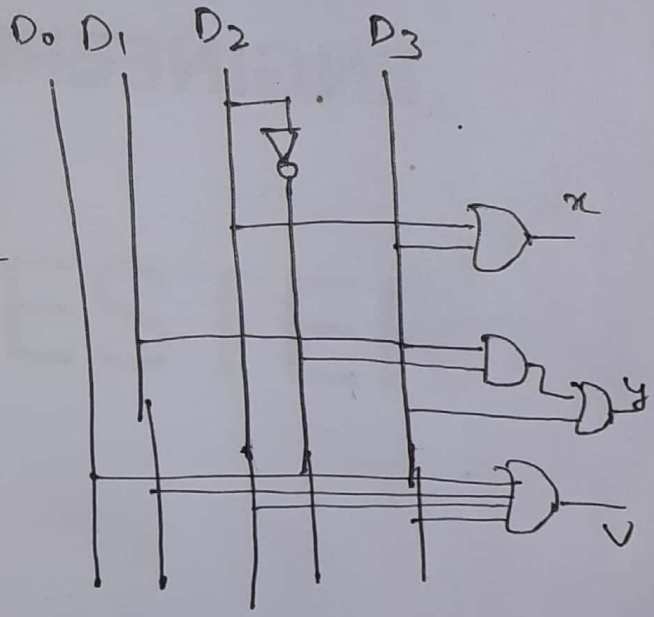


2×4 decoder with Enable

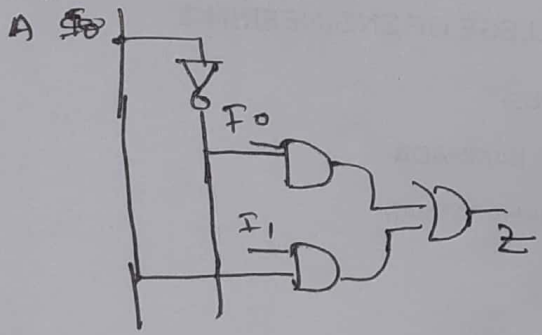


$Y = AB + A'C$

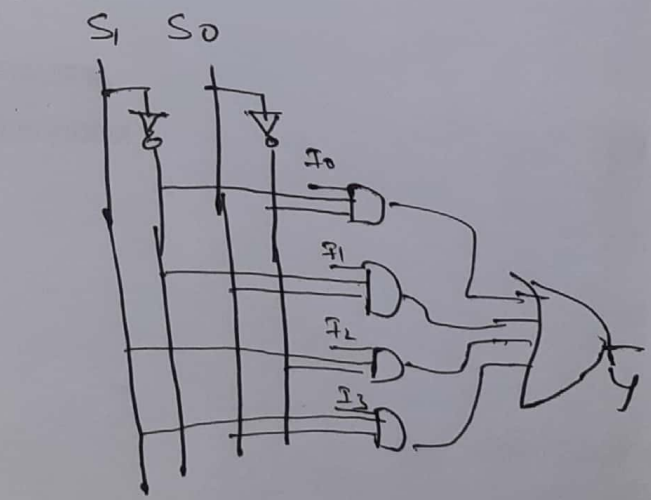




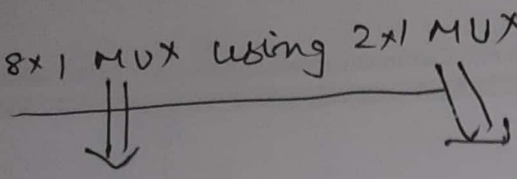
2×1 MUX



4×1 MUX



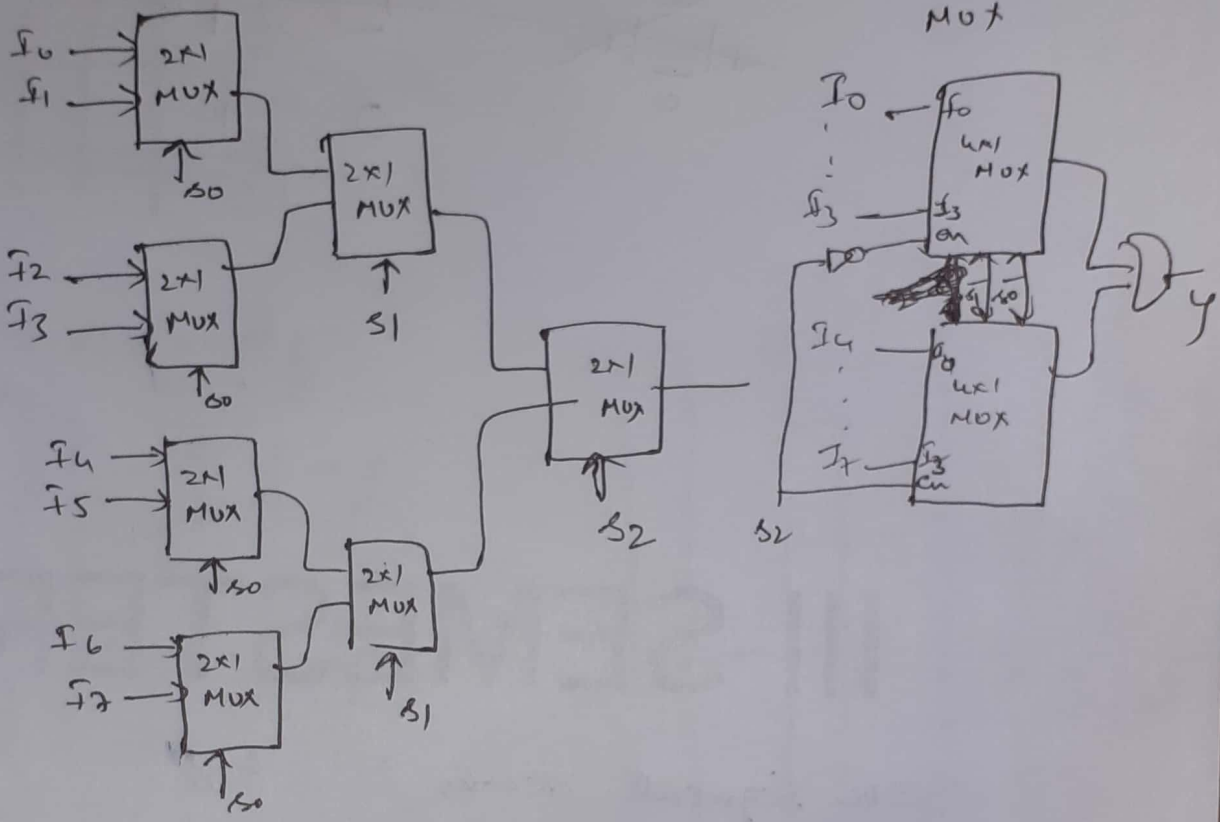
8×1 MUX using 2×1 MUX



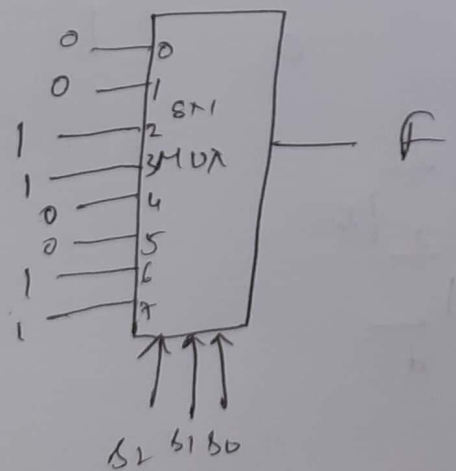
3 Select line
8 data i/ps

1 Select line
2 data i/p

8×1 MUX using 4×1 MUX



Design a 8×1 MUX design a logic circuit to realize the following Boolean function $F(A,B,C) = \Sigma m(2,3,6,7)$



$f(A,B,C) = \Sigma m(4,5,6,7)$ using 8×1 MUX

Seven Segment decoder